



RF OUT

RF CLK
27.042MHz

CLK
76.8kHz

PULSE
CONTROL

IMPULSE OUTPUT
SWITCH

VCC

BOOSTED CIRCUIT

BATTERY

CHARGE CONTROL CIRCUIT

POWER INPUT

POWER
SAMPLING

LOW POWER
REPORT

KEY CODE
PULSE OUT

POWER ADJUST

SCLK IN

SDIO IN

PHOTOTUBE



CLK
13.5MHz

Title		
See	Number	Revision
Ad		
Drawn	1006-6-90	Sheet of
File	E:\proj\MyDrawings\KMD10W-FX-SCHEM.DWG	