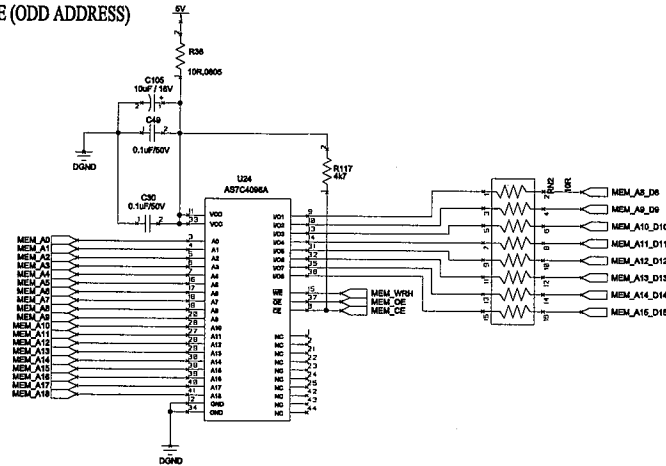
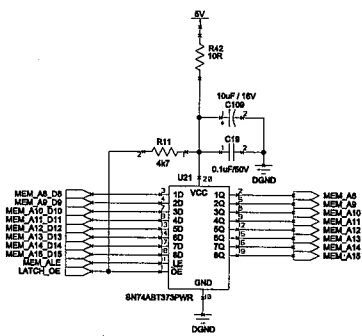
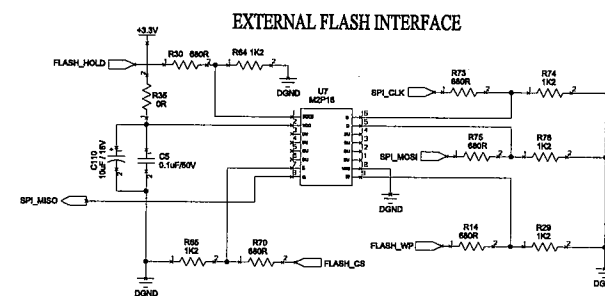
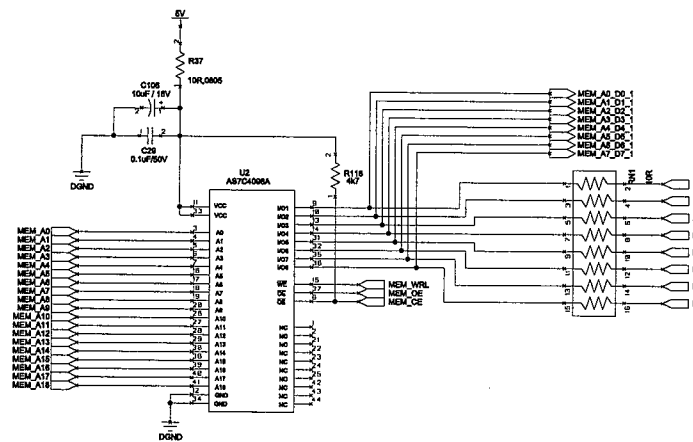
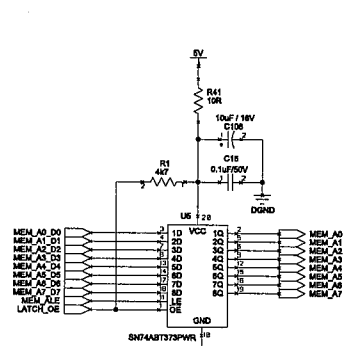




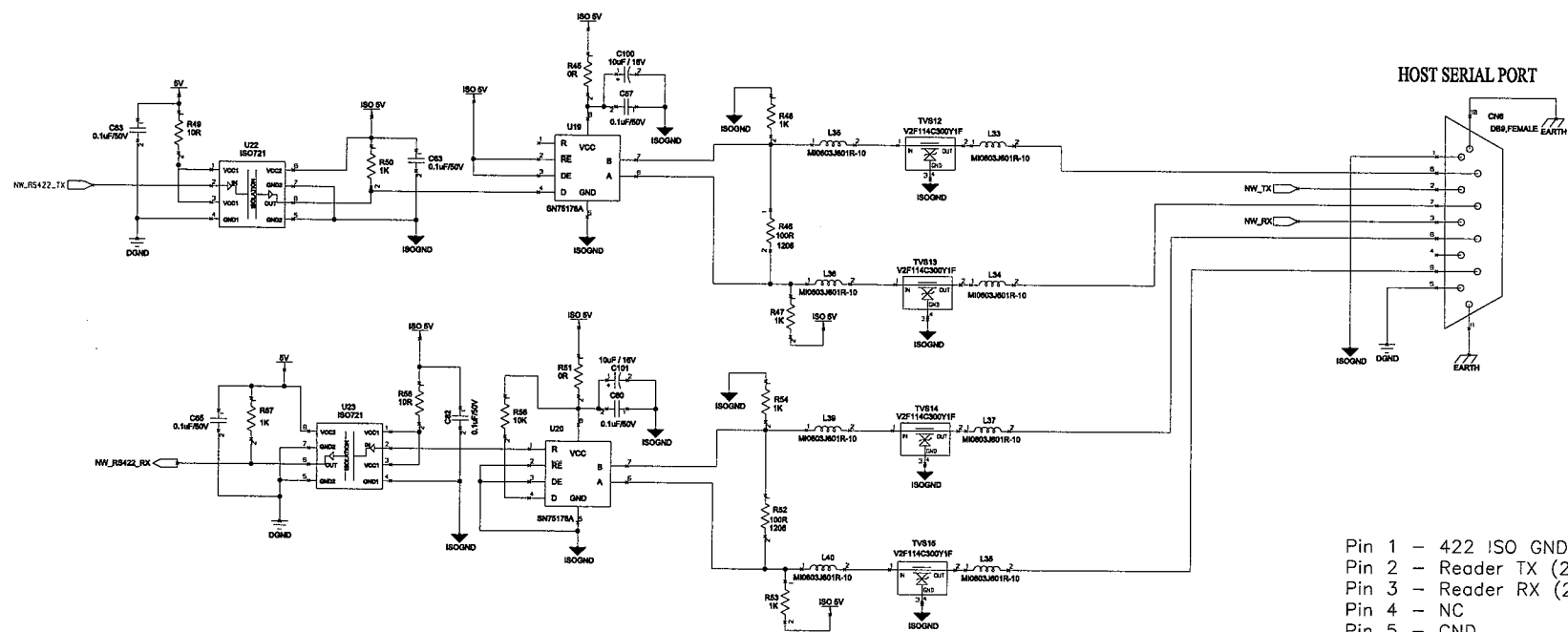
### EXTERNAL MEMORY INTERFACE (ODD ADDRESS)



**NOTE:**  
ALL RESISTORS ARE -0603 PACKAGE UNLESS OTHERWISE SPECIFIED  
ALL CAPACITORS ARE -0805 PACKAGE UNLESS OTHERWISE SPECIFIED

[illegible]

# RS422 / RS232 HOST COMMUNICATION MODULE



- Pin 1 - 422 ISO GND
- Pin 2 - Reader TX (232)
- Pin 3 - Reader RX (232)
- Pin 4 - NC
- Pin 5 - GND
- Pin 6 - Reader SD- (422)
- Pin 7 - Reader SD+ (422)
- Pin 8 - Reader RD- (422)
- Pin 9 - Reader RD+ (422)

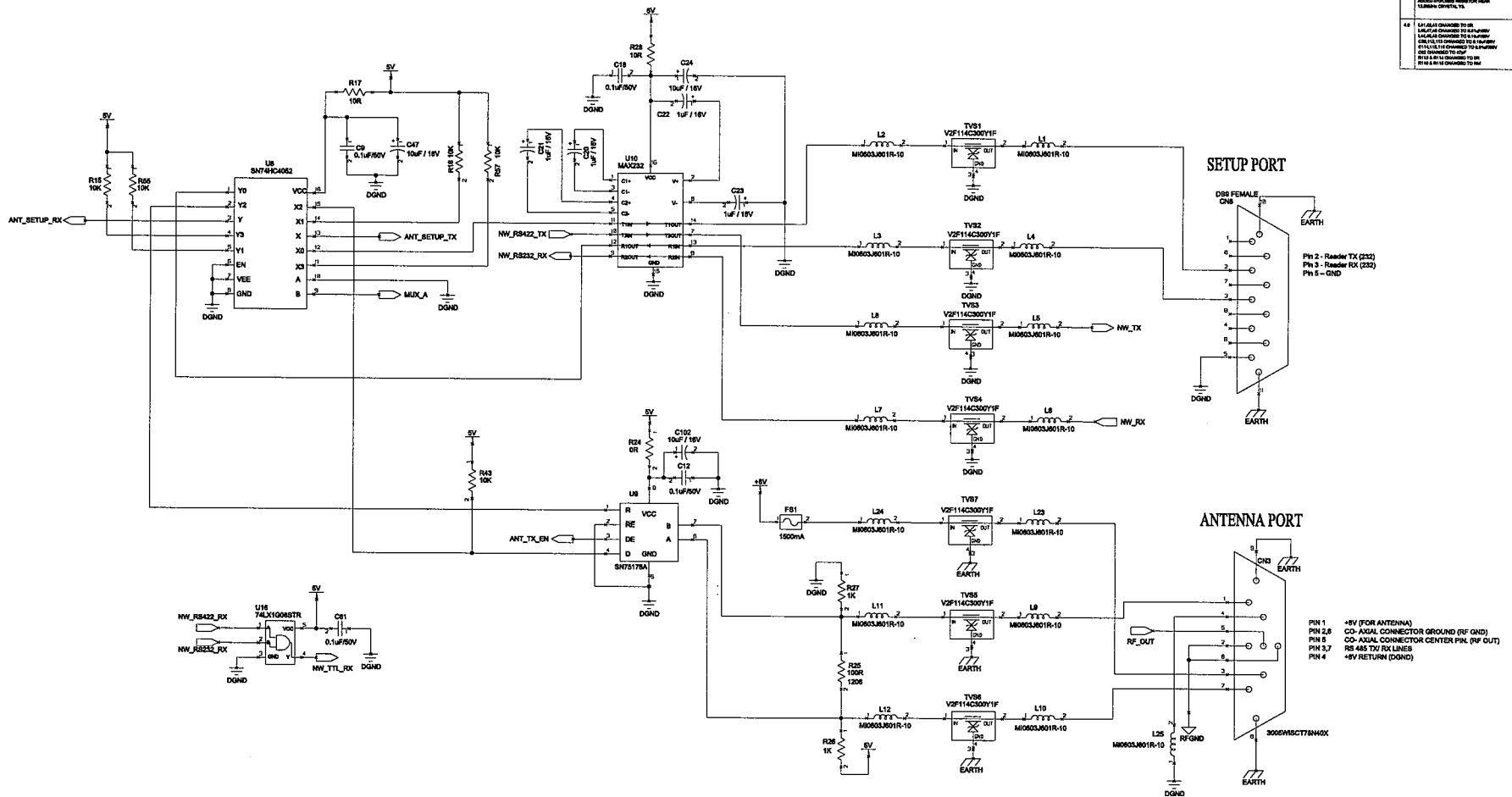
REV	DATE	BY	CHK
1.1	01/11/01	10/11/01	10/11/01
1.2	01/11/01	10/11/01	10/11/01
1.3	01/11/01	10/11/01	10/11/01
1.4	01/11/01	10/11/01	10/11/01
1.5	01/11/01	10/11/01	10/11/01
1.6	01/11/01	10/11/01	10/11/01
1.7	01/11/01	10/11/01	10/11/01
1.8	01/11/01	10/11/01	10/11/01
1.9	01/11/01	10/11/01	10/11/01
2.0	01/11/01	10/11/01	10/11/01

REV	DATE	BY	CHK
1.1	01/11/01	10/11/01	10/11/01
1.2	01/11/01	10/11/01	10/11/01
1.3	01/11/01	10/11/01	10/11/01
1.4	01/11/01	10/11/01	10/11/01
1.5	01/11/01	10/11/01	10/11/01
1.6	01/11/01	10/11/01	10/11/01
1.7	01/11/01	10/11/01	10/11/01
1.8	01/11/01	10/11/01	10/11/01
1.9	01/11/01	10/11/01	10/11/01
2.0	01/11/01	10/11/01	10/11/01

REV	DATE	BY	CHK
1.1	01/11/01	10/11/01	10/11/01
1.2	01/11/01	10/11/01	10/11/01
1.3	01/11/01	10/11/01	10/11/01
1.4	01/11/01	10/11/01	10/11/01
1.5	01/11/01	10/11/01	10/11/01
1.6	01/11/01	10/11/01	10/11/01
1.7	01/11/01	10/11/01	10/11/01
1.8	01/11/01	10/11/01	10/11/01
1.9	01/11/01	10/11/01	10/11/01
2.0	01/11/01	10/11/01	10/11/01

[illegible][illegible]

## ANTENNA & SETUP COMMUNICATION MODULE



**NOTE :**

ALL RESISTORS ARE -0603 PACKAGE UNLESS OTHERWISE SPECIFIED  
ALL CAPACITORS ARE -0805 PACKAGE UNLESS OTHERWISE SPECIFIED

[illegible]


## SETUP PORT

DB9 FEMALE  
CNS

Pin 2 - Reader TX (232)  
Pin 3 - Reader RX (232)  
Pin 5 - GND

ANTENNA PORT

PIN 1	+5V (FOR ANTENNA)
PIN 2,6	CO-AXIAL CONNECTOR GROUND (RF GND)
PIN 5	CO-AXIAL CONNECTOR CENTER PIN. (RF OUT)
PIN 3,7	RS 485 TX/ RX LINES
PIN 4	+5V RETURN (DGND)

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		CLASSIFICATION AUTHORITY		ORIGIN	DATE	REVISION	
		CLASSIFICATION AUTHORITY		ORIGIN	DATE	REVISION	





8

7

6

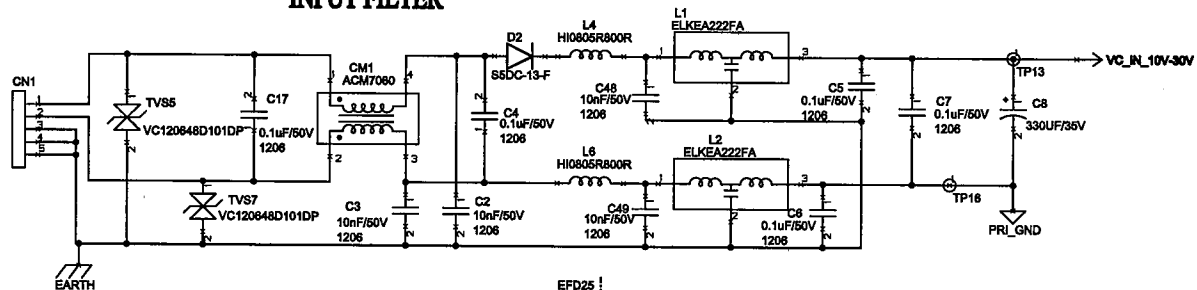
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4

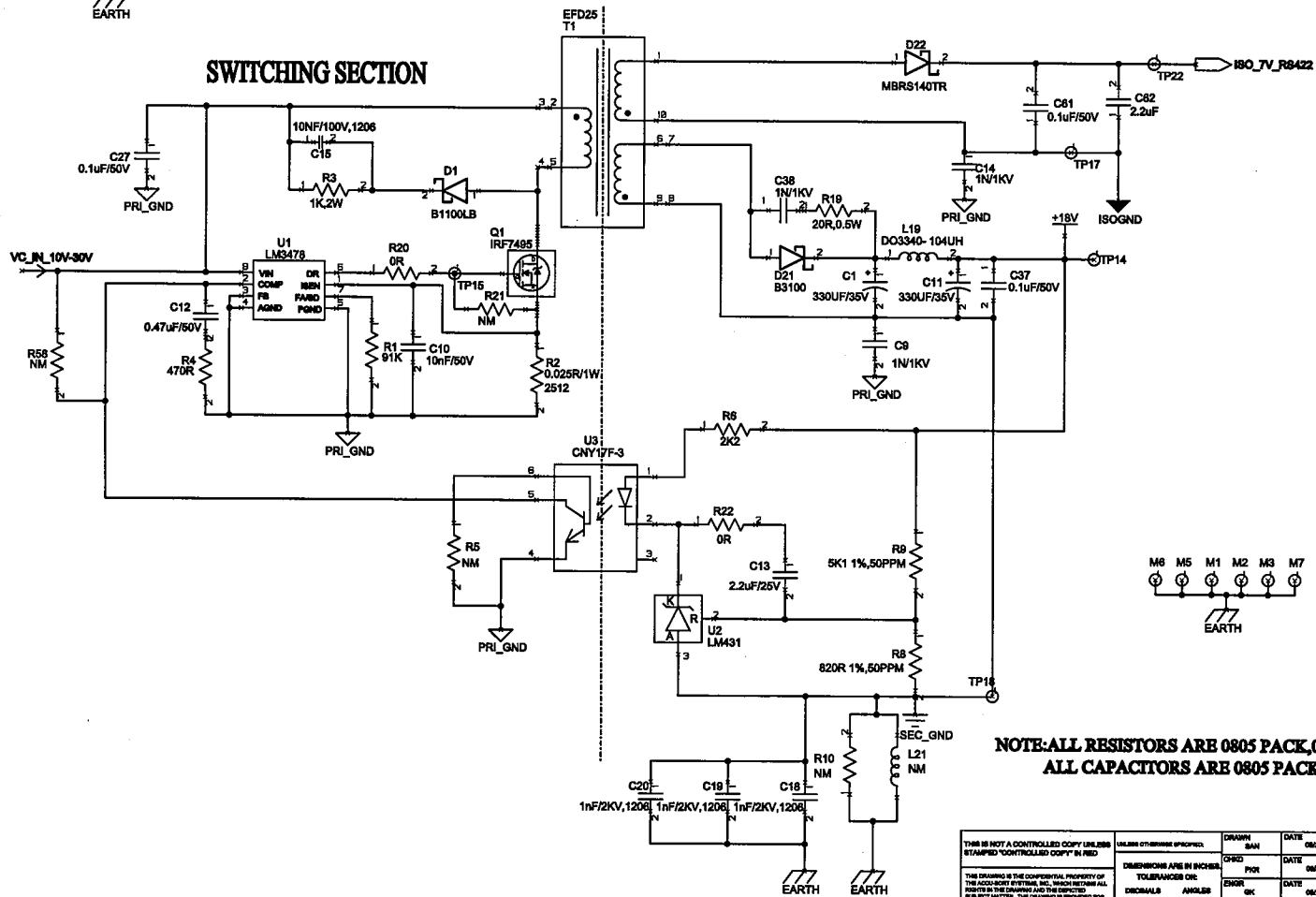
3

2

## INPUT FILTER




## SWITCHING SECTION



NOTE: ALL RESISTORS ARE 0805 PACK, 0.125W UNLESS OTHERWISE SPECIFIED.  
ALL CAPACITORS ARE 0805 PACK, 50V UNLESS OTHERWISE SPECIFIED.

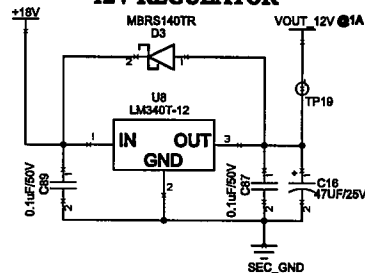
REVISIONS			
REV	CHANGES	BY	DATE
1.0	DRAFT RELEASE	OK	06/29/09
1.1	OP VOLTAGE CHANGED TO 14.5V AND TVS ADDED AT PRIMARY SHIELDS. COMMON MODE CHOKER ADDED AND FUSE REMOVED. FUSE CHANGED TO PTC TYPE.	SCOK	08/30/07
2.0	OP VOLTAGE CHANGED TO 18V. RV SWAPS SECTION INCLUDED.	SCOK	07/18/07
2.5	18V-20V RHPMIL TO 18V-20V. RV SWAPS SECTION INCLUDED.	BAWAK	06/23/07

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				CHECKED POK		DATE 06/28/2008			
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		TOLERANCES ARE:							
		DECIMALS ANGLES							
		JOC 4-01 4-12							
		JOC 4-006							
				DEPT. MFG					

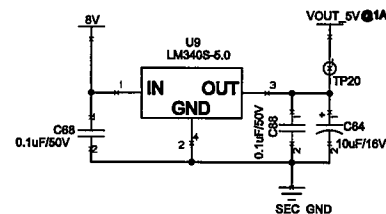


REVISIONS			
REV	CHANGES	BY	DATE
1.0	DRAFT RELEASE	OK	06/25/05
1.1	QIP VOLTAGE CHANGED TO 14.5V AND TVS ADDED AT PRIMARY INPUTS. COMMON MODE CHOKES ADDED AND FUSE REMOVED. FUSE CHANGED TO PTD TYPE.	BOCK	08/06/07
2.0	QIP VOLTAGE CHANGED TO 19V. 5V BSMPS SECTION INCLUDED.	BOCK	07/19/07
2.5	5V BSMPS 200-MAIL FUSE-ADP, R1=10K. U8 CHANGED TO LM340T-12.	BOCK	06/26/07

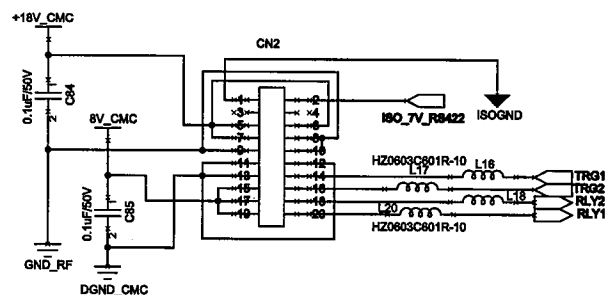
### 12V REGULATOR



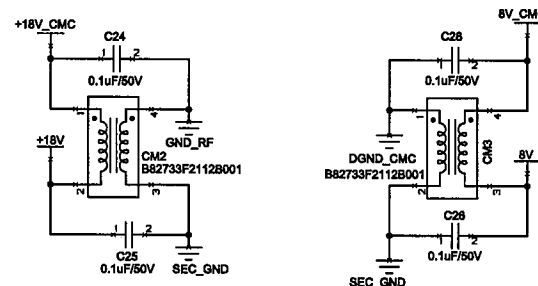
### 5V REGULATOR FOR IO SECTION



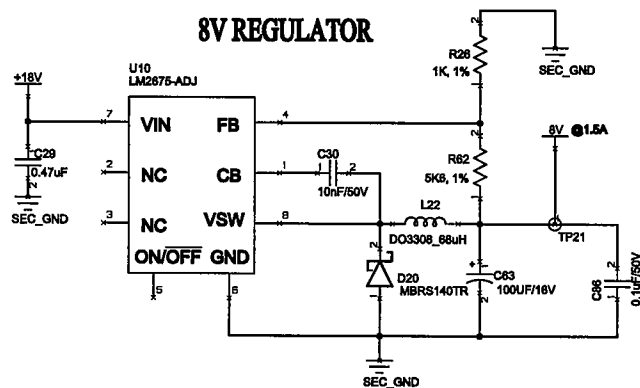
### MAIN BOARD CONNECTOR




### COMMON MODE CHOKES

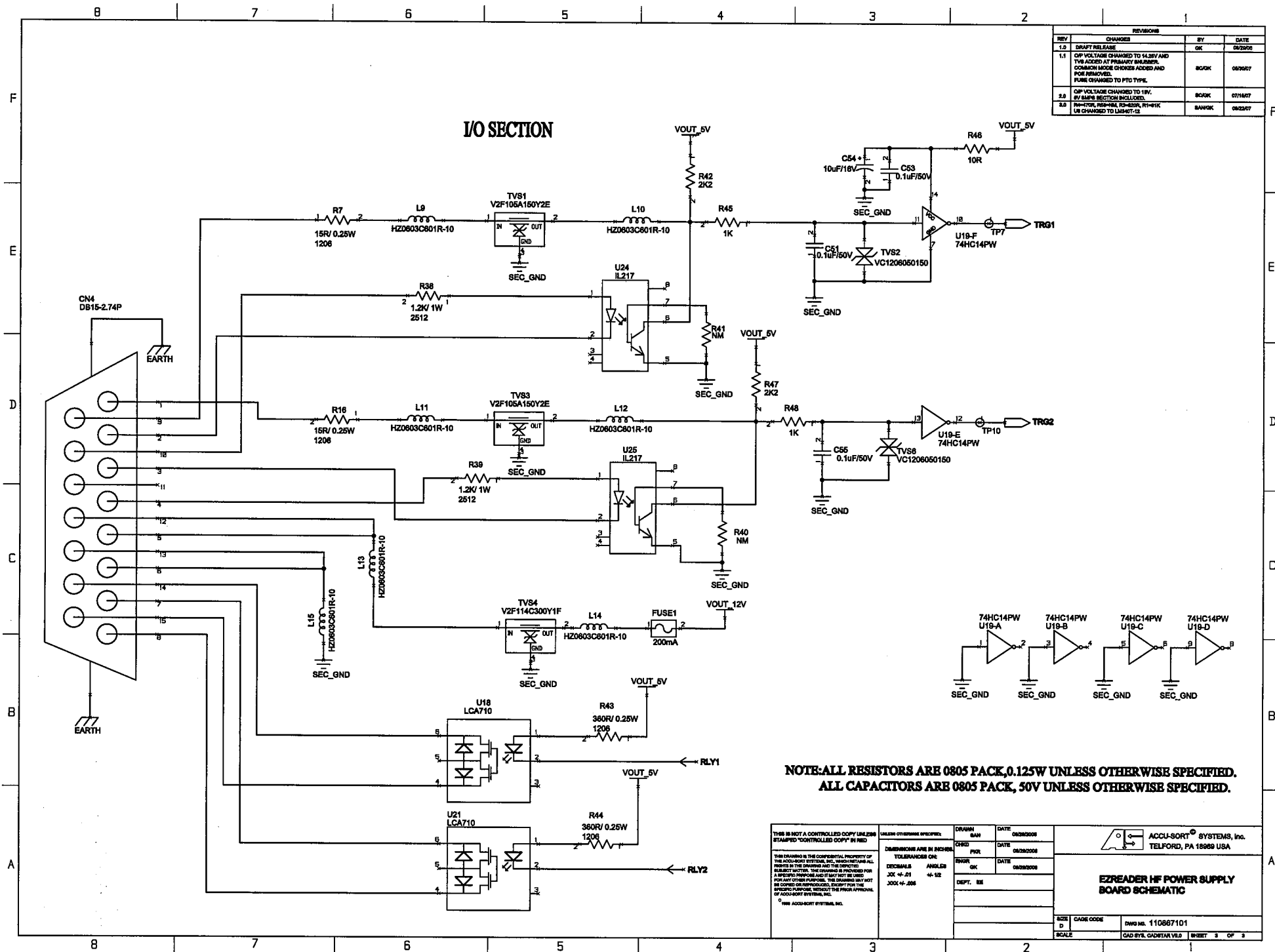


### 8V REGULATOR

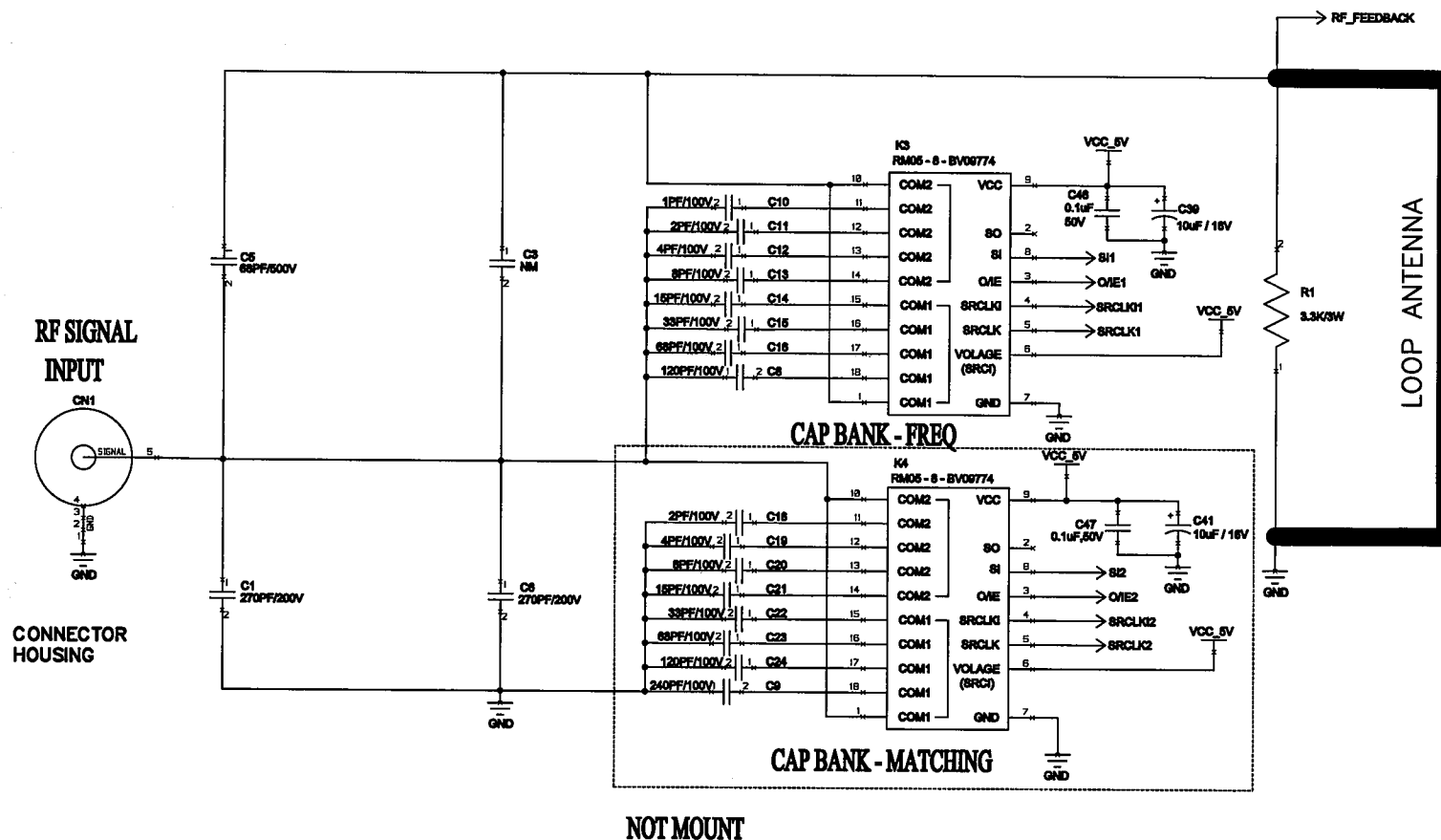


NOTE: ALL RESISTORS ARE 0805 PACK, 0.125W UNLESS OTHERWISE SPECIFIED.  
ALL CAPACITORS ARE 0805 PACK, 50V UNLESS OTHERWISE SPECIFIED.  
MAXIMUM CURRENT RATINGS ARE INDICATED AT REGULATOR.

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		FRACTIONS ANGLES		DATE	DATE	
		DECIMALS ANGLES		DATE	DATE	
		30° 45° 90°		30° 45° 90°		<b>EZREADER HP POWER SUPPLY BOARD SCHEMATIC</b>
		30° 45° 90°		30° 45° 90°		
		30° 45° 90°		30° 45° 90°		
		30° 45° 90°		30° 45° 90°		SIZE D SCALE
		30° 45° 90°		30° 45° 90°		DWG NO. 110867101 CAD BY: L. CAMPBELL VLD SHEET 2 OF 3



REV	CHANGES	BY	DATE
1.0	DRAFT RELEASE	OK	06/28/2008
1.1	R1 CHANGED TO 3.3K $\Omega$ W. ENVELOPE DETECTOR ADDED.	800K	06/30/07
2.0	CN1 CHANGED TO COAXIAL TYPE. PRT IS ADDED FOR DISCHARGING CBL. LOG DETECTOR REMOVED.	800K	07/18/07
3.0	MATCH CAPACITOR BANK CHANGED TO NM R1=3.3K $\Omega$ W, TVR1,TVR2=VSP14320VUF CONNECTION AT ONE REVERSED.	800K	06/23/07



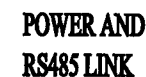
NOTE: ALL CAPACITORS ARE 0805 PACKAGE, 100V UNLESS MENTIONED.  
C1, C3, C5, C6 ARE AQ14(AVX) SIZE.


NOT MOUNT

H1 H2 H3 H4  
FD6 FD5 FD4 FD3 FD1 FD2  
⊗ ⊗ ⊗ ⊗ ⊗ ⊗

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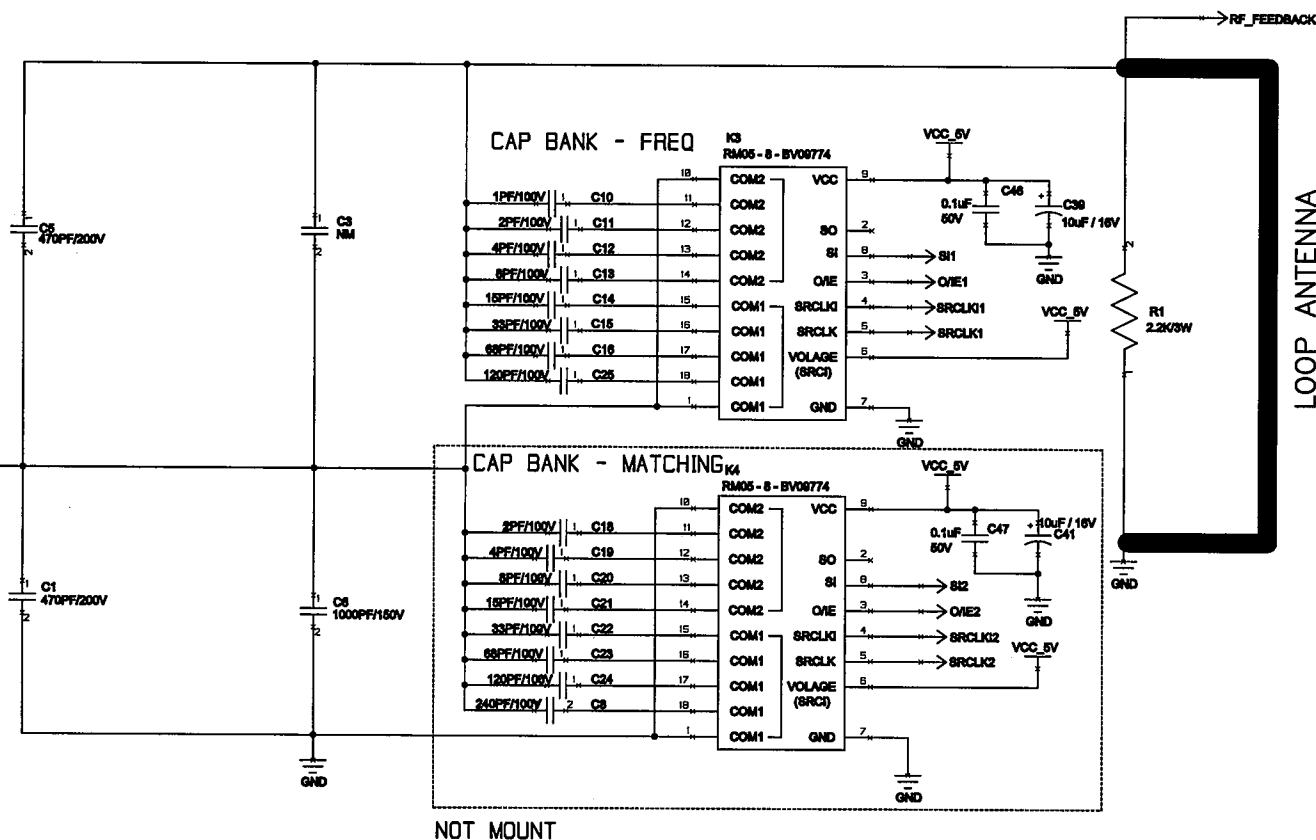
## ANTENNA BOARD POWER SUPPLY



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© 1996 ACCU-SORT SYSTEMS, INC.							
SIZE CAGE CODE DWG NO. <b>110986101</b> SCALE <b>D</b> CAG BYL CAPTAIN/09 SHEET <b>2</b> OF <b>3</b>							

**RF SIGNAL  
INPUT**

**CONNECTOR HOUSING**



NOT MOUNT


NOTE:ALL CAPACITORS ARE 0805 PACKAGE,100V UNLESS MENTIONED.  
C1,C3,C5,C6 ARE AQ14(AVX) SIZE.

H1 H2 H3 H4 H5 H6 H7  
 (x) (x) (x) (x) (x) (x) (x)  
 FD3 FD1 FD2  
 (x) (x) (x)  
 FD6 FD4 FD5  
 (x) (x) (x)

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DIMENSIONS ARE IN INCHES TOLERANCES ON:	DECIMALS	ANGLES
	.XXX $\pm .01$	$\pm .15$
	.XXX $\pm .005$	

DRAWN BAH	DATE 08/28/2008
CHECKED PKR	DATE 08/28/2008
ENGOR OK	DATE 08/28/2008
DEPT. EE	



**ACCU-SORT<sup>®</sup> SYSTEMS, Inc.**  
**TELFORD, PA 18969 USA**

### 5X16 AUTO TUNE ANTENNA BOARD SCHEMATIC

SIZE D	CAGE CODE	DWG NO. 110000101
SCALE	CAGE CODE	SHEET 1 OF 2

