OPERATIONAL DESCRIPTION

1. Features

➢Code Format   : POCSAG ,1200BPS

➢Frequency   : 457.5750MHz

➢LED   : 1 EA (FULL COLOR LED)

➢LCD   : 128 * 64 dots (B/W)

➢Alert   : Vibrator & Buzzer & LED

➢Code process   : Alpha 64 characters & Numeric message

➢Battery   : 3.7V Re-chargeable

➢ID setting   : 4 CAPCODE

➢Key   : 1 key   (Alert stop)

➢Out of range melody and display

➢Low battery melody and display

2. Circuit operational description

- Receiver : super heterodyne receiver

Carrier frequency (457.575MHz), passed thru antenna, amplifies at Q1, Q2.

Q3 receives signals of Q4, Q1, Q2 and makes 1st IF (21.4MHz) to send it to U1.

U1 mixes 1st IF with 2nd local oscillation frequency (20.945MHz) and makes 2nd IF

(455KHz).   The 455Khz is demodulated at Dis101(quadrature detect).

U1 Sends FSK out to the Pin no 15 of CPU(U2).