

# Operating Principle Explanation

**FCC ID: UTBHLC830W**

1. Input power source through the AC Adapter to EUT and the interior produced 5V, 3.3V, 1.8V to the entire system.
2. Chip carries out the following function by SOC FIC8120
  - A. SDRAM---- The memory body for the FIC8120 to run the operations or storing data temporarily when operating.
  - B. the FLASH---- The function of Flash memory is storing formula and advance value of the system, which providing for FIC8120 to use.
  - C. MINI PCI---- When transferring and receiving network signals, the chip FIC8120 used wireless card as the connection interface.
  - D.PHY+RJ45---- When transferring and receiving network signals, the chip FIC8120 used PHY+RJ45 as the connection interface.
  - E. COMS SENSOR----CMOS sending the measured images to FIC8120 for processing.