

# 9511-29-1RX Multi-ply Block Diagram

49.860MHz  
ANT

49.860MHz  
Super-regenerative  
Receiver  
Q1

Decoder  
SCRX2B  
(150KHz)

Sound  
Synthesizer  
3W500  
(600Hz)

Audio  
Amplifier

Speaker

Motor Driver  
For  
M1

Motor  
M1

Motor Driver  
For  
M2

Motor  
M2

DRAWN:	DATE:	COMPANY:	
CHECK:	08/01/25		
APPROVED:		TITLE:	REV:
RELEASED:		9511-29-1	0
		SCALE:	DWG NO.

Title		
Rev	Number	Revision
B		
Date:	2-Apr-2008	Sheet of
File:	D:\9511-29-1\9511-29-1.dwg	Drawn By: