Block diagram for Bodyworn transmitter

ANTENNA MIC **INPUT FINAL** AF LEVEL AF AMP COMPRESS VCO DRIVER CONTROL **AMP** GT **INPUT RAM** -→ MHz **PLL** MCU LCD CONTROL **BATTERY** DC-DC **POWER** IR → +7.5V RECEIVER SWITCH **SWITCH DRIVERS SWITCH** 3V 701.5-805MHz LCD **PANEL**