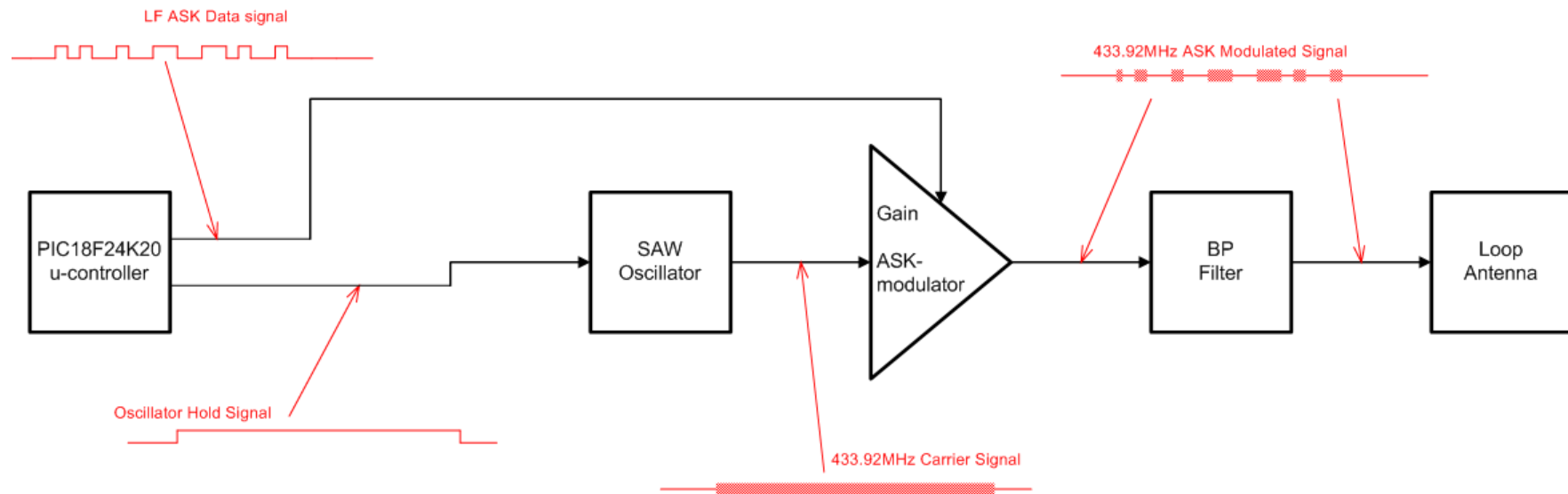**LINAK®**
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HBLP-Uxxx-xx Block Diagram

Rev. A
Date:
2009-08-26

Init: TOP

The calibrated internal RC Oscillator provides a 16 MHz clock.
The PIC18F system clock can be divided by setting the OSCCON.
In this application, the OSCCON is set to 4, and the clock frequency of the CPU is 4MHz.

The frequency of the 'Watchdog' is 32kHz.
The PWM frequency for backlight and transmitter LED management is 100KHz.