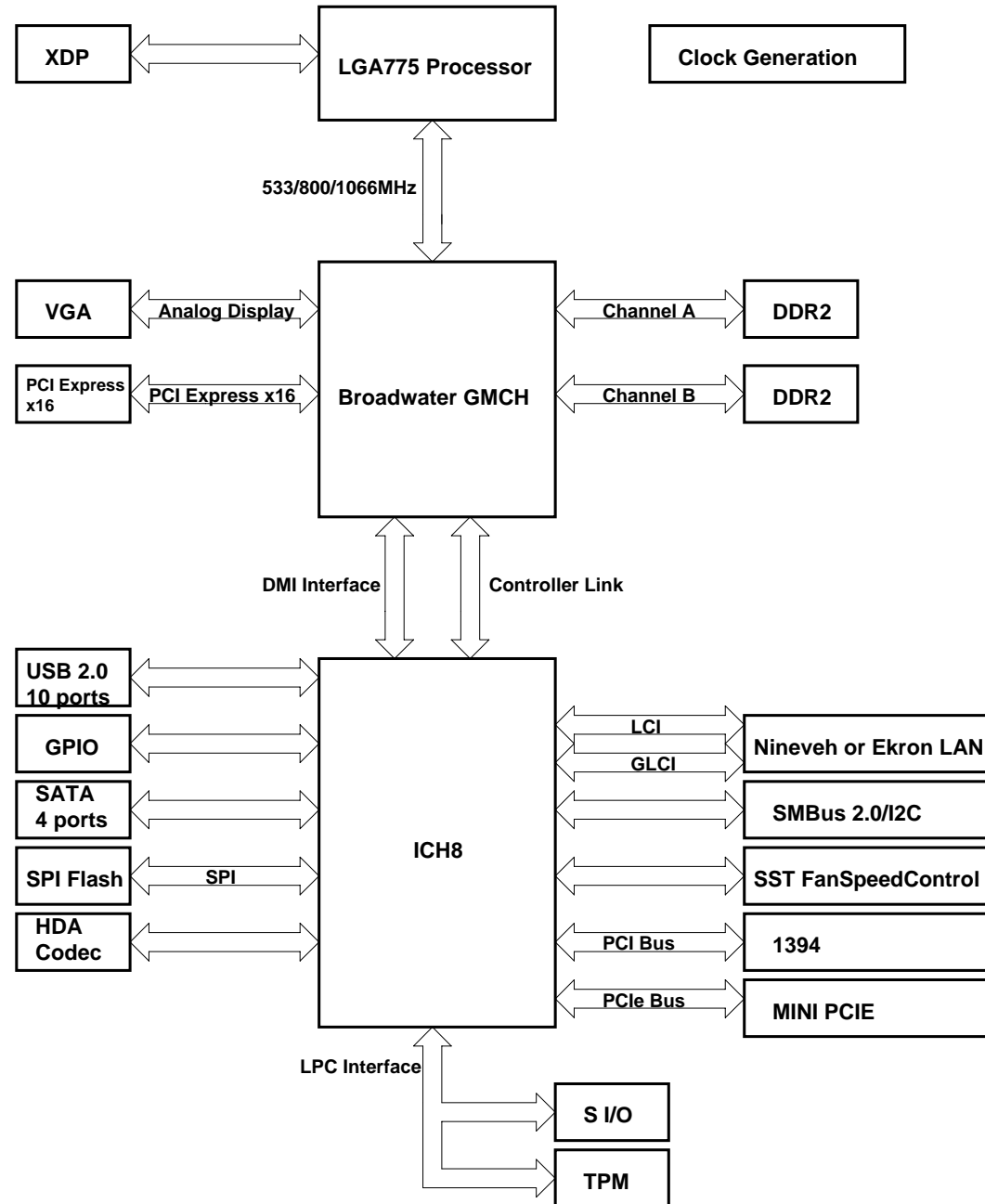


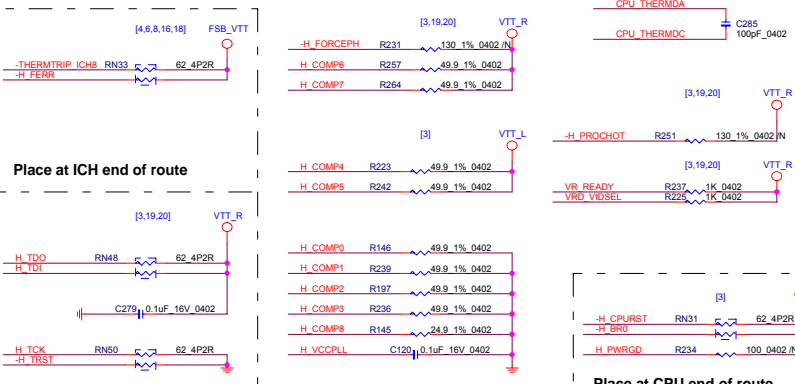
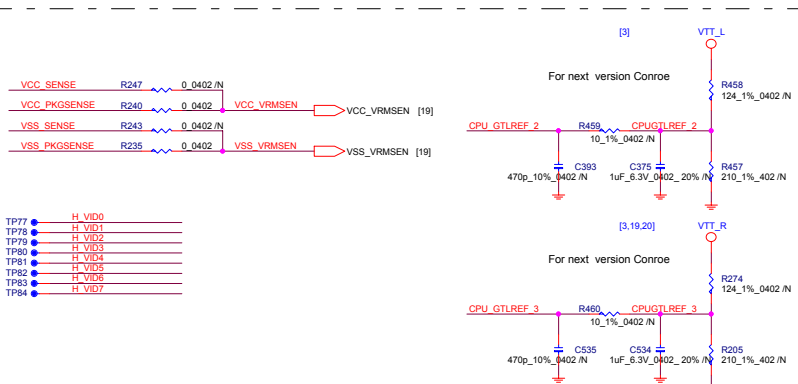
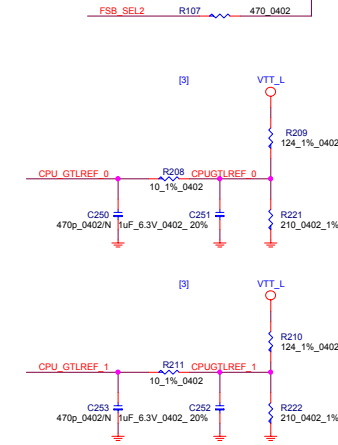
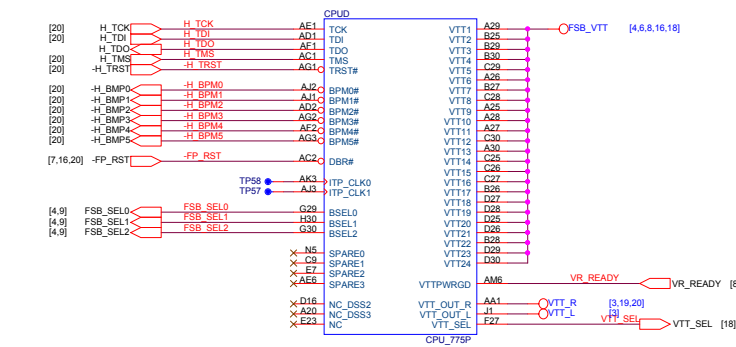
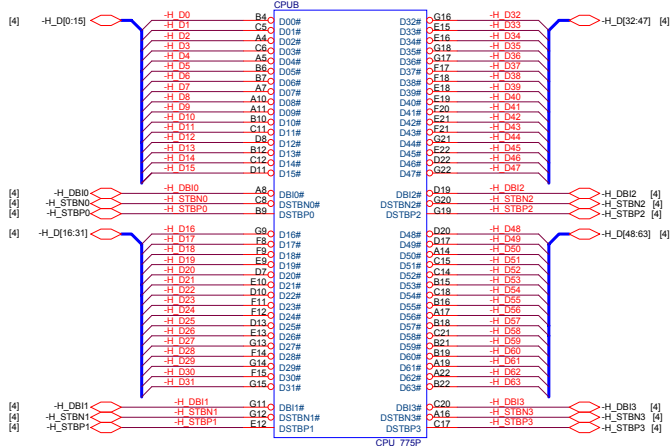
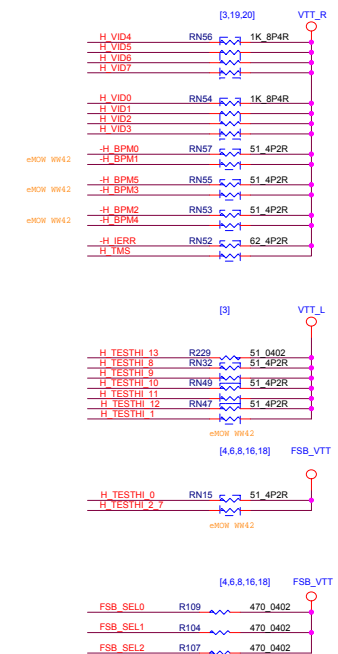
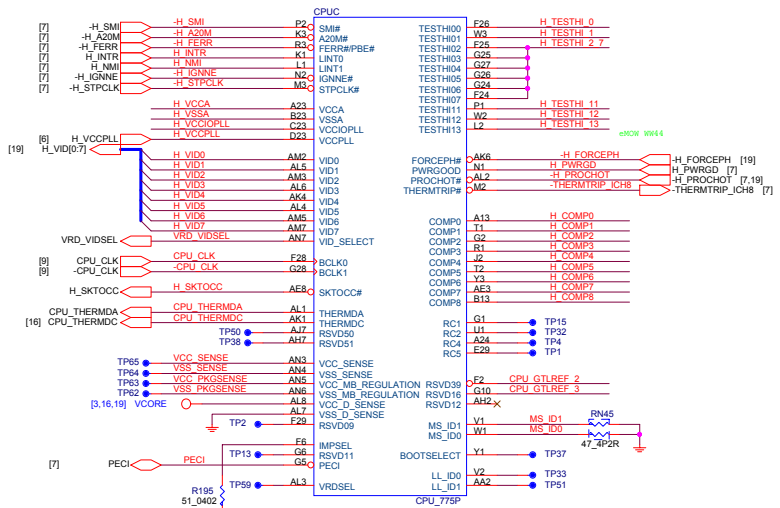
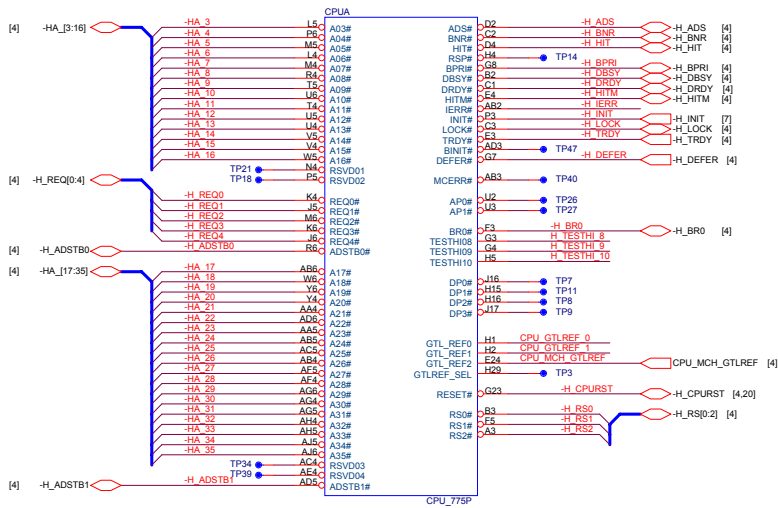
PTCQ965

PAGE 1: Cover Sheet & Block Diagram
 PAGE 2: CPU - LGA 775 - 1
 PAGE 3: CPU - LGA 775 - 2
 PAGE 4: GMCH 1
 PAGE 5: GMCH 2
 PAGE 6: GMCH 3
 PAGE 7: ICH 8 - 1
 PAGE 8: ICH 8 - 2
 PAGE 9: Clock Generator
 PAGE 10: DDR2
 PAGE 11: DDR2(Termination)
 PAGE 12: 1394(VT6307S)
 PAGE 13: LAN
 PAGE 14: AUDIO(ALC262)
 PAGE 15: PCI EXPRESS(x1 , x16)
 & TPM & MINI PCIE
 PAGE 16: SIO
 PAGE 17: USB
 PAGE 18: POWER
 PAGE 19: VRM
 PAGE 20: SATA & VGA & CPU FAN
 & XDP

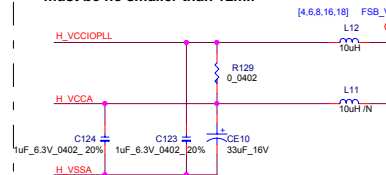
Polyswitch Name	Current	Voltage	Page
USBFS1	1.1A~2.0A	6V~8V	17
USBFS2	1.1A~1.5A	6V~8V	17
USBFS3	1.1A~2.0A	6V~8V	17
FS2	1.1A~1.5A	6V~8V	20
1394FS	1.1A~3.0A	15~16V	12



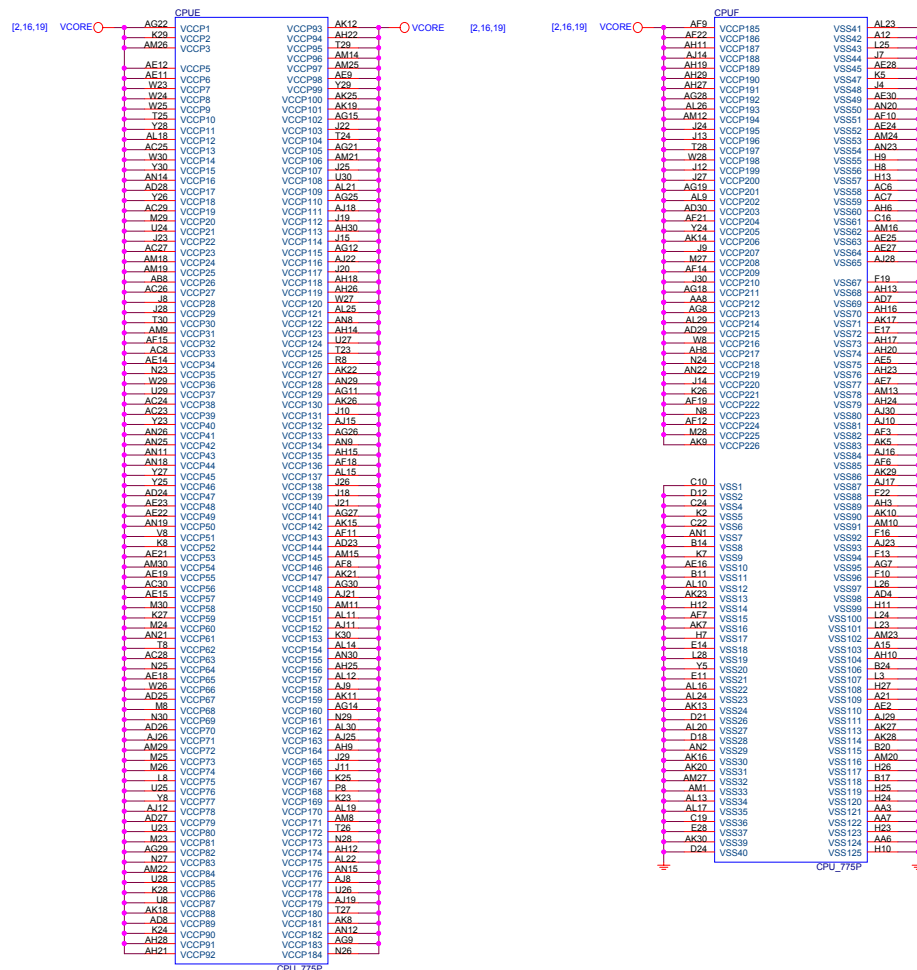
LGA775



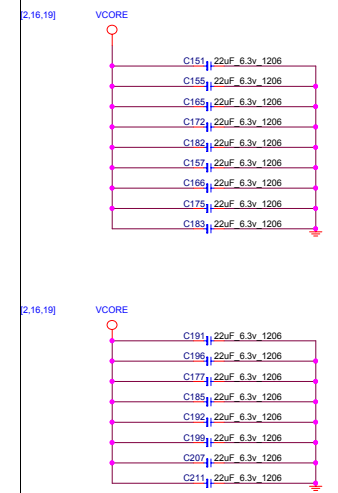
Place components as close as possible to processor socket trace width to caps must be no smaller than 12mil



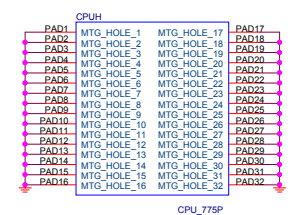
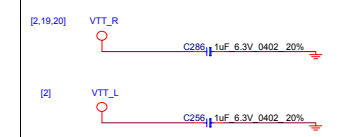
LGA775



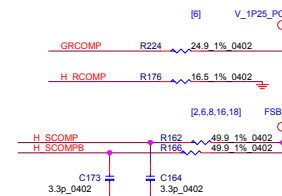
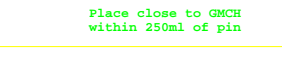
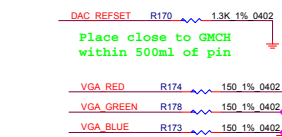
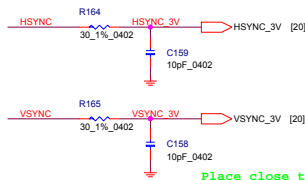
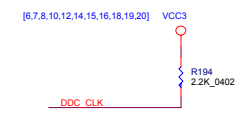
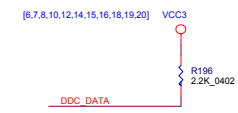
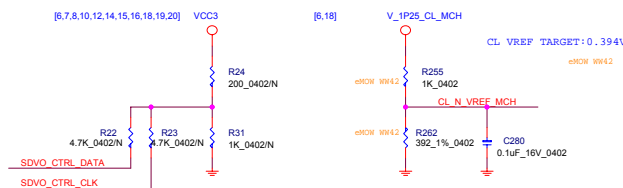
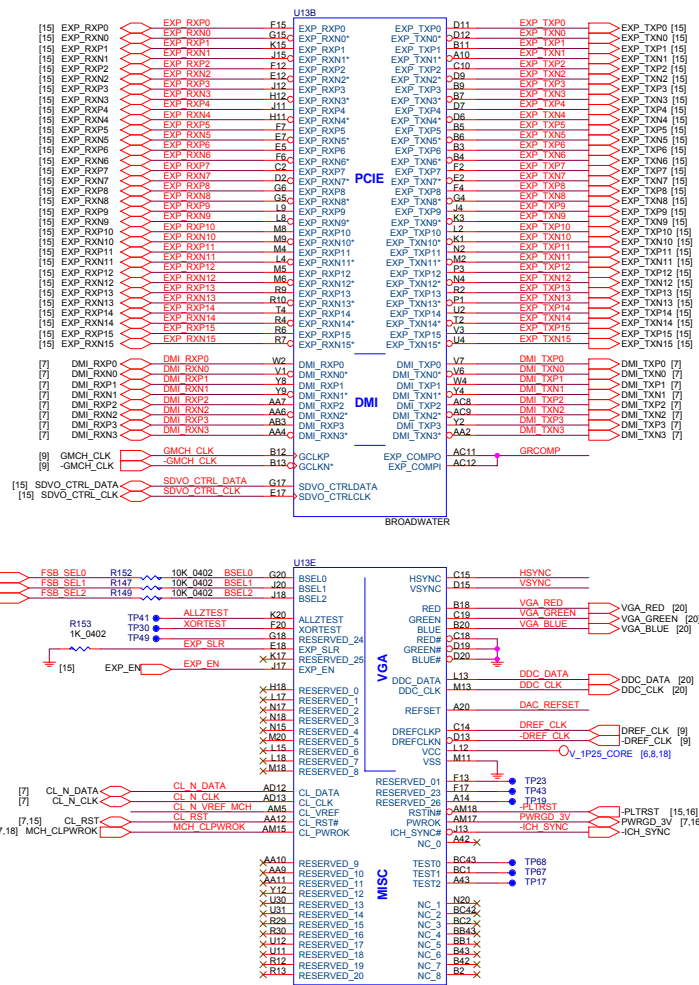
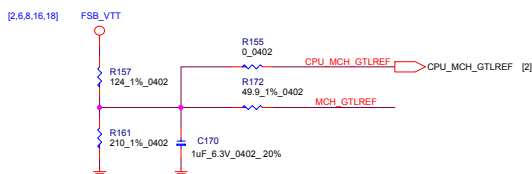
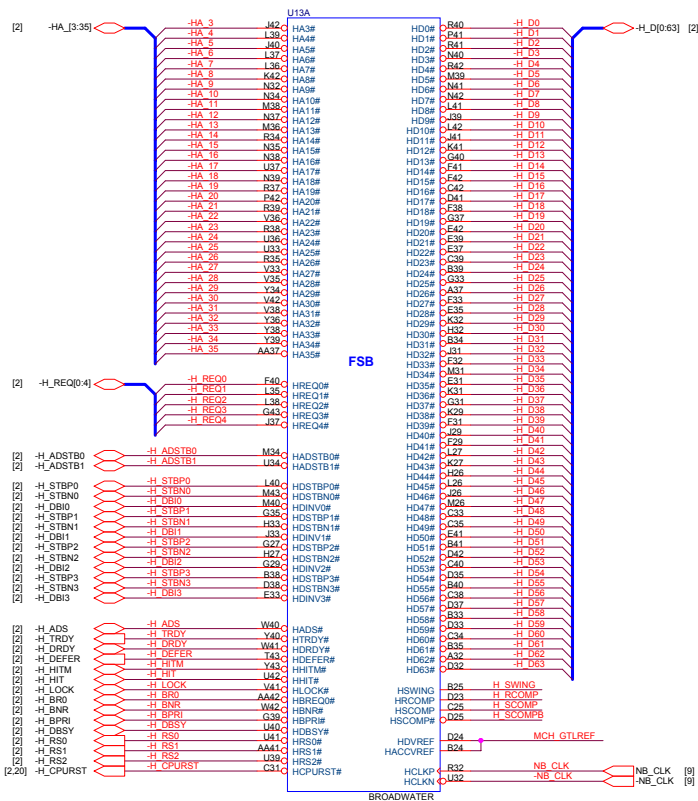
CAD NOTE: PLACE AS MANY 1206 CAPACITORS AS POSSIBLE WITHIN CPU CAVITY



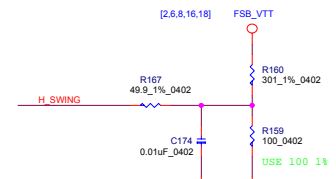
THESE CAPS GO INSIDE CPU CAVITY
PLACEMENT NOTE FOR 1206:
PLACE 18 INSIDE CPU SOCKET



Broadwater

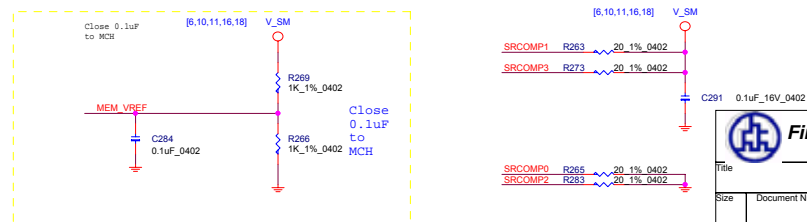
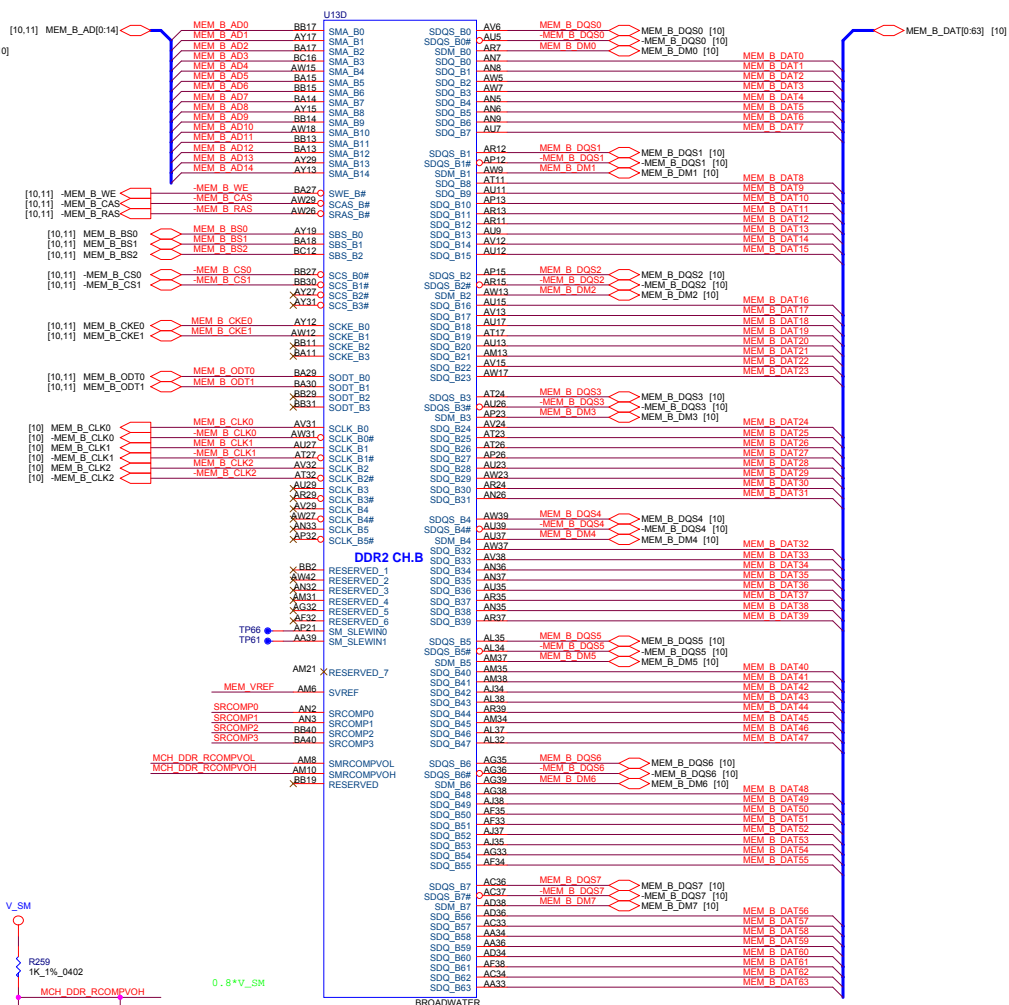
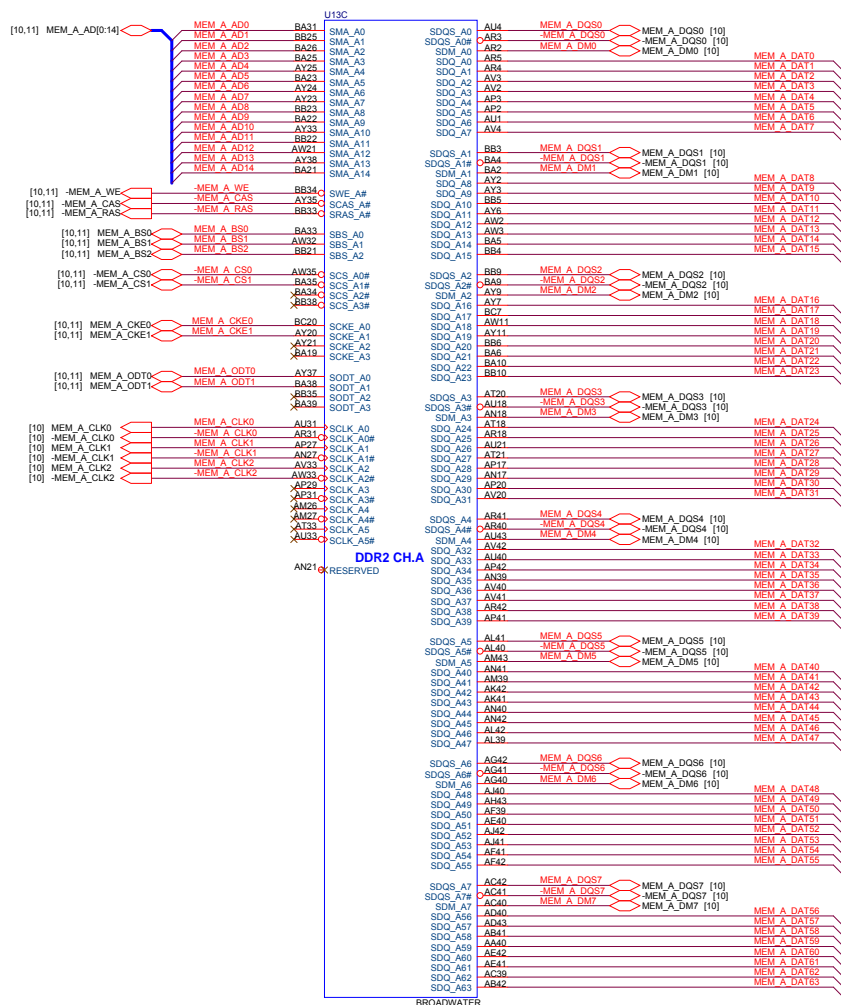


COMP SIGNAL TERMINATION

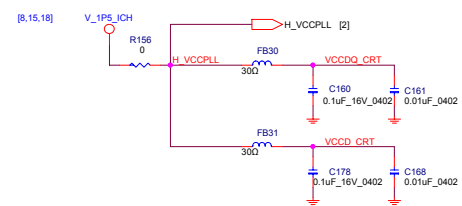
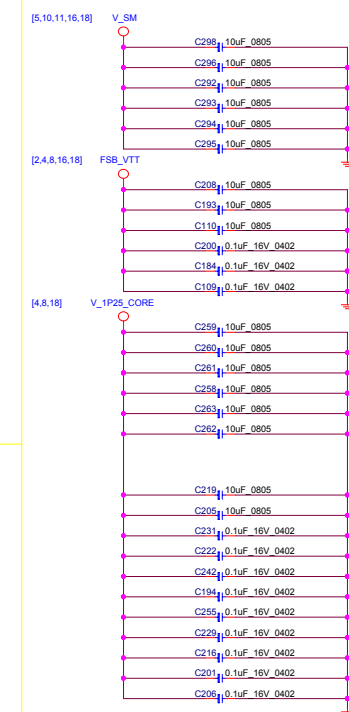
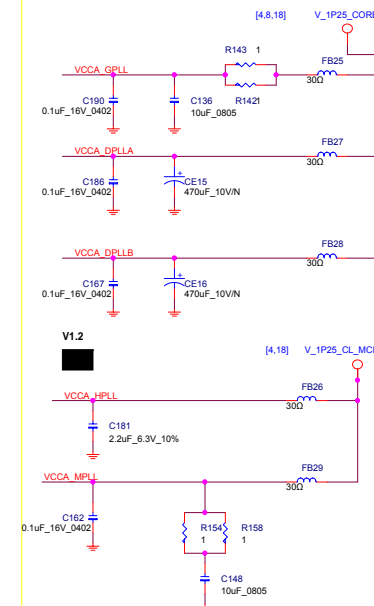


HXSWING voltage : 10mil
trace, 7mil space.
Place divider resistor
near VTT.

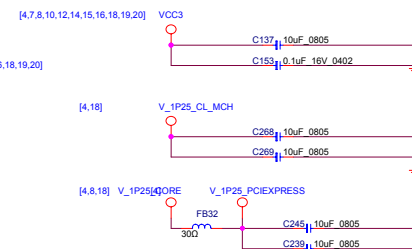
Broadwater



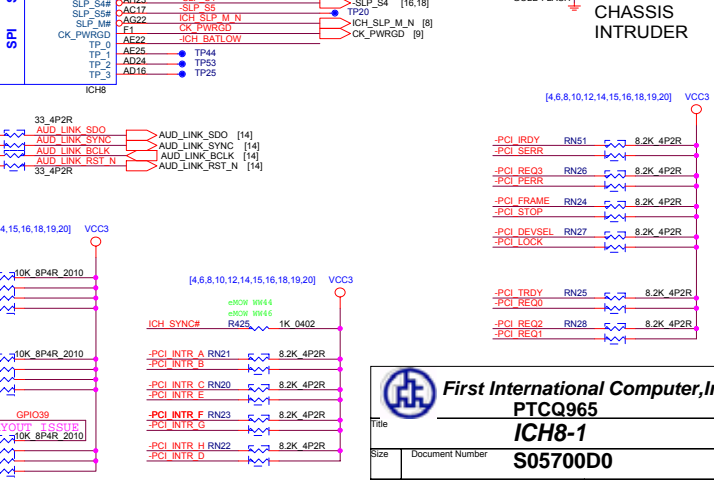
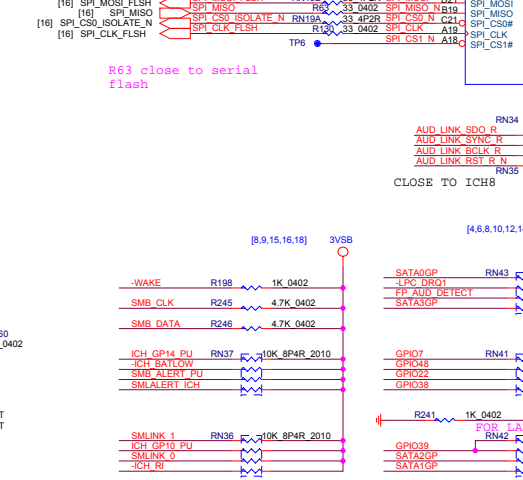
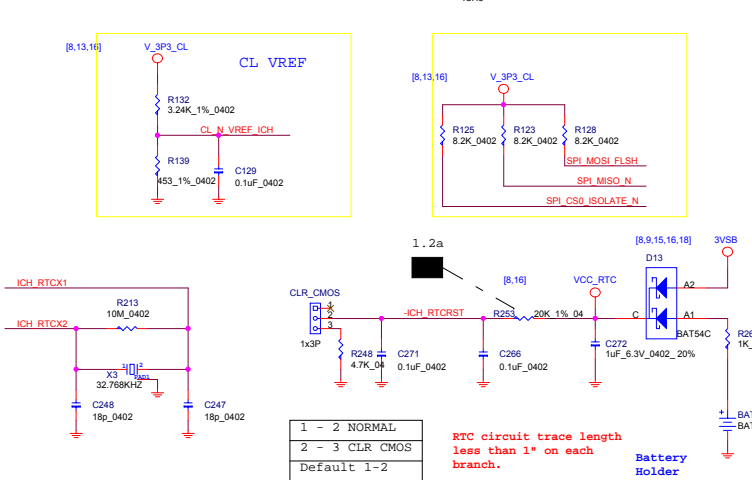
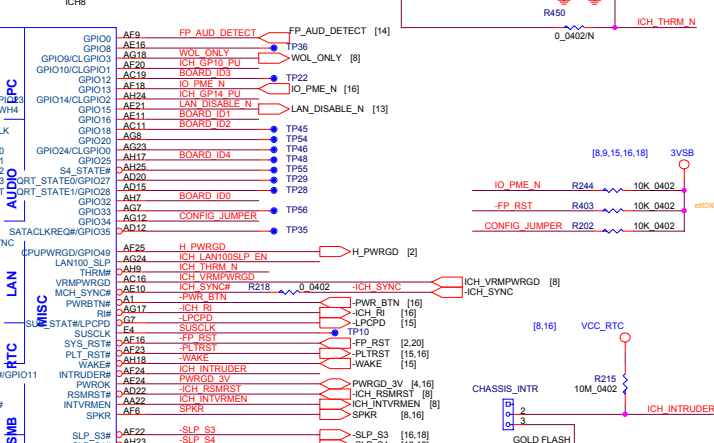
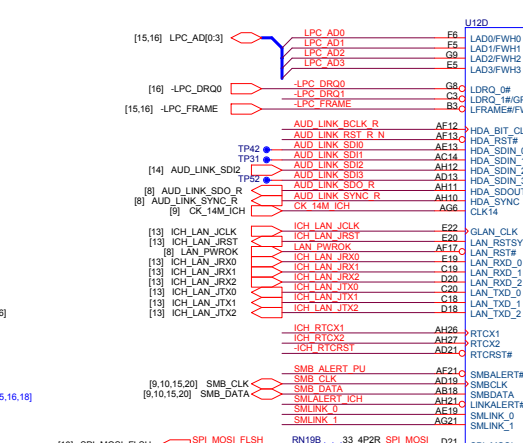
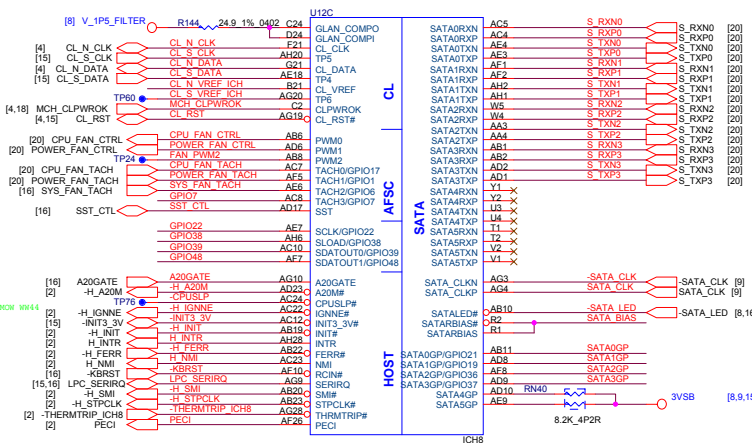
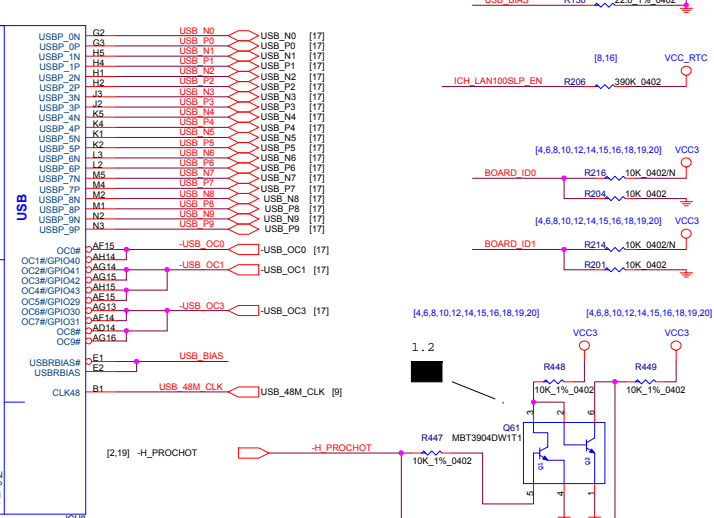
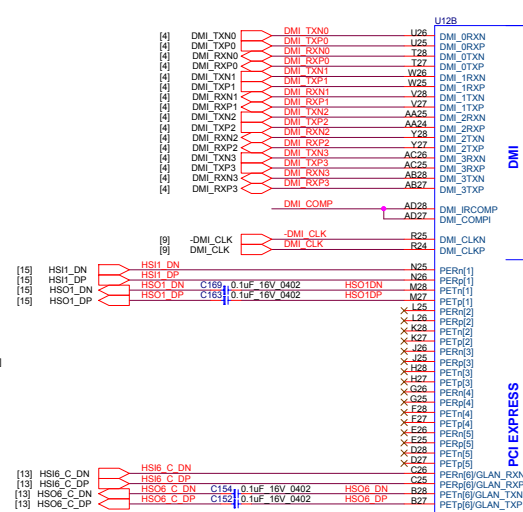
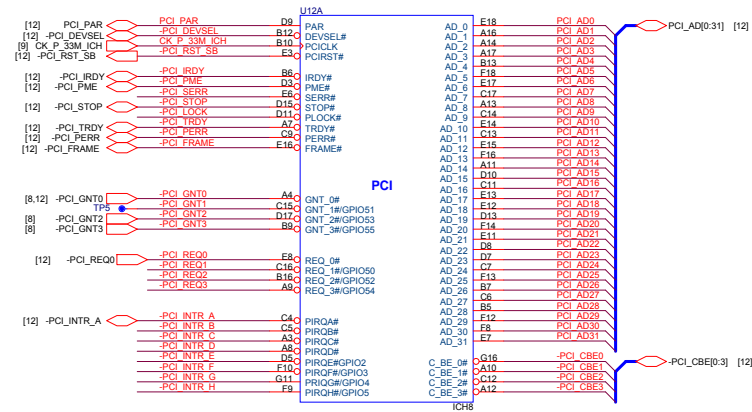
VCCA PLL



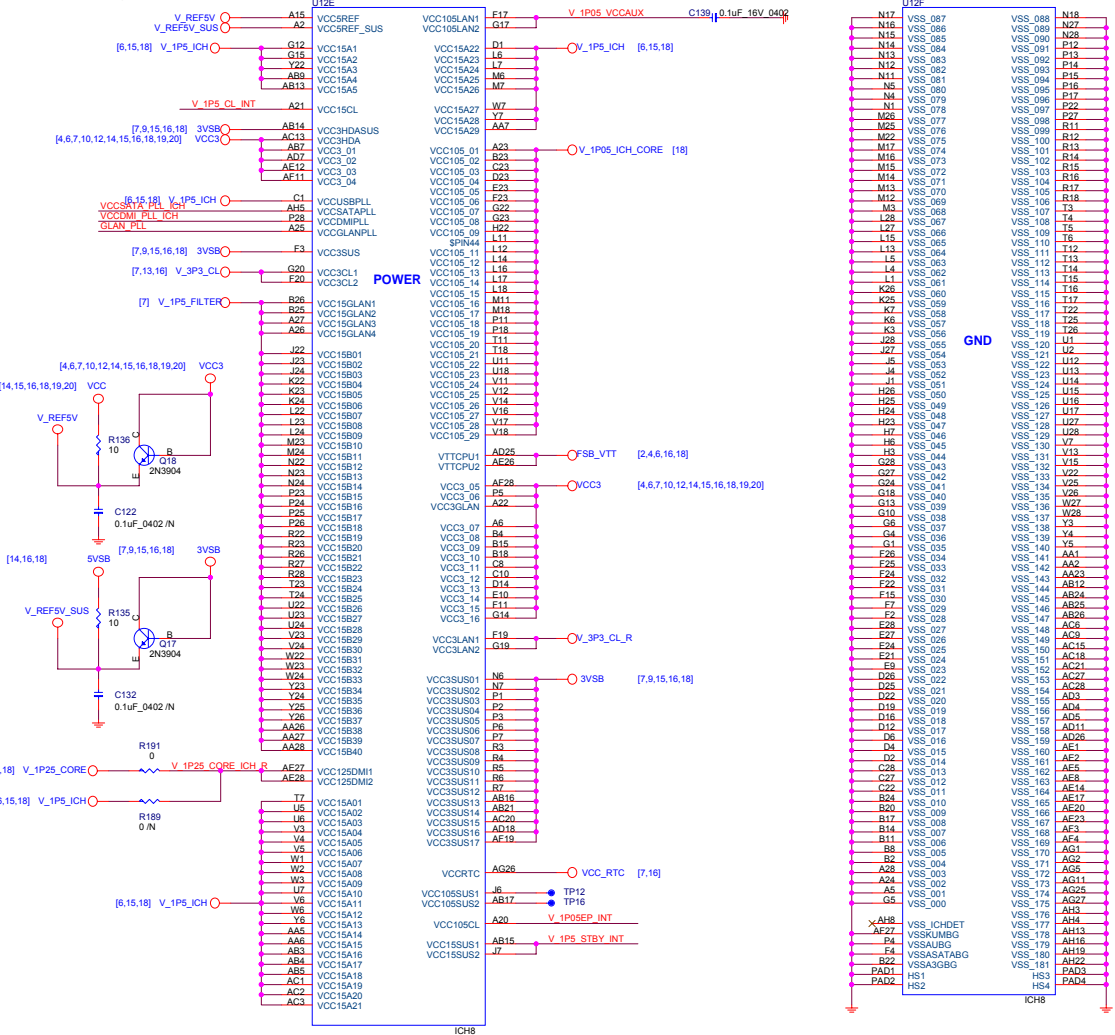
For ICH8 Power Sequencing



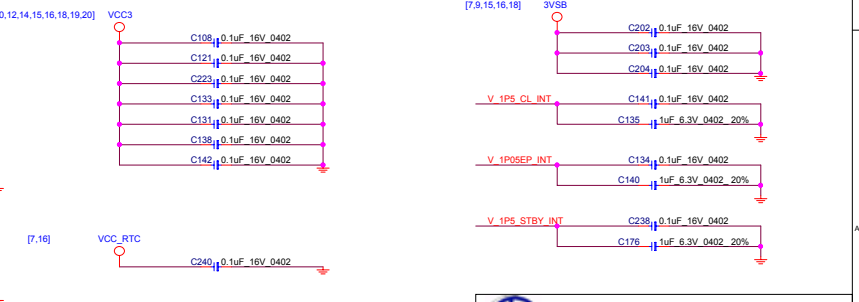
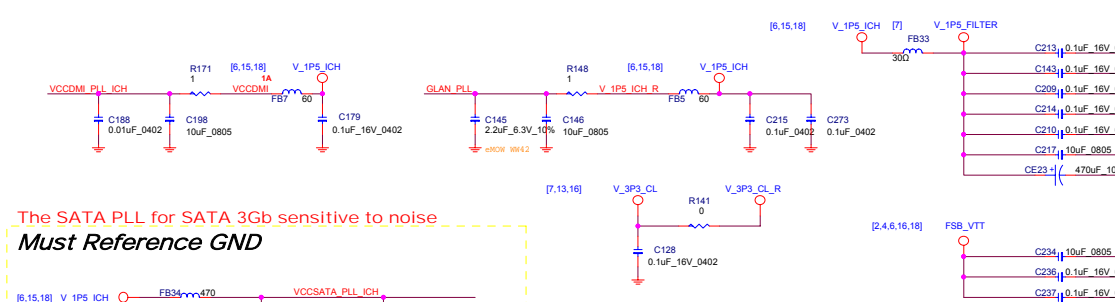
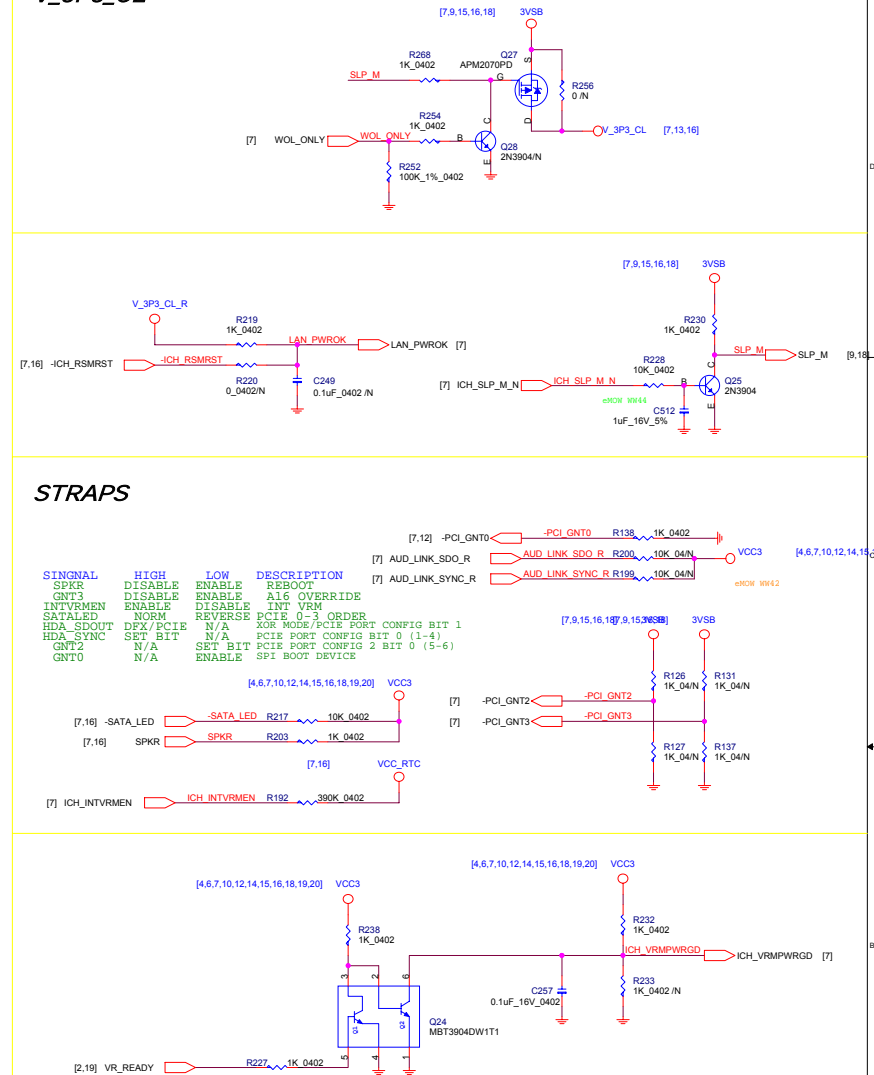
ICH8



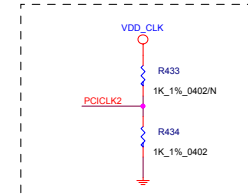
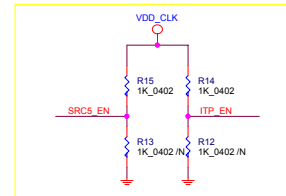
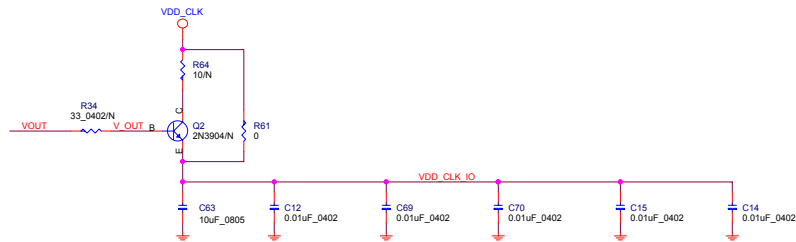
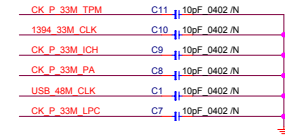
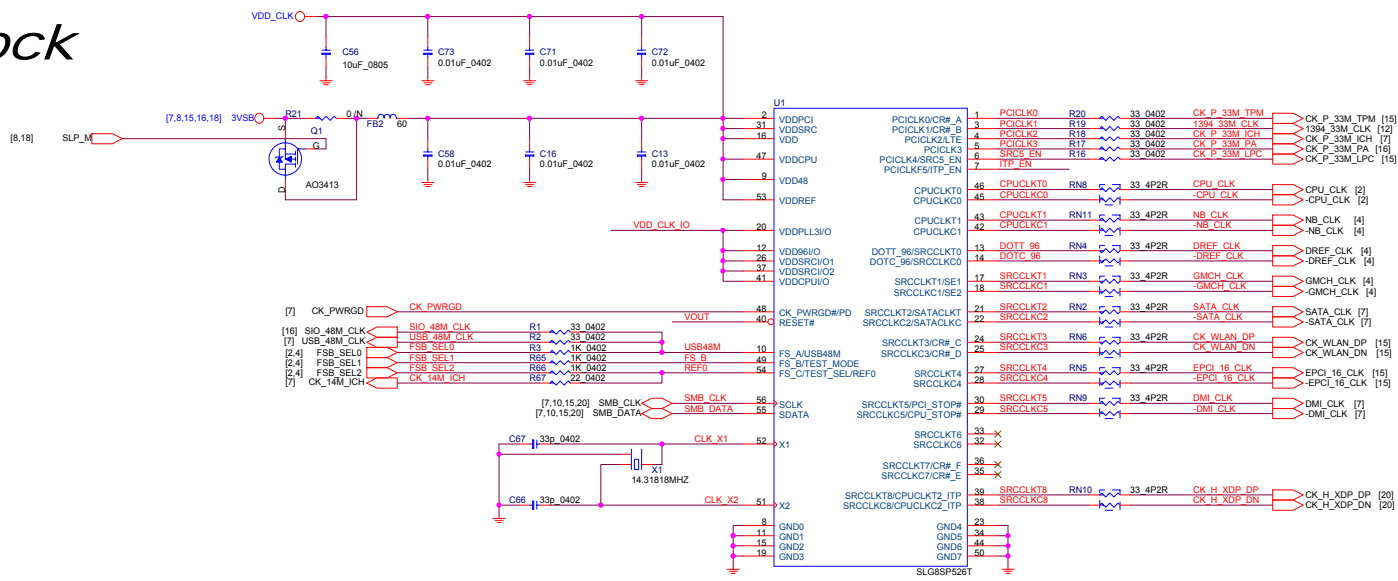
ICH8



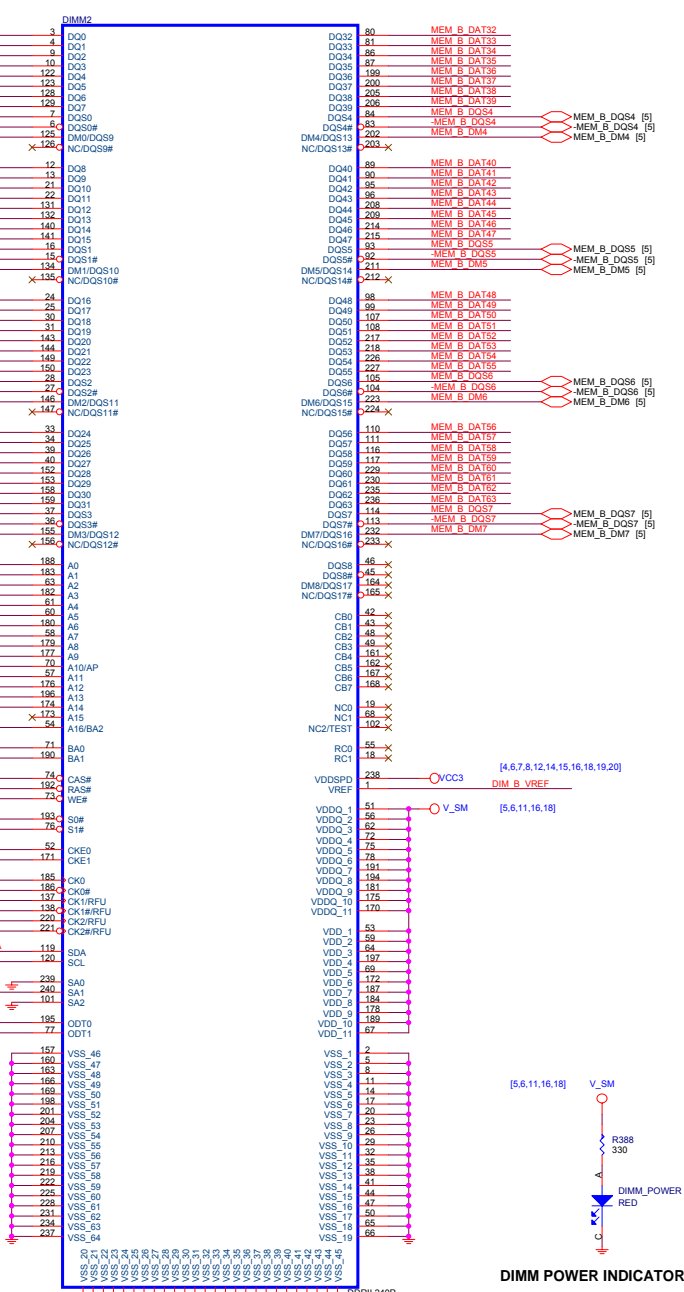
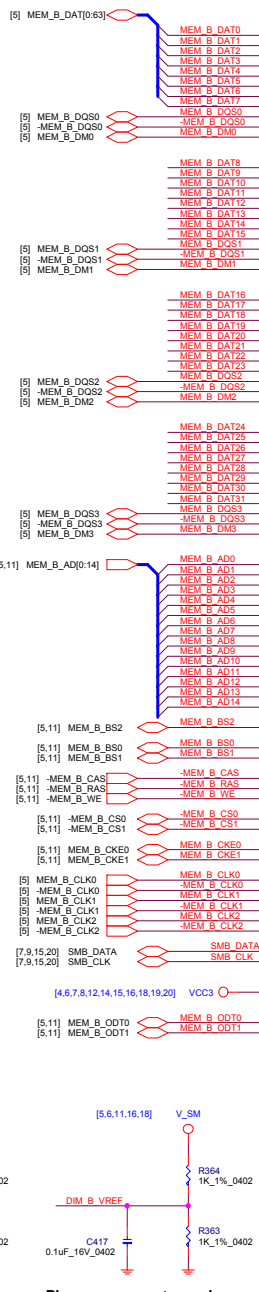
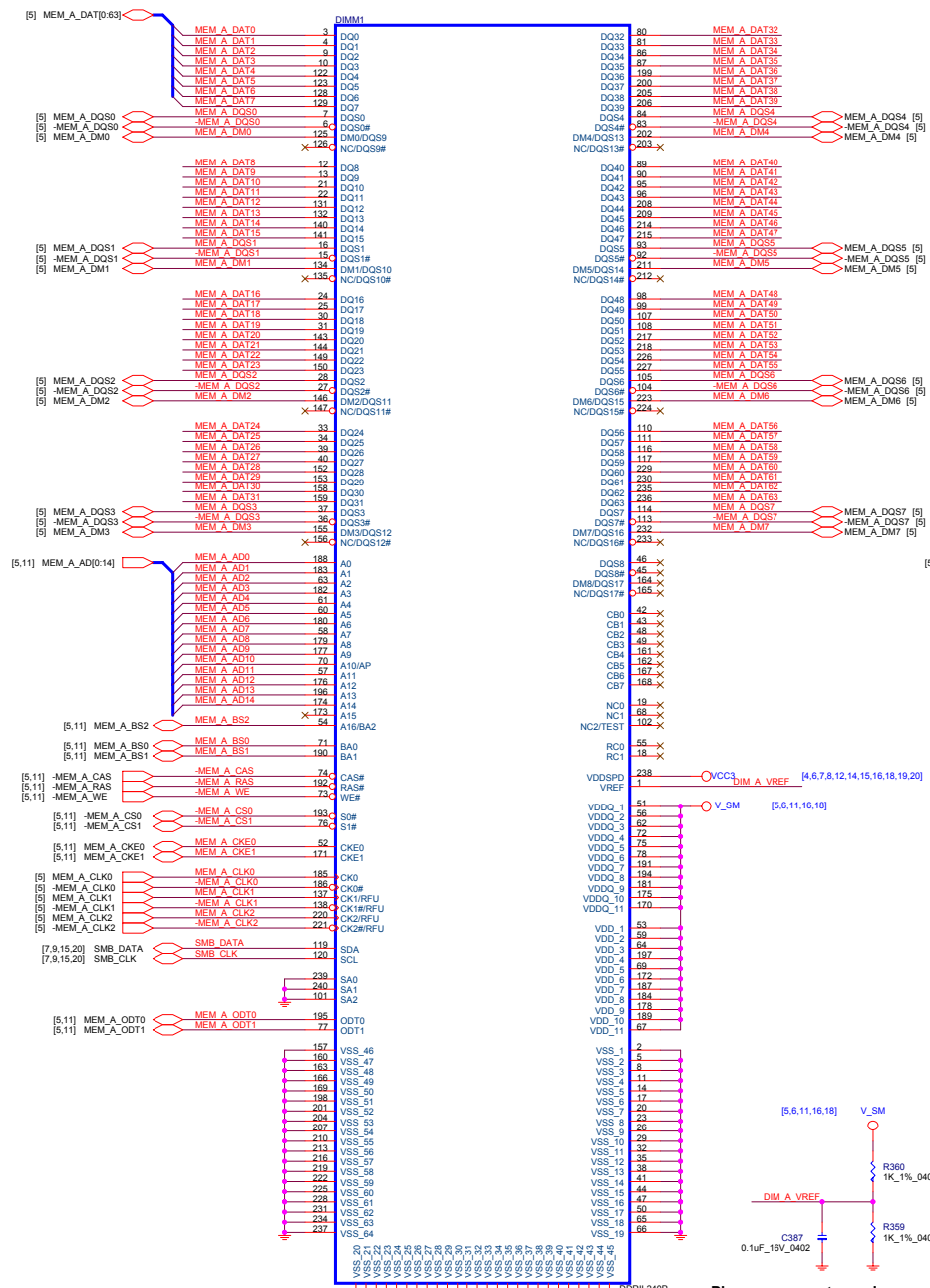
V_3P3_CL



Clock



DDR2



Place components as close as possible to DIMM socket

Place components as close as possible to DIMM socket

DIMM POWER INDICATOR



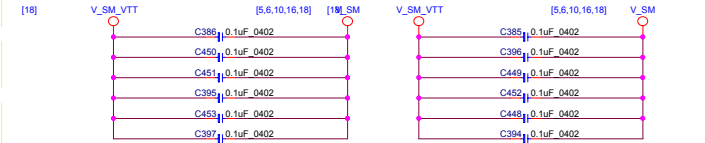
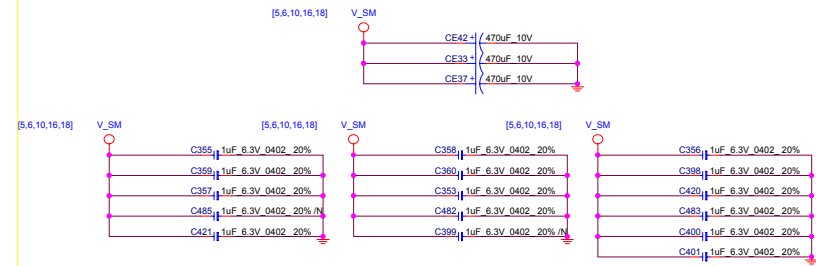
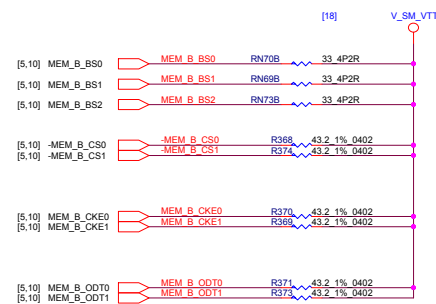
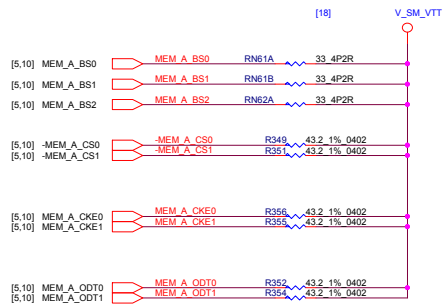
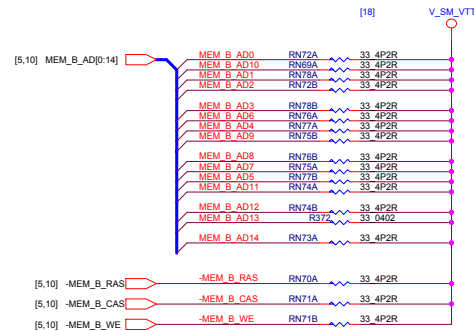
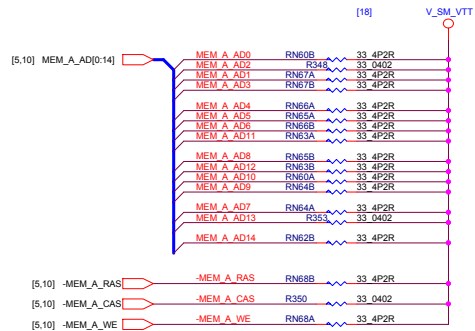
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DDR2

S05700D0

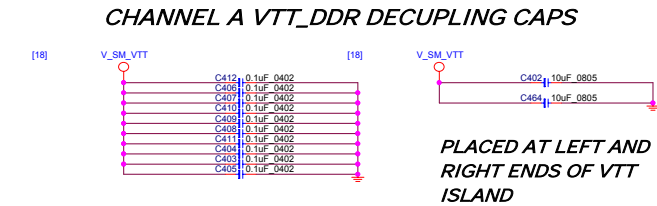
Date: Thursday, January 18, 2007 Sheet 10 of 20

DDR2 Termination

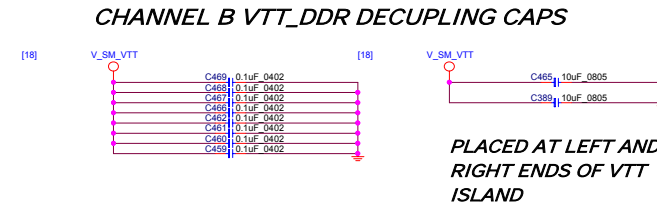


CHANNEL A ADDRESS/CONTROL STITCHING CAPS

CHANNEL B ADDRESS/CONTROL STITCHING CAPS

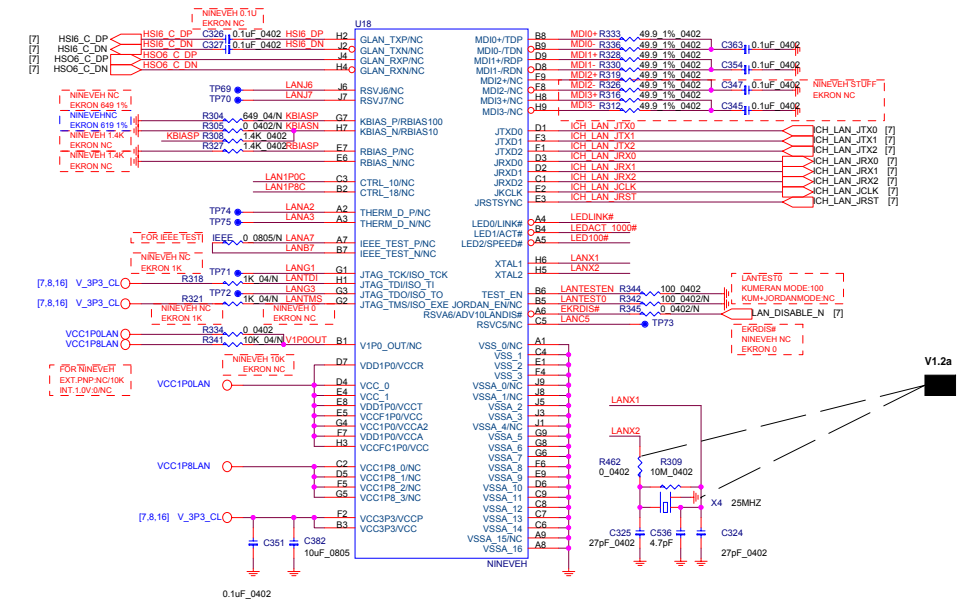


PLACED AT LEFT AND RIGHT ENDS OF VTT ISLAND

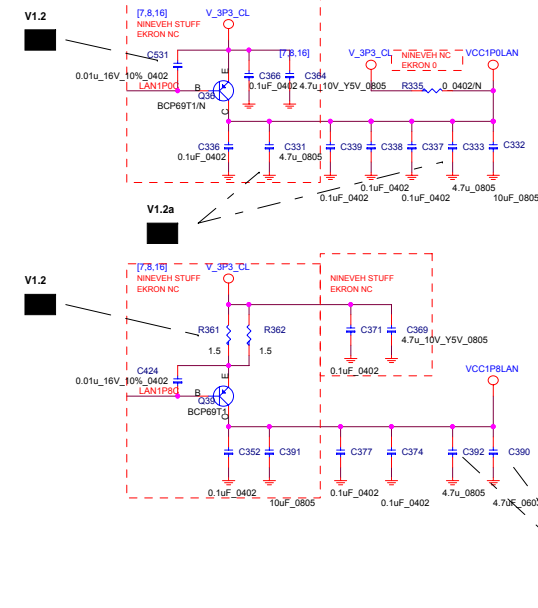


PLACED AT LEFT AND RIGHT ENDS OF VTT ISLAND

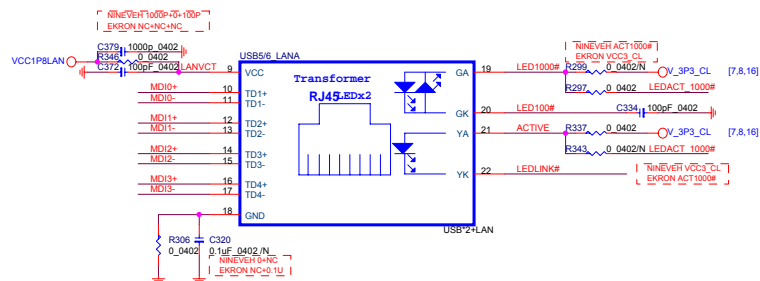
NINEVEH EKRON



1.0V 1.8V REGULATOR



LAN CONNECTOR



LAN DUAL COLOR LED

10MBPS OFF

100MBPS GREEN

1000MBPS YELLOW

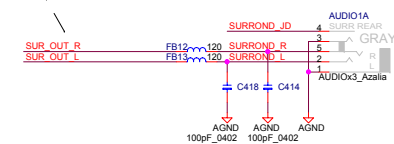
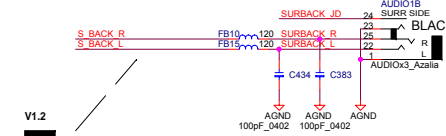
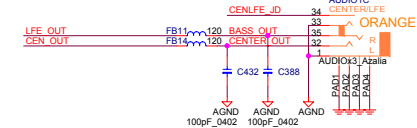
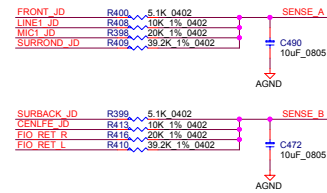
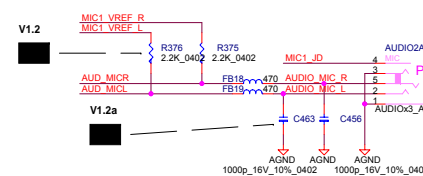
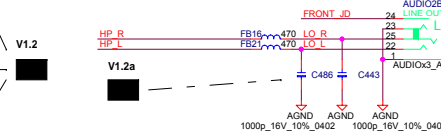
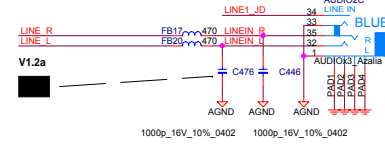
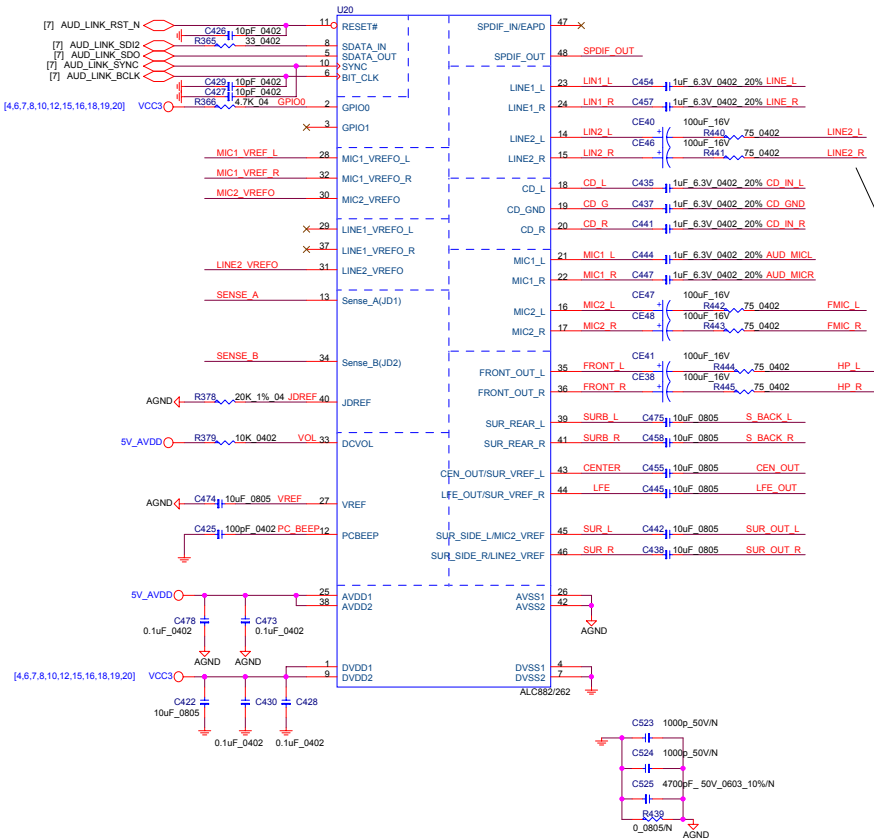
LAN ACTIVE/LINK LED

LINK ON

ACTIVE BLINK

ALC262

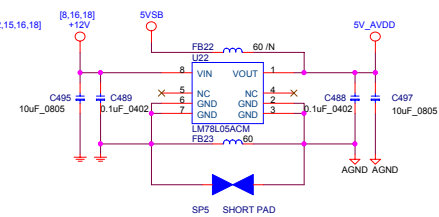
AUDIO CONNECTOR



Alzalia Jack Sense Network

Jack Sense A	Jack Sense B
5K Rear I/O Front Out	5K Rear I/O Surround Back Out
10K Rear I/O Line IN	10K Rear I/O Centel/LFE Out
20K Rear I/O MIC IN	20K Front I/O MIC IN
39K Rear I/O Surround Out	39K Front I/O HP IN

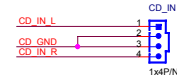
5V_AVDD



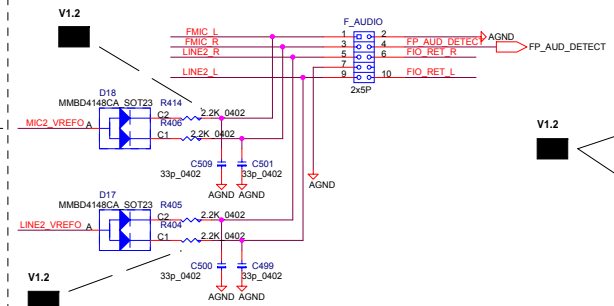
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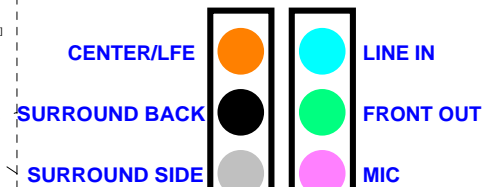
CD_IN



FRONT AUDIO



REAR IO AUDIO CONNECTOR PLACEMENT



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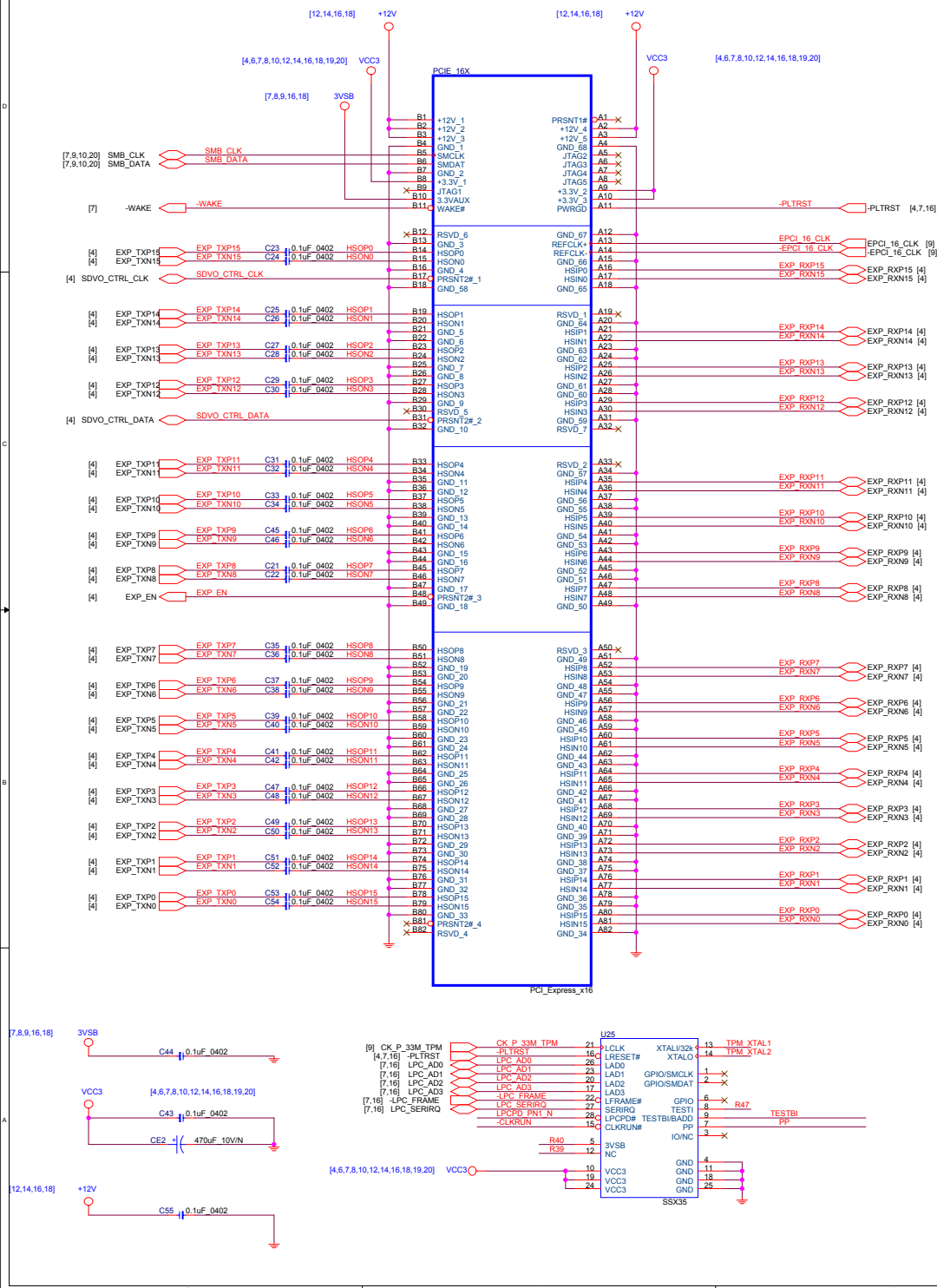
AUDIO

S05700D0

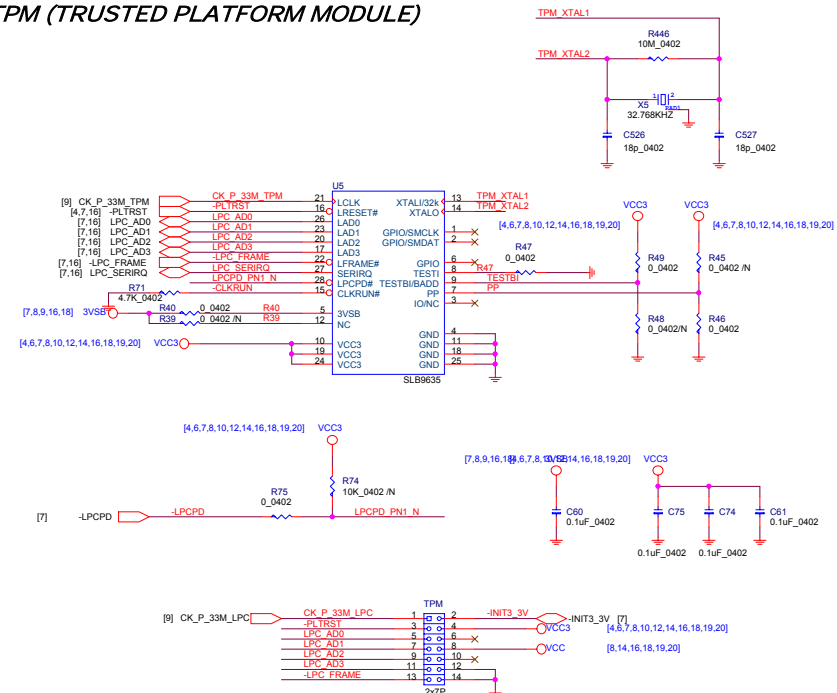
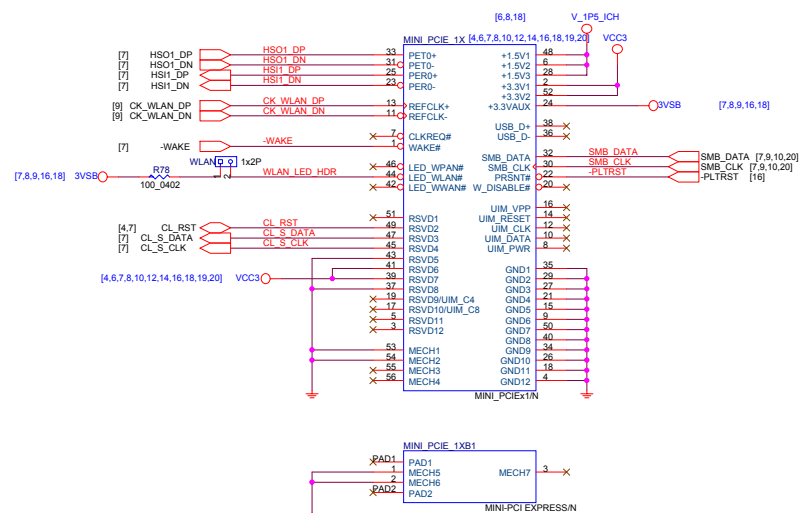
Date: Thursday, January 18, 2007 Sheet 14 of 20

Rev 1.2a

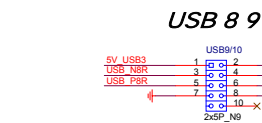
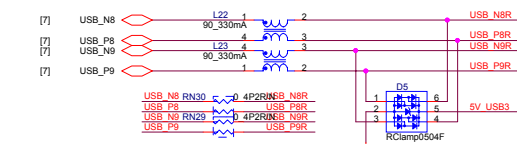
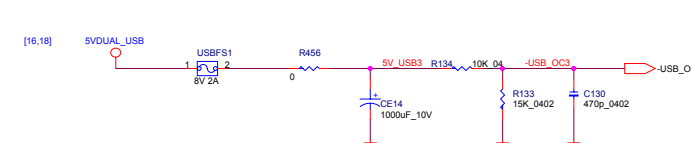
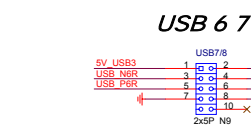
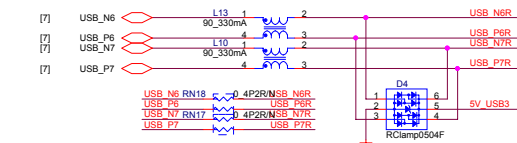
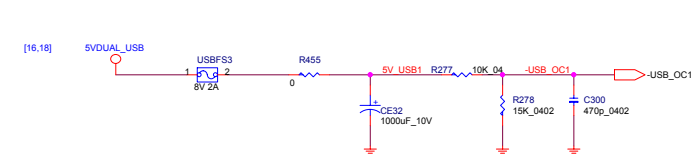
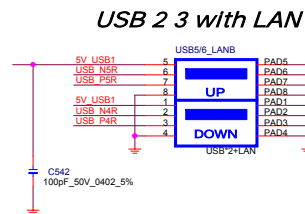
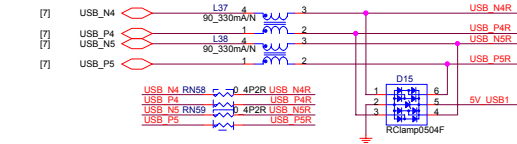
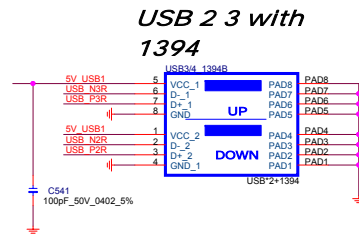
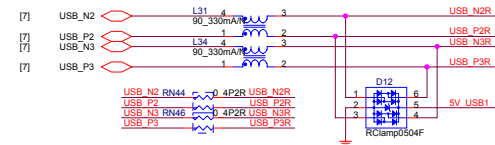
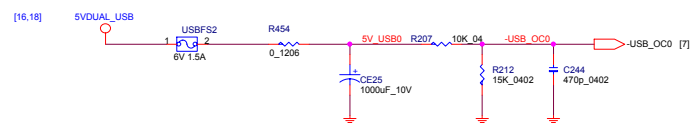
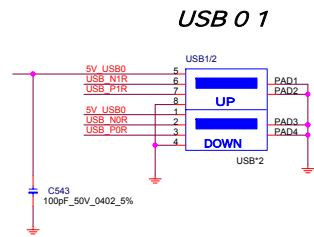
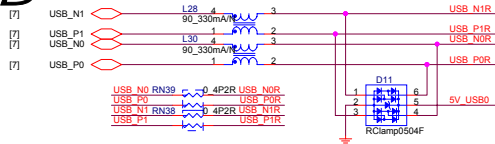
PCI express x16



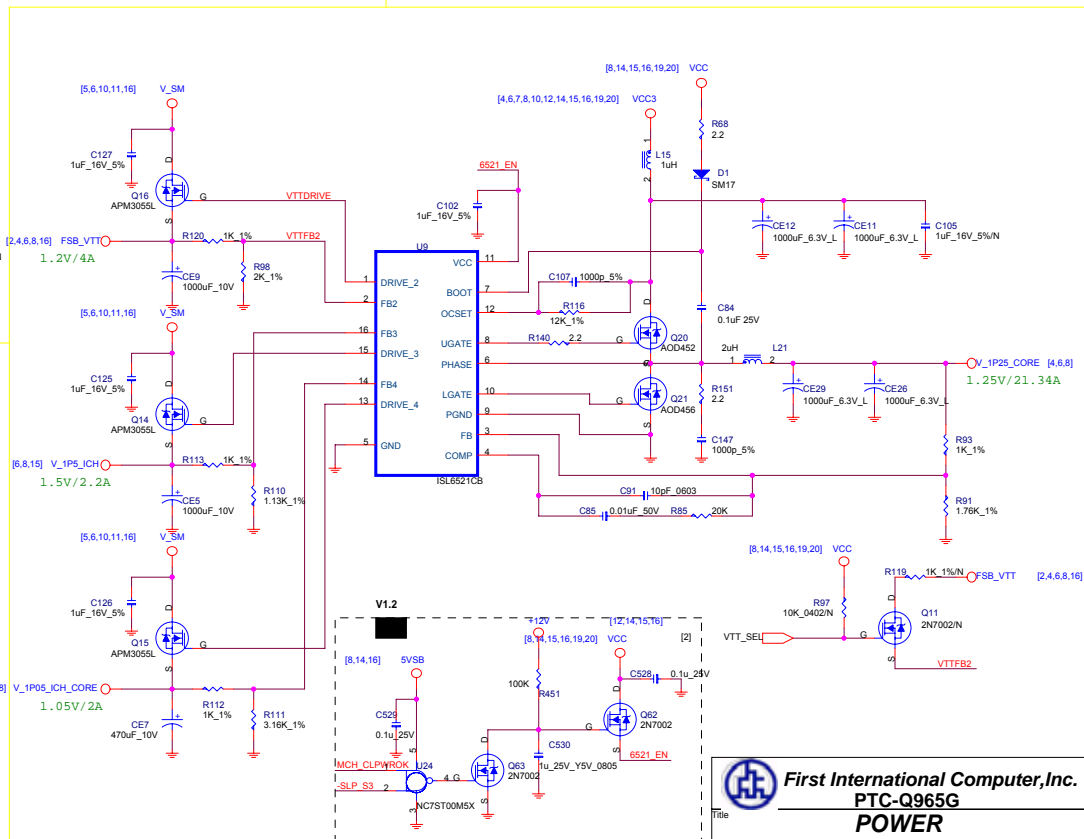
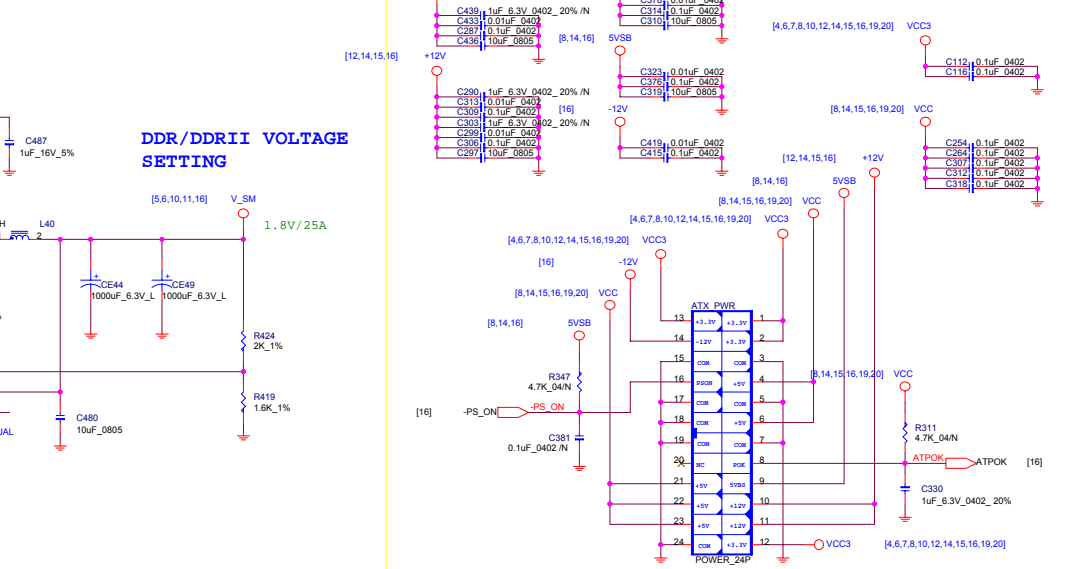
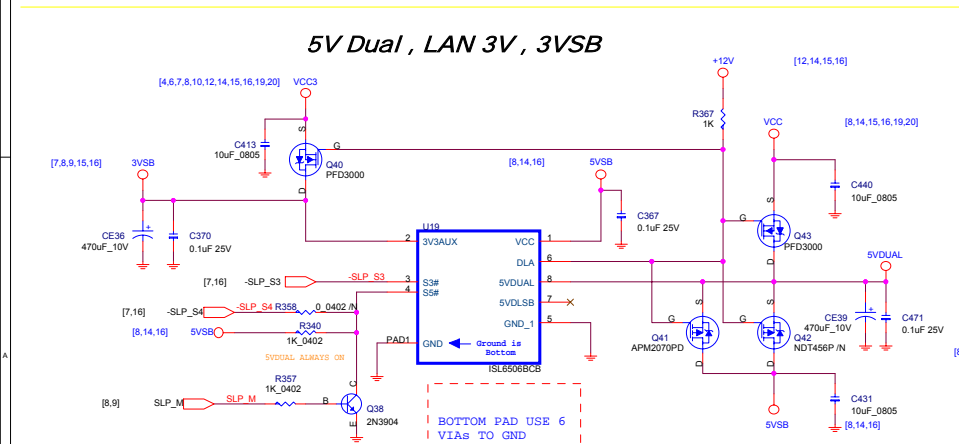
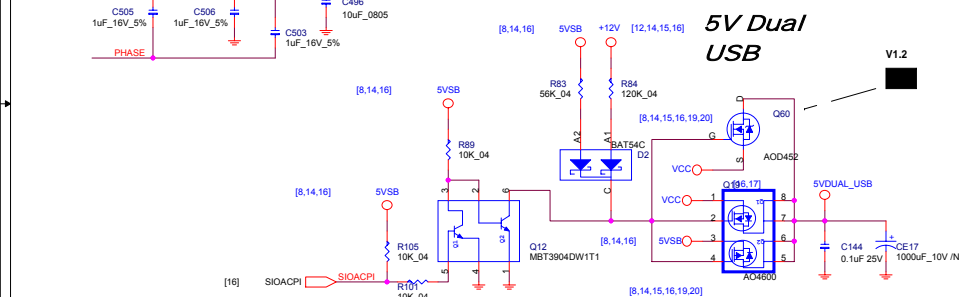
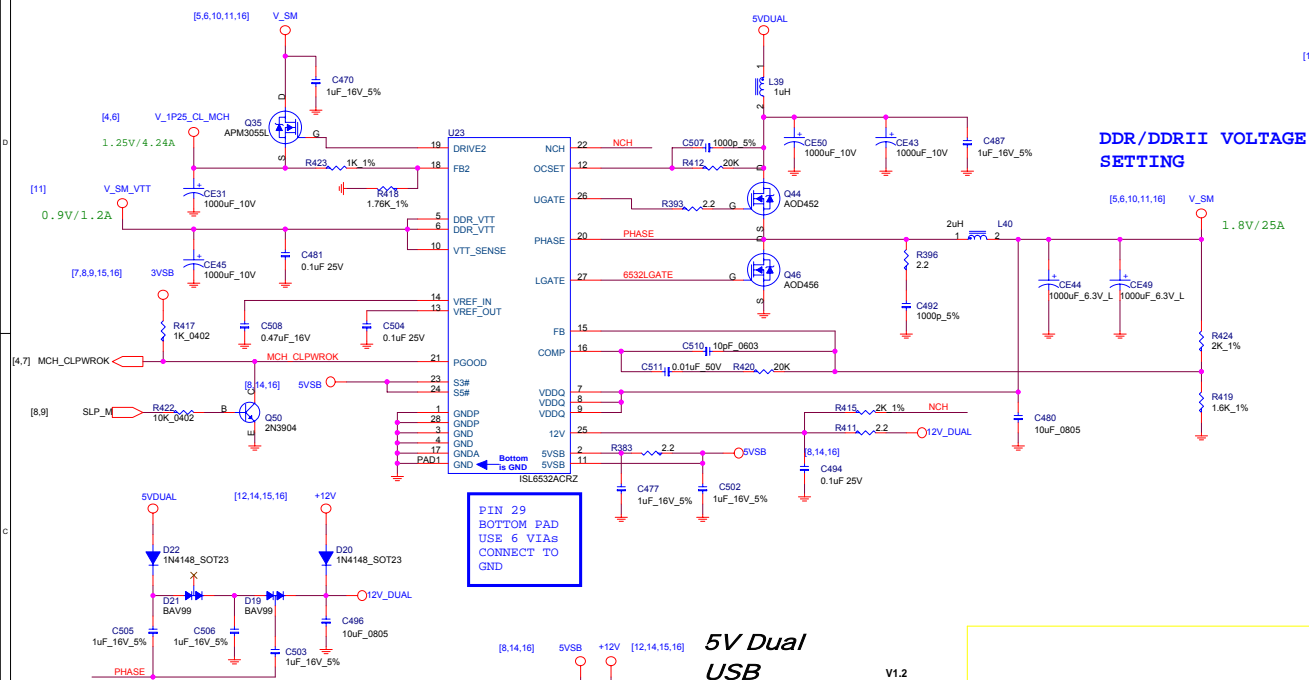
TPM (TRUSTED PLATFORM MODULE)

**WLAN MINI-PCIE**

USB



POWER



VRM

