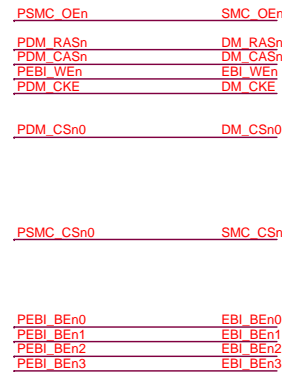
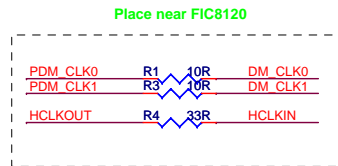


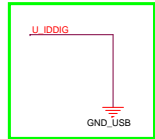
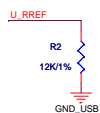
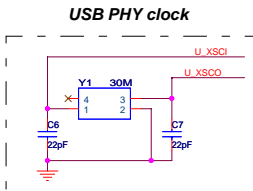
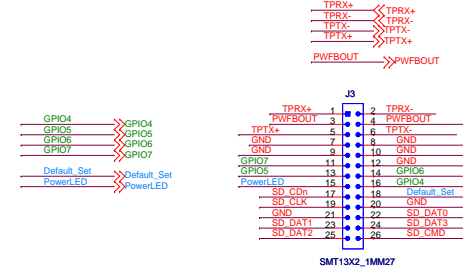
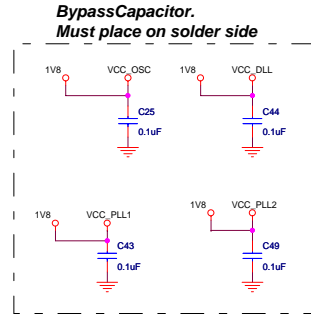
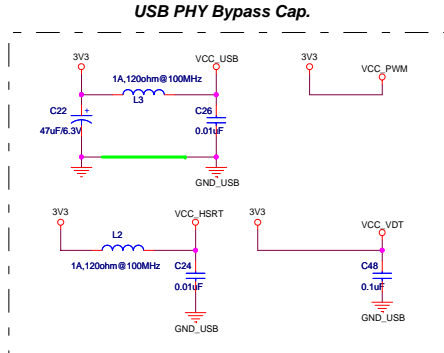
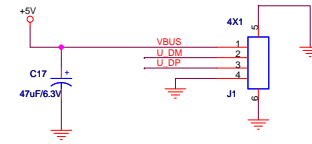
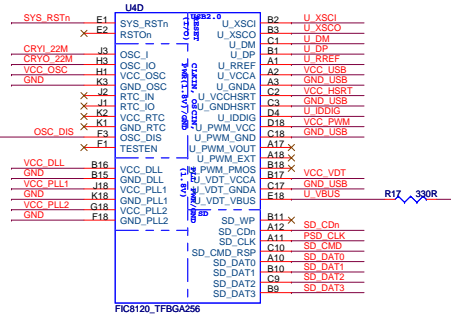
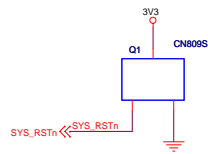
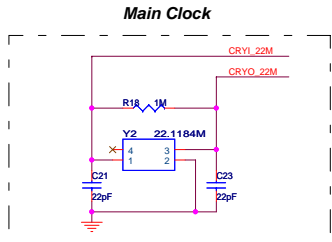
Data code/ Version	Modification
1.0	-First release

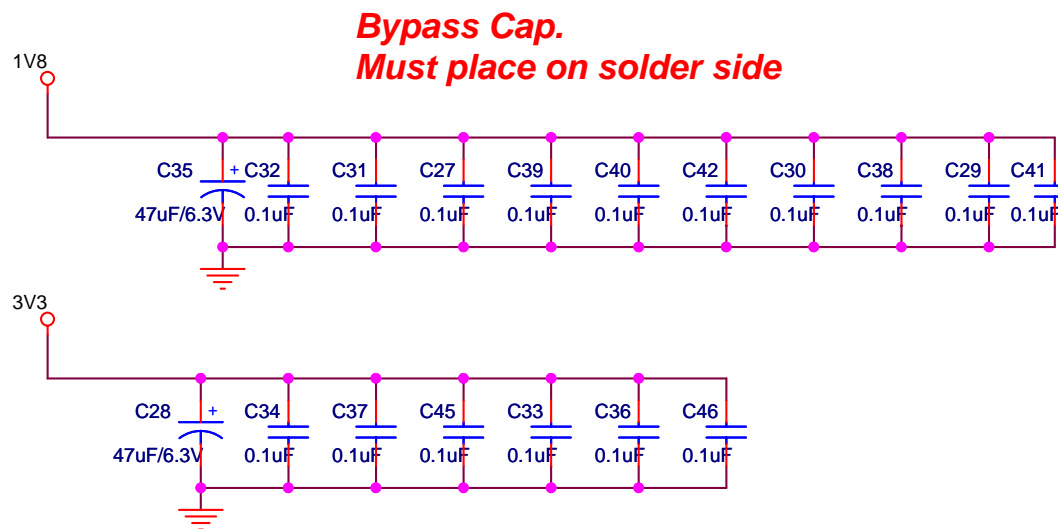
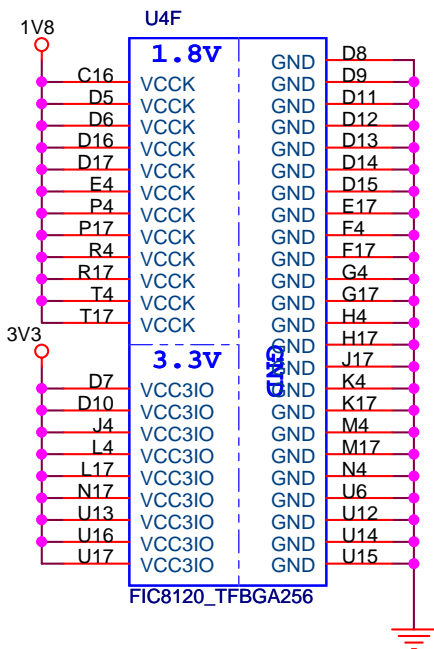
Change log:

Title		
Note		
Size	Document Number	Rev
	OTIPCAMCORE1VD	
Date:	Thursday, April 15, 2010	Sheet 1 of 9

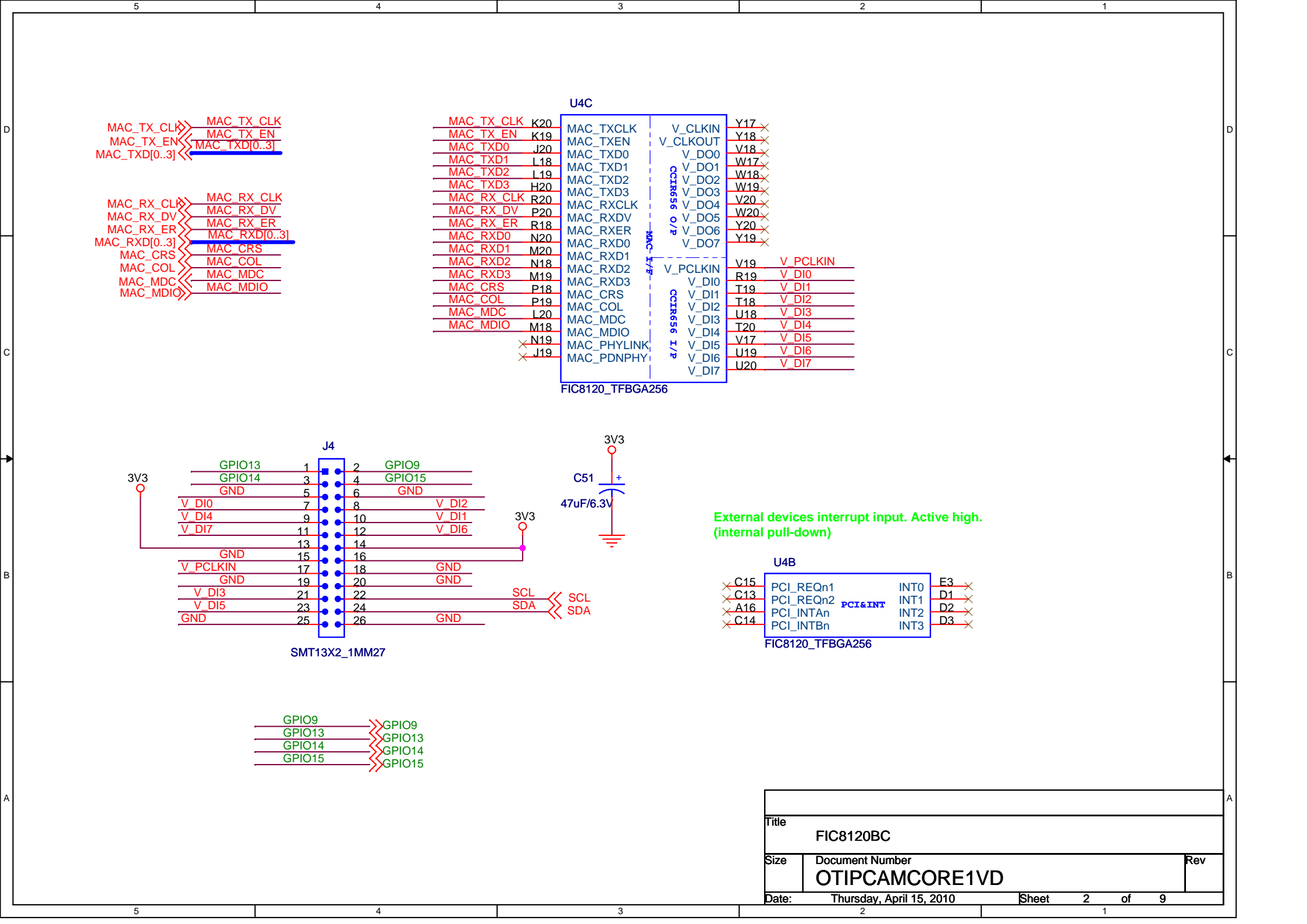


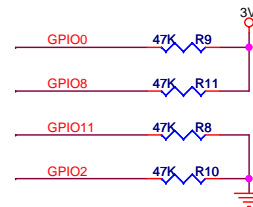
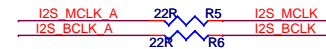
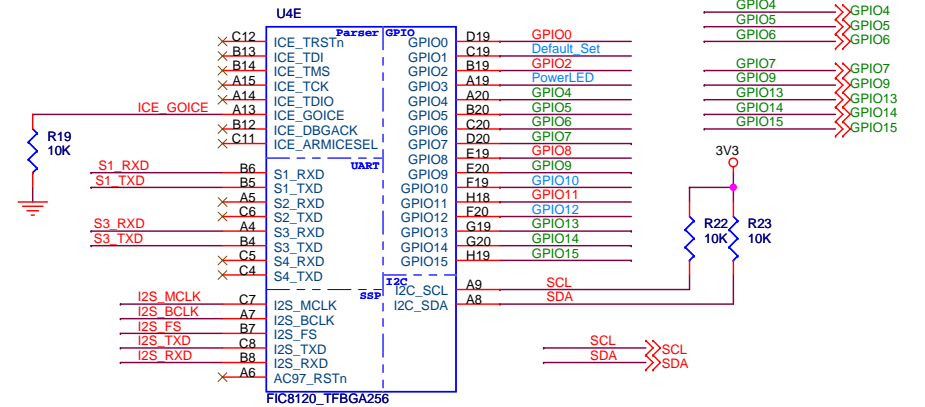
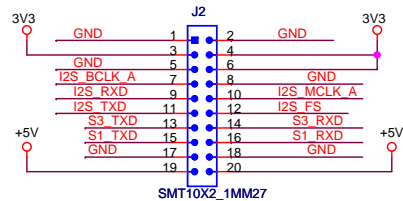
Title			
FIC8120C			
Size	Document Number		Rev
	OTIPCAMCORE1VD		
Date:	Thursday, April 15, 2010	Sheet	1 of 9



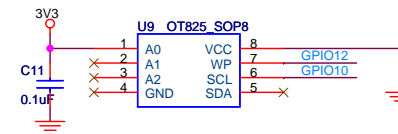
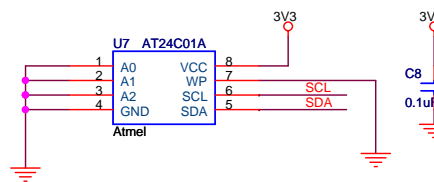
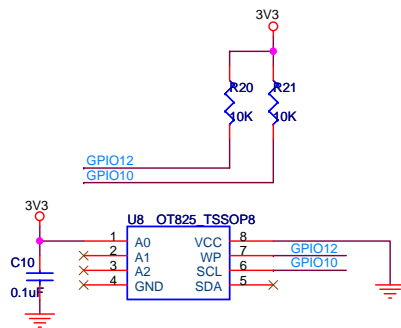


Title		
FIC8120F		
Size	Document Number	Rev
	OTIPCAMCORE1VD	
Date:	Thursday, April 15, 2010	Sheet 2 of 9

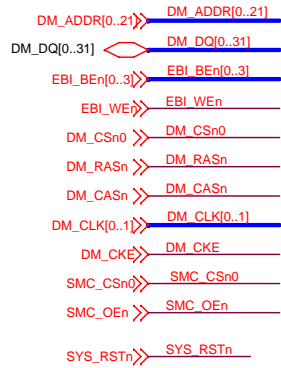




GPIO[11] Timer test. Should be pull-low in normal use.
GPIO[8] CPU operating mode. 0=sync mode, 1=asyn mode
At-speed testing use sync mode, but normal operation use asyn mode.
Should be pull-high in normal use.
GPIO[2] Bypass PLL or not. IC Tester use this jumper setting.
Should be pull-low in normal use.
GPIO[0] Define 8-bit or 16-bit booting.
pull-low: 8-bit booting pull-high: 16-bit booting

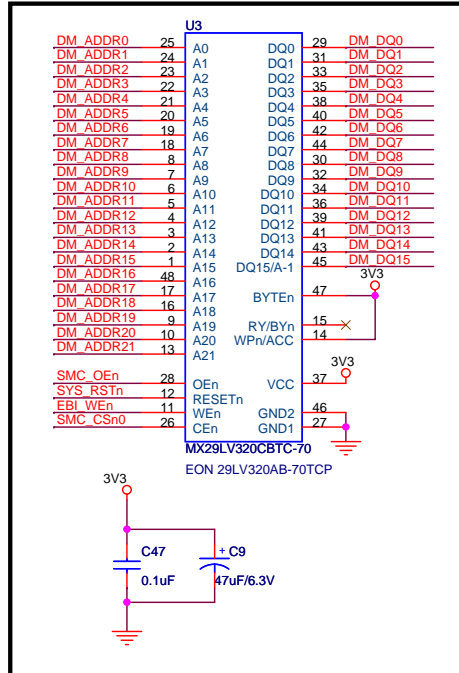


Title		
FIC8120E		
Size	Document Number	Rev
OTIPCAMCORE1VD		
Date:	Thursday, April 15, 2010	Sheet 2 of 9

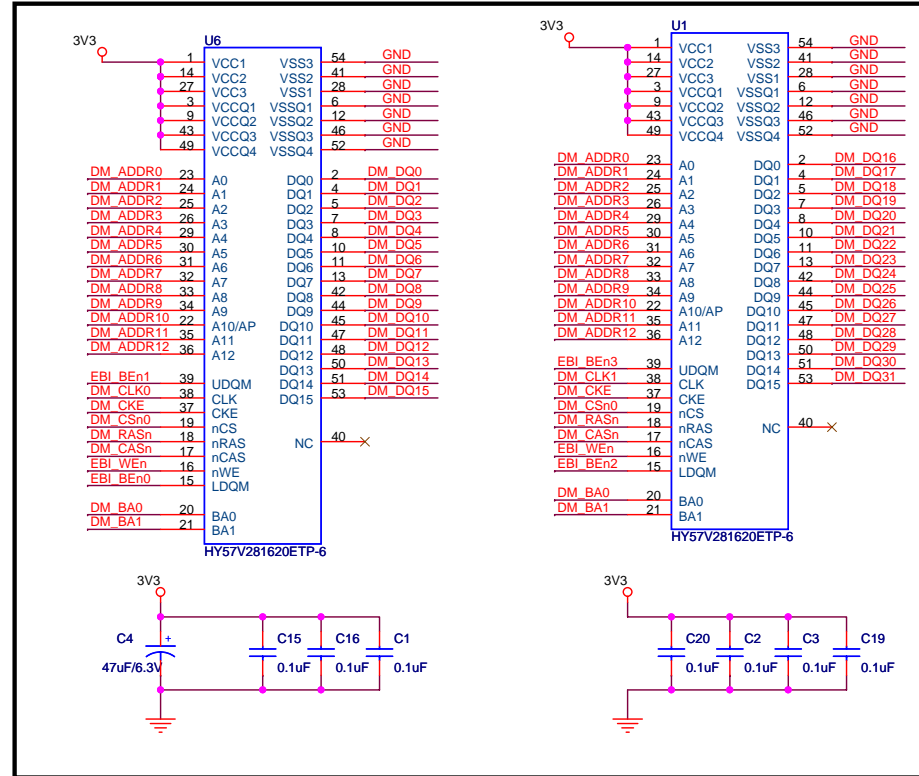


DM_ADDR13 DM_BA0
DM_ADDR14 DM_BA1

On Board Flash (2Mx16)



On Board SDRAM(8Mx16)x2



Title		
Memory		
Size	Document Number	Rev
	OTIPCAMCORE1VD	
Date:	Thursday, April 15, 2010	Sheet 4 of 9

MAC_RXD[0..3] << MAC_RXD[0..3]
 MAC_RX_CLK << MAC_RX_CLK
 MAC_RX_DV << MAC_RX_DV
 MAC_RX_ER << MAC_RX_ER

MAC_CRS << MAC_CRS
 MAC_COL << MAC_COL

MAC_MDIO << MAC_MDIO
 MAC_MDC << MAC_MDC

MAC_TXD[0..3] >> MAC_TXD[0..3]

MAC_TX_CLK << MAC_TX_CLK

MAC_TX_EN >> MAC_TX_EN

Connect to MAC which
 has MII interface

MAC_MDC 25
 MAC_MDIO 26
 MAC_TXD0 6
 MAC_TXD1 5
 MAC_TXD2 4
 MAC_TXD3 3
 MAC_TX_EN 2
 MAC_TX_CLK 7
 MAC_RX_DV 22
 MAC_RXD0 21
 MAC_RXD1 20
 MAC_RXD2 19
 MAC_RXD3 18
 MAC_RX_CLK 16
 MAC_COL 1
 MAC_CRS 23
 MAC_RX_ER 24

PHYAD3
 PHYAD0
 PHYAD1
 PHYAD2
 PHYAD3
 PHYAD4

PWFBIN 8
 DVDD33 14
 DVDD33 48

DGND 11
 DGND 17
 DGND 45

RTL8201CL /
 RTL8201CP

RTL8201BL LQFP48

PWFBOUT 32
 AVDD33 36
 AGND 29
 AGND 35
 NC 27
 TPRX+ 31
 TPRX- 30
 TPTX- 33
 TPTX+ 34
 RTSET 28
 ISOLATE 43
 RPTR 40
 SPEED 39
 DUPLEX 38
 ANE 37
 LDPS 41
 MII/SNIB/RTT3 44
 RESETB 42

PWFBOUT >> PWFBOUT

TPRX+ << TPRX+
 TPRX- << TPRX-
 TPTX- << TPTX-
 TPTX+ << TPTX+

R14 49.9/1%
 R13 49.9/1%
 C13 0.1uF

R15 2K/1%

R12 10K
 C12 0.1uF

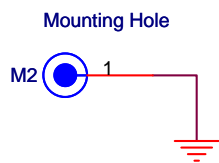
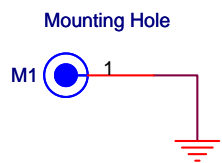
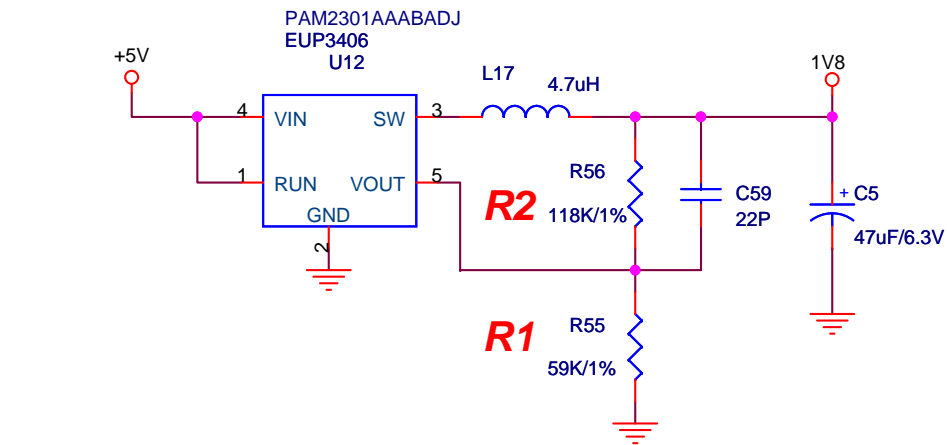
Y3 25M
 C50 22pF
 C52 22pF

R16 10K
 PHYAD3

PWFBOUT
 C55 47uF/6.3V
 C54 0.1uF
 L4 1A,120ohm@100MHz
 C56 0.1uF
 PWFBIN

3V3
 L1 1A,120ohm@100MHz
 C14 0.1uF
 AVDD33
 C58 0.1uF
 C57 47uF/6.3V
 C53 0.1uF
 3V3

Title		
Network		
Size	Document Number	Rev
OTIPCAMCORE1VD		
Date:	Thursday, April 15, 2010	Sheet 6 of 9



Title			
Power			
Size	Document Number		Rev
	OTIPCAMCORE1VD		
Date:	Thursday, April 15, 2010	Sheet 9 of 9	