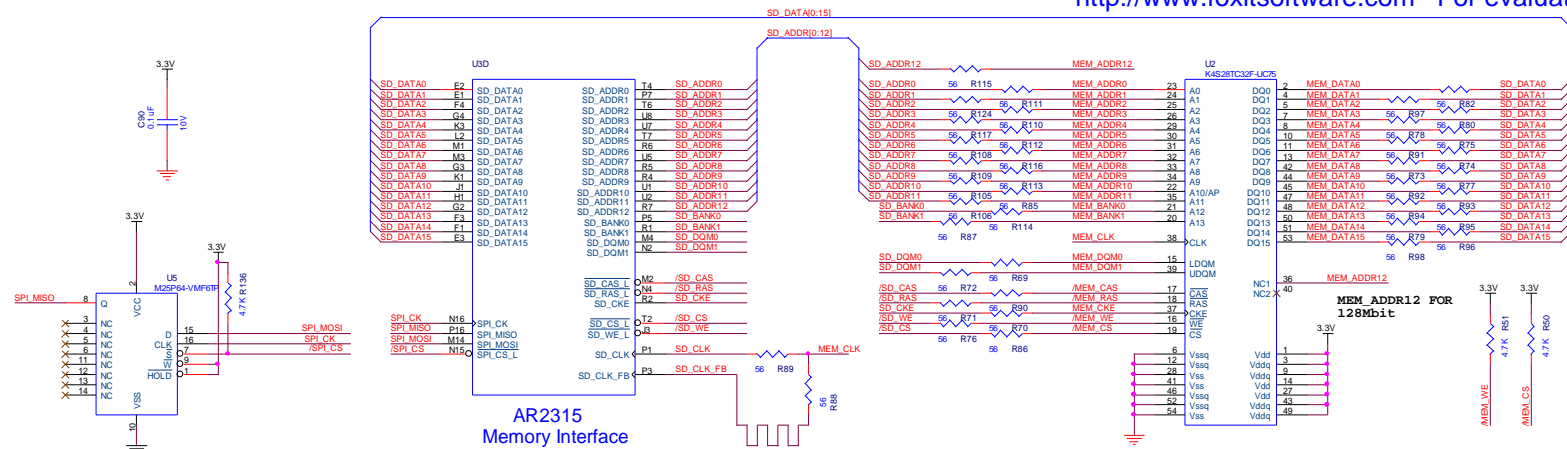


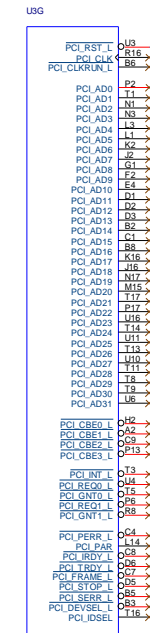
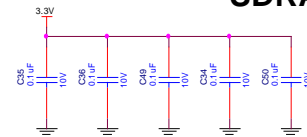
AP- AR2315 BASED, 802.11b/g BRIDGE

DATE	REVISION NUMBER	Page	DESCRIPTION
2007/03/09	Revision 01		Initial release.

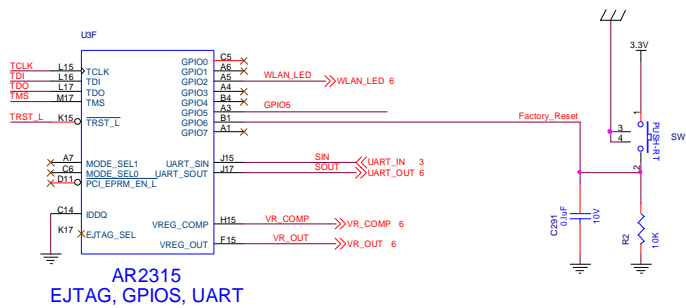


CONNECT SD_CLK_FB AT
HALF WAY POINT OF
SD_CLK

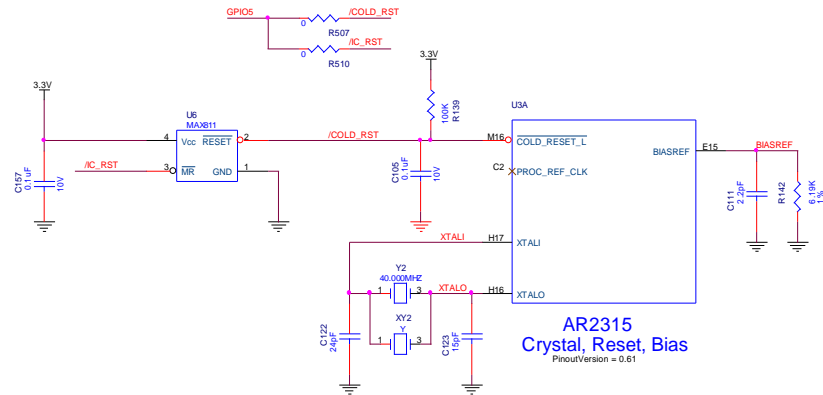
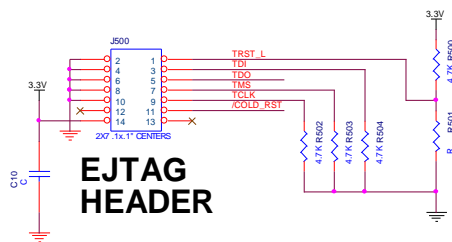
SDRAM



AR2315 PCI Interface

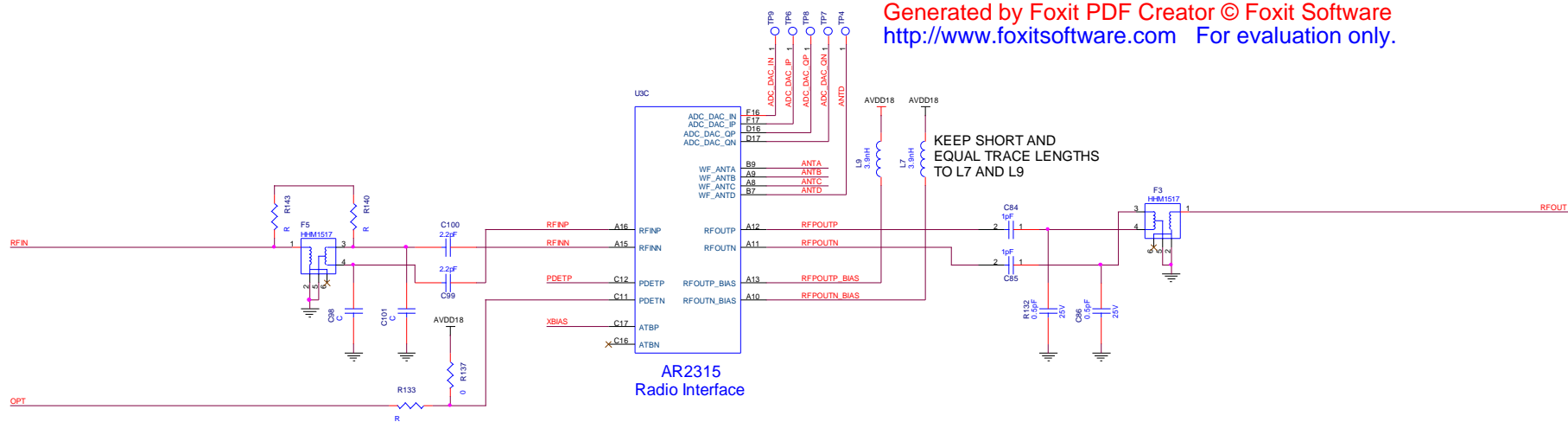


EJTAG HEADER

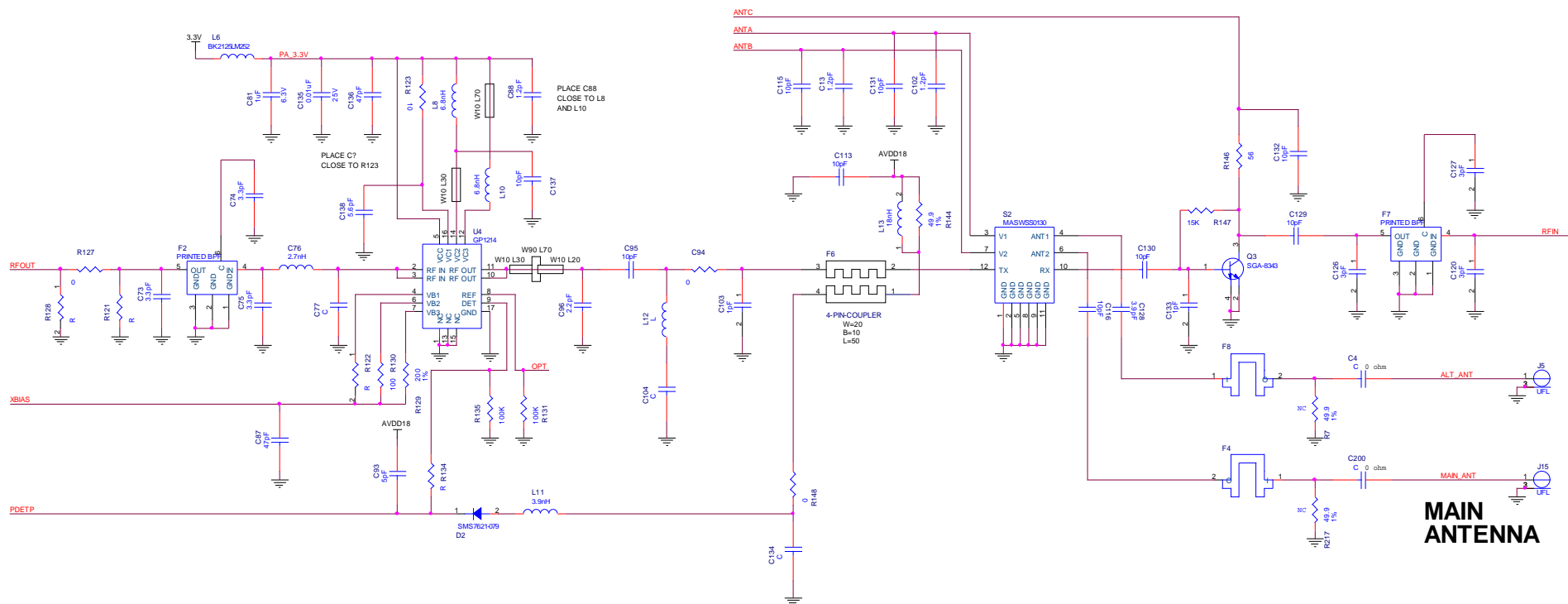


AR2315
Crystal, Reset, Bias
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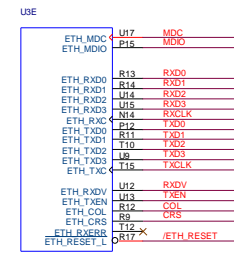
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Size C	Document Number <Doc>		Rev <RevCode>
Date:	Thursday, September 27, 2007	Sheet	3 of 7



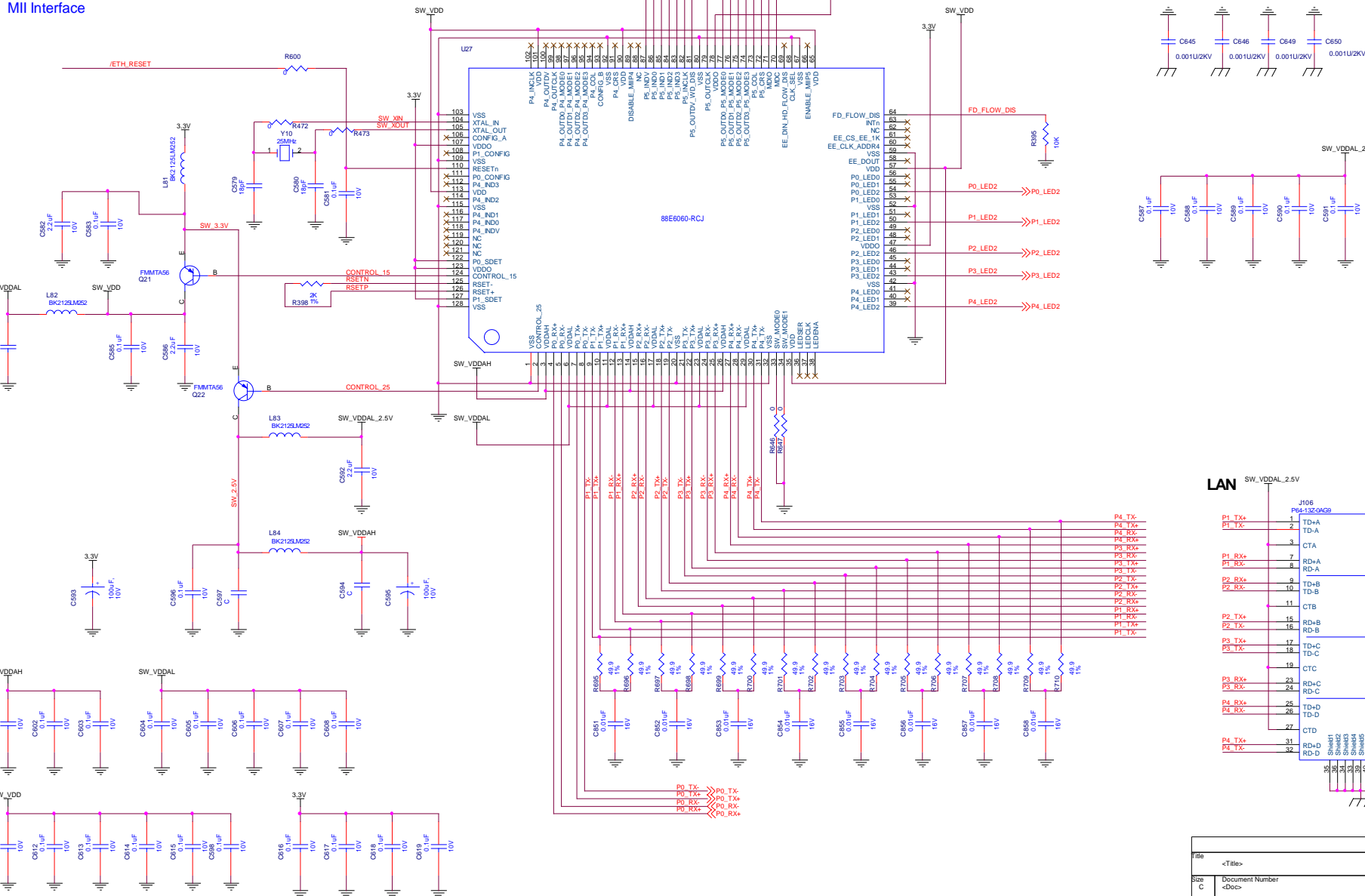
**ATHEROS CONFIDENTIAL
PRELIMINARY**

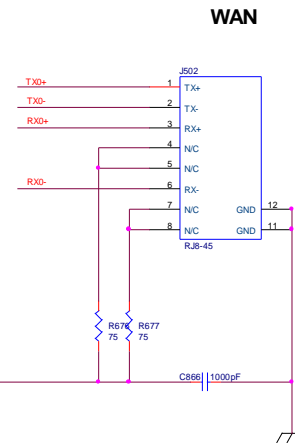
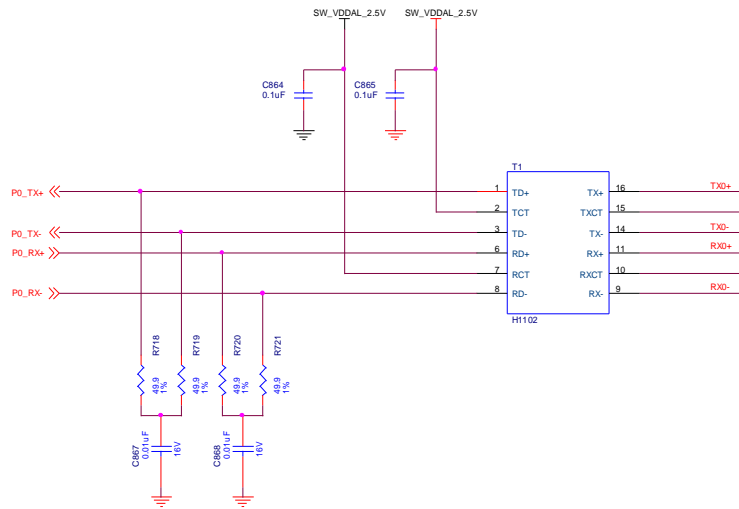
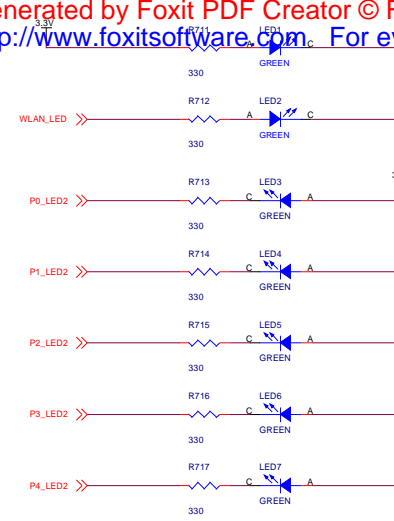
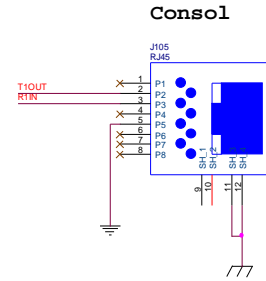
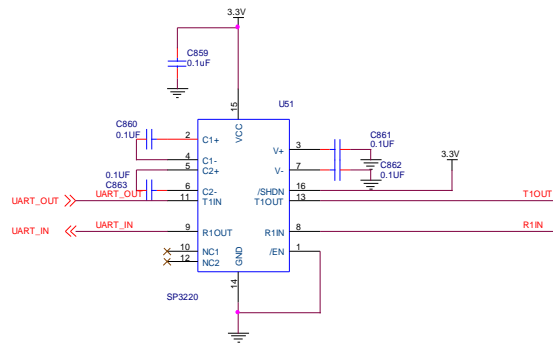


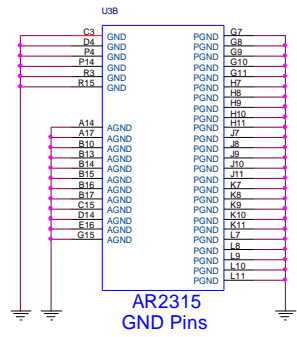
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Date:	Thursday, September 27, 2007		Sheet 4 of 7



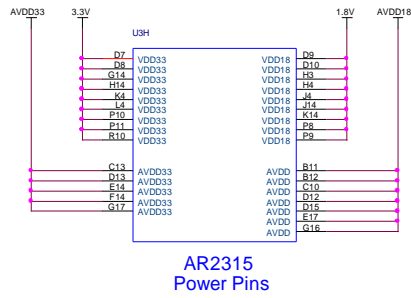
AR2315
MII Interface



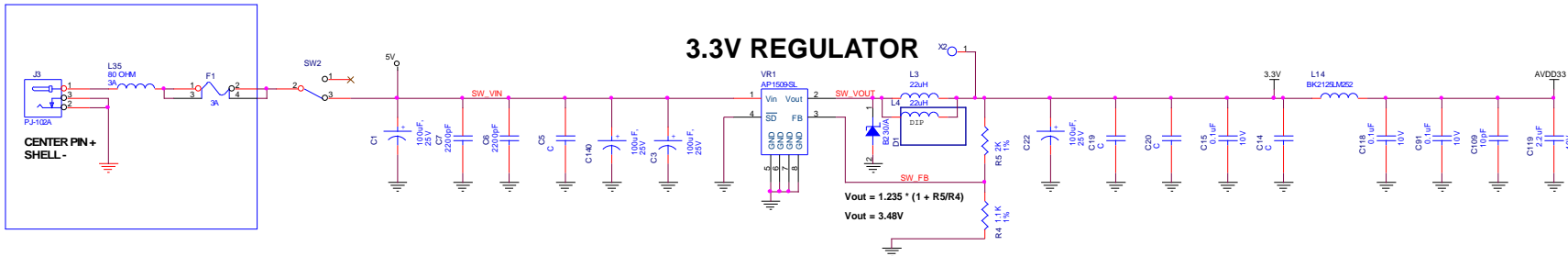




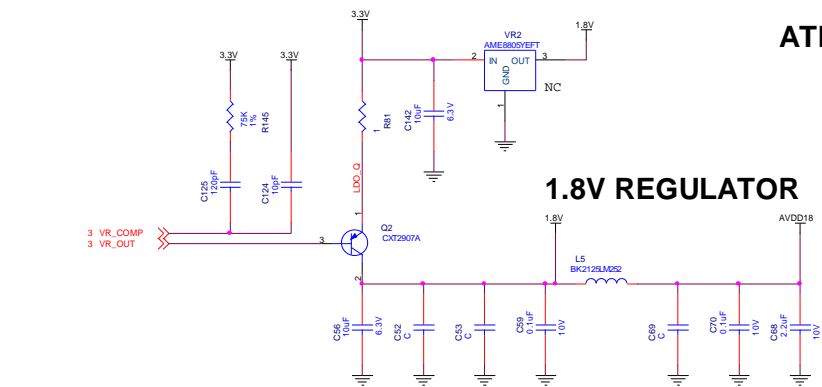
GROUND PINS



POWER PINS

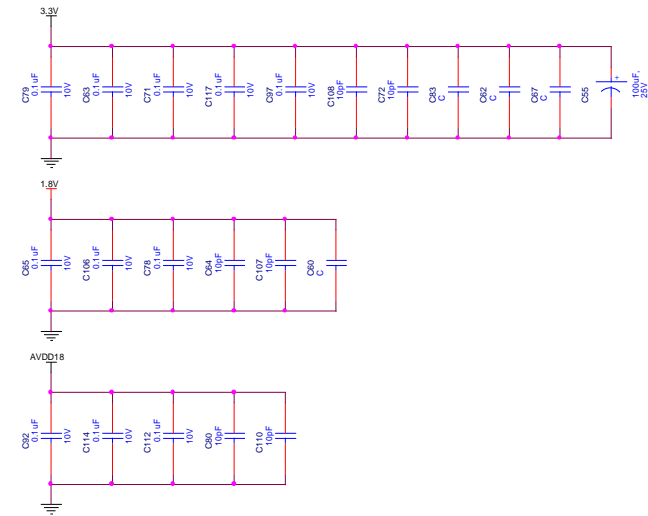


3.3V REGULATOR



ATHEROS CONFIDENTIAL
PRELIMINARY

1.8V REGULATOR



Title <Title>			
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