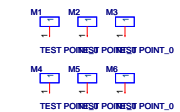
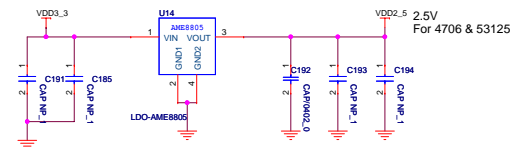
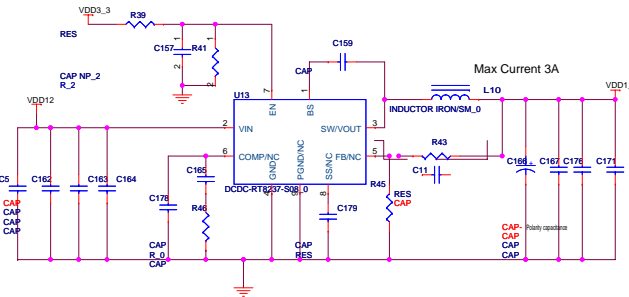
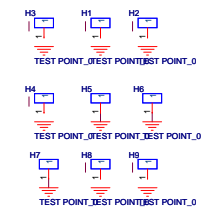
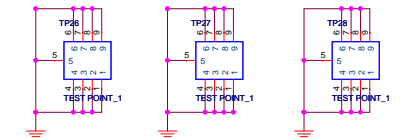
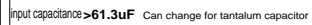
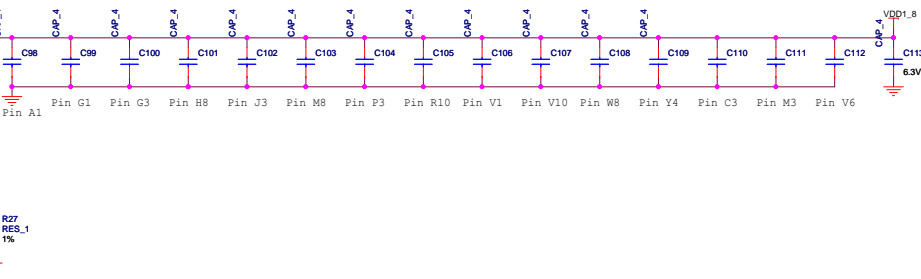
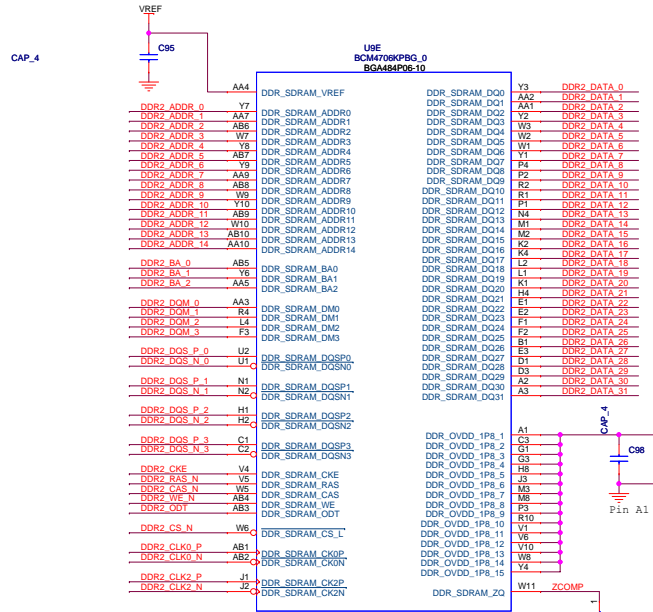
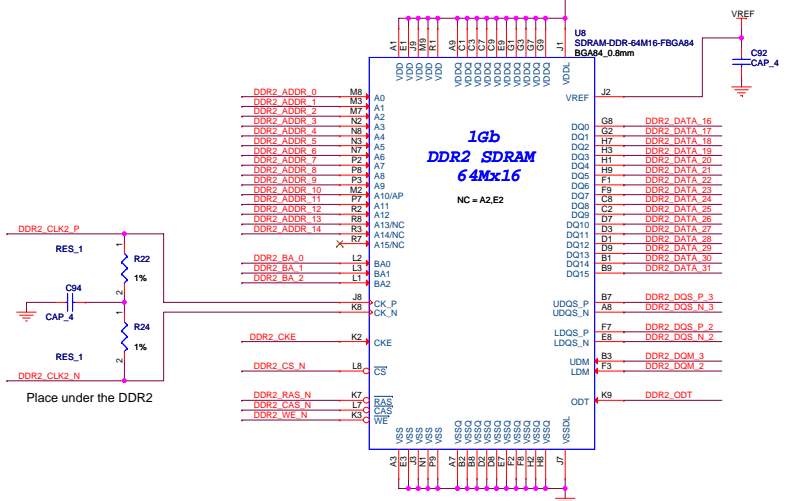
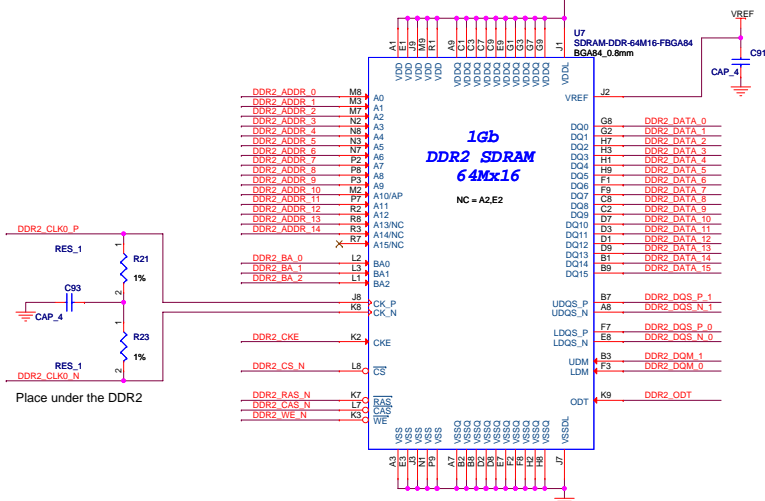
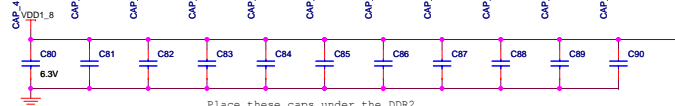
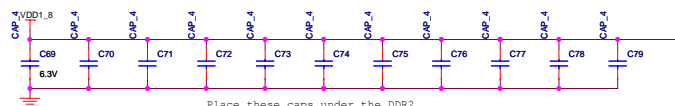


Product Name: W1800R && W80E  
Common Product: N/A

WIRELESS ROUTER

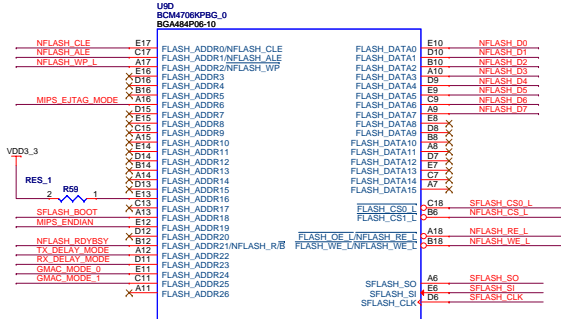
**W80E/W1800R**



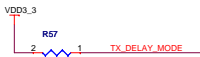


4706 Strapping option pin	Function	Setting	Default Pull
FLASH_ADDR6	0 = Standard JTAG port [default] 1 = MIPS EJTAG port	0	PD
FLASH_ADDR8	0 = GMAC port voltage is 2.5V (for RGMII) [default] 1 = GMAC port voltage is 3.3V (for MII)	0*	PD
FLASH_ADDR12	0 = PCIe port #1 is enabled [default] 1 = PCIe port #1 is disabled for power savings.	0*	PD
FLASH_ADDR16	0 = Use internal PLL to generate REFCLK and output it [default] 1 = Use the external REFCLK input	0*	PD
FLASH_ADDR17	If FLASH_ADDR18 (SFLASH_BOOT) is set to "0" then: 0 = 8-bit parallel flash [default] 1 = 16-bit parallel flash  If FLASH_ADDR18 (SFLASH_BOOT) is set to "1" then: 0 = ST Micro serial flash [default] 1 = Amel serial flash	0*	PD
FLASH_ADDR18	0 = Boot from paralld flash [default] 1 = Boot from serial flash [setting]	1	PD
FLASH_ADDR19	0 = Little Endian [default] 1 = Big Endian	0	PD
FLASH_ADDR22	0 = No delay on GMAC_TXCLK [default] 1 = 2ns delay on GMAC_TXCLK [setting]	1	PD
FLASH_ADDR23	0 = No delay on GMAC_RXCLK [default] 1 = 2ns delay on GMAC_RXCLK [setting]	1	PD
FLASH_ADDR[25:24]	00 = RWII [default] 01 = WII mode 1X = RGMII [setting]	10	PD,PD

\* = Strapping option set at the default with no option to change.



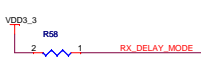
FLASH\_ADDR6 Strapping Option:  
Load R12 for EJTAG mode.



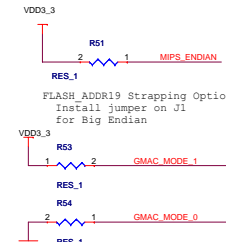
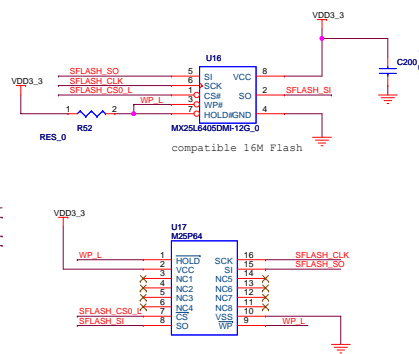
FLASH\_ADDR22 Strapping Option:  
Set 2ns delay on GMAC\_TXCLK.



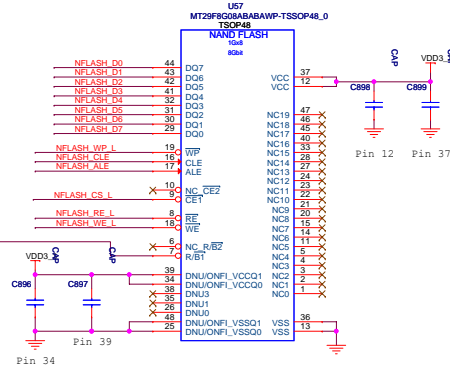
FLASH\_ADDR18 Strapping Option:  
Set to boot from serial flash.



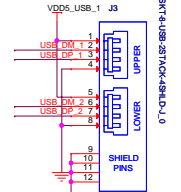
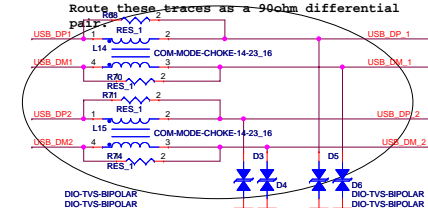
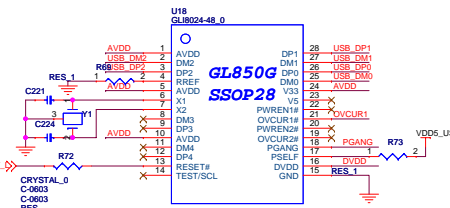
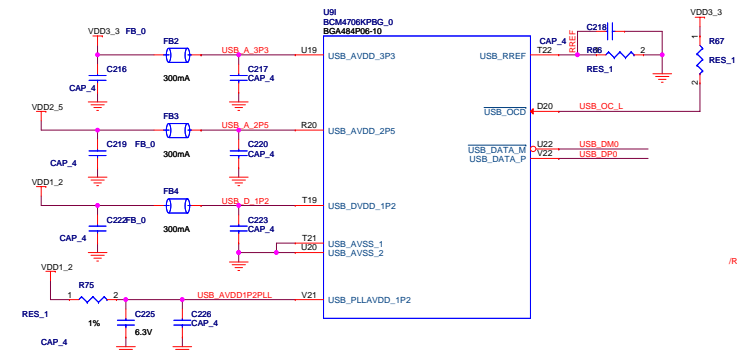
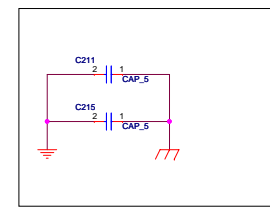
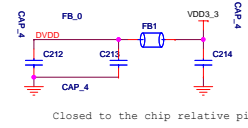
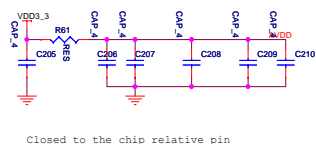
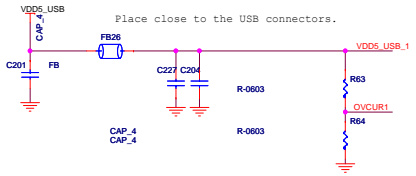
FLASH\_ADDR23 Strapping Option:  
Set 2ns delay on GMAC\_RXCLK.



FLASH\_ADDR[25:24] Strapping Option:  
Set RGMII mode.

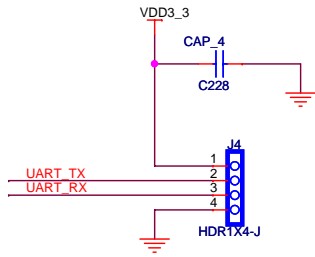


- 1) Completely remove all the components connected to the USB pins.
- 2) Connect all the pins on the left side of the above symbol to GROUND.
- 3) Leave all the pins on the right side of the above symbol floating.

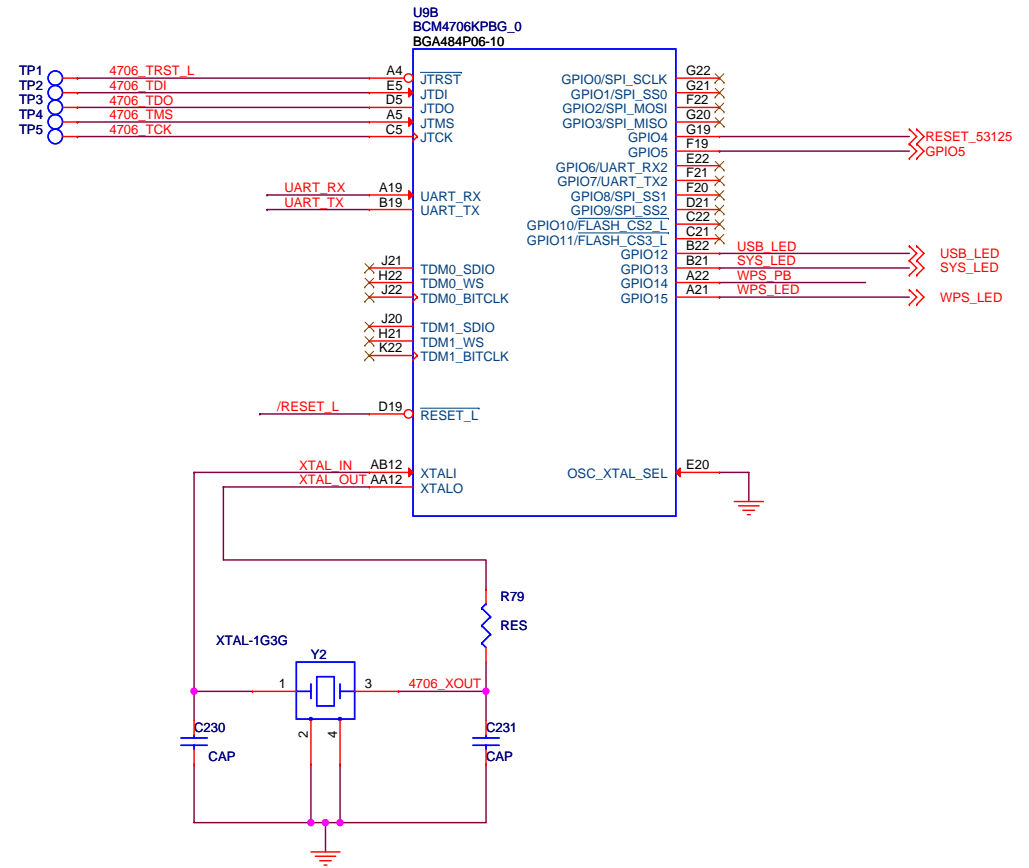
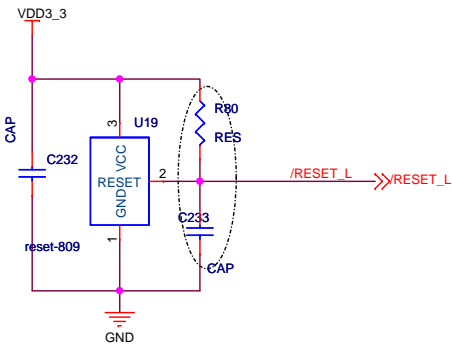
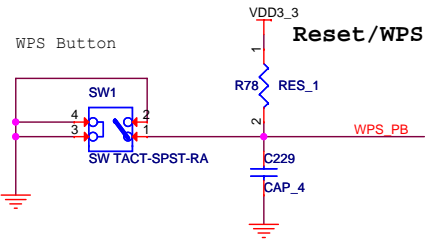


NOTE: If the USB port is not used in a design, then make the following changes to the schematic:

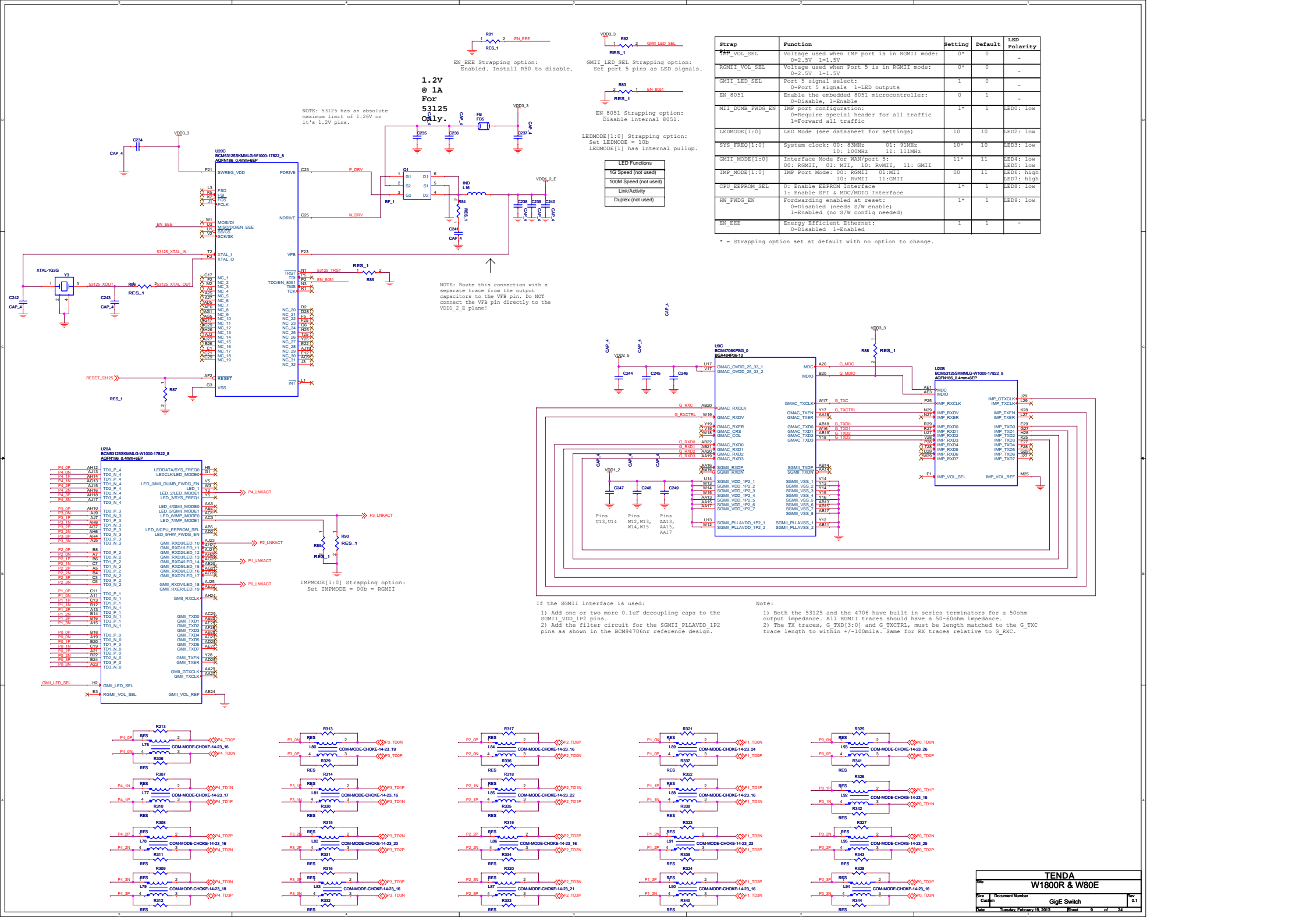
Customer designs should use this circuit to save cost.  
Change the parts to "loaded".

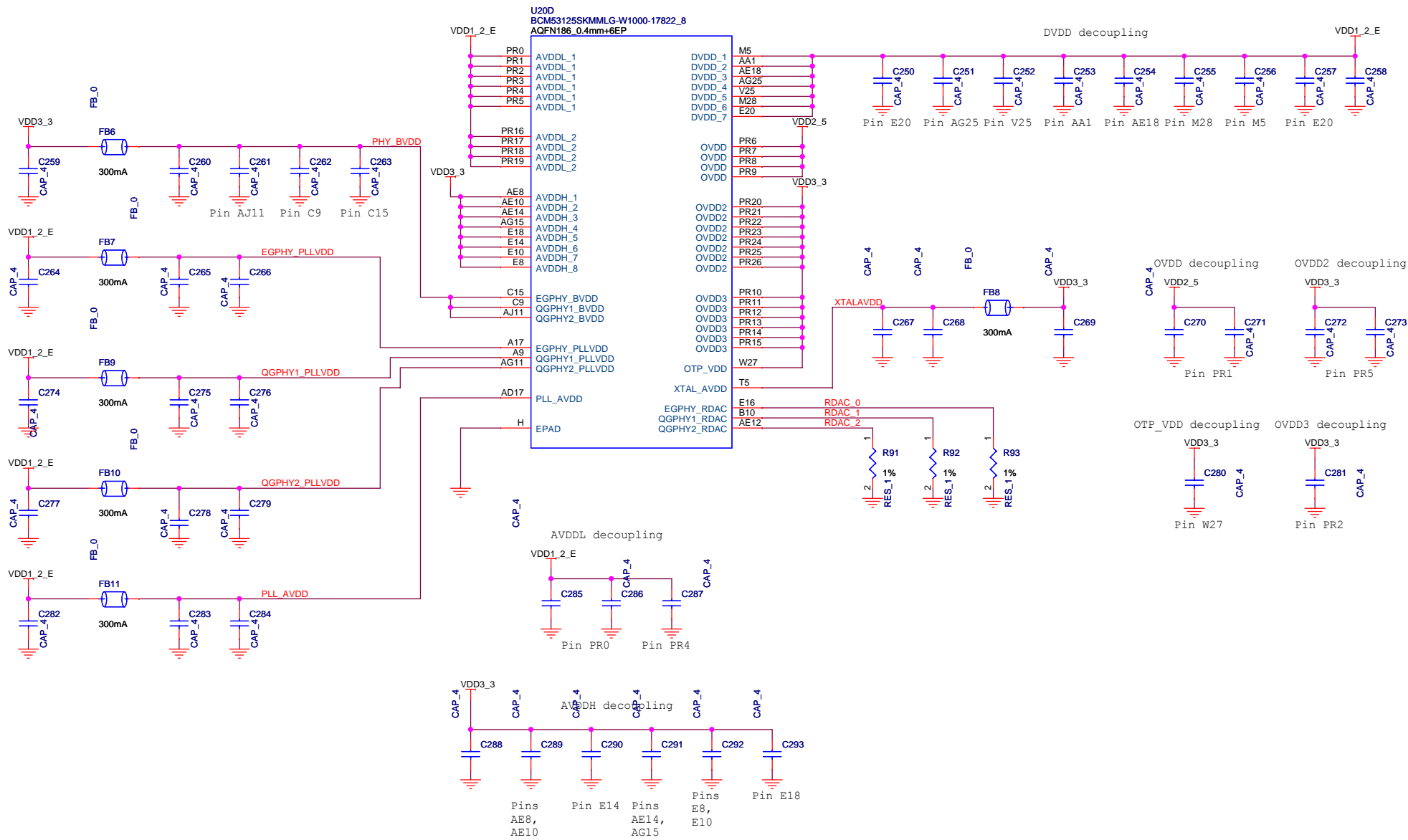


Optional WPS functionality. Remove along  
with the WPS LED, if not required.

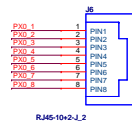
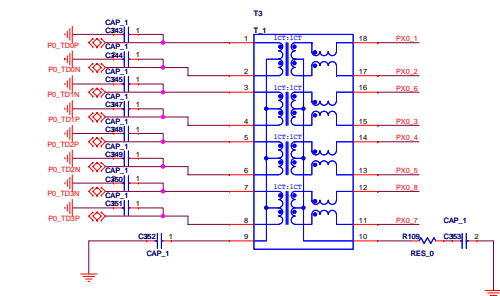
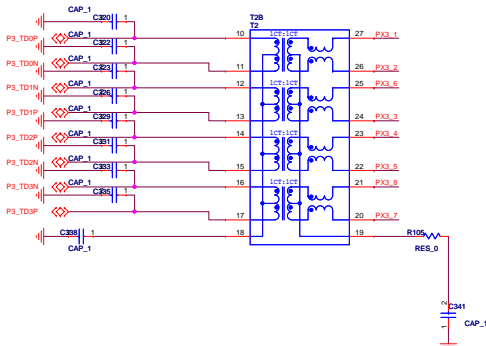
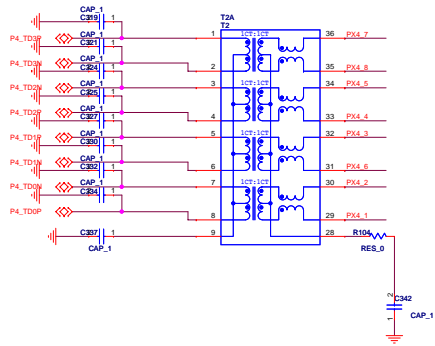
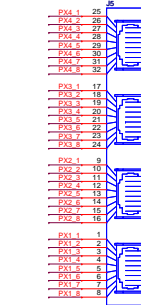
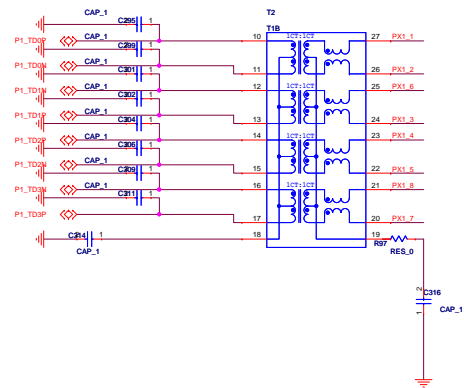
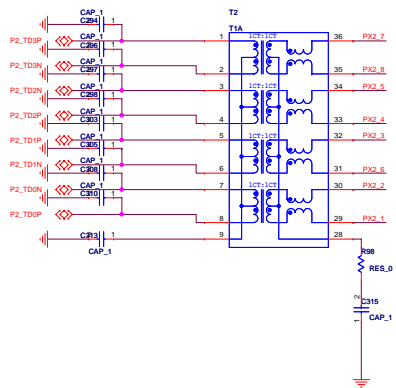


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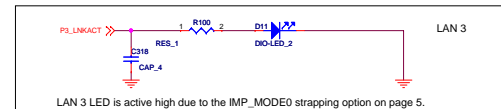
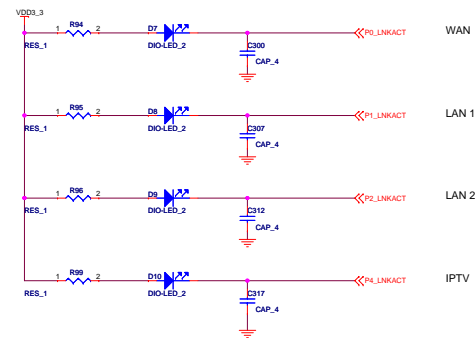




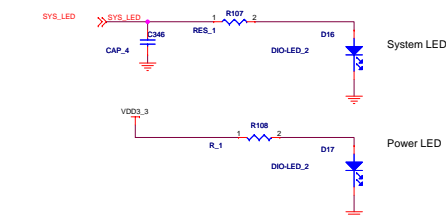
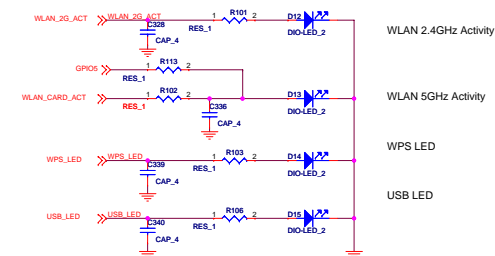
TENDA			
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NOTE:  
1. Place magnetic close to RJ45 .  
2. The 75R resistors should be placed closed to the magnetic .  
The prupose of the resistors is to suppress EMI radiation.

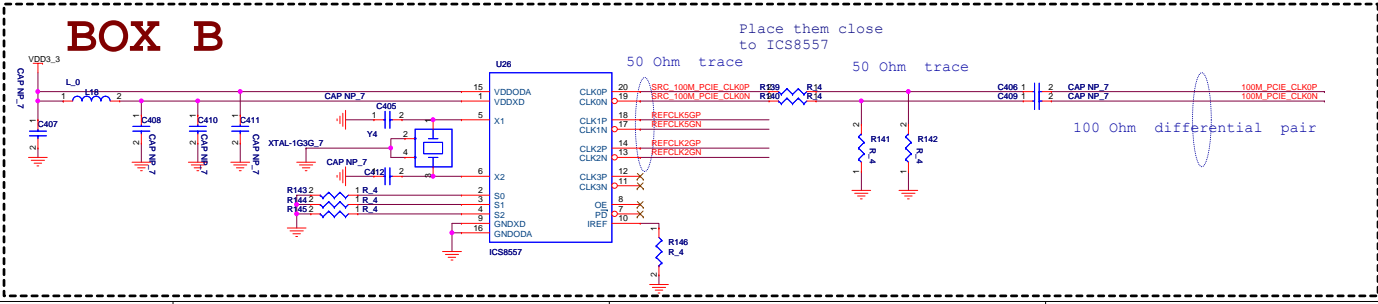
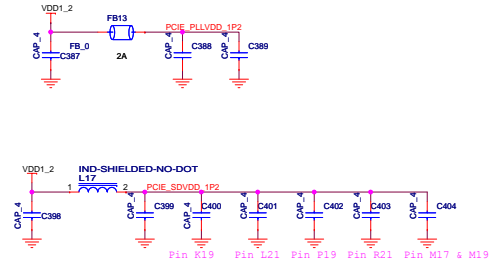


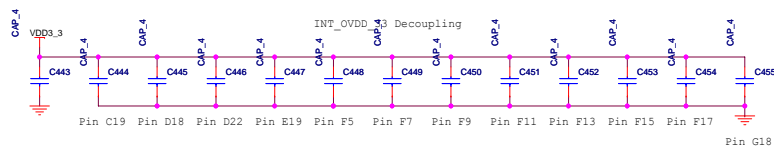
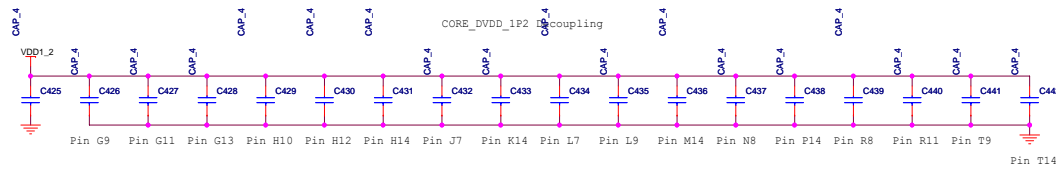
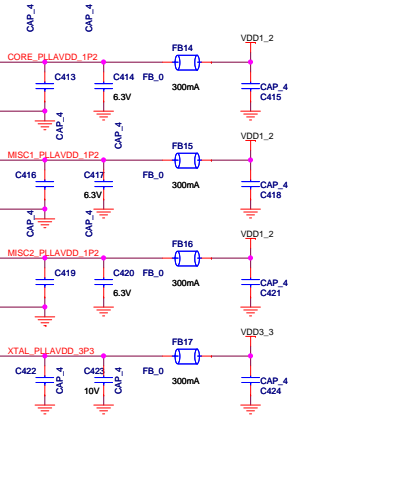
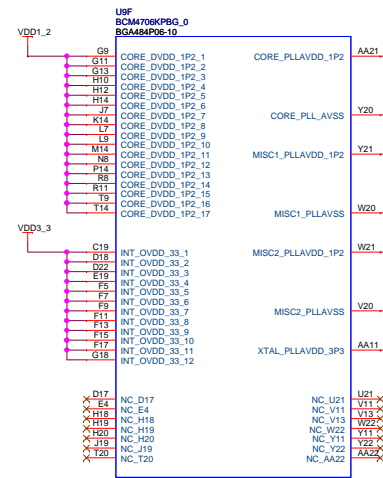
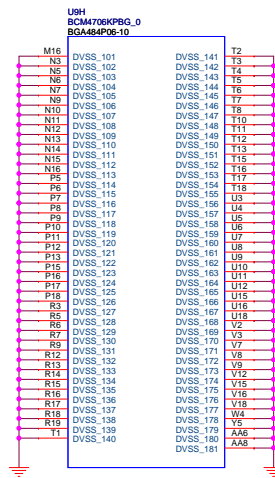
NOTE: 100pF caps on LED signals are optional bypass caps to suppress EMI radiation from long LED traces. Place caps near LEDs.



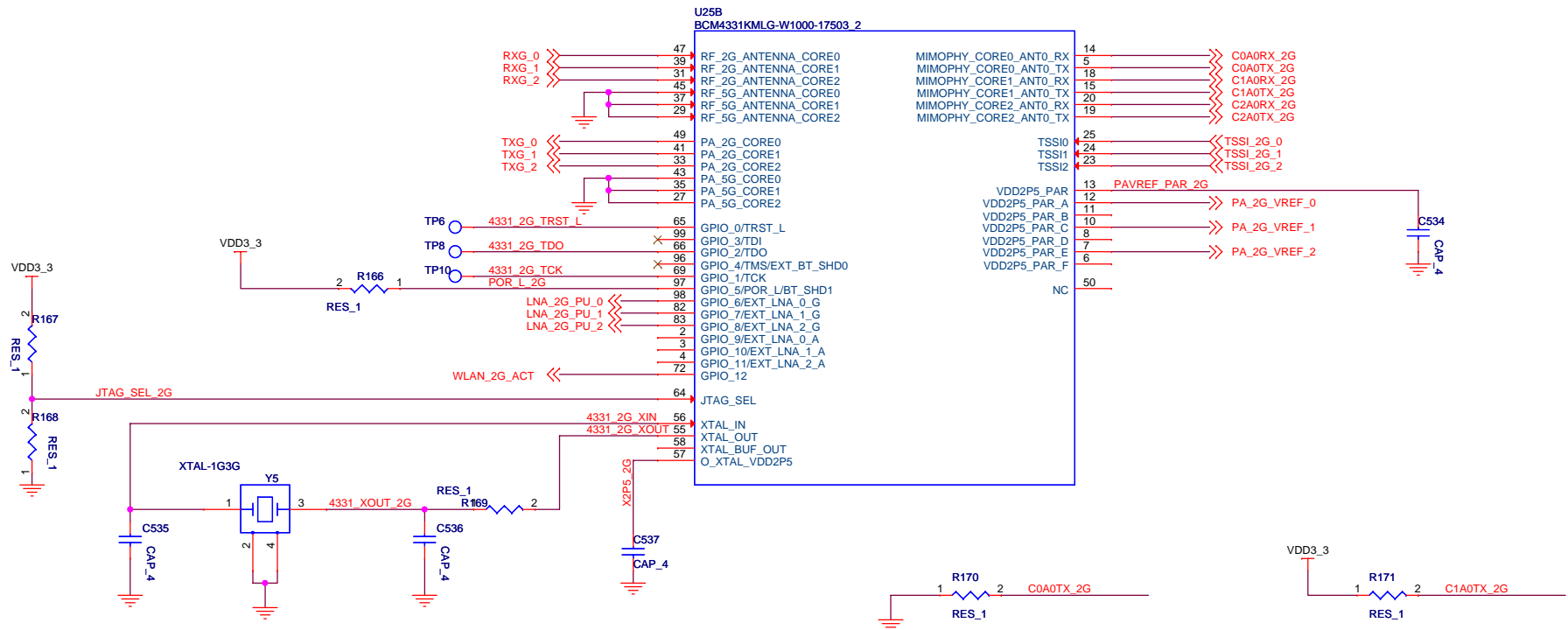
Port	State	Function
LAN/WAN	LINK/ACT	Ethernet Link/Activity
WPS		WPS Status
WLAN 2.4GHz	UP/ACT	2.4GHz Up/Activity
WLAN 5GHz	UP/ACT	5GHz Up/Activity



[illegible]





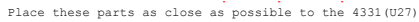
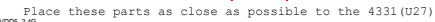
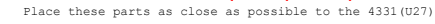


MIMOPHY\_CORE0\_ANT0\_TX Strapping Option:  
Do not use OTP for configuration.

MIMOPHY\_CORE1\_ANT0\_TX Strapping Option:  
Set SROM present so NVRAM can configure 4331.

4331 Strapping	Function	Setting	Default Pull
Option Pin			
MIMOPHY_CORE0_ANT0_TX	0 = Do not use OTP for configuration (setting) 1 = Use OTP for configuration [default]	0	PU
MIMOPHY_CORE1_ANT0_TX	0 = SROM NOT present [default] 1 = SROM present (setting)	1	PD

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Custom			Rev 0.1
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