

W311R, 1T1R, RT3050, 2M FLASH, 16M SDRAM, 外置1根5dbi不可拆卸天线



原理说明:

Operational Principle

1. CPU: U1, The RT3050 SOC combines Ralink's 802.11n draft compliant 1T1R MAC/BBP/2.4G ISM band Transceiver, a high performance 384MHz MIPS24KEc CPU core, 5-port integrated 10/100 Ethernet switch/PHY; An 40MHz crystal(U2) for CPU's reference frequency;

2. SDRAM: U8 Hynix HY57V281620FTP-H CMOS Synchronous DRAM; 16M Byte (16M X 16bit X 1PCU)

3. FLASH: U6 MXIC KH29LV320(160)CBTC-70G/MX29LV320(160)CBTC-70G 4(2)M Byte FLASH, used for store firmware and user's settings;

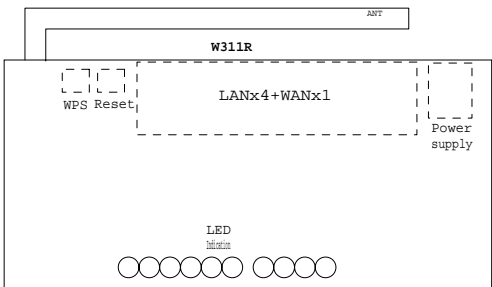
4. PA: U10 RichWave RTC6691, Freq.Range:2.4GHz~2.5GHz. PA For transmitter, Gain:33.5db (Max);

5. LNA: Q1 SiRENda SGA8343S, Freq.Range:2.4GHz~2.5GHz.LNA For RF receiver, Gain:17db (Max);

6. BPF: U9, RF2012-SERADA, Freq.Range:2.4GHz~2.5GHz, IL@SW:2.5-3db;

7. RF Switch: U15, skyworks AS179-92, transmit /receive SPDT Switch;

8. Power part: U16 and U14, CP2894 Step-Down PWM Converter, used for transfer DC8-14V to DC3.3V and DC1.5V; ; U25 SE8117TALF LDO used for transfer DC3.3V to DC2.0V;



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