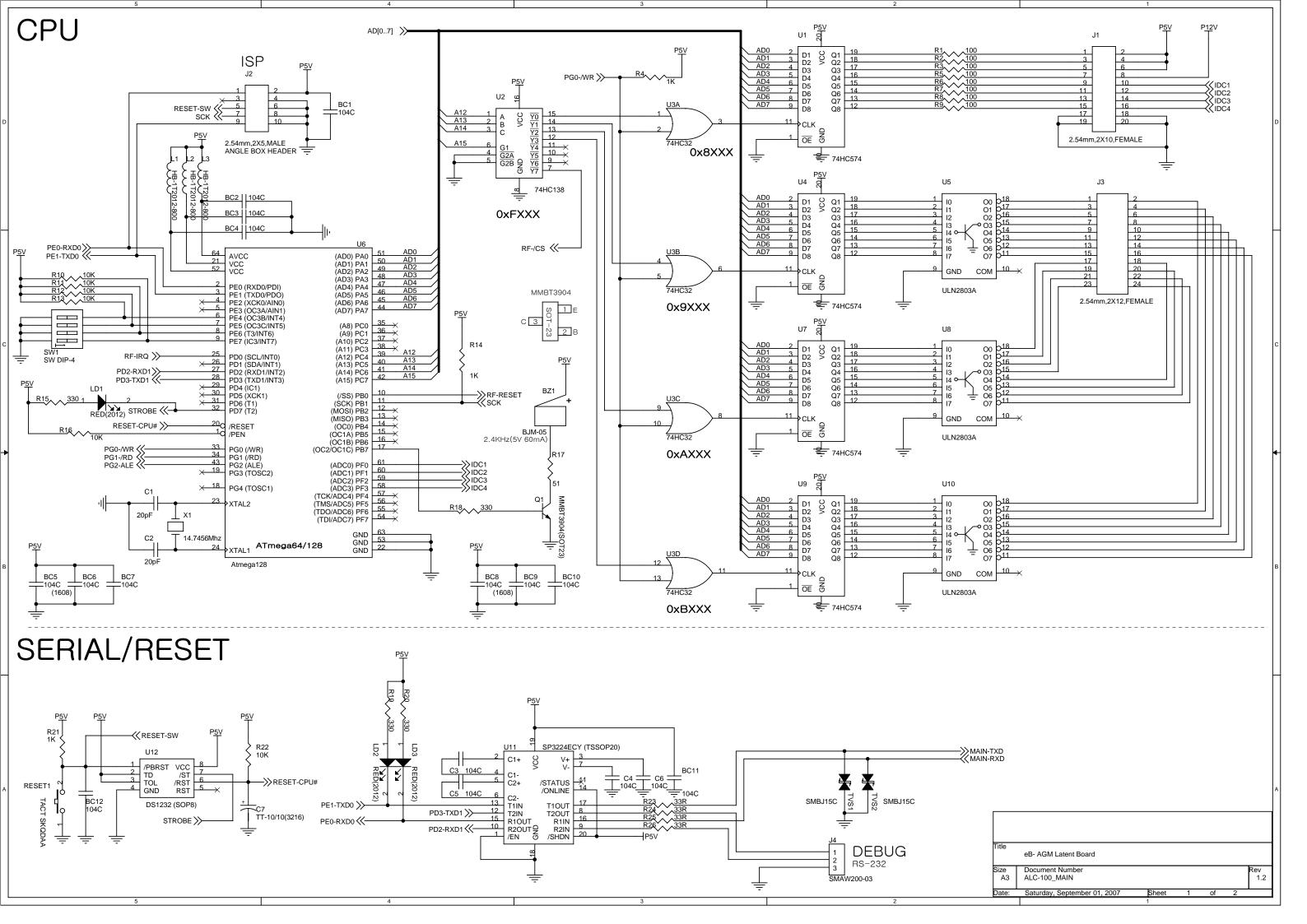
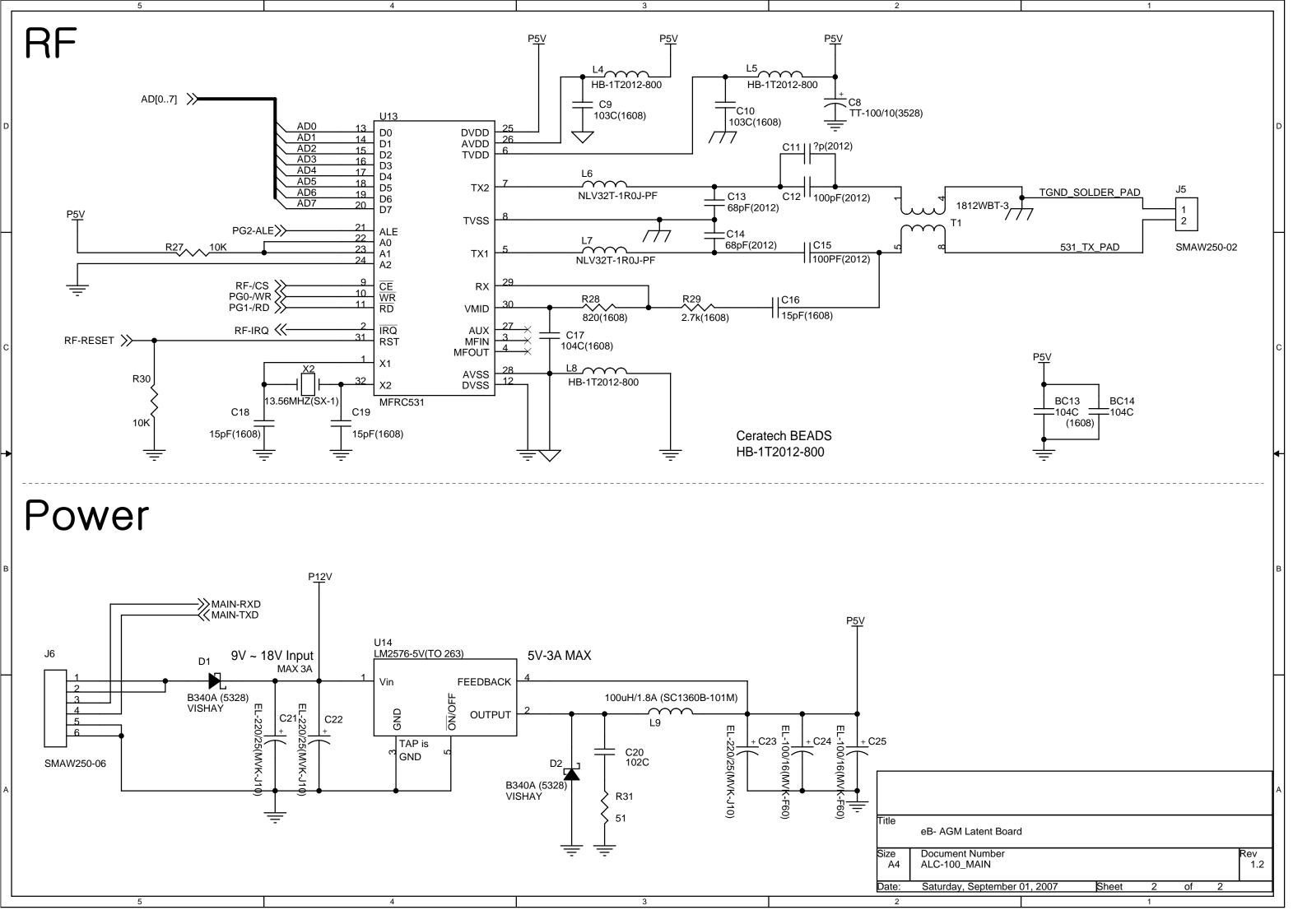


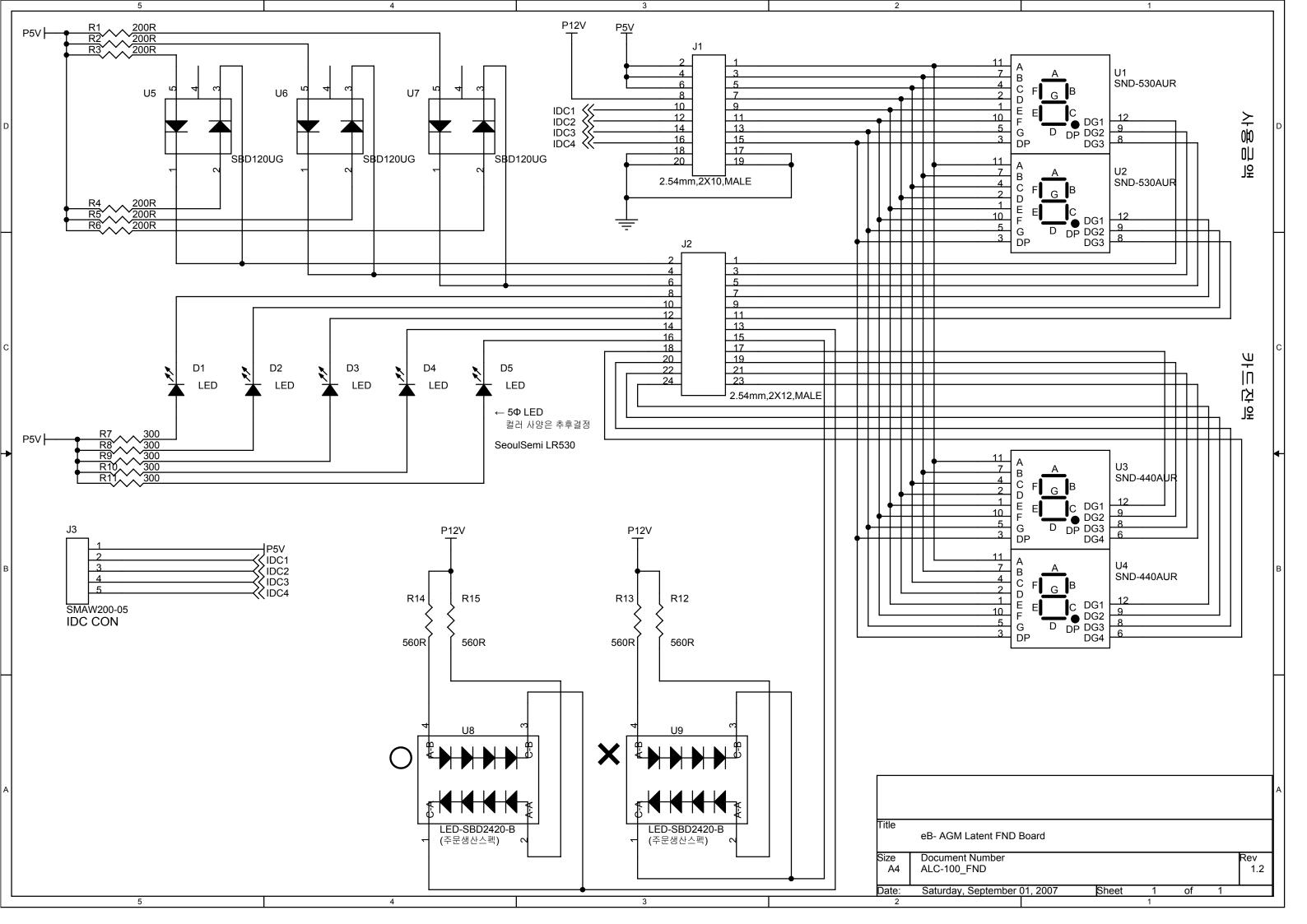
ATTACHMENT G.

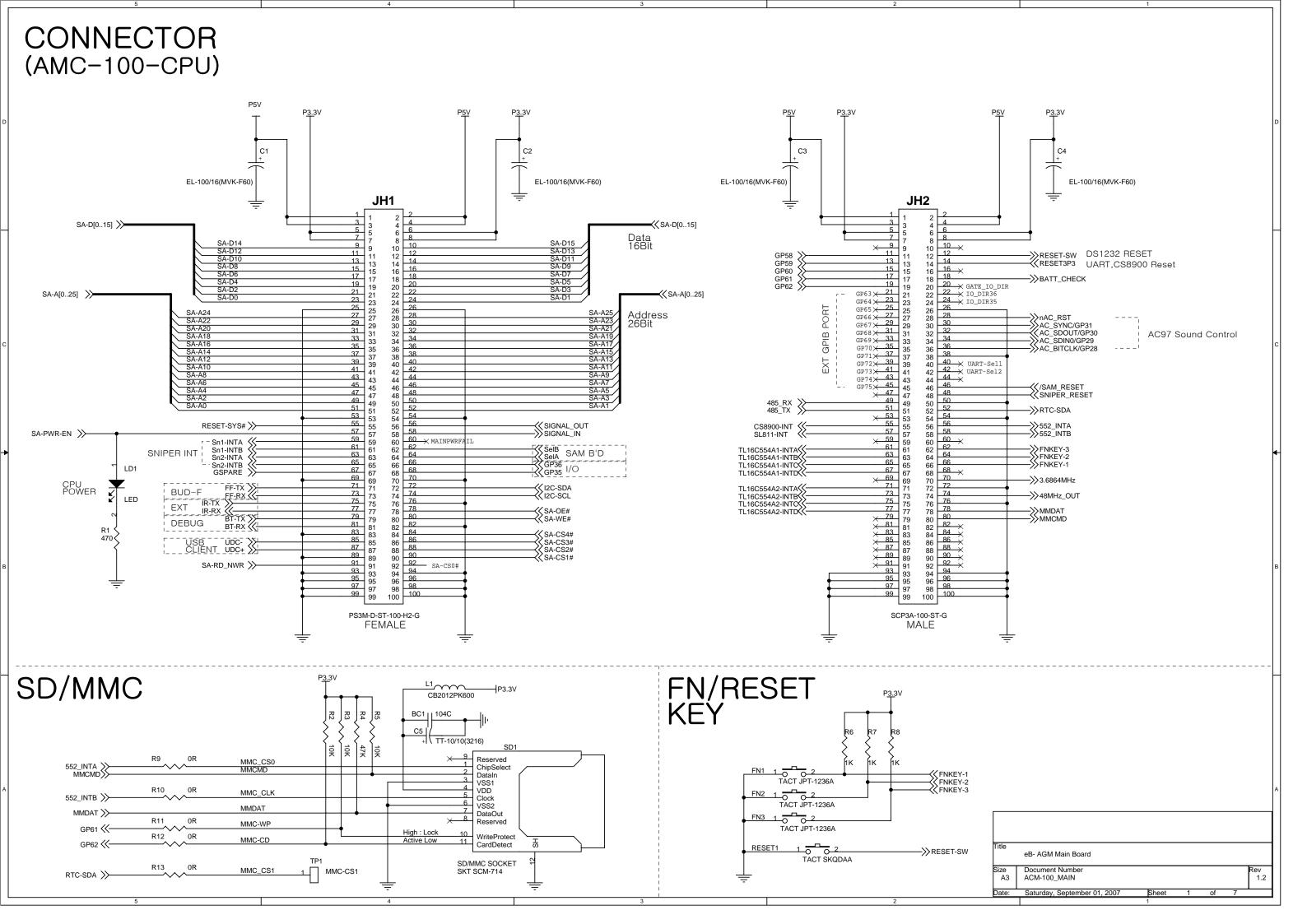
- SCHEMATIC DIAGRAM -

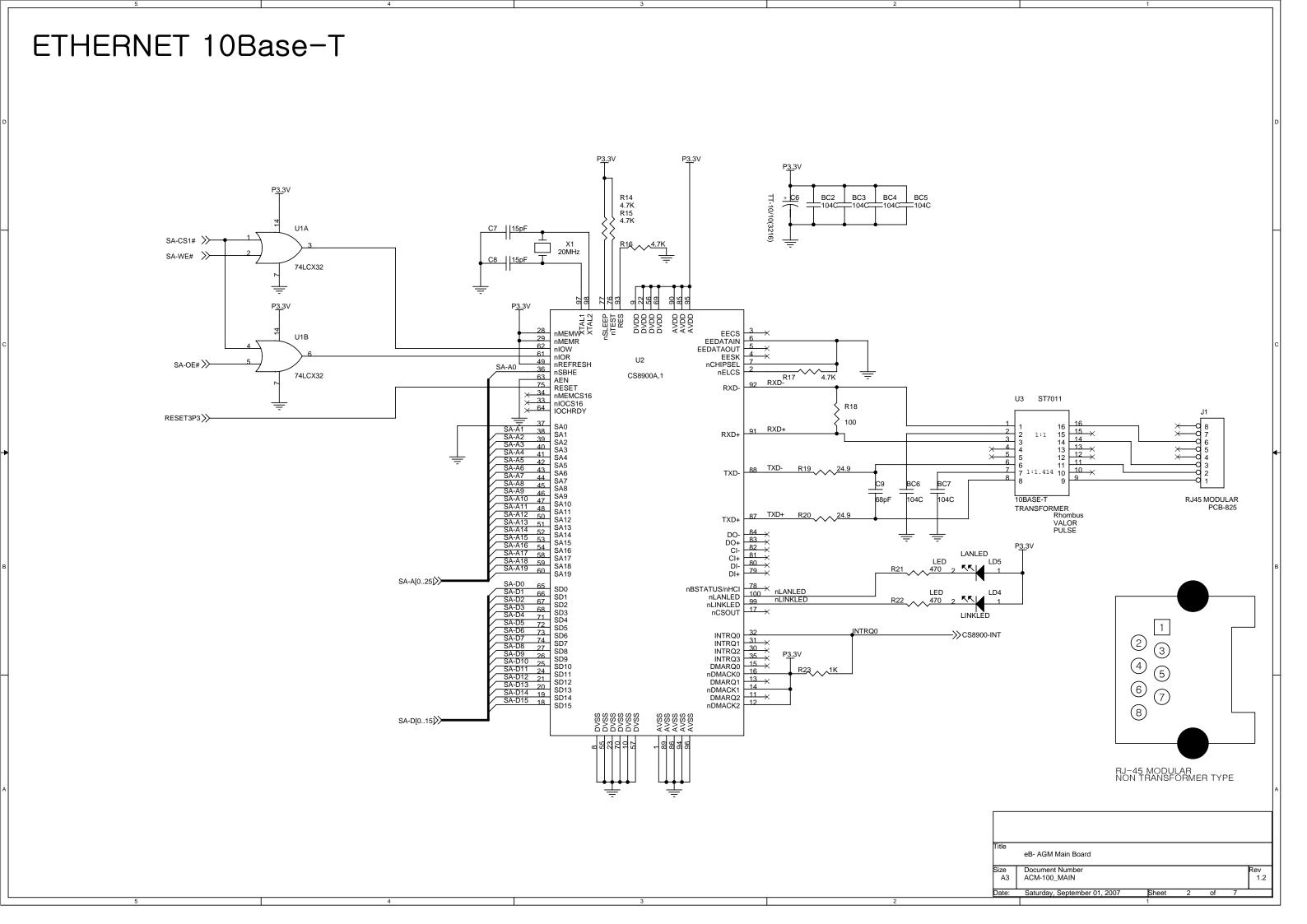
Report No.: HCT-R07-036 1/1

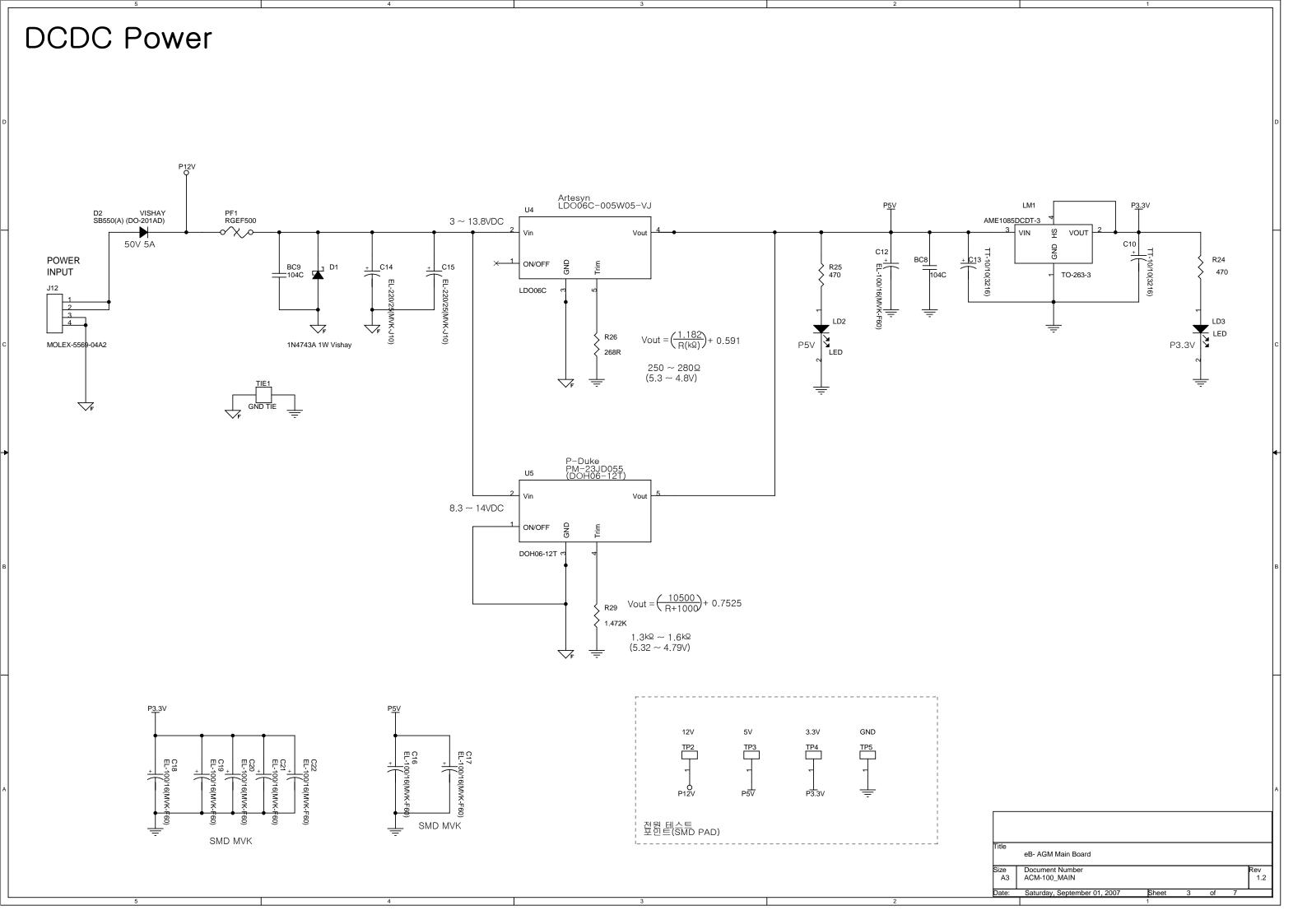


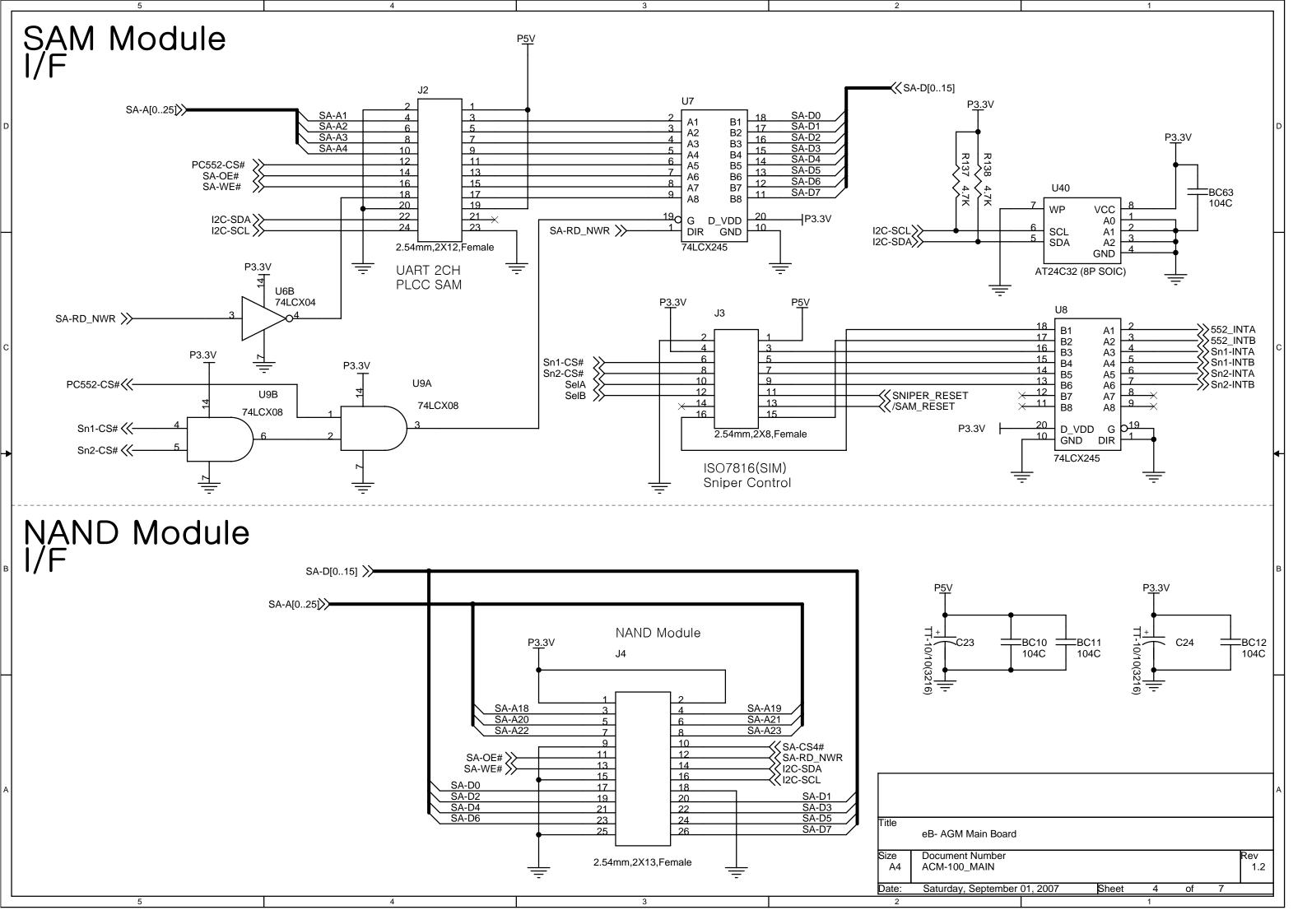


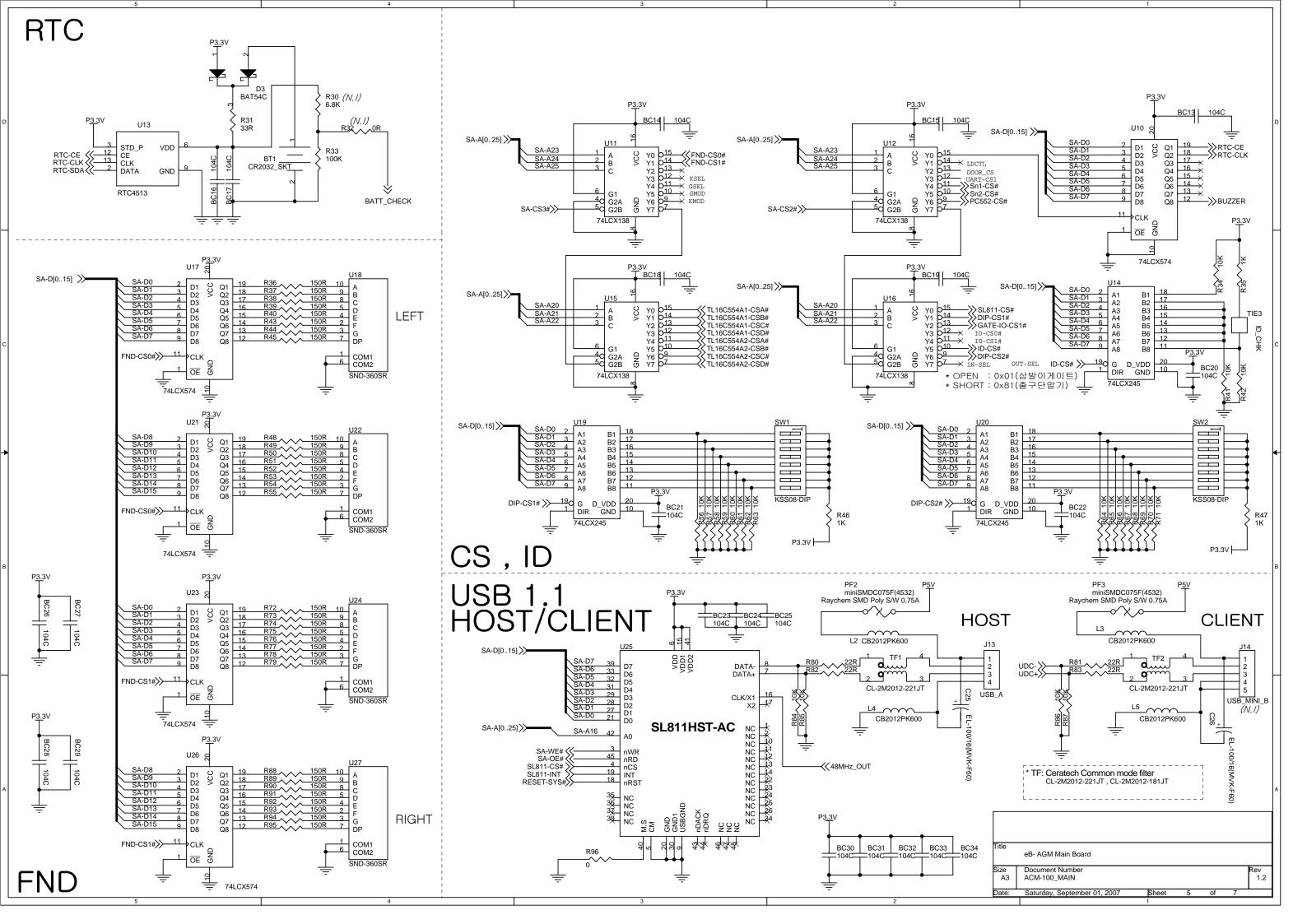


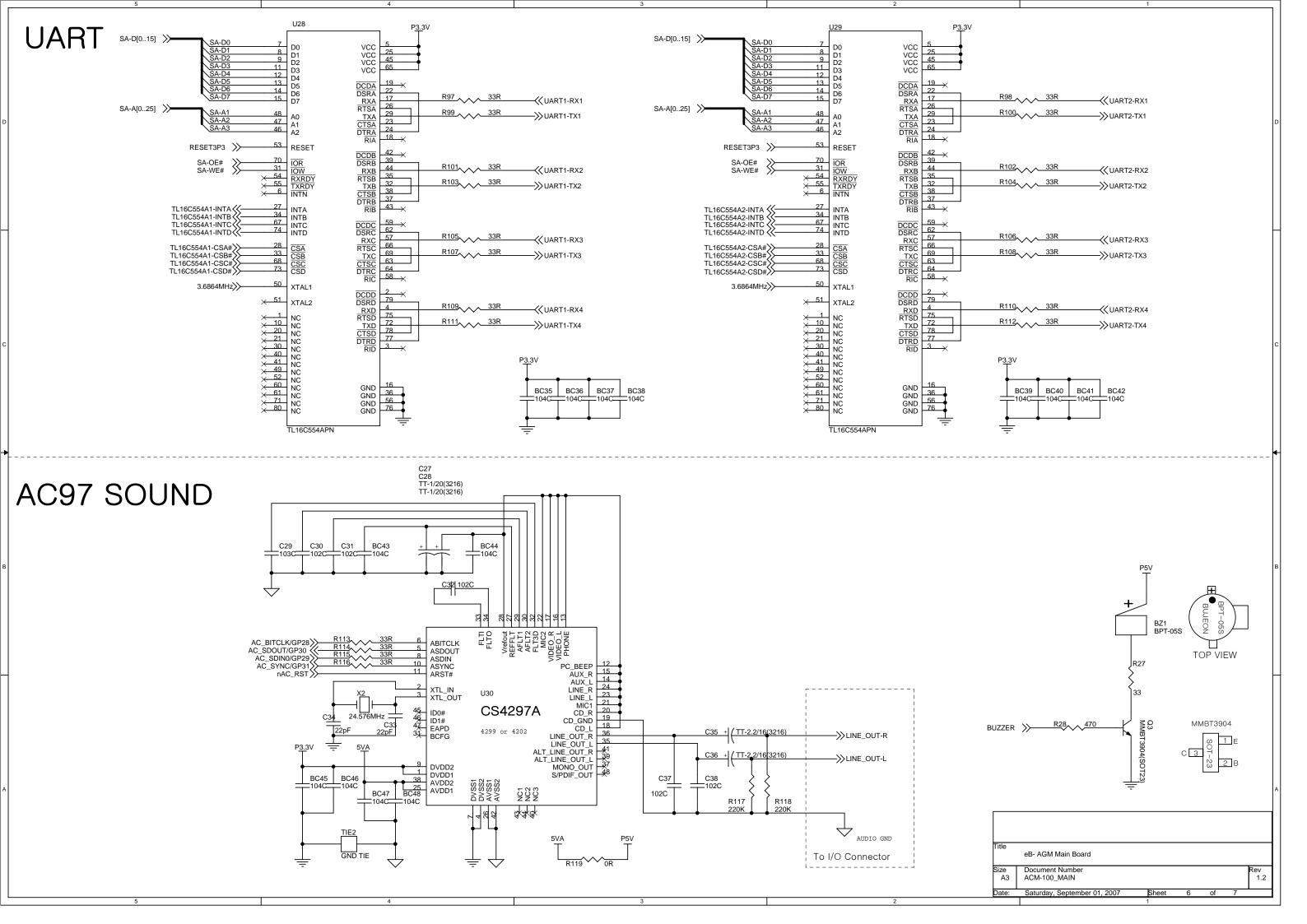


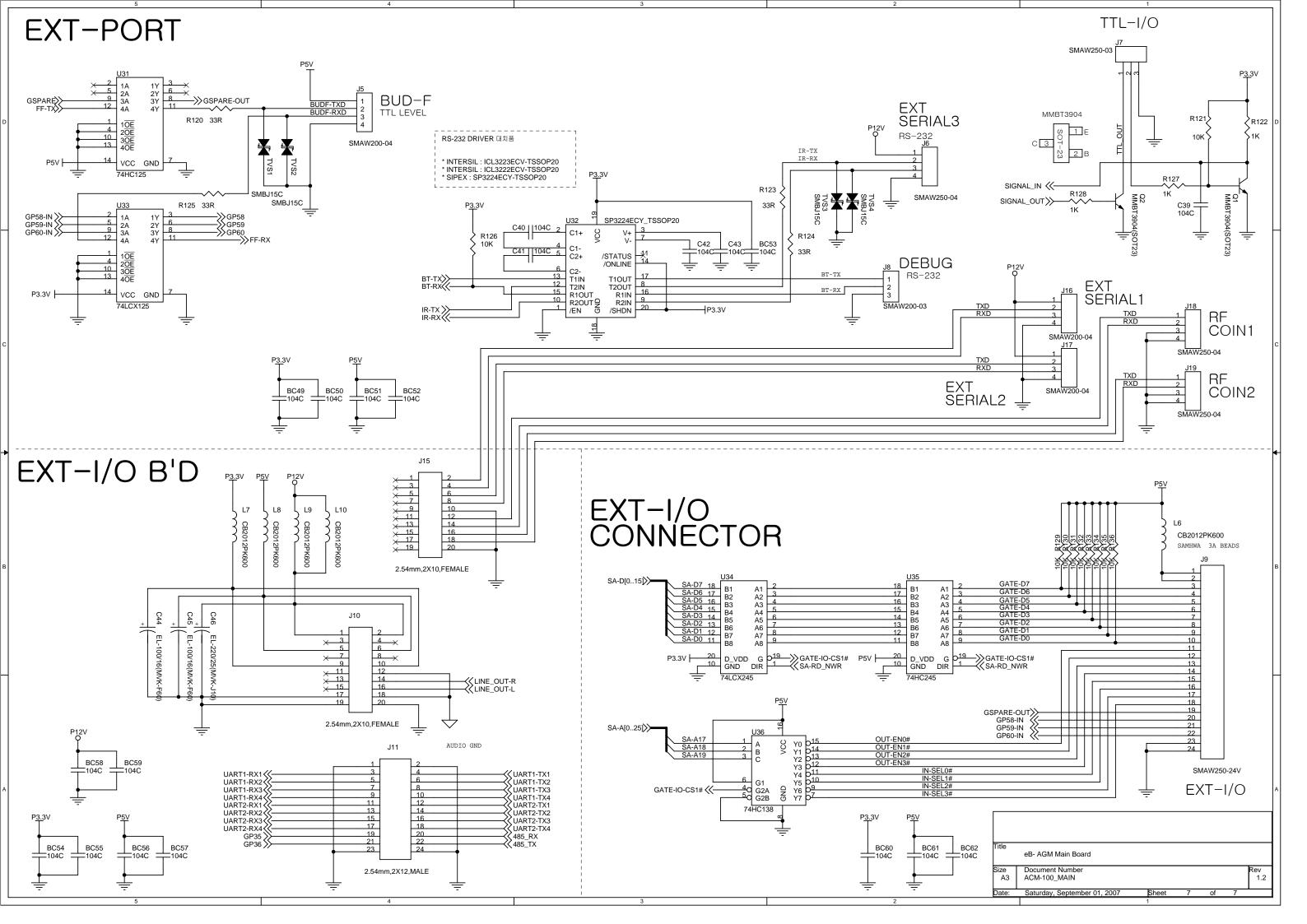


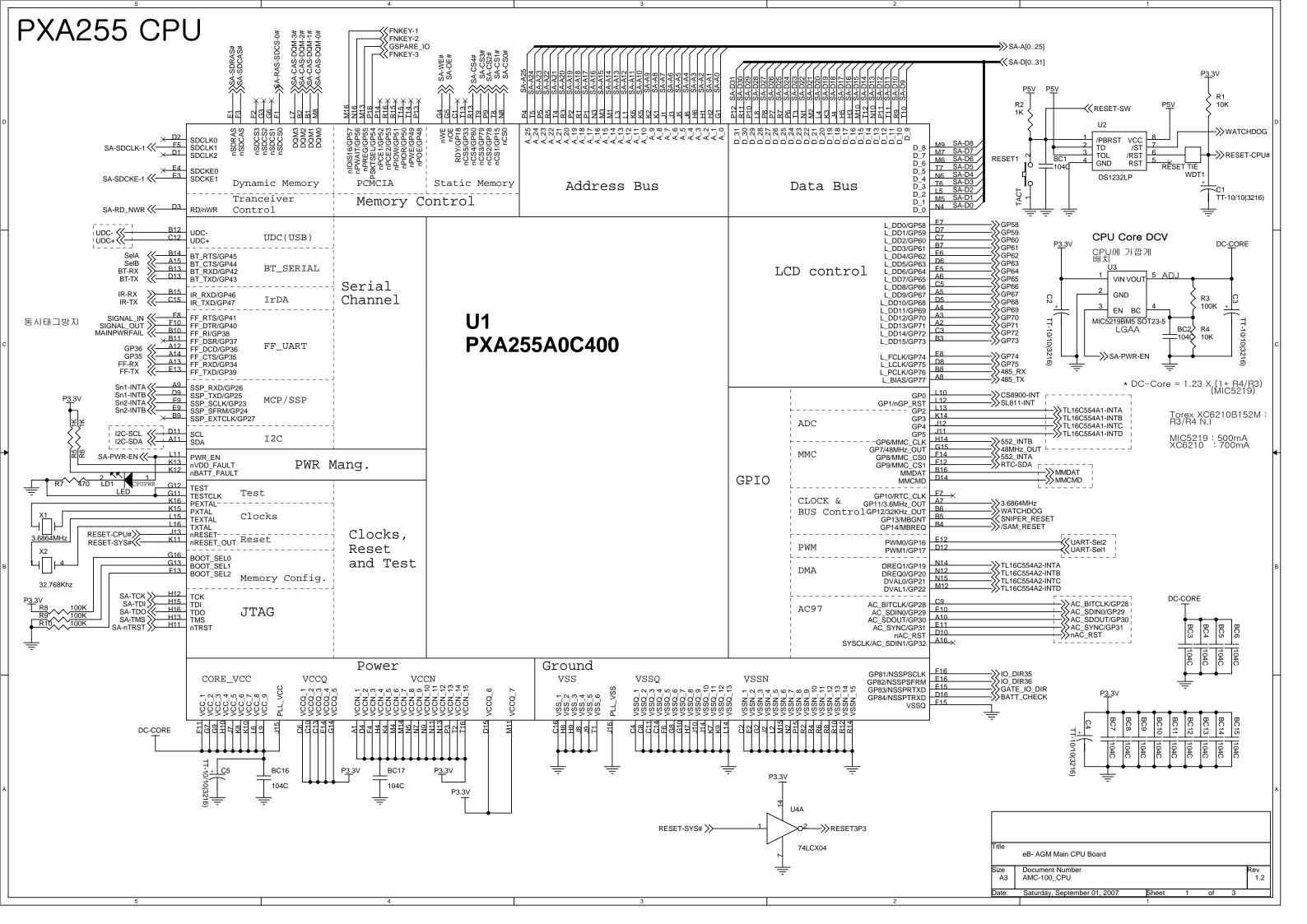




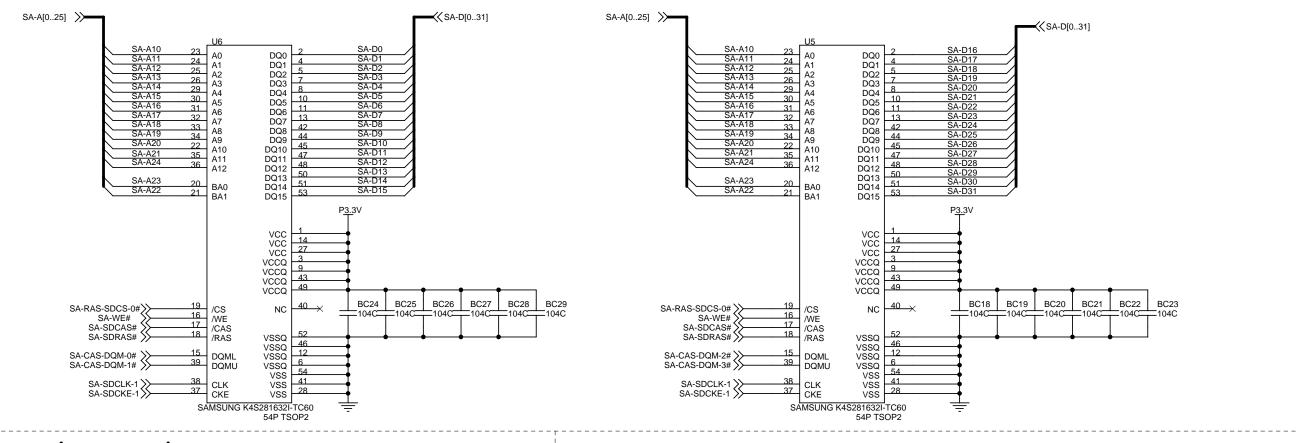




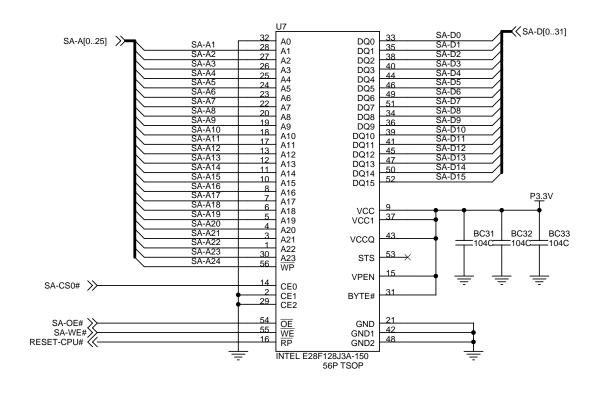




SDRAM(32MB)



NOR FLASH(16MB)



JTAG I/F

