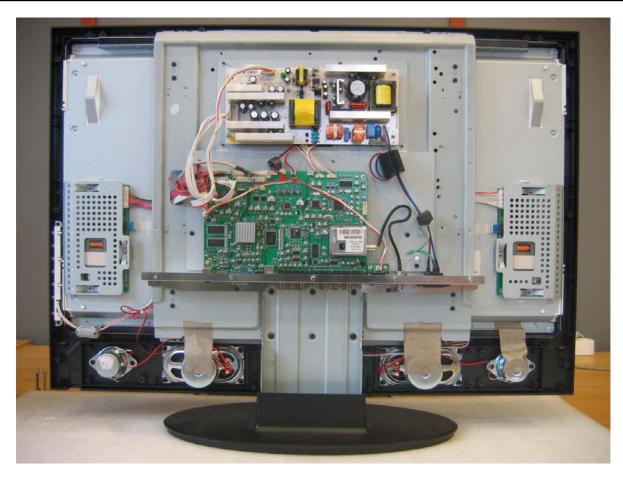


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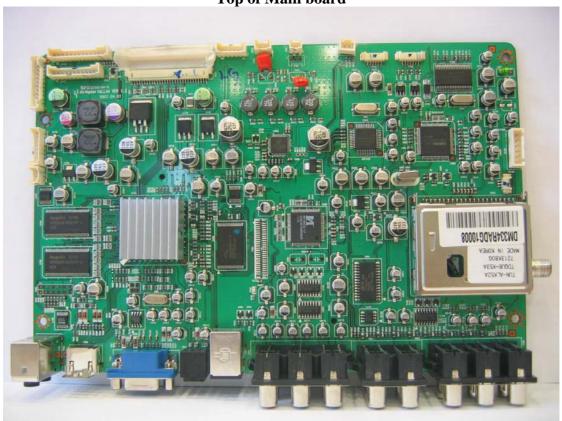
Appendix 4. Internal Photographs of EUT



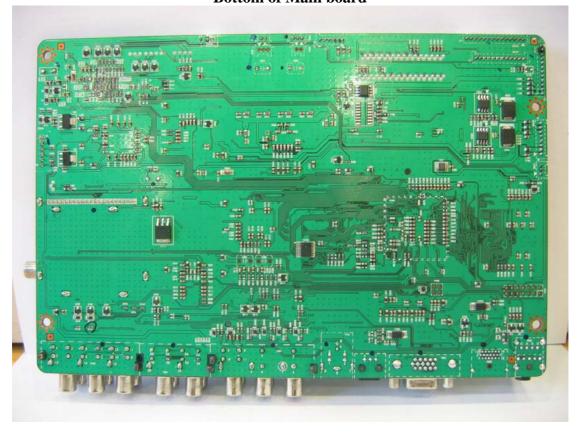


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Top of Main board



Bottom of Main board

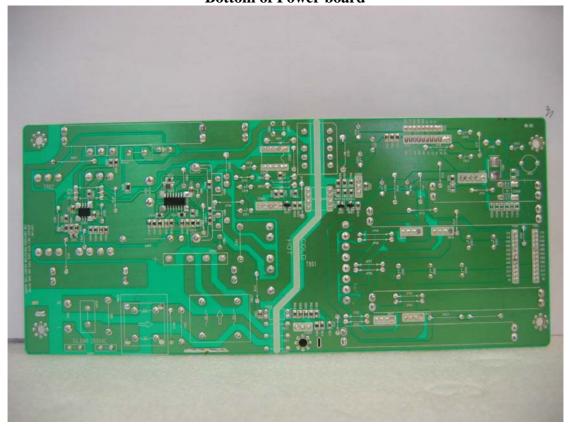




Top of Power board

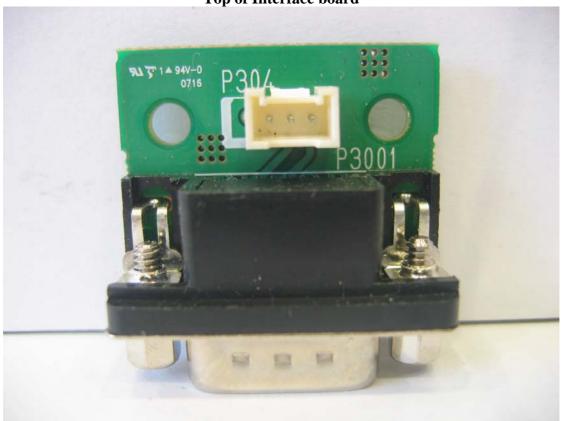


Bottom of Power board





Top of Interface board



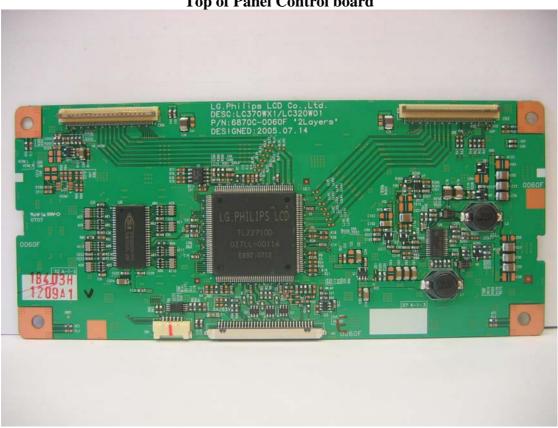
Bottom of Interface board





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Top of Panel Control board

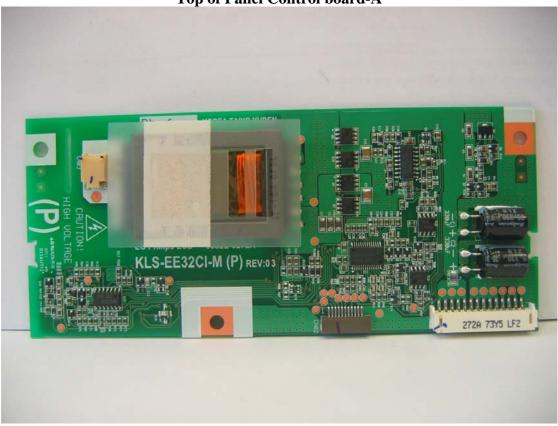


Bottom of Panel Control board

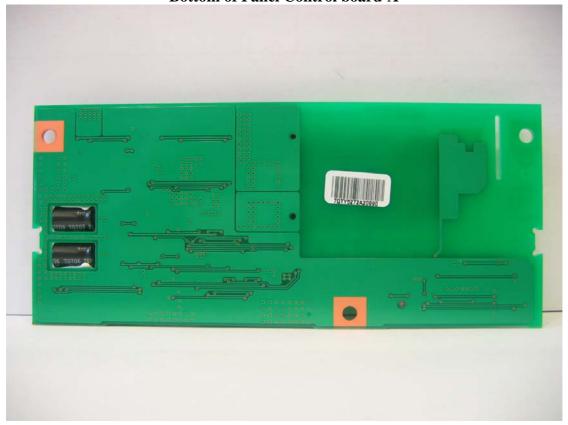




Top of Panel Control board-A



Bottom of Panel Control board-A

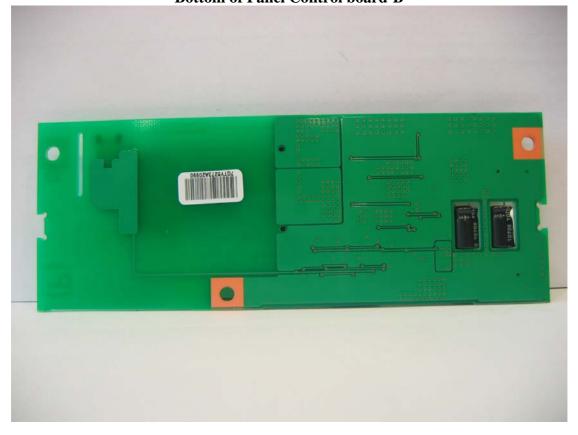




Top of Panel Control board-B



Bottom of Panel Control board-B





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Date of Test : May 07, 2007

LCD Label

