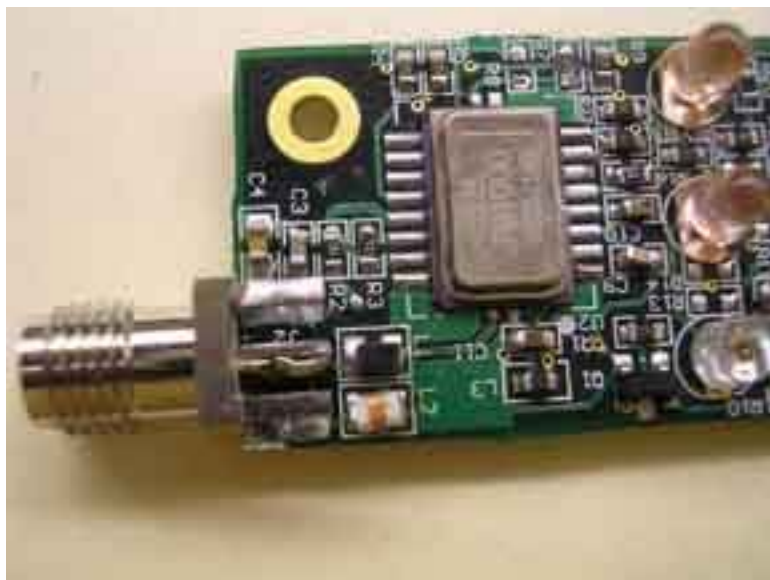


PCB Front



PCB Back

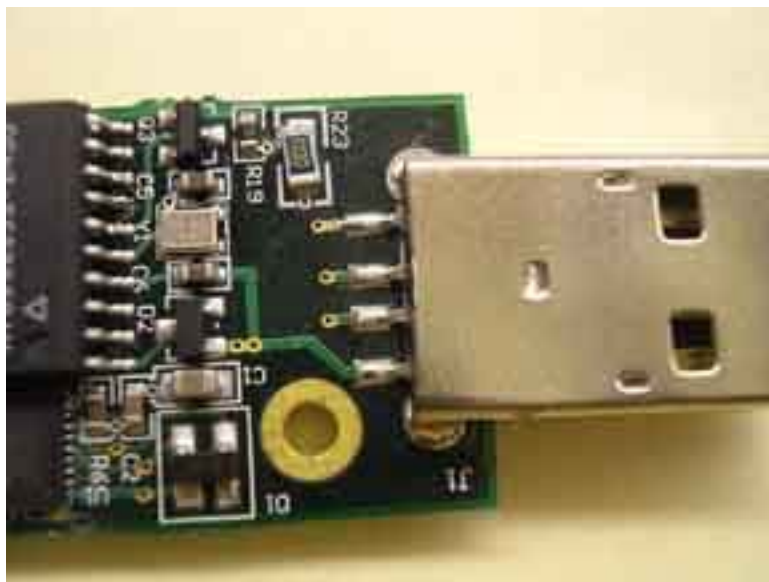


PCB 1

*DRAFT*



PCB 2



PCB 3

*DRAFT*