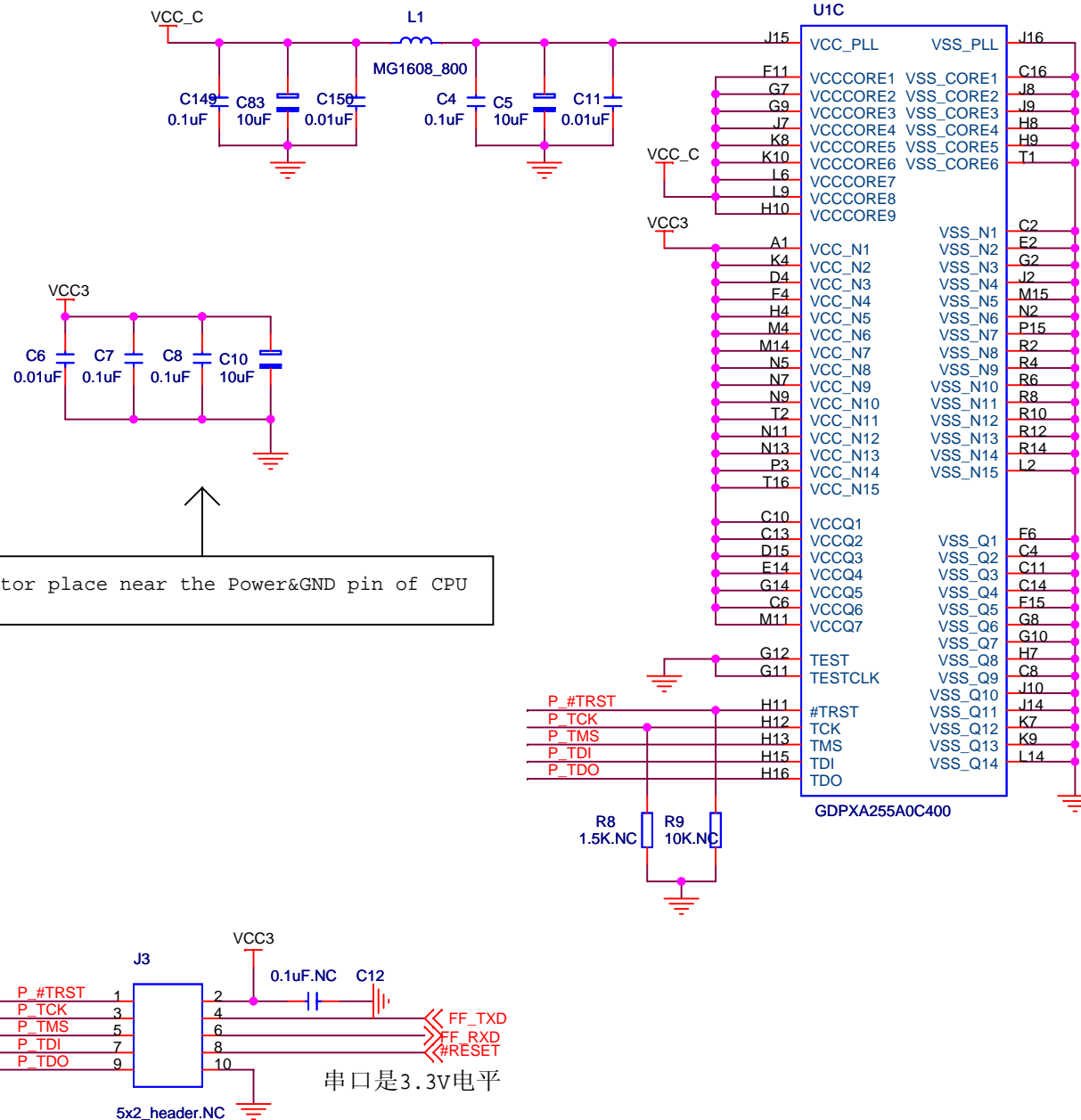
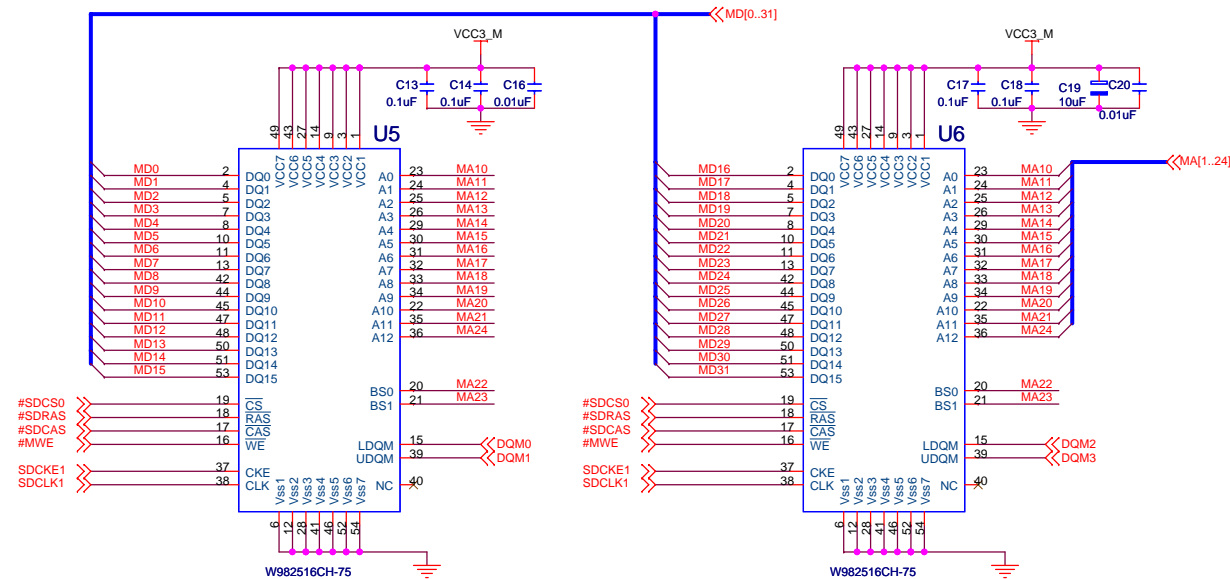


10. 加入加密芯片AT88SC0104C-SI。

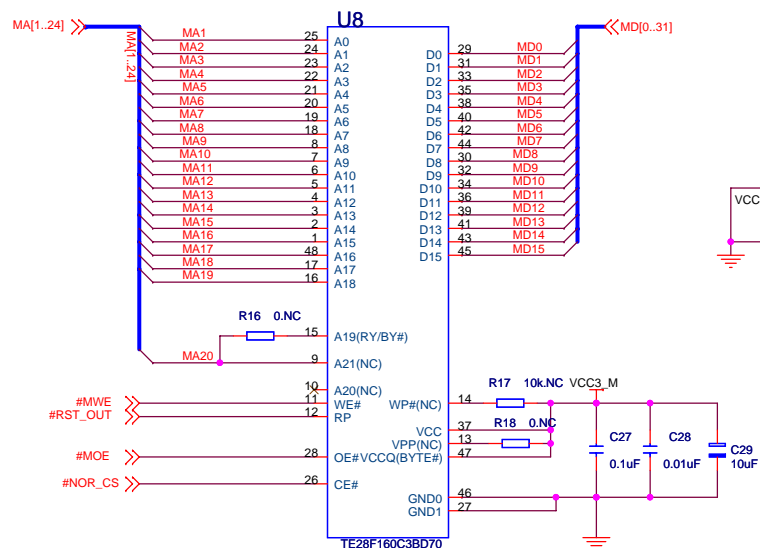
Company		SEUIC	
Title		CPU_IO_SECTION	
Size		Document Number	
Custom		SEUIC_Malata_D003_PG_64304B	
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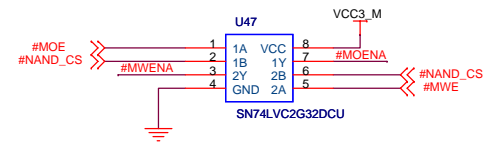
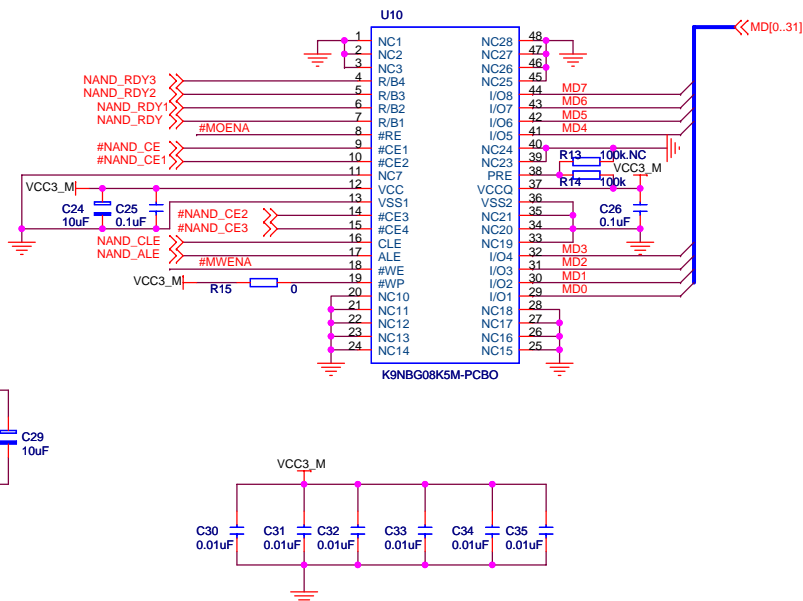
Company		SEUIC	
Title		CPU_POWER_JTAG_SECTION	DRN BY XY.CHEN
Size		Document Number	Rev
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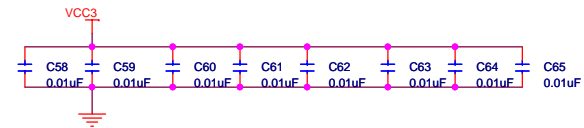
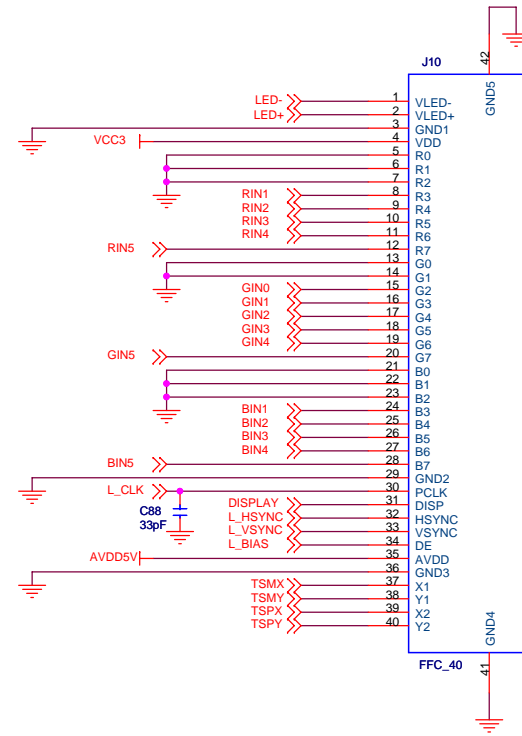
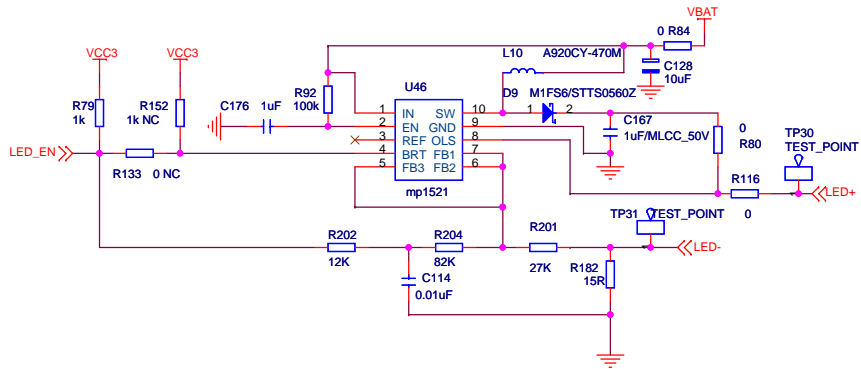
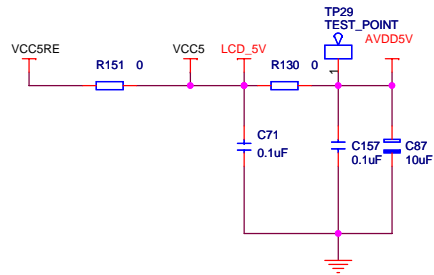


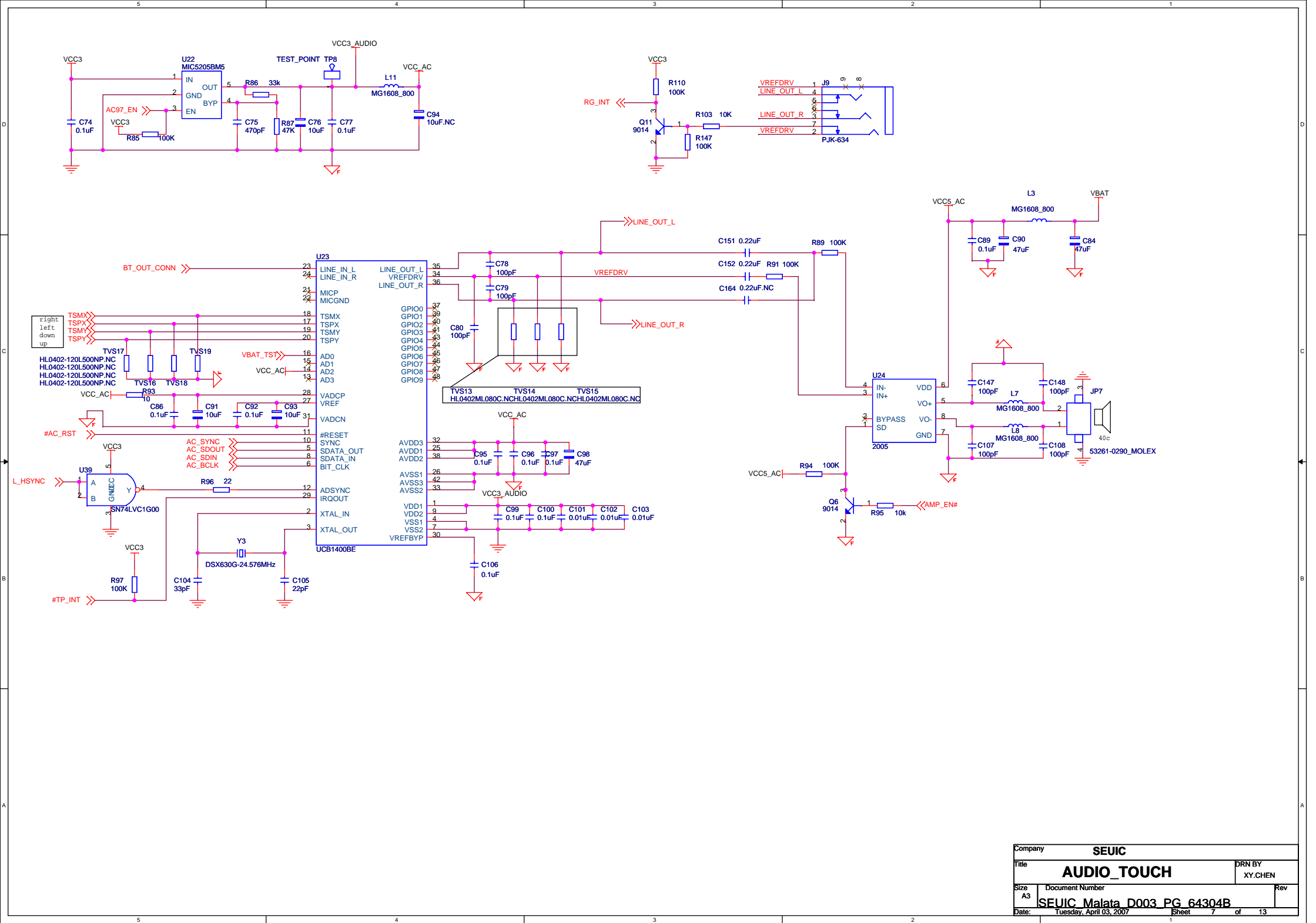
尽量保证CPU到SDROM的地址，数据总线距离较短，阻抗一致。

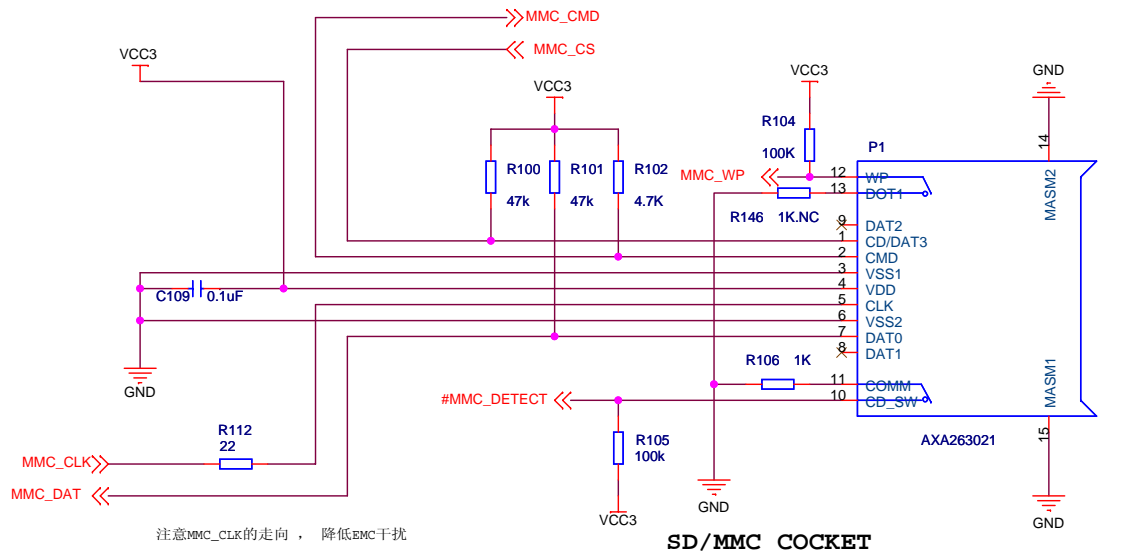


地址总线尽可能的保持阻抗一致，分组布线，过孔数保持一致



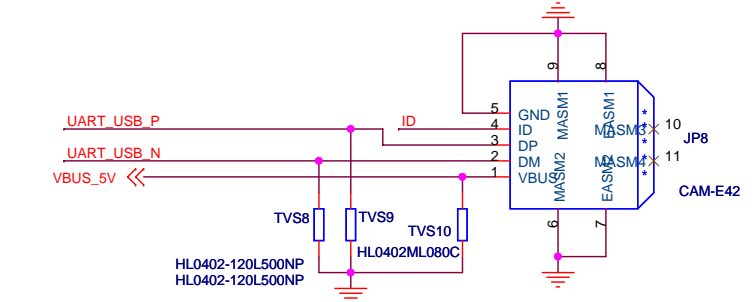
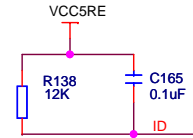






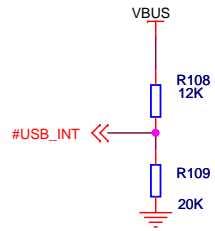
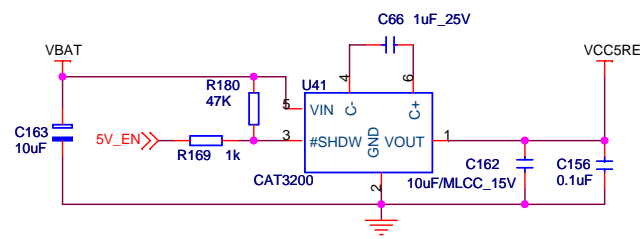
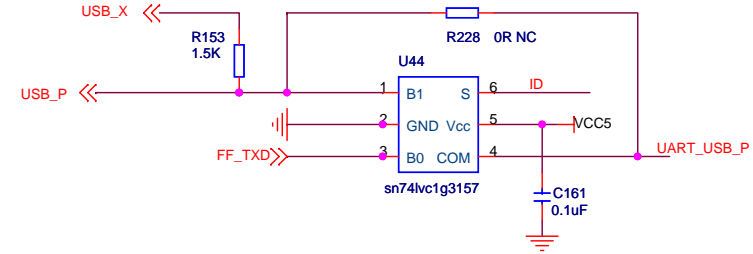
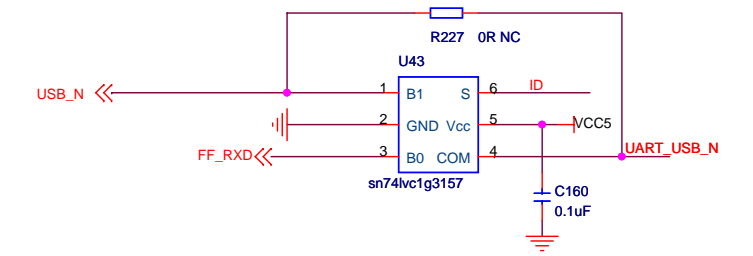
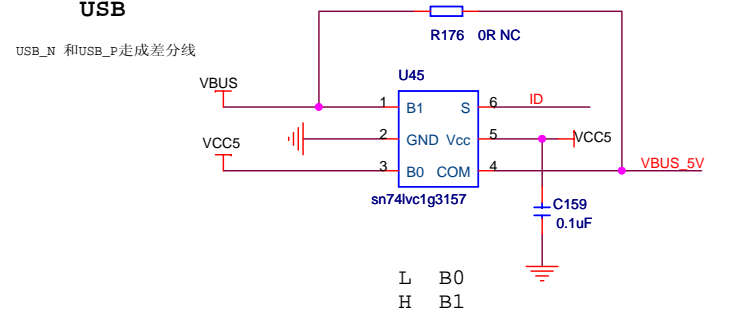
注意MMC_CLK的走向，降低EMC干扰

SD/MMC COCKET

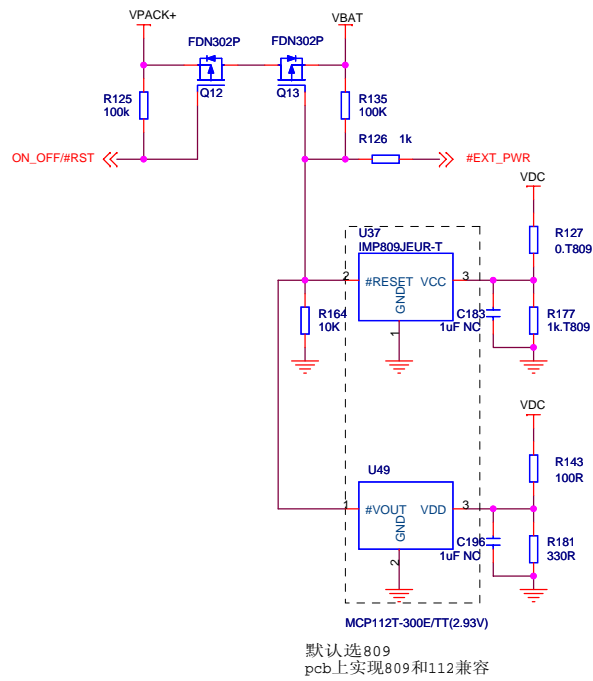


USB

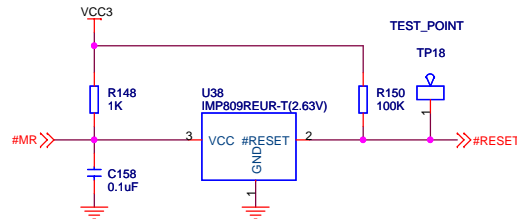
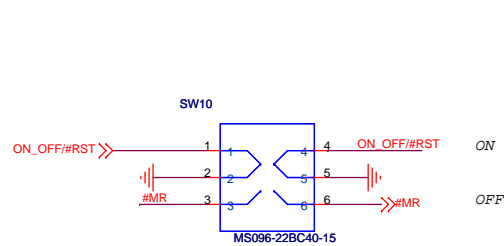
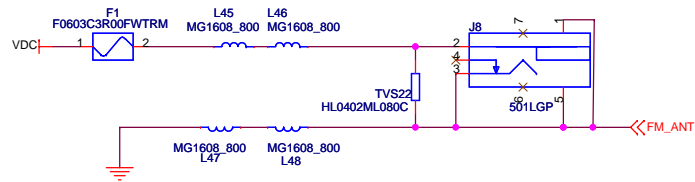
USB_N 和USB_P走成差分线



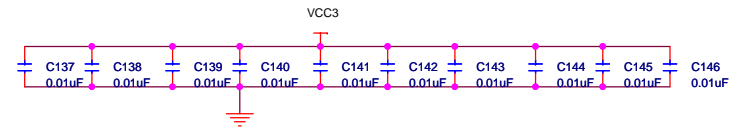
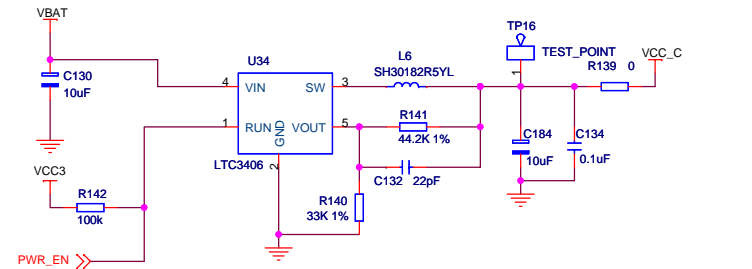
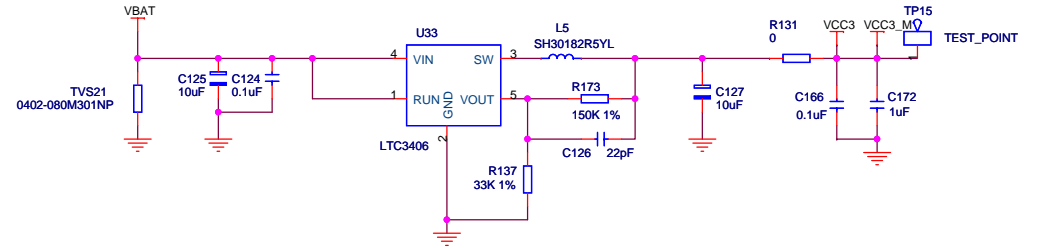
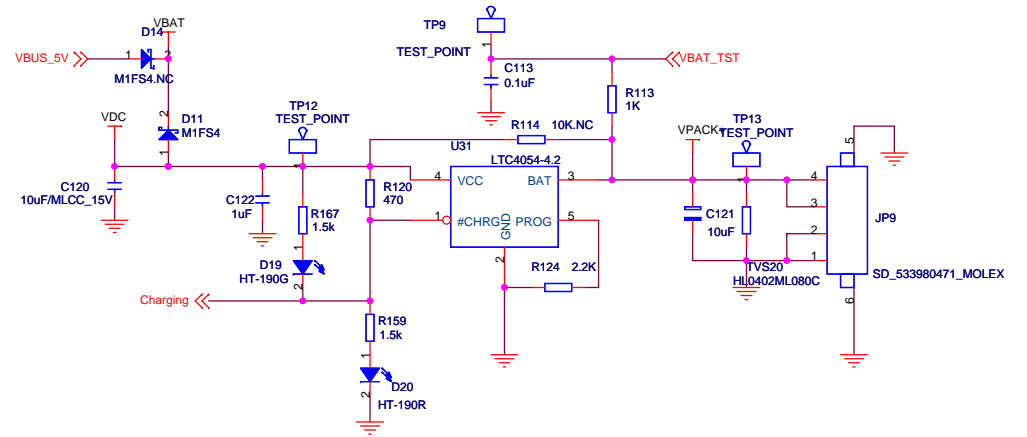
Company		SEUIC	
Title		COMMUNICATION	DRN BY XY.CHEN
Size	Document Number	Rev	
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默认选809
pcb上实现809和112兼容



复位电路，布局时远离易发热，强数字干扰区。



Company		SEUIC	
Title		POWER	
Size		Document Number	
A3		SEUIC_Malata_D003_PG_64304B	
Date:		Wednesday, April 04, 2007	
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