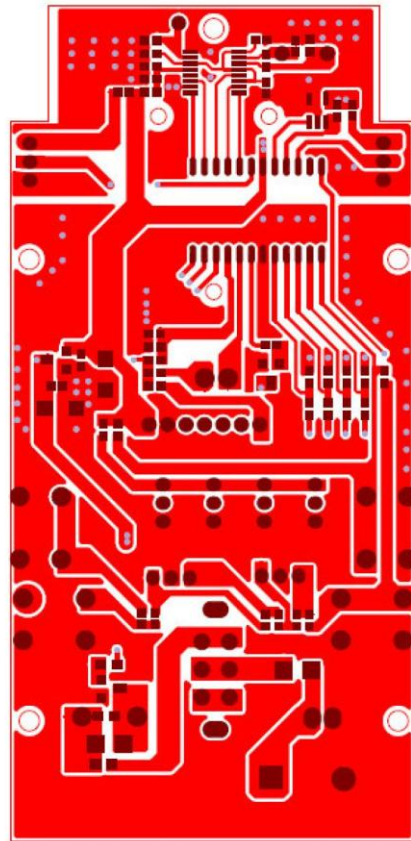
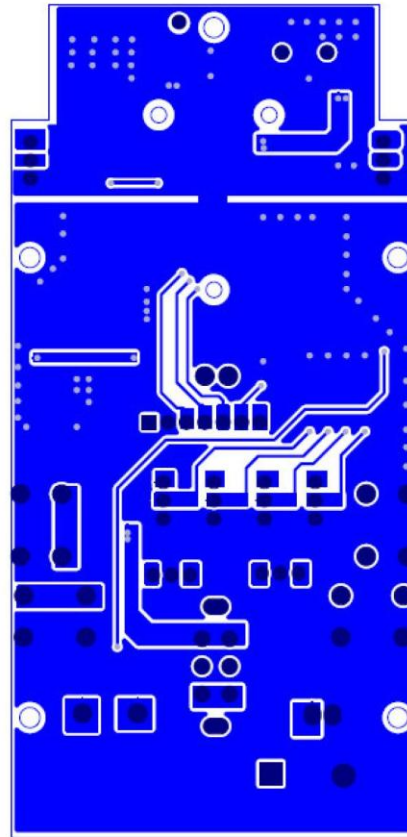


## PCB Layout

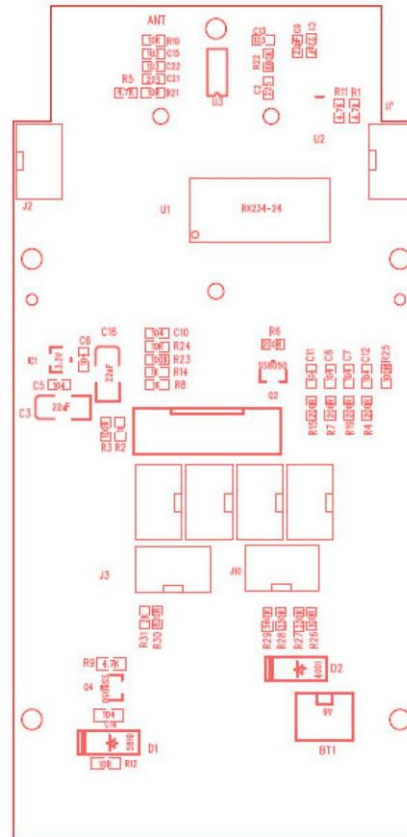
TX BOTTOM LAYER:



## TX BOTTOM LAYER



## TX TOP OVERLAY



TX BOTTOM OVERLAY

