page 01 Title Page
page 02 CPU(I/O)
page 03 CPU(Data, Addr)
page 04 CPU(PMU)
page 05 CPU(Function)
page 06 DDR
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page 9 DEBUG
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Title	<Title>	
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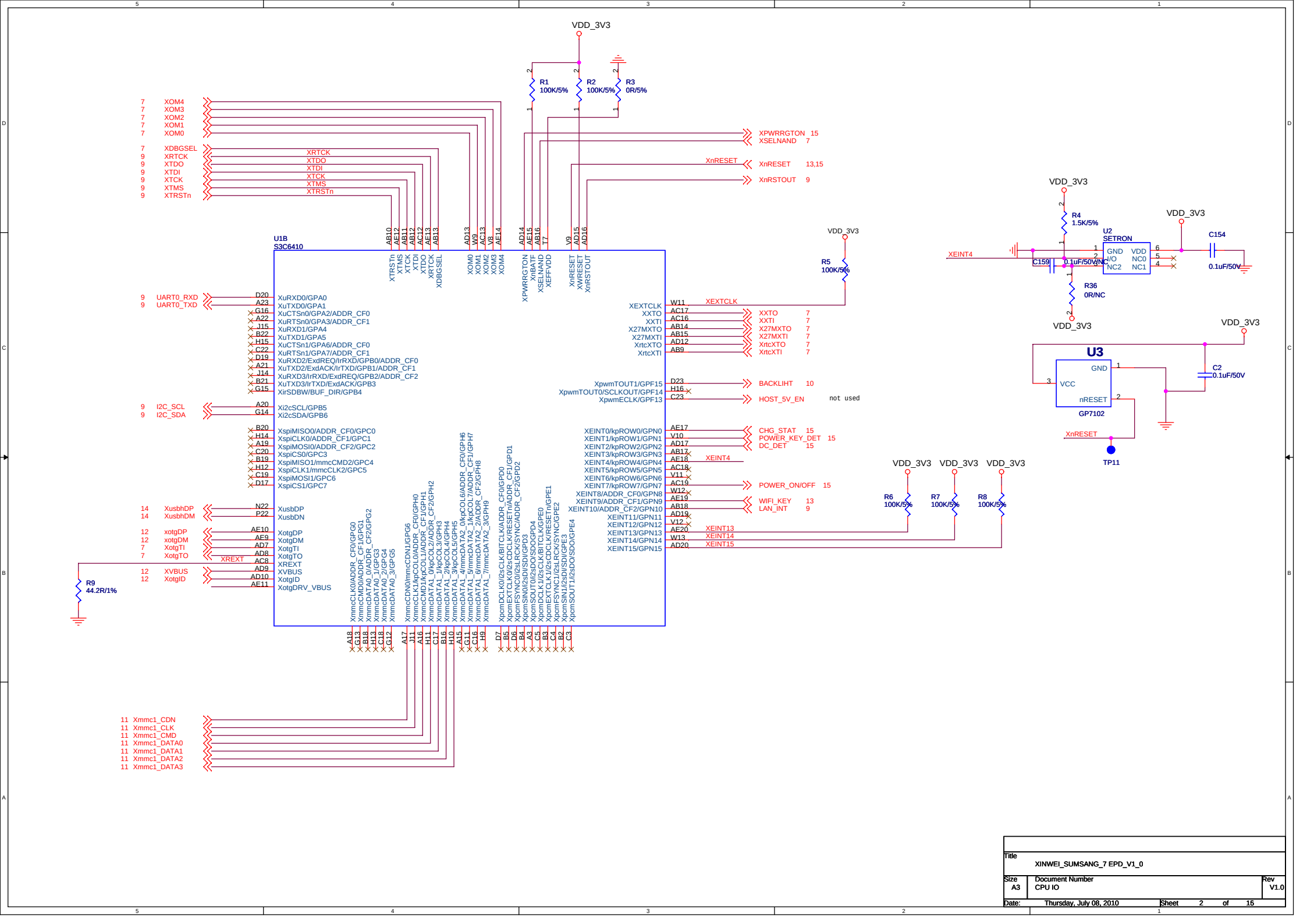
Date: Thursday, February 25, 2010	Sheet 1 of 16	



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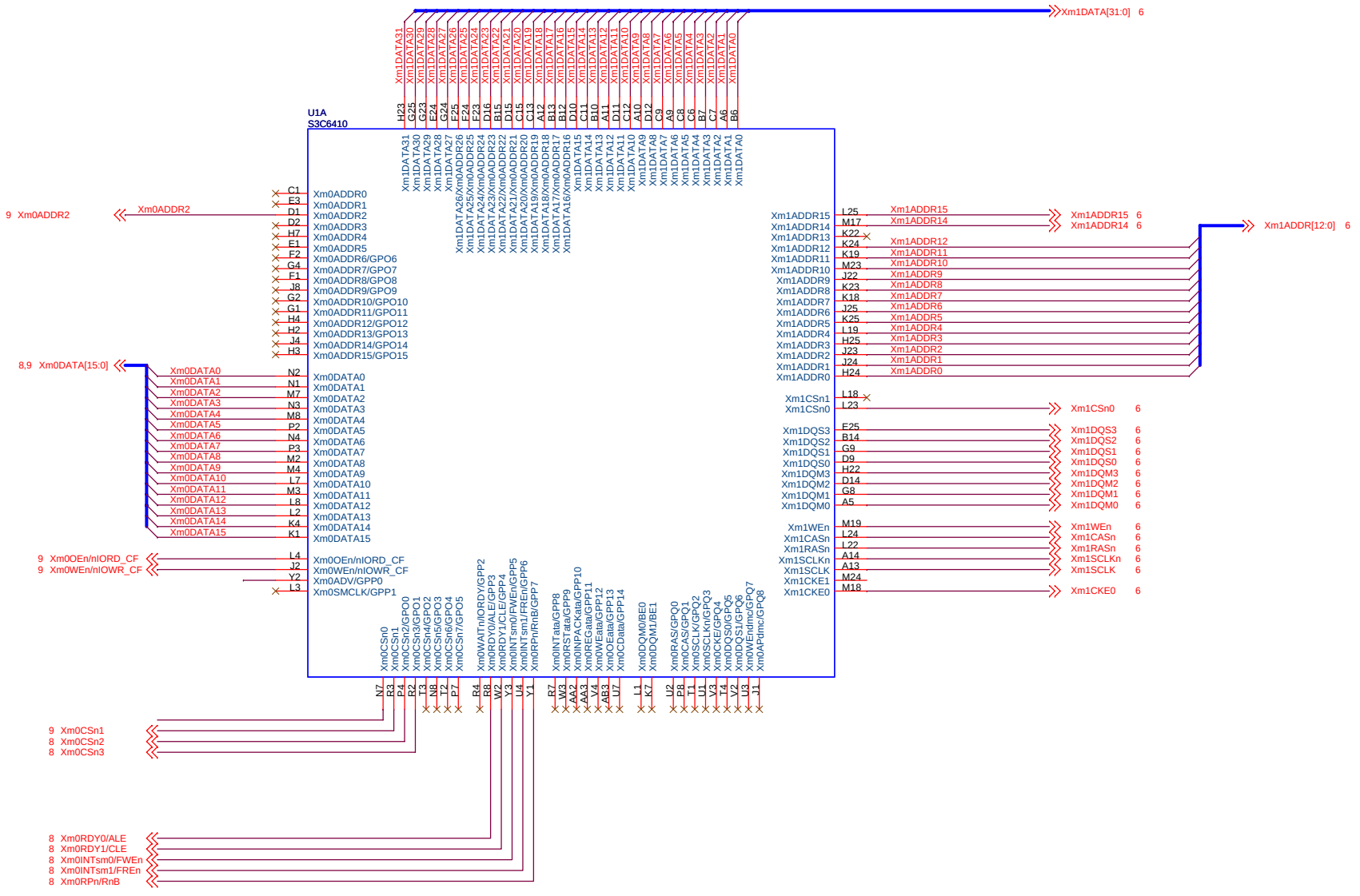
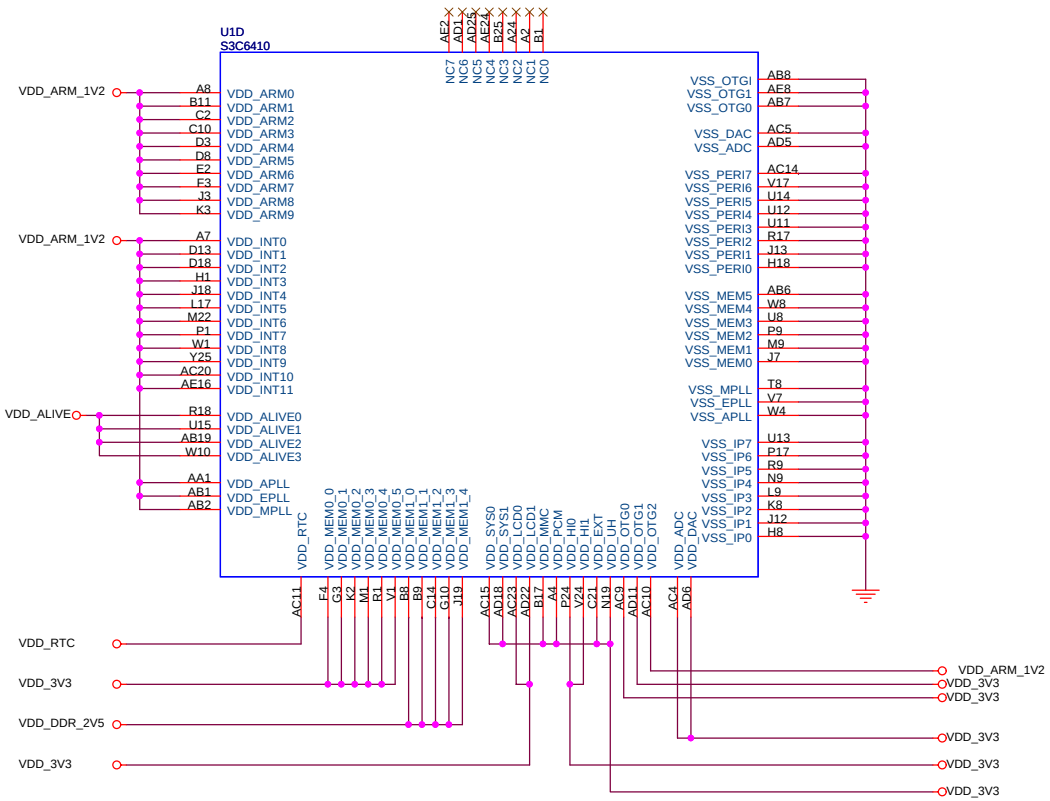
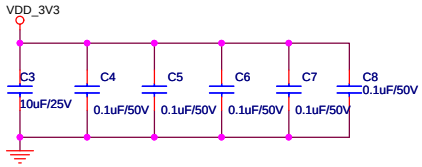
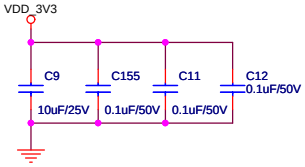
Size A3	Document Number CPU IO	Rev V1.0
Date: Thursday, July 08, 2010	Sheet 2 of 15	

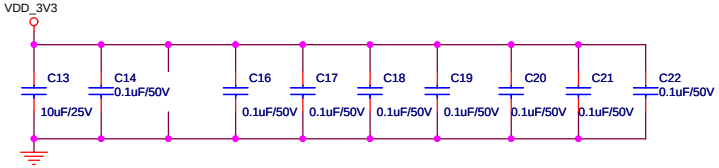


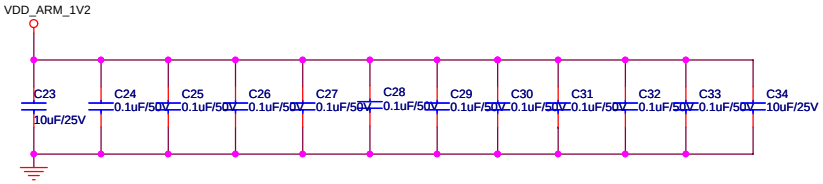
Title	XINWEI_SUMSANG_7 EPD_V1_0	
Size A3	Document Number CPU MEM	Rev V1.0
Date: Tuesday, April 20, 2010	Sheet 3 of 15	

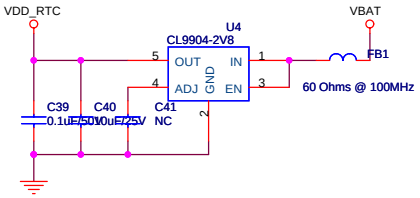


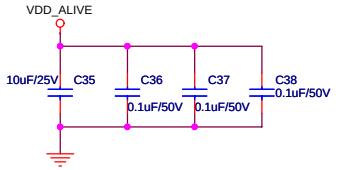


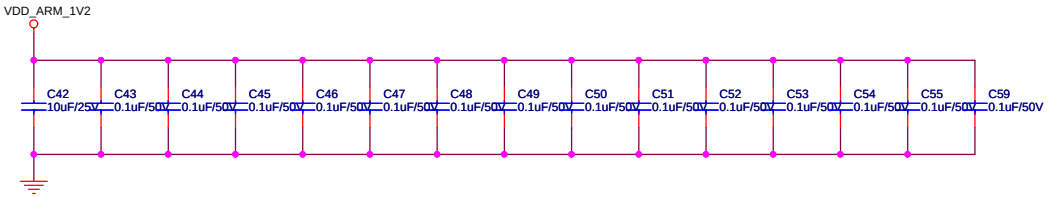


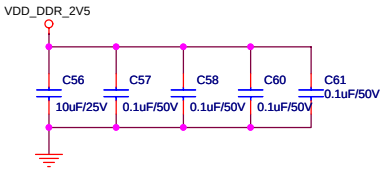












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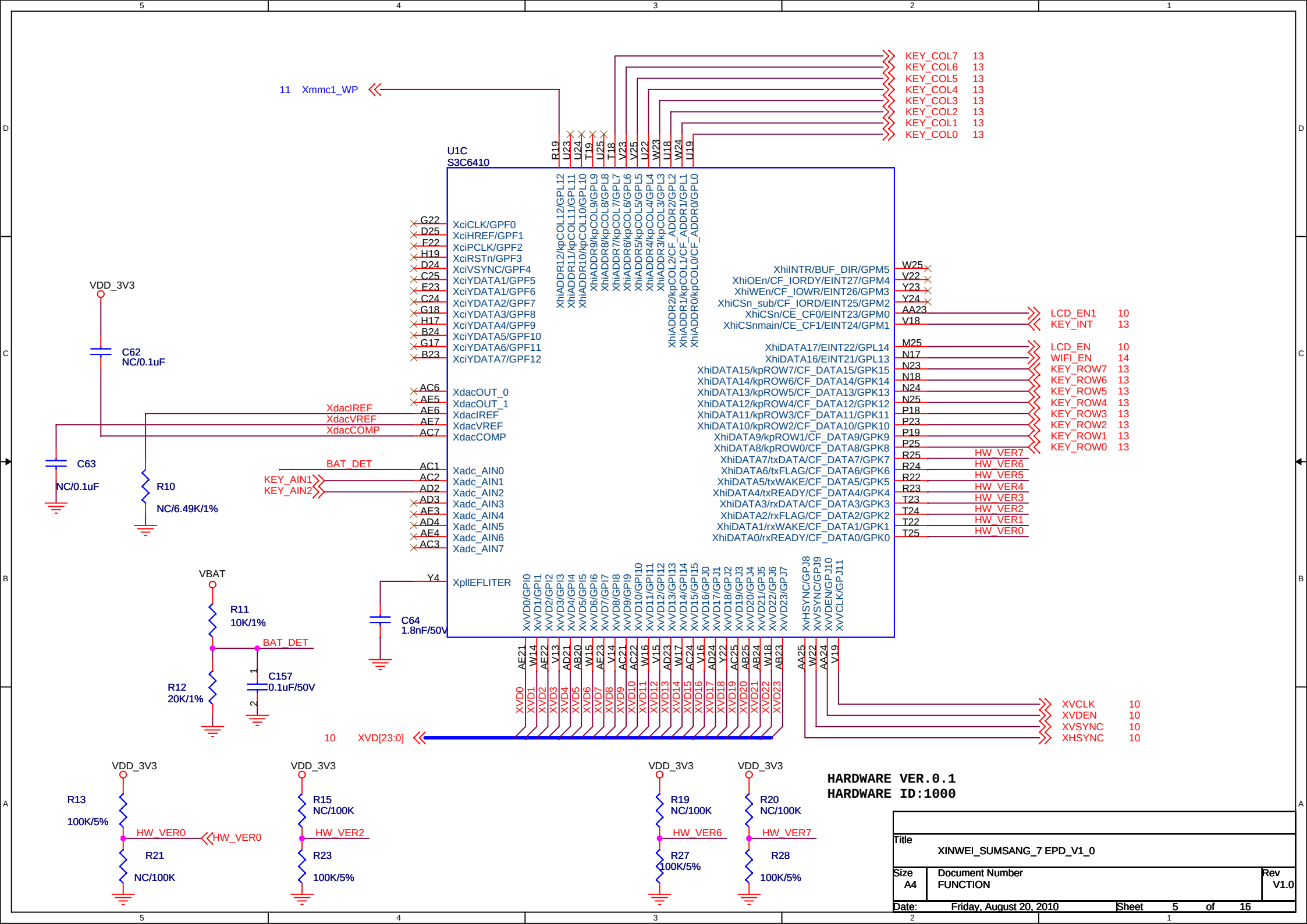
Size A3	Document Number CPU POWER	Rev V1.0
Date: Friday, August 20, 2010	Sheet 4 of 15	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KEY_COL7 13
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KEY_COL5 13
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KEY_COL3 13
KEY_COL2 13
KEY_COL1 13
KEY_COL0 13
U1C
S3C6410
R19 U23 U24 T19 U25 T18 V23 V25 U22 W23 U18 W24 U19
XhiADDR12/kpCOL12/GPL12
XhiADDR11/kpCOL11/GPL11
XhiADDR10/kpCOL10/GPL10
XhiADDR9/kpCOL9/GPL9
XhiADDR8/kpCOL8/GPL8
XhiADDR7/kpCOL7/GPL7
XhiADDR6/kpCOL6/GPL6
XhiADDR5/kpCOL5/GPL5
XhiADDR4/kpCOL4/GPL4
XhiADDR3/kpCOL3/GPL3
XhiADDR2/kpCOL2/CF_ADDR2/GPL2
XhiADDR1/kpCOL1/CF_ADDR1/GPL1
XhiADDR0/kpCOL0/CF_ADDR0/GPL0
G22 XciCLK/GPF0
D25 XciHREF/GPF1
E22 XciPCLK/GPF2
H19 XciRSTn/GPF3
D24 XciVSYNC/GPF4
C25 XciYDATA1/GPF5
E23 XciYDATA1/GPF6
C24 XciYDATA2/GPF7
G18 XciYDATA3/GPF8
H17 XciYDATA4/GPF9
B24 XciYDATA5/GPF10
G17 XciYDATA6/GPF11
B23 XciYDATA7/GPF12
XhiINTR/BUF_DIR/GPM5 W25
XhiOEn/CF_IORDY/EINT27/GPM4 V22
XhiWEn/CF_IOWR/EINT26/GPM3 Y23
XhiCSn_sub/CF_IORD/EINT25/GPM2 Y24
XhiCSn/CE_CF0/EINT23/GPM0 AA23
XhiCSnmain/CE_CF1/EINT24/GPM1 V18
LCD_EN1 10
KEY_INT 13
XhiDATA17/EINT22/GPL14 M25
XhiDATA16/EINT21/GPL13 N17
XhiDATA15/kpROW7/CF_DATA15/GPK15 N23
XhiDATA14/kpROW6/CF_DATA14/GPK14 N18
XhiDATA13/kpROW5/CF_DATA13/GPK13 N24
XhiDATA12/kpROW4/CF_DATA12/GPK12 N25
XhiDATA11/kpROW3/CF_DATA11/GPK11 P18
XhiDATA10/kpROW2/CF_DATA10/GPK10 P23
XhiDATA9/kpROW1/CF_DATA9/GPK9 P19
XhiDATA8/kpROW0/CF_DATA8/GPK8 P25
XhiDATA7/txDATA/CF_DATA7/GPK7 R25
XhiDATA6/txFLAG/CF_DATA6/GPK6 R24
XhiDATA5/txWAKE/CF_DATA5/GPK5 R22
XhiDATA4/txREADY/CF_DATA4/GPK4 R23
XhiDATA3/rxDATA/CF_DATA3/GPK3 T23
XhiDATA2/rxFLAG/CF_DATA2/GPK2 T24
XhiDATA1/rxWAKE/CF_DATA1/GPK1 T22
XhiDATA0/rxREADY/CF_DATA0/GPK0 T25
LCD_EN 10
WIFI_EN 14
KEY_ROW7 13
KEY_ROW6 13
KEY_ROW5 13
KEY_ROW4 13
KEY_ROW3 13
KEY_ROW2 13
KEY_ROW1 13
KEY_ROW0 13
HW_VER7
HW_VER6
HW_VER5
HW_VER4
HW_VER3
HW_VER2
HW_VER1
HW_VER0
AC6 XdacOUT_0
AE5 XdacOUT_1
AE6 XdacIREF
AE7 XdacVREF
AC7 XdacCOMP
AC1 Xadc_AIN0
AC2 Xadc_AIN1
AD2 Xadc_AIN2
AD3 Xadc_AIN3
AE3 Xadc_AIN4
AD4 Xadc_AIN5
AE4 Xadc_AIN6
AC3 Xadc_AIN7
Y4 XpllEFLITER
XvVD0/GPI0 XvVD1/GPI1 XvVD2/GPI2 XvVD3/GPI3 XvVD4/GPI4 XvVD5/GPI5 XvVD6/GPI6 XvVD7/GPI7 XvVD8/GPI8 XvVD9/GPI9 XvVD10/GPI10 XvVD11/GPI11 XvVD12/GPI12 XvVD13/GPI13 XvVD14/GPI14 XvVD15/GPI15 XvVD16/GPJ0 XvVD17/GPJ1 XvVD18/GPJ2 XvVD19/GPJ3 XvVD20/GPJ4 XvVD21/GPJ5 XvVD22/GPJ6 XvVD23/GPJ7
XvHSYNC/GPJ8 XvVSYNC/GPJ9 XvVDEN/GPJ10 XvVCLK/GPJ11
AE21 W14 AE22 V13 AD21 AB20 W15 AE23 V14 AC21 AC22 W16 V15 AD23 W17 AC24 V16 AD24 Y22 AC25 AB25 AB24 W18 AB23
AA25 W22 AA24 V19
XVD0 XVD1 XVD2 XVD3 XVD4 XVD5 XVD6 XVD7 XVD8 XVD9 XVD10 XVD11 XVD12 XVD13 XVD14 XVD15 XVD16 XVD17 XVD18 XVD19 XVD20 XVD21 XVD22 XVD23
10 XVD[23:0]
XVCLK 10
XVDEN 10
XVSYNC 10
XHSYNC 10
VDD_3V3
C62 NC/0.1uF
C63 NC/0.1uF
R10 NC/6.49K/1%
BAT_DET
KEY_AIN1
KEY_AIN2
VBAT
R11 10K/1%
R12 20K/1%
C157 0.1uF/50V
C64 1.8nF/50V
R13 100K/5%
R21 NC/100K
R15 NC/100K
R23 100K/5%
R19 NC/100K
R27 100K/5%
R20 NC/100K
R28 100K/5%
HARDWARE VER.0.1
HARDWARE ID:1000
Title XINWEI_SUMSANG_7 EPD_V1_0
Size A4
Document Number FUNCTION
Rev V1.0
Date: Friday, August 20, 2010
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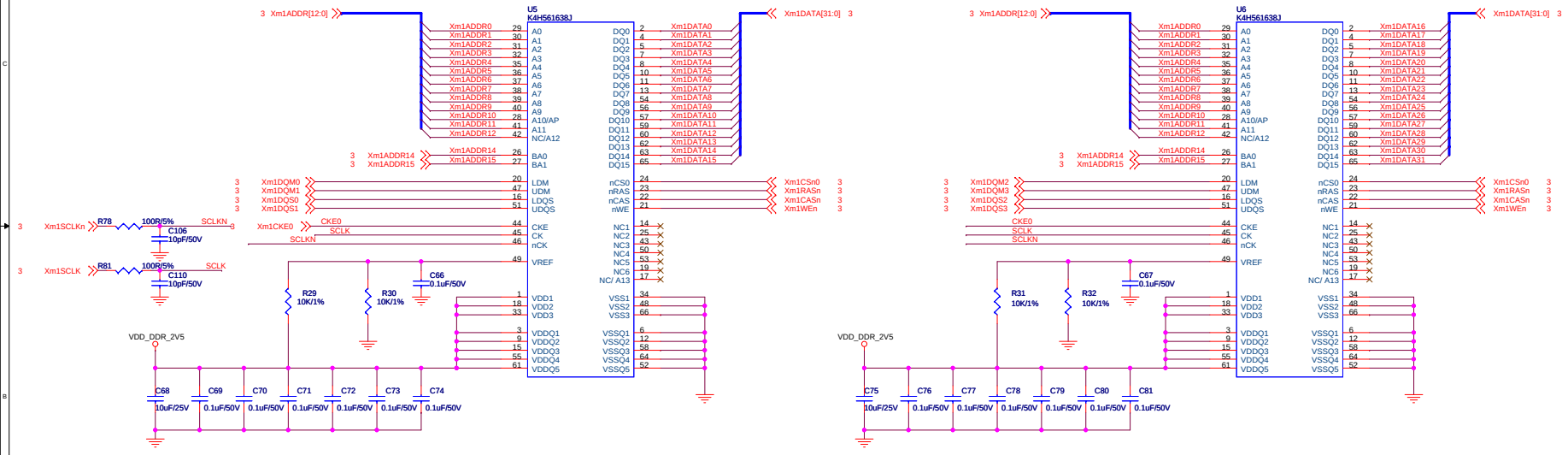

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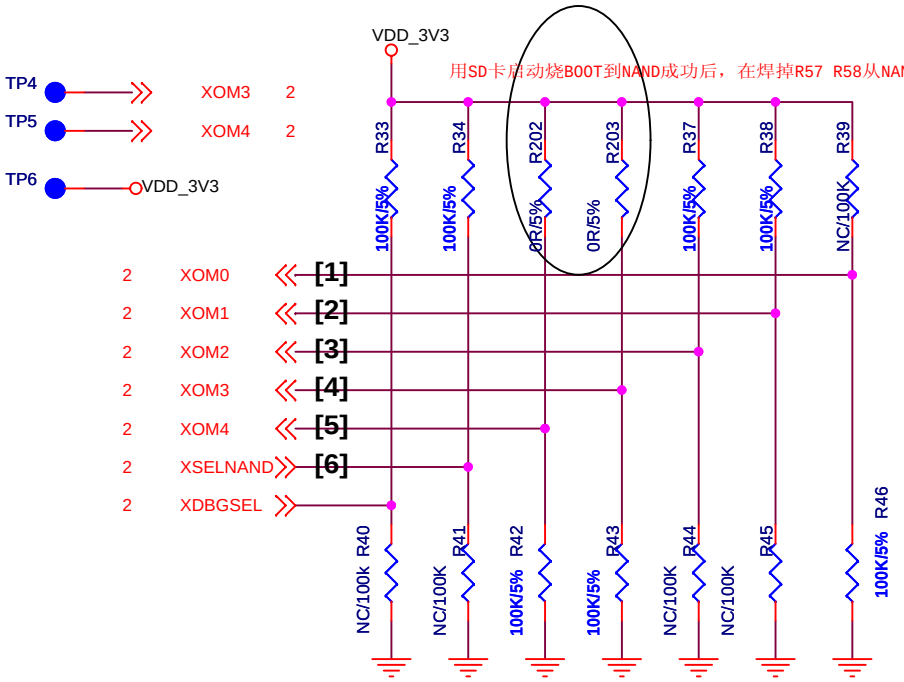
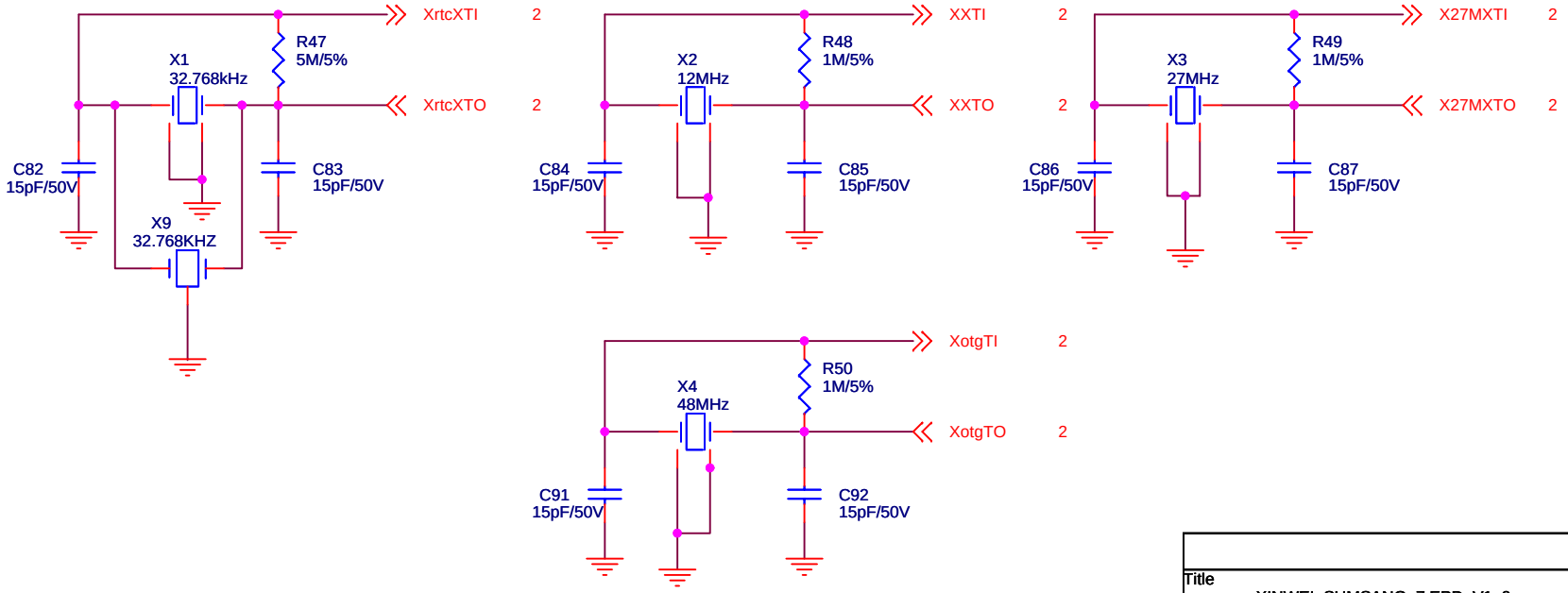
Size C	Document Number DDR	Rev V1.0
Date: Friday, August 20, 2010	Sheet 6 of 15	



Booting Mode	[6]	[5]	[4]	[3]	[2]	[1]
NAND, 512/3 cycle	1	0	0	0	0	ON :EXTCLK
NAND, 512/4 cycle	1	0	0	0	1	
ANAND, 2K/4 cycle	1	0	0	1	0	
ANAND, 2K/5 cycle	1	0	0	1	1	
NOR/SROM	x	0	1	0	0:8bit 1:16bit	
OneNAND	0	0	1	1	0	OFF :XTI
Modem	x	0	1	1	1	
Internal ROM	x	1	1	1	1	



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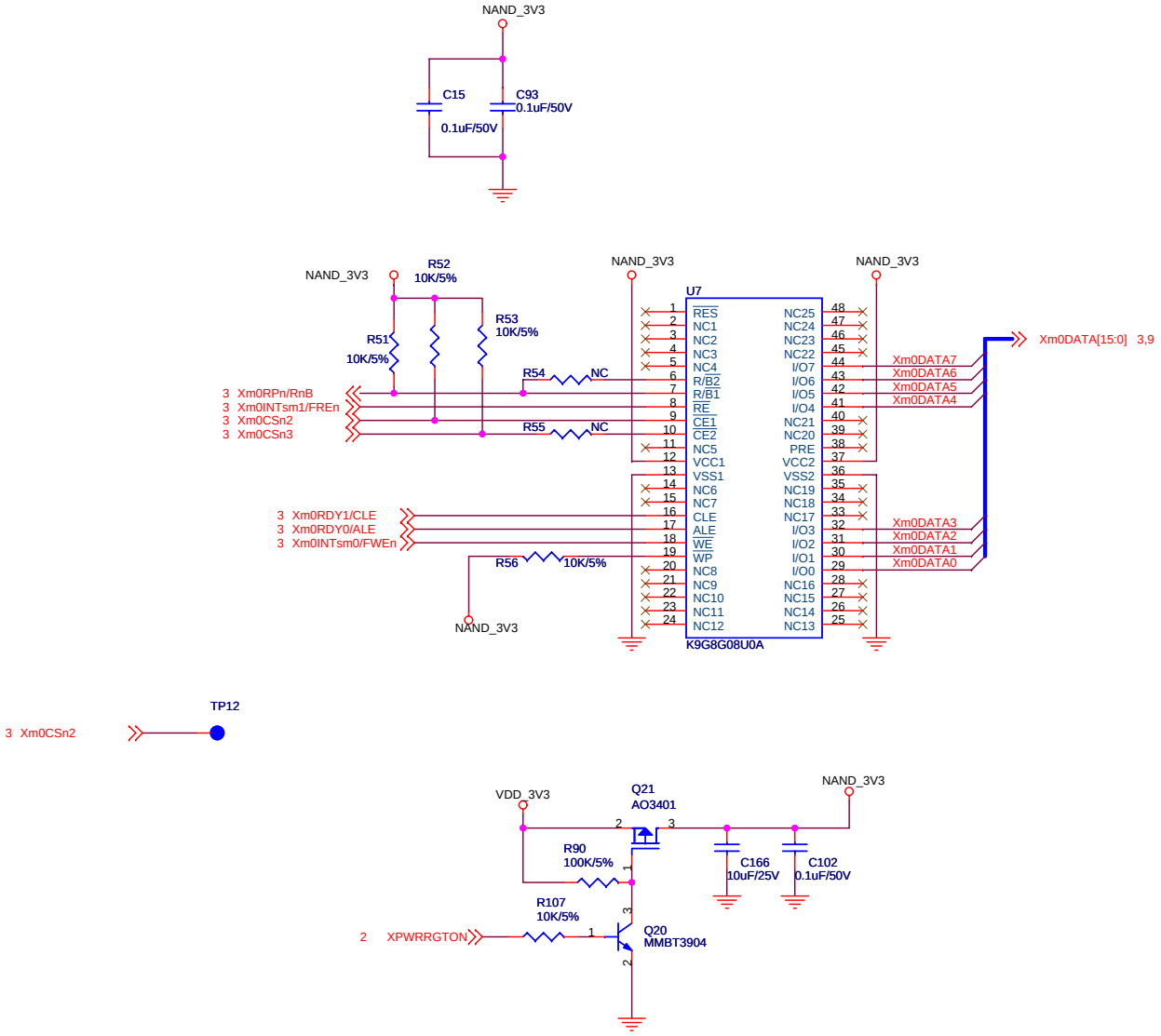
Size A4	Document Number BOOTING CLOCK	Rev V1.0
Date: Friday, August 20, 2010	Sheet 7 of 15	



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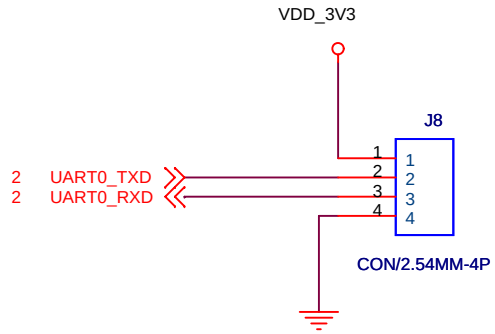
Size B	Document Number NAND	Rev V1.0
Date: Friday, August 20, 2010	Sheet 8 of 15	



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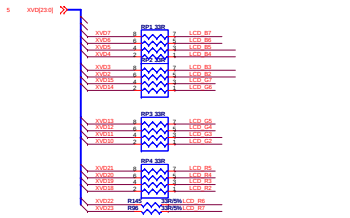
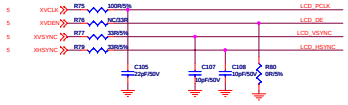
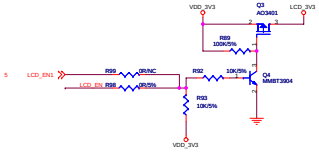
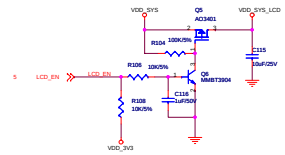
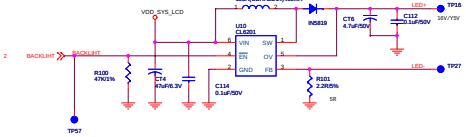
Size A4	Document Number DEBUG	Rev V1.0
Date: Friday, August 20, 2010	Sheet 9 of 15	

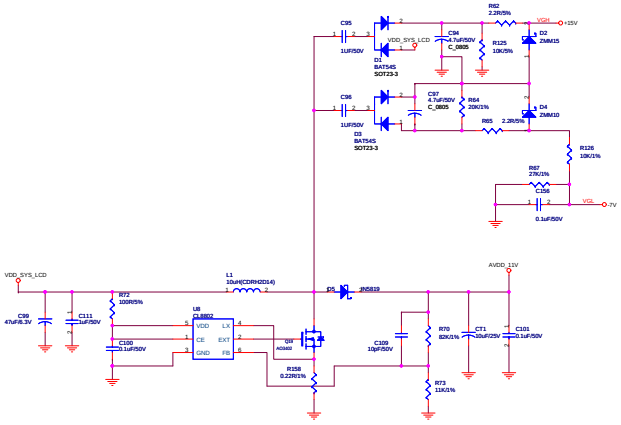


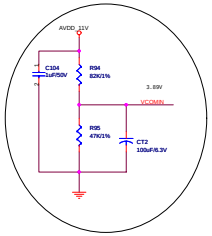


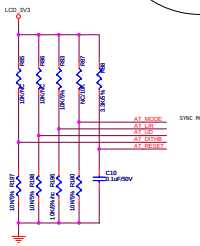


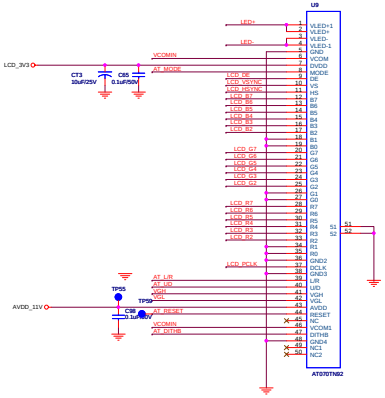


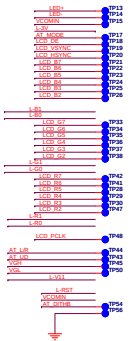












AO70VW04的VCOM电压为3.3—3.9V
HSD070IDW1-A的VCOM电压为3.9V
AT070TN82 的VCOM电压为3.9—4.3V

Title	X6W85_SUMSANG_7 EPD_V1_0		
Size	Document Number		Rev
A1	<Doc>		V1.0
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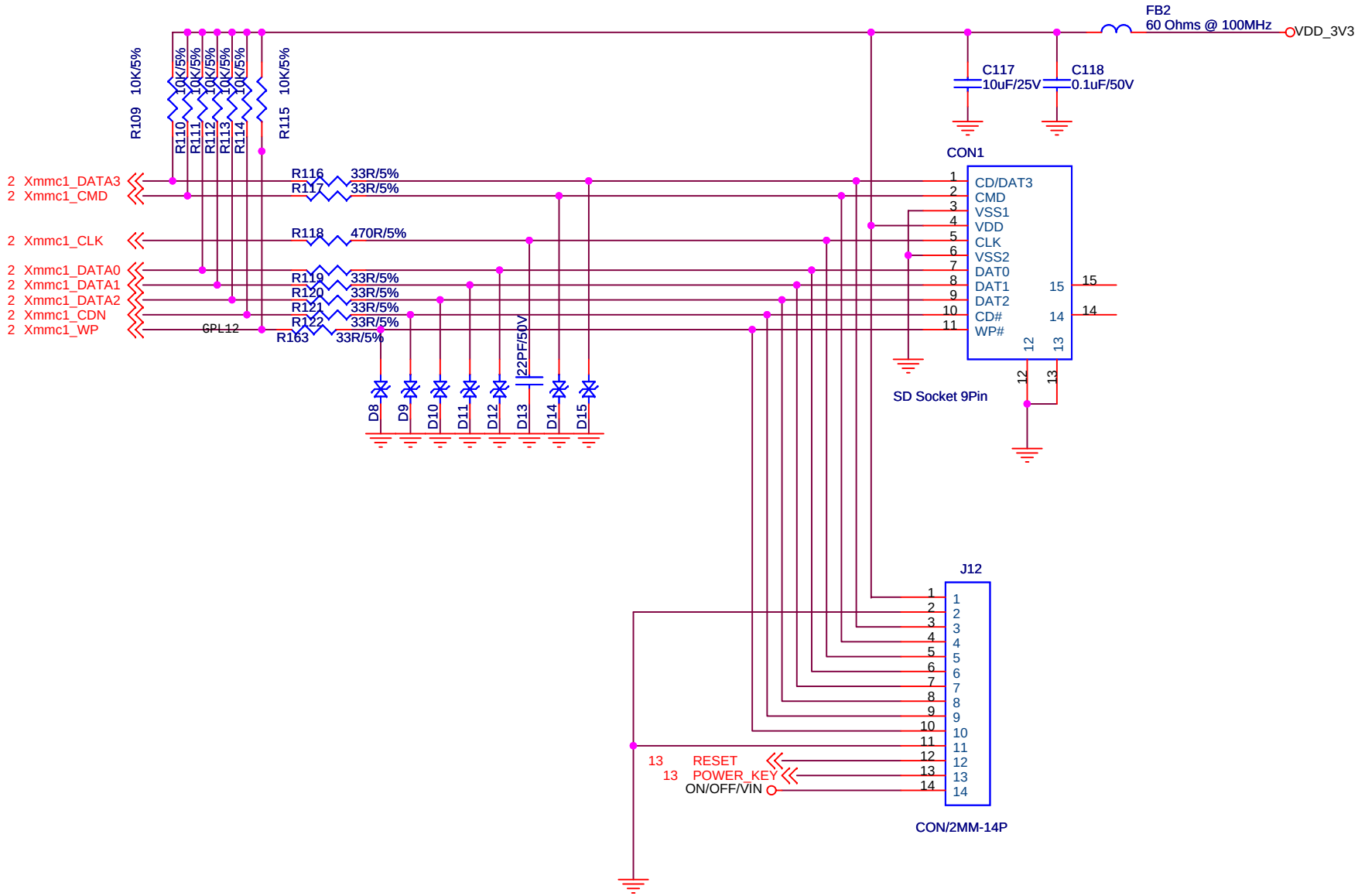

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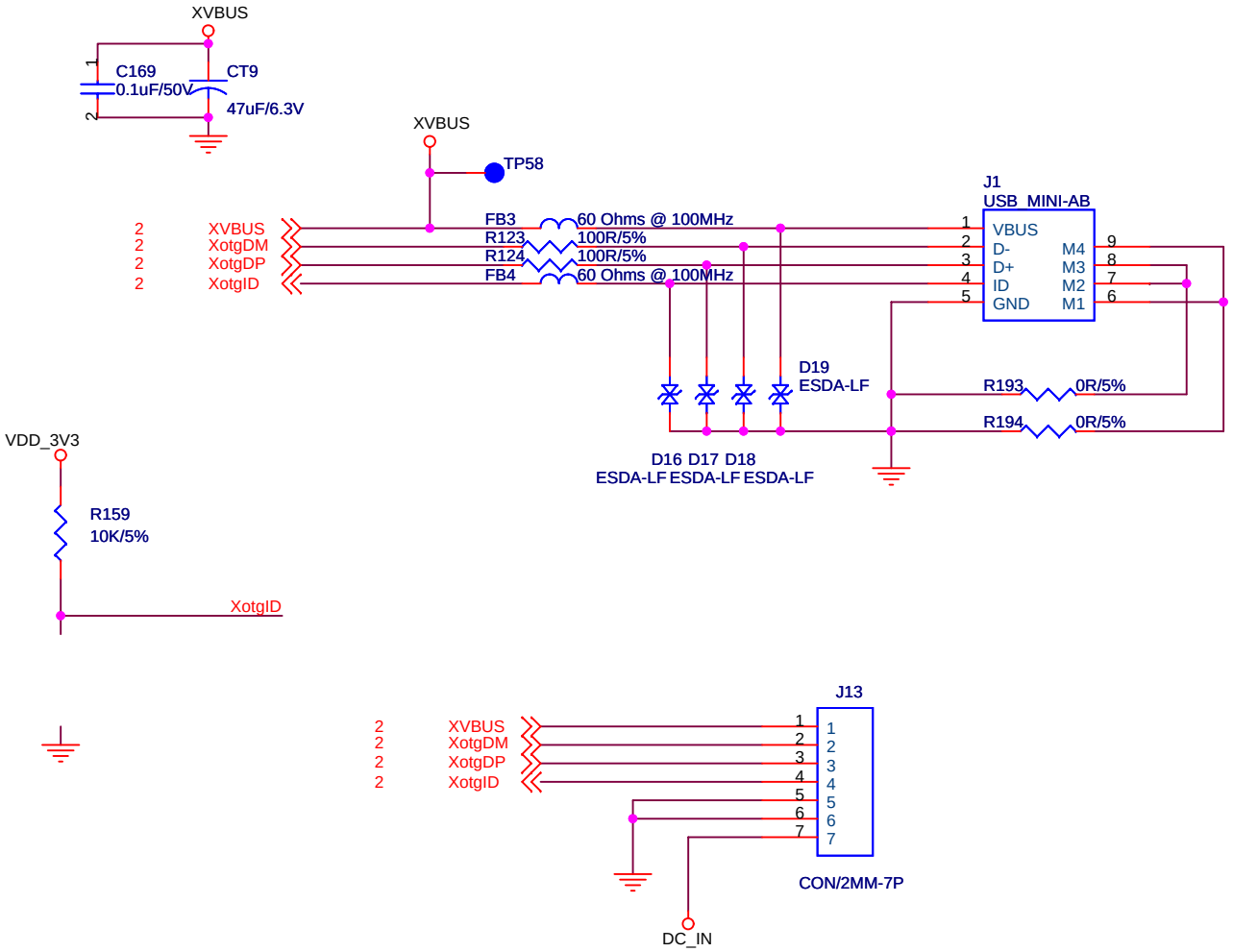
Size A4	Document Number <Doc>	Rev V1.0
Date: Friday, August 20, 2010	Sheet 11 of 15	



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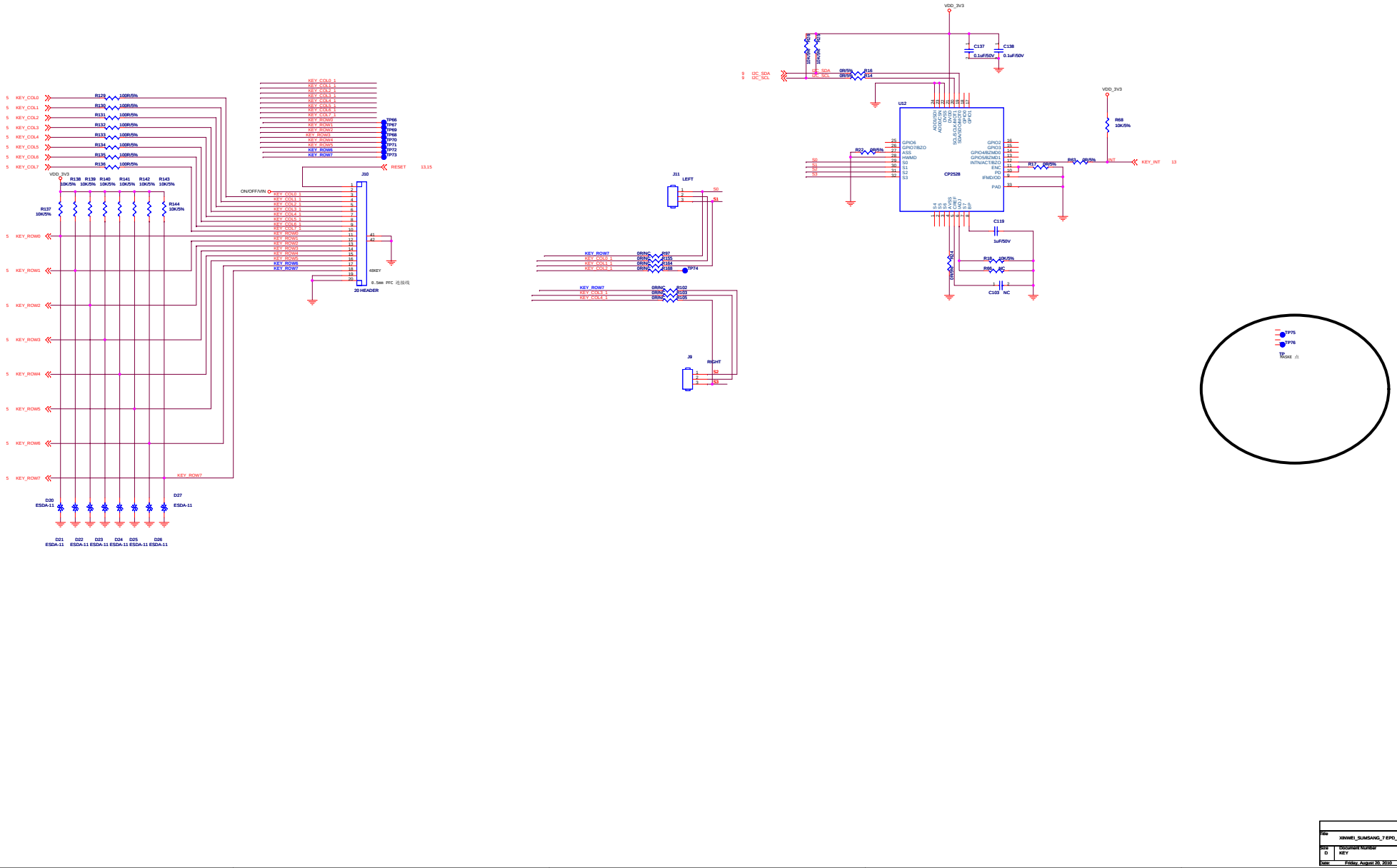
Size A4	Document Number SUB DEVICE	Rev V1.0
Date: Friday, August 20, 2010	Sheet 12 of 15	



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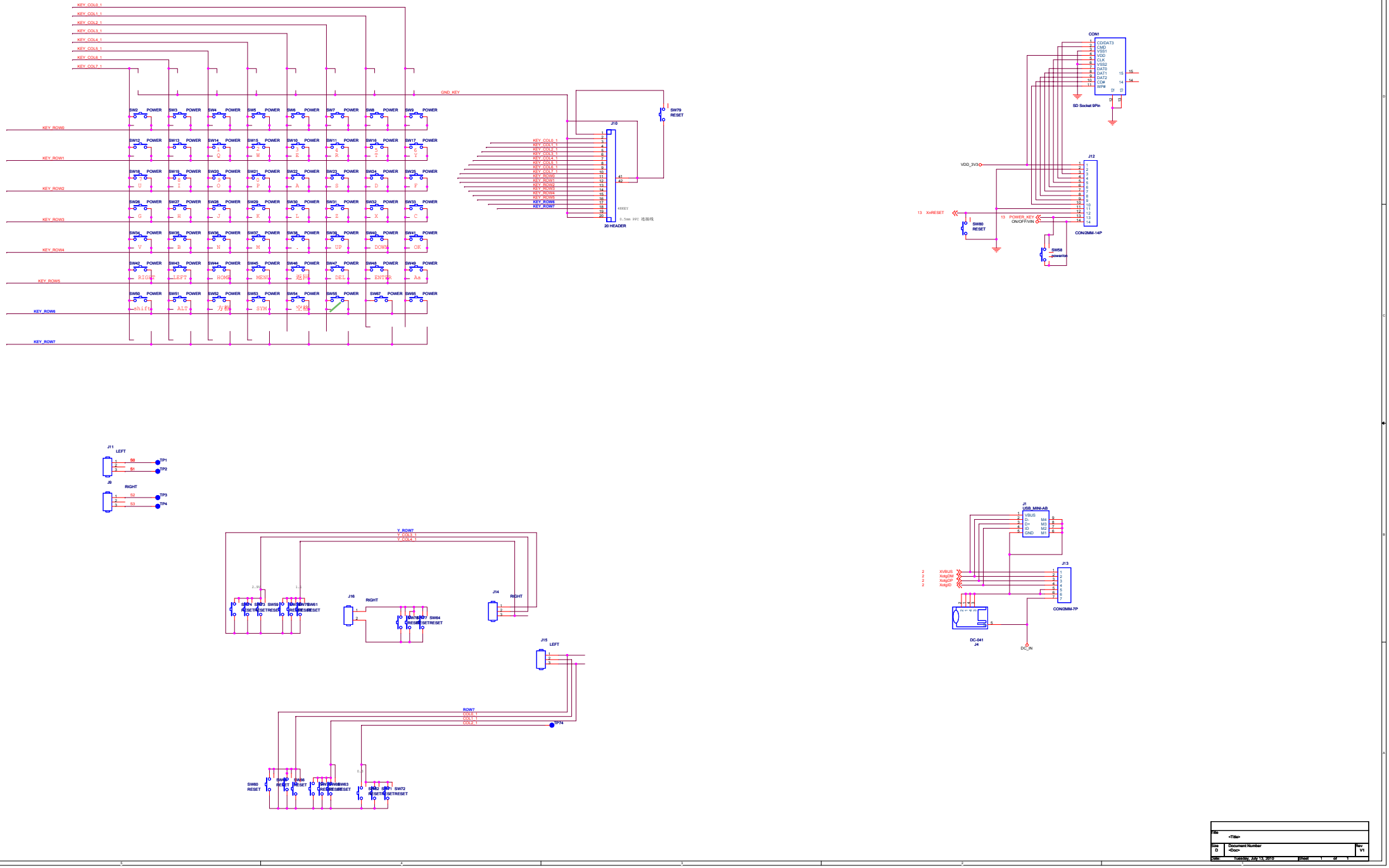
Size: D	Document Number: KEY		Rev: V1.0
Date: Friday, August 20, 2010	Sheet 13 of 15		



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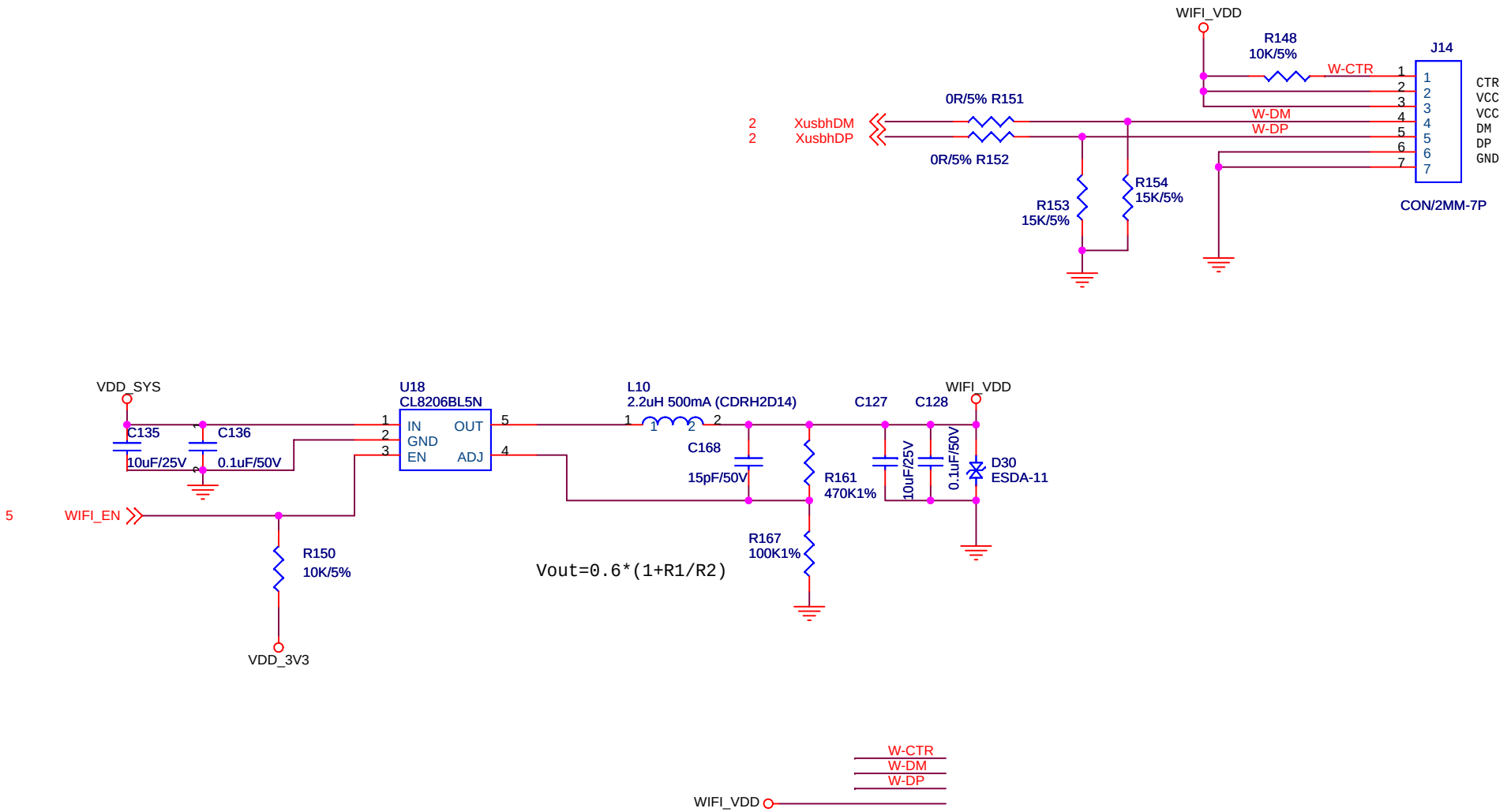
Date: Tuesday, July 13, 2010	Sheet 1 of 1		



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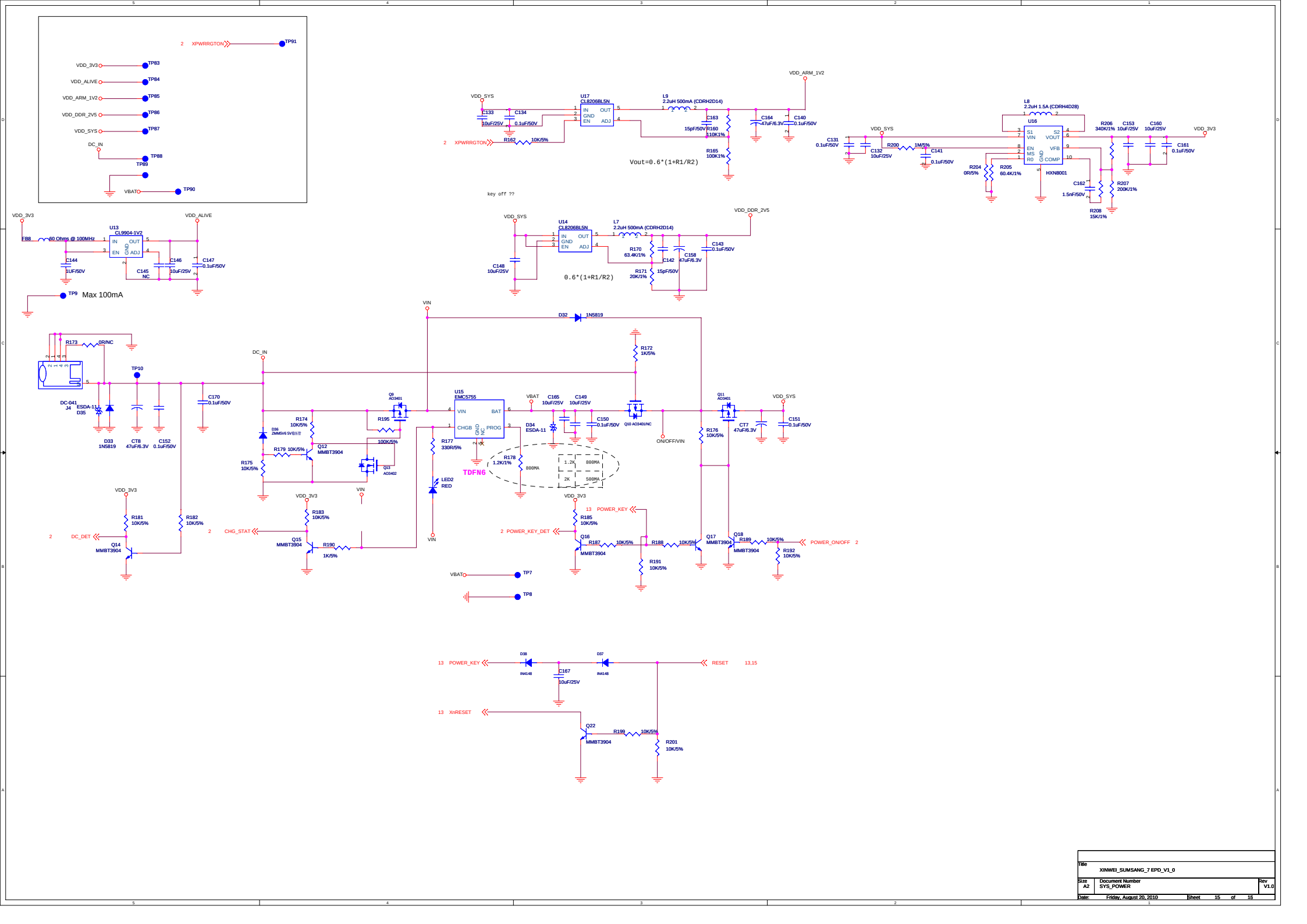
Size A4	Document Number <Doc>	Rev V1.0
Date: Friday, August 20, 2010	Sheet 14 of 15	



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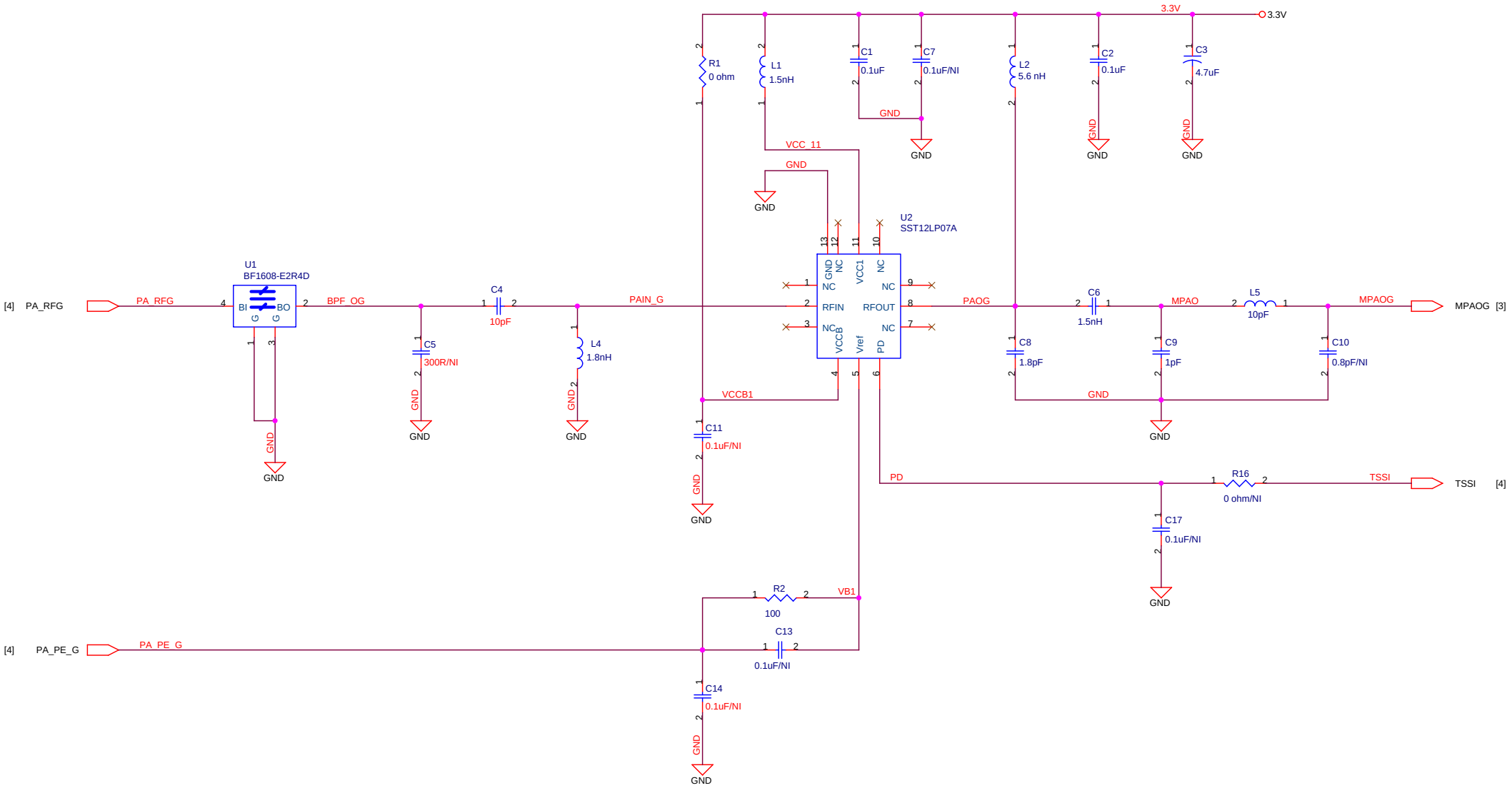
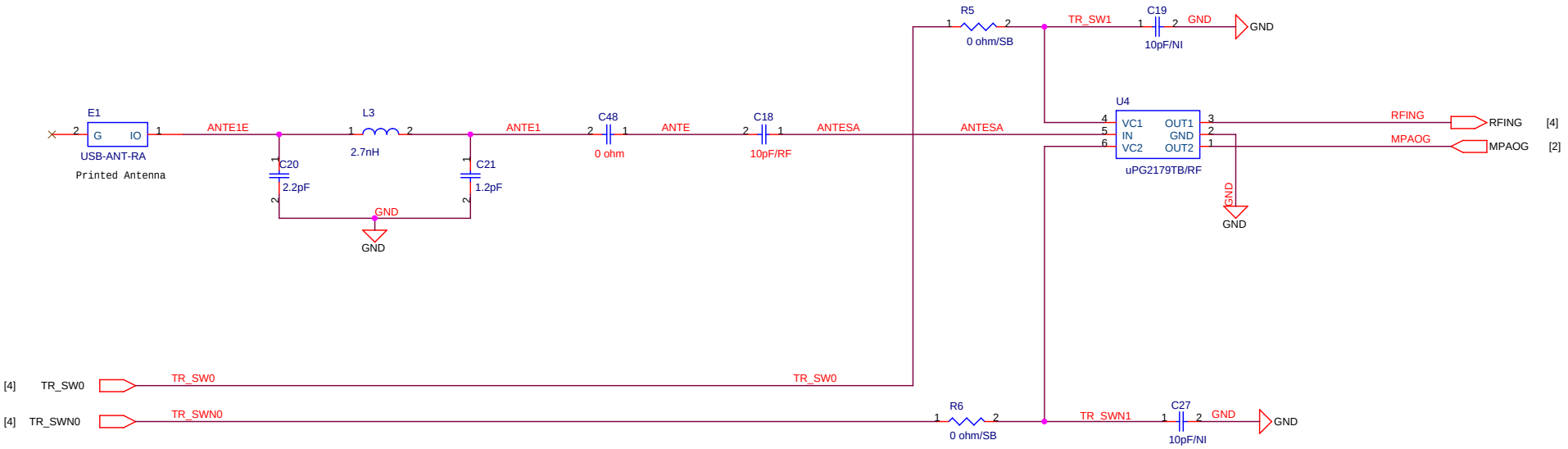
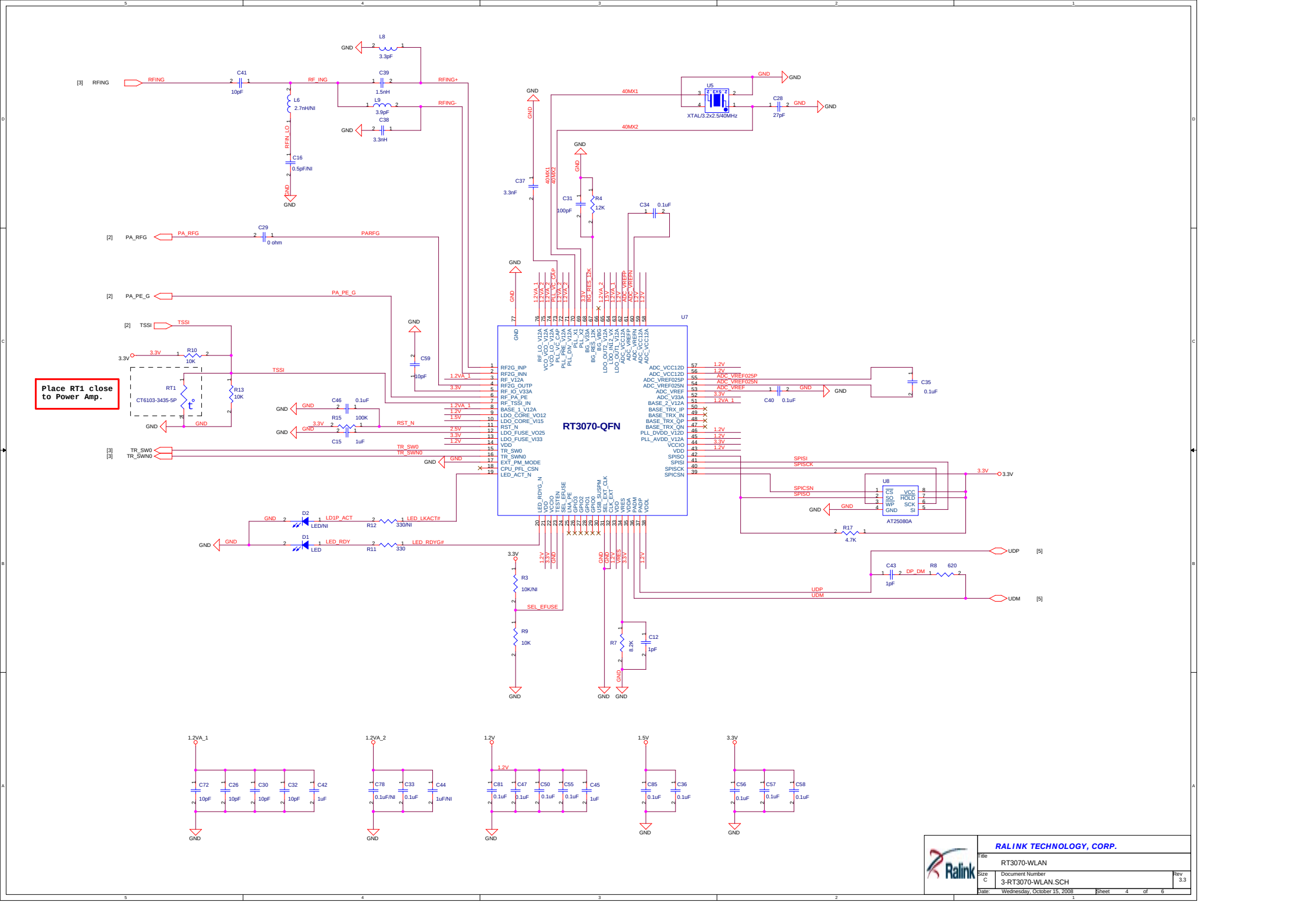
Size A2	Document Number SYS_POWER		Rev V1.0
Date: Friday, August 20, 2010	Sheet 15 of 15		

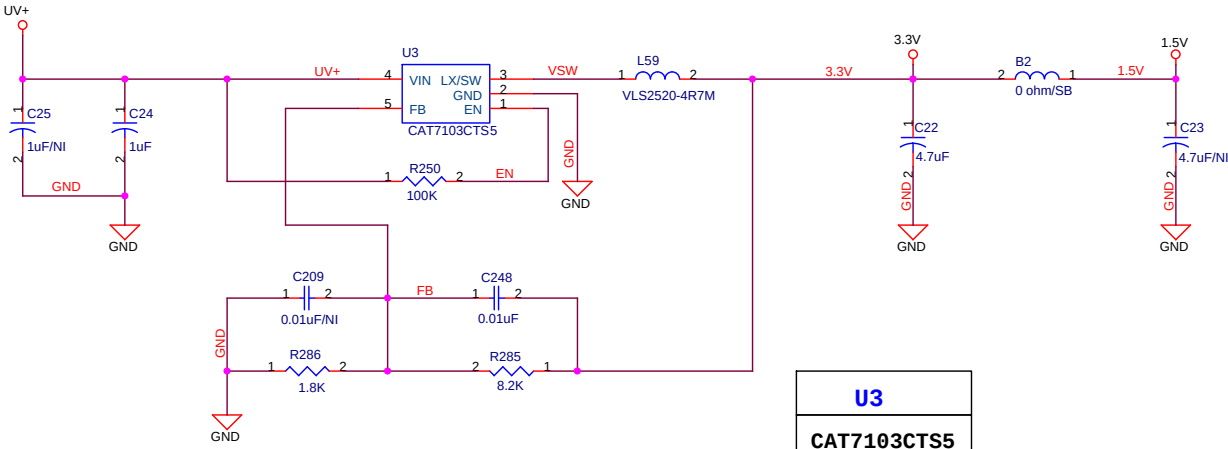


RALINK TECHNOLOGY, CORP.		
Title: 2.4GHz PA		
Size: B	Document Number: 1-PA-SST12LP07A.SCH	Rev: 3.3
Date: Wednesday, October 15, 2008	Sheet 2 of 6	

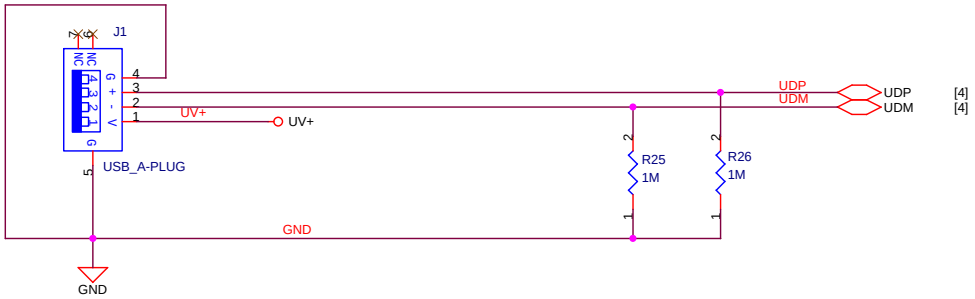


RALINK TECHNOLOGY, CORP.	
Title	Front-End
Size B	Document Number 2-FrontEnd.SCH
Rev	3.3
Date: Wednesday, October 15, 2008	Sheet 3 of 6


RT3070-QFN
Place RT1 close to Power Amp.
XTAL/3.2x2.5/40MHz
AT25080A
CT6103-3435-5P
RALINK TECHNOLOGY, CORP.
RT3070-WLAN
3-RT3070-WLAN.SCH
Wednesday, October 15, 2008



U3
CAT7103CTS5
APW7102-BTI



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Title: PEN USB Interface and Power			
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