

1 Annex C – documentation

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EMV **TESTHAUS** GmbH
Gustav-Hertz-Straße 35
94315 Straubing
Germany

Xylem Analytics Germany GmbH
RFID module
NFC-Module1

170586-AU01+W05

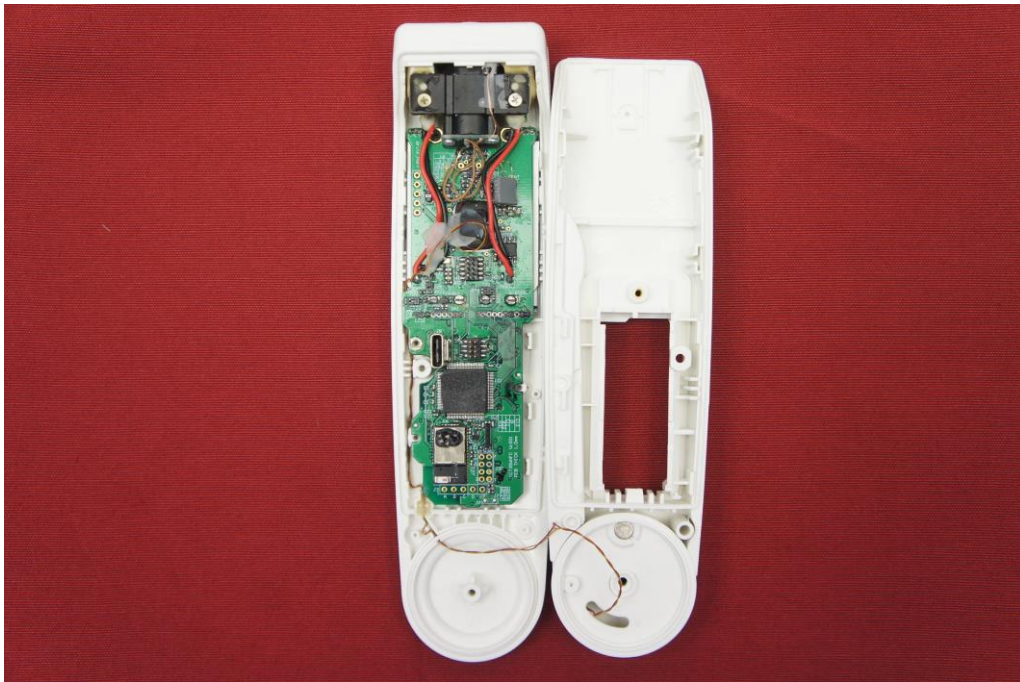
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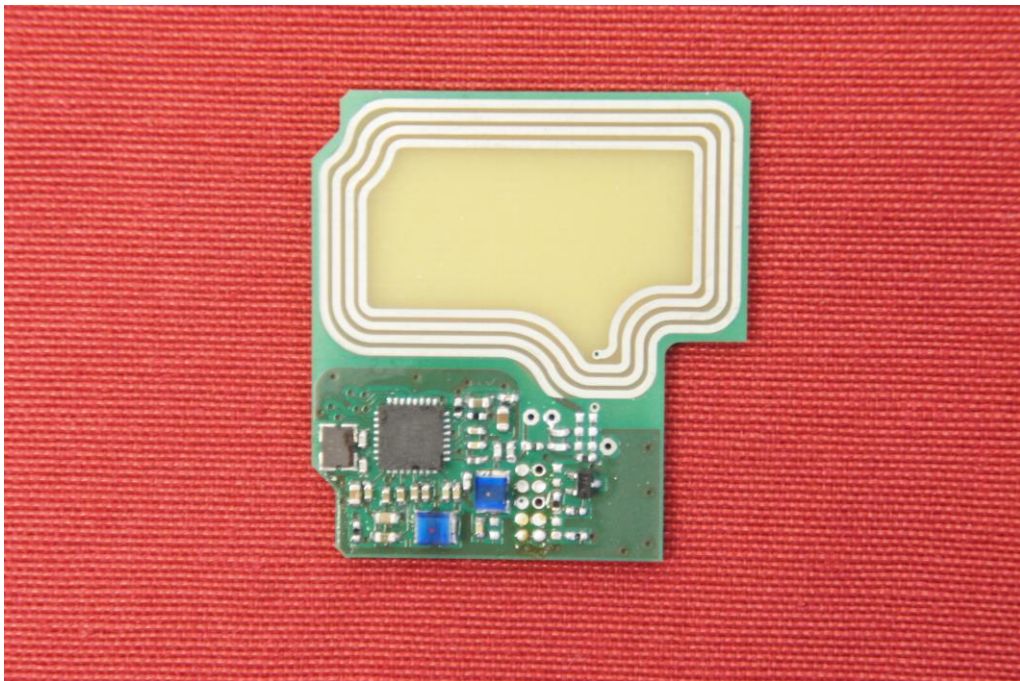
Picture 1: Host test sample - top side, open



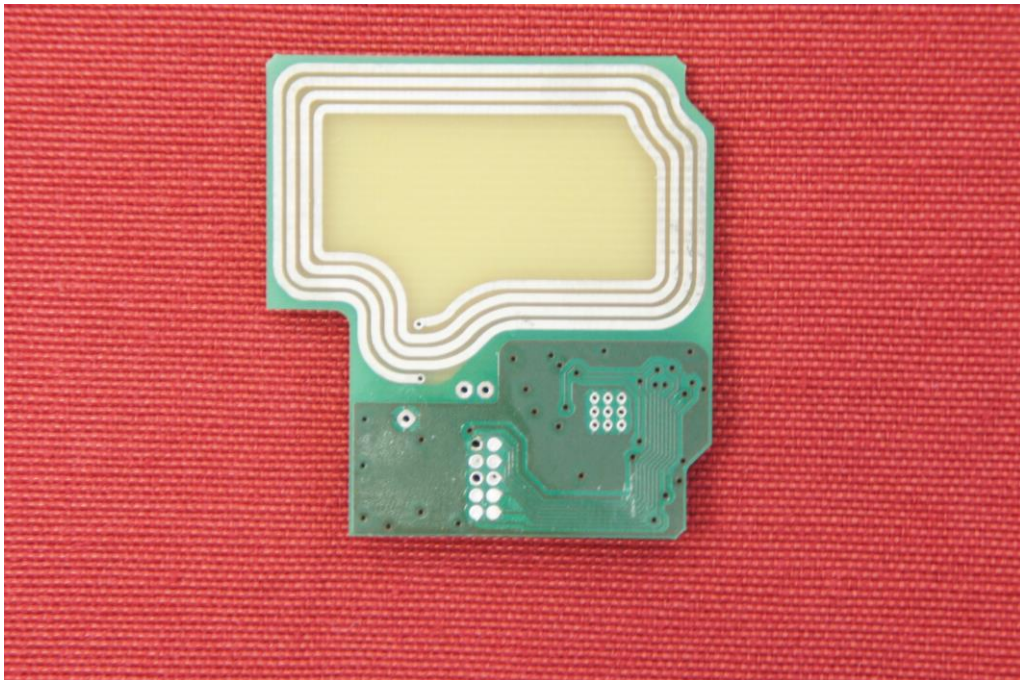
Picture 2: Host test sample – bottom side, open



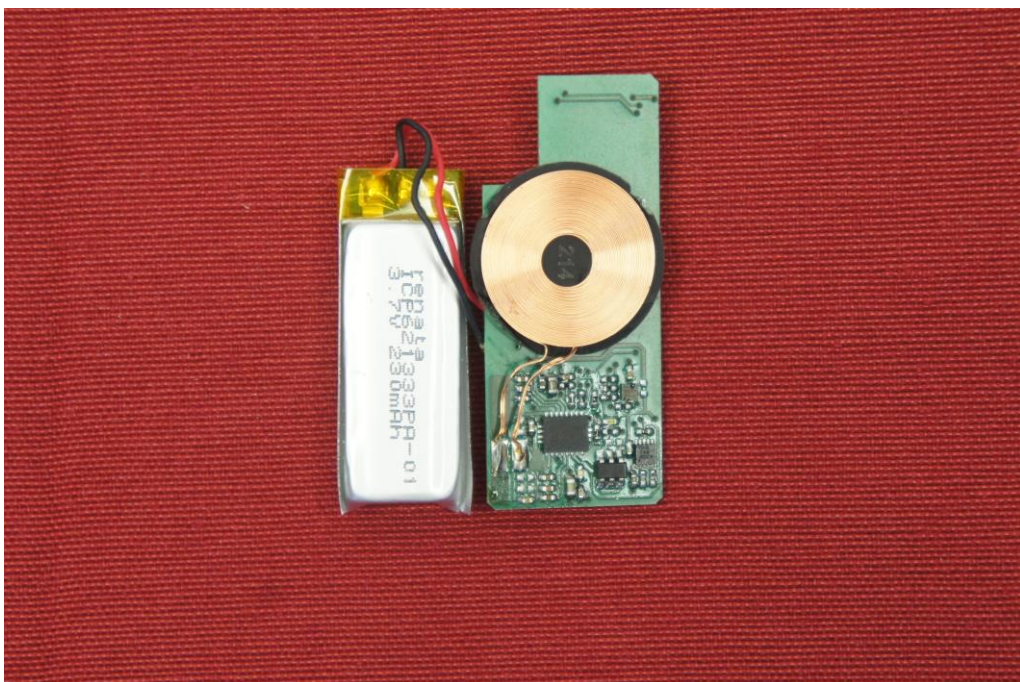
Picture 3: Host test sample – bottom side, without EUT test sample



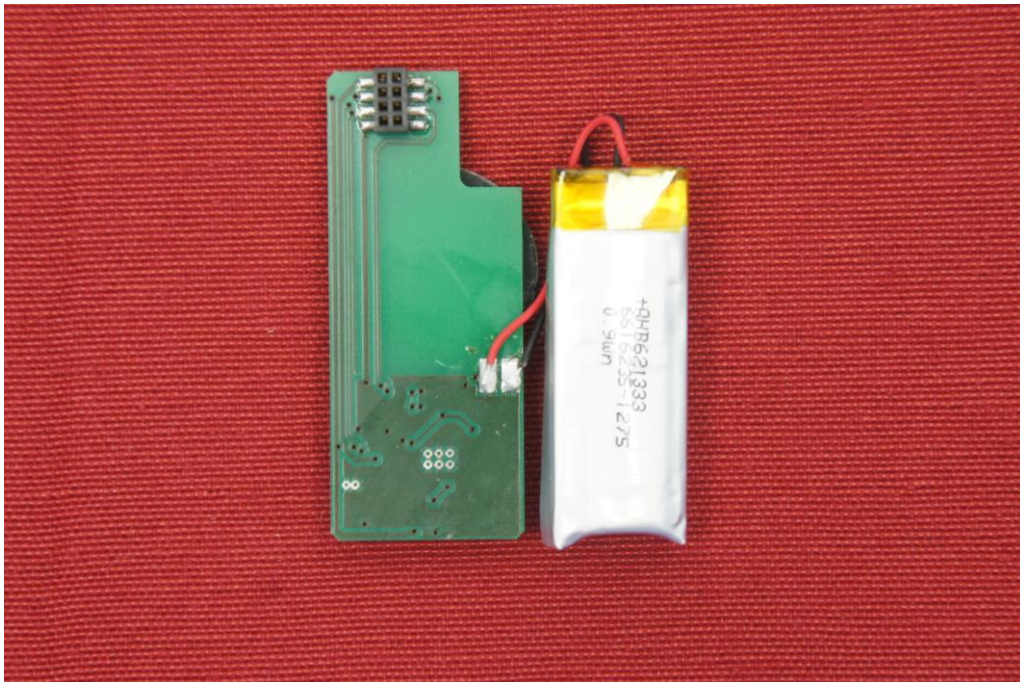
Picture 4: EUT test sample – top side



Picture 5: EUT test sample – bottom side



Picture 6: WPT receiver antenna + battery – top side



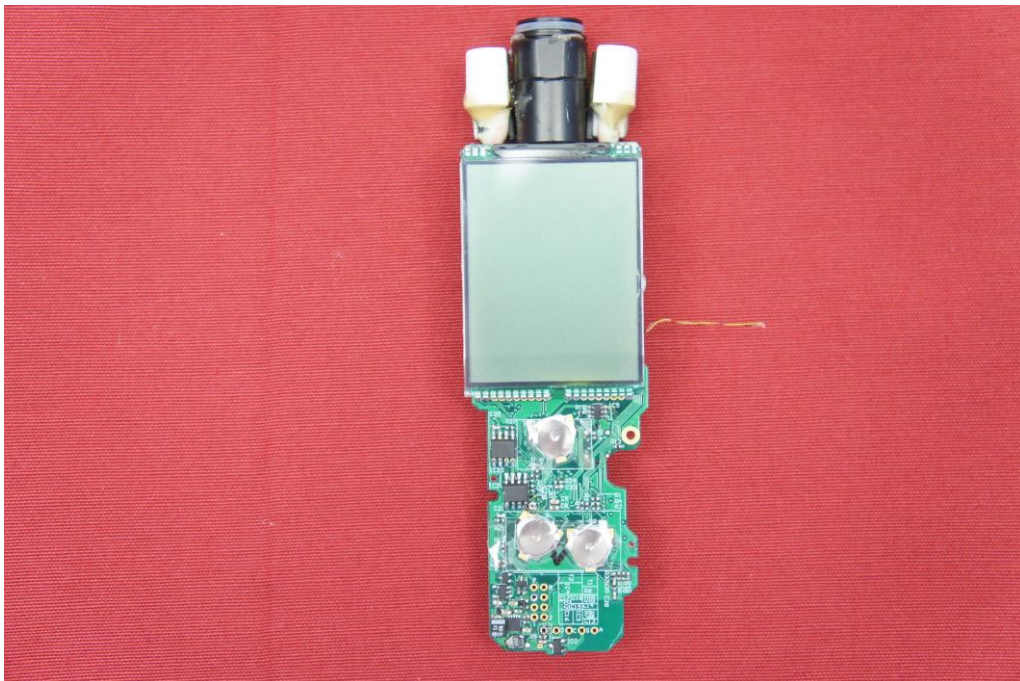
Picture 7: WPT receiver antenna + battery – bottom side



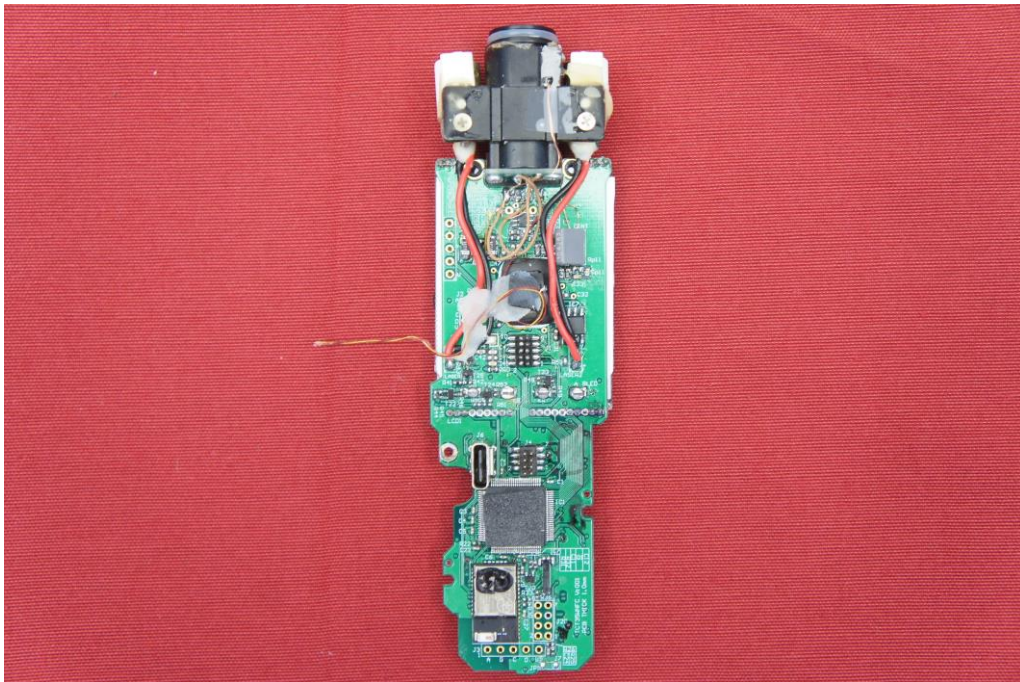
Picture 8: Host test sample cover – top side



Picture 9: Host test sample cover – bottom side



Picture 10: Main pcb host test sample – top side



Picture 11: Main pcb host test sample – bottom side