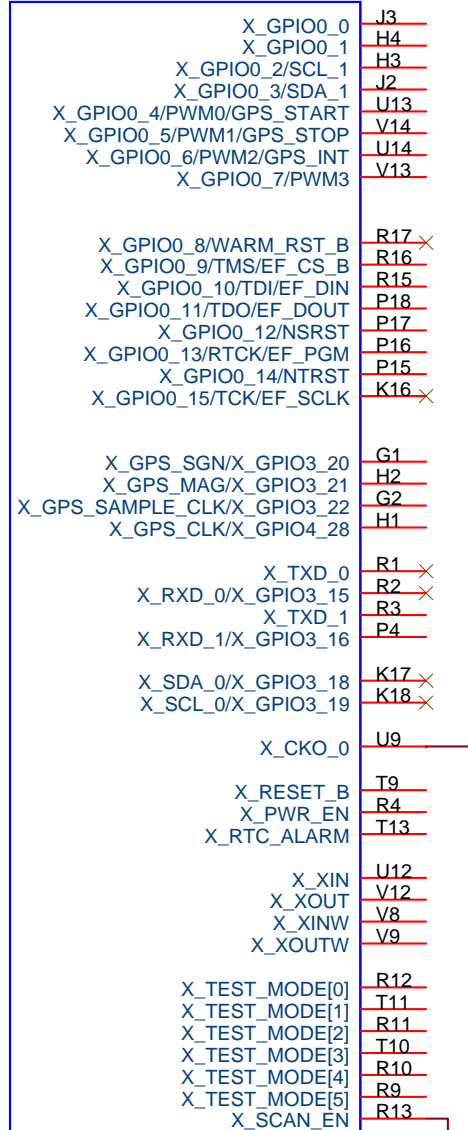
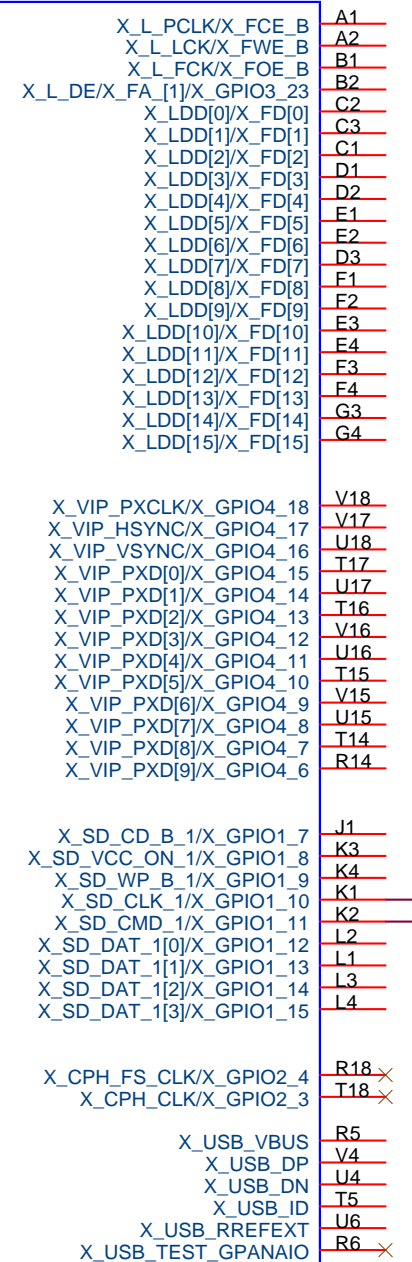


U1A AT8401



U1B AT8401



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Title

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Size

Document Number

CPU(1)

Rev

V01

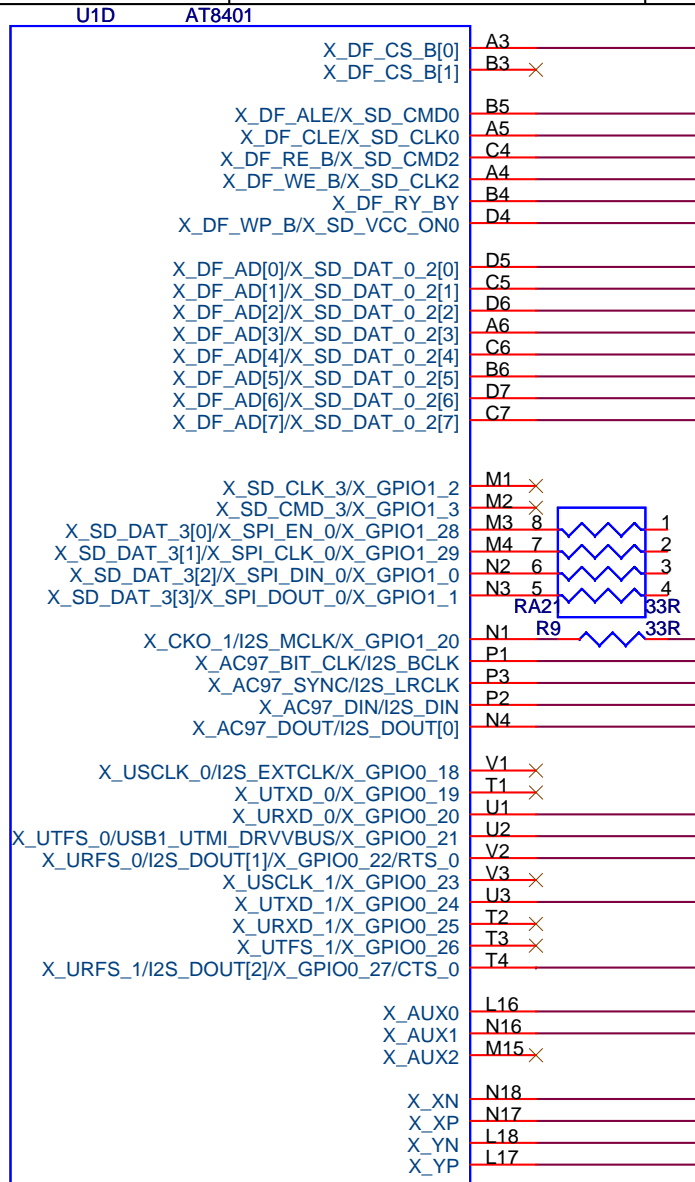
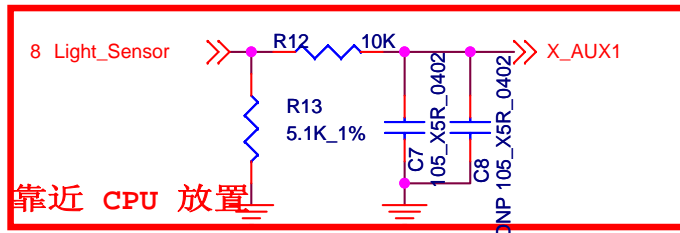
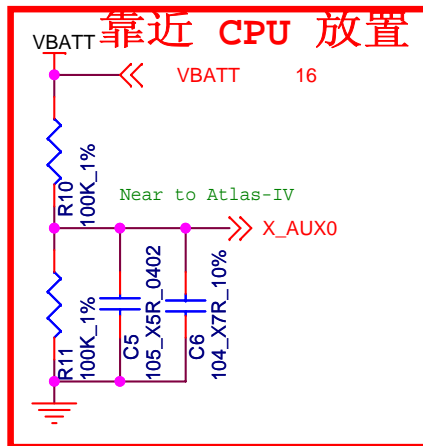
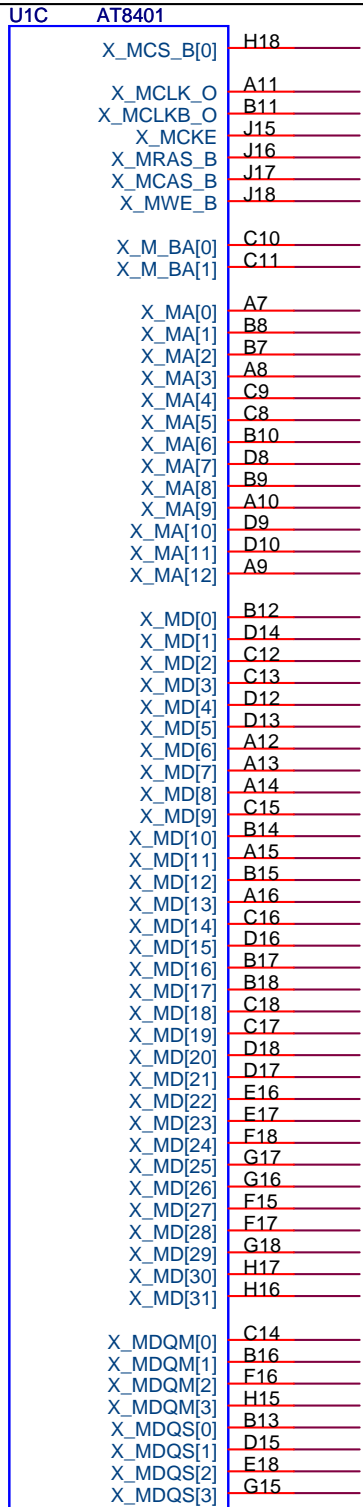
Date:

Friday, December 04, 2009

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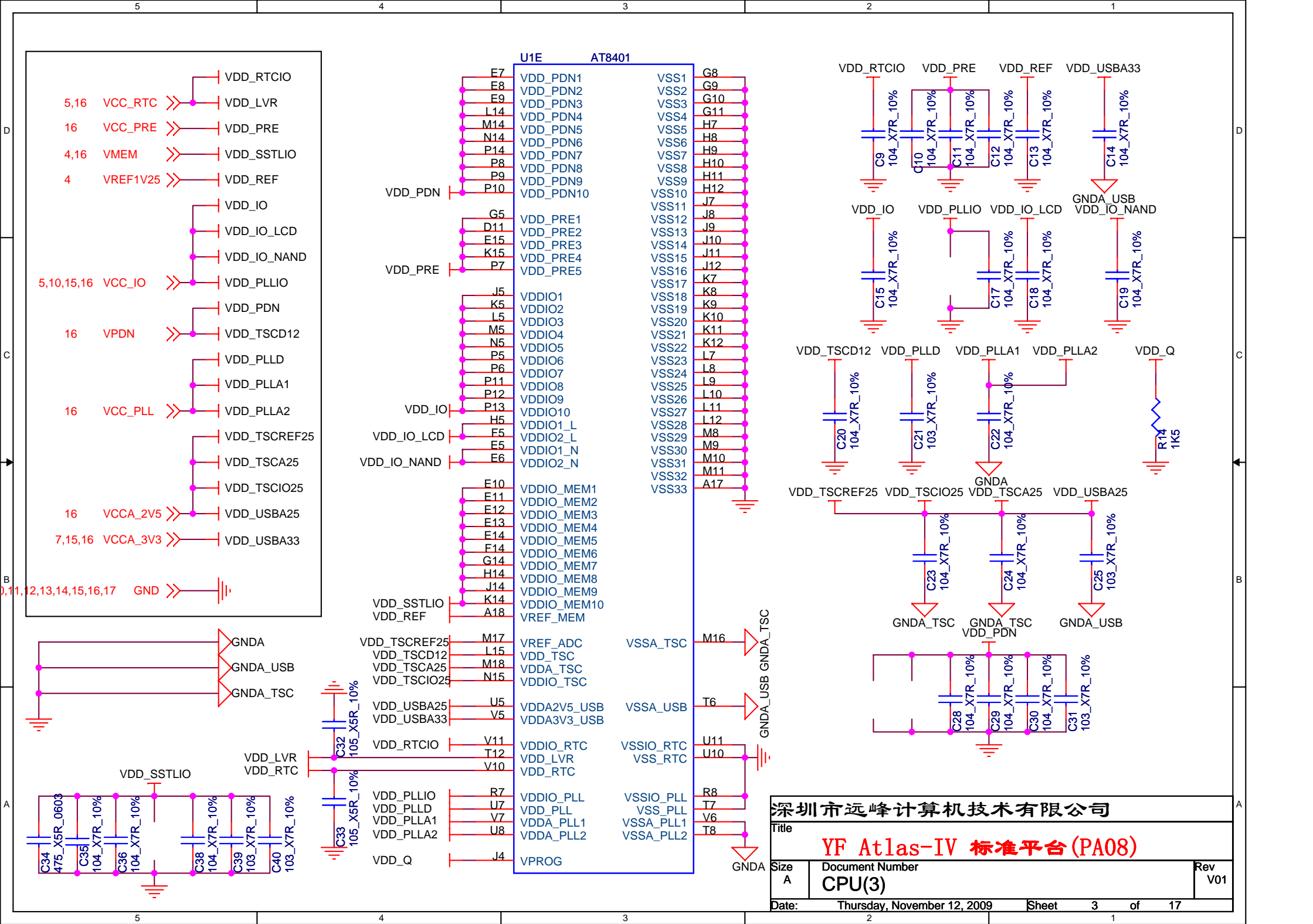
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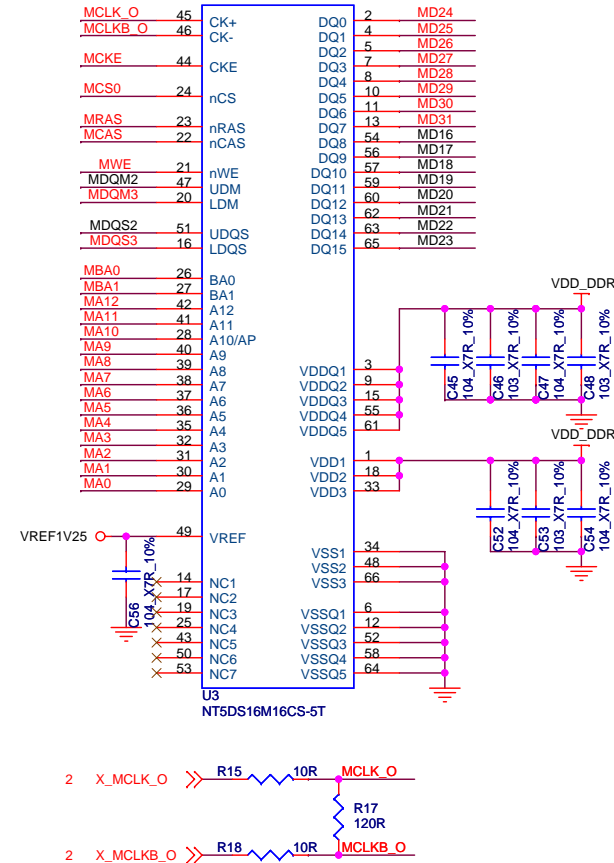
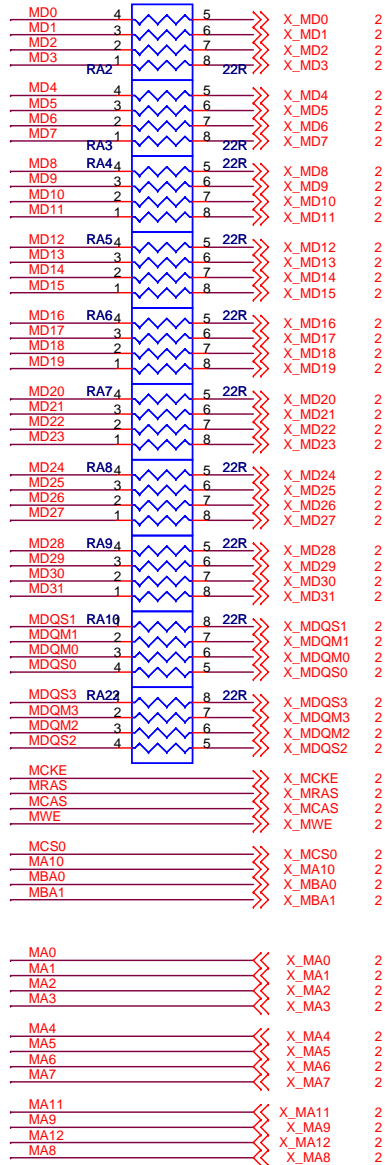
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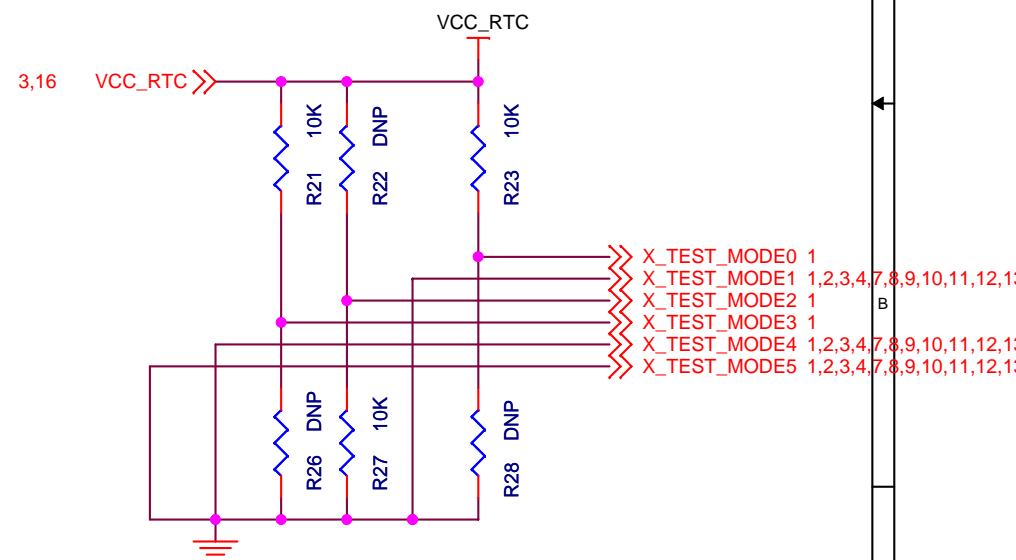
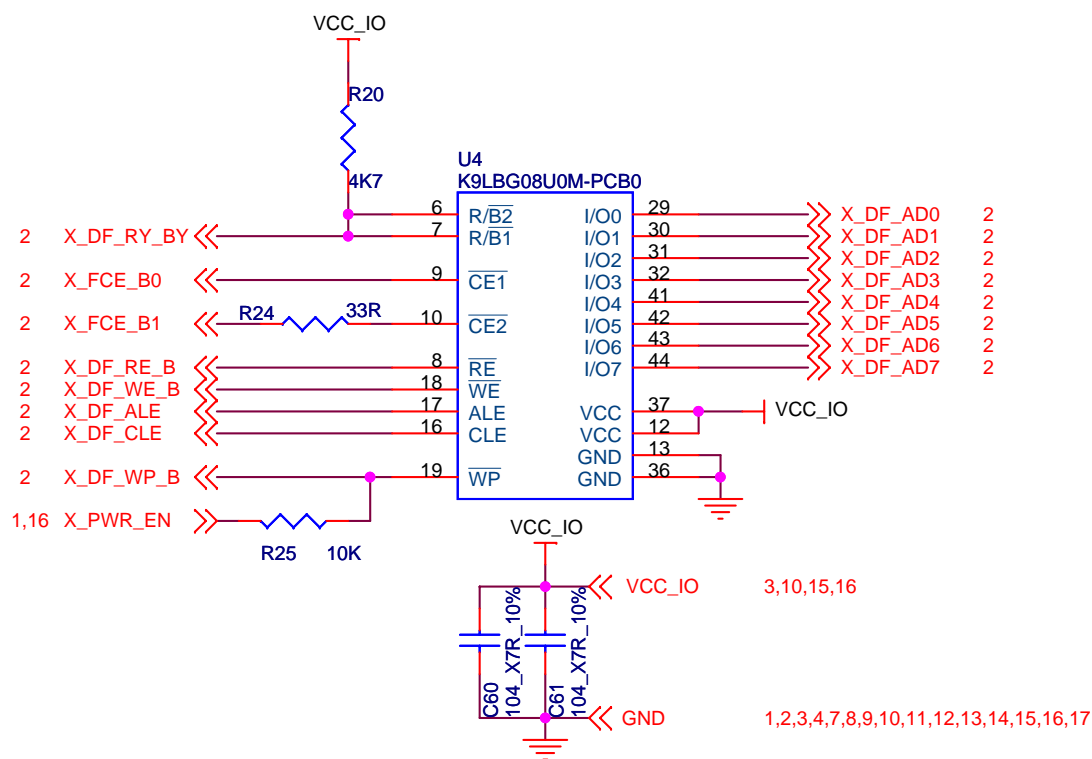
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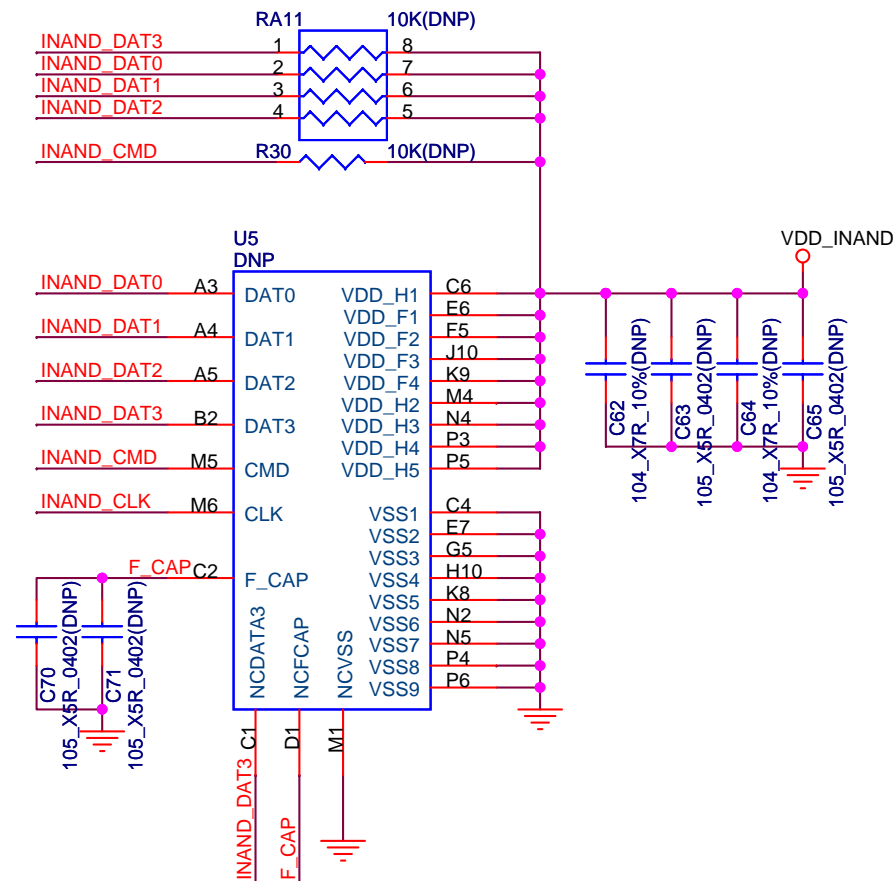
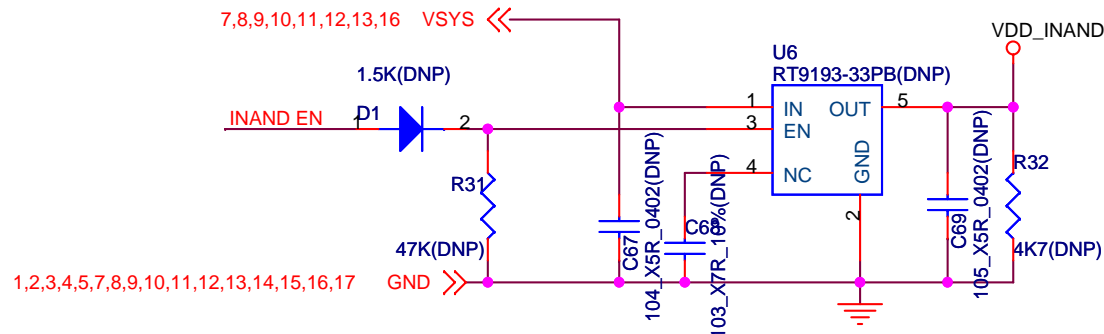
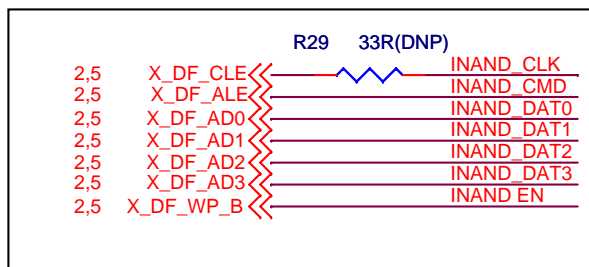
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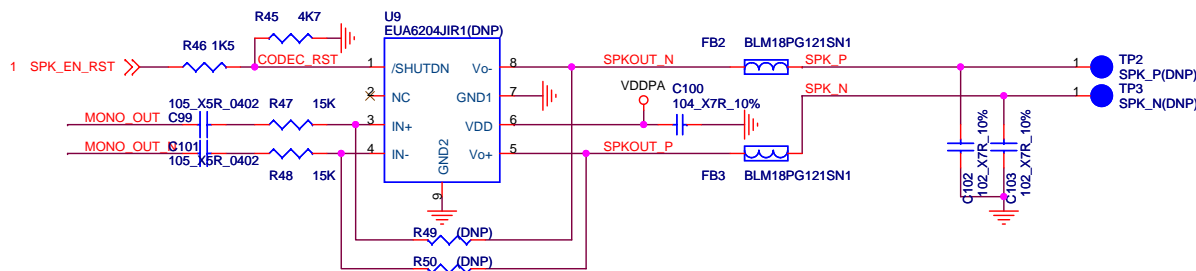
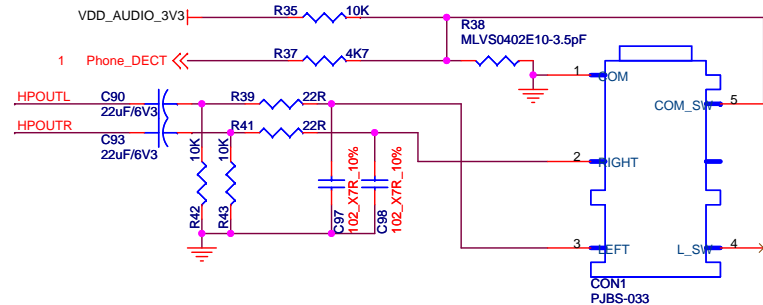
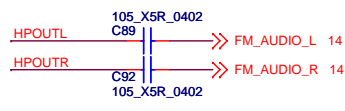
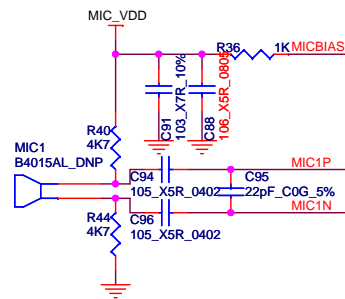
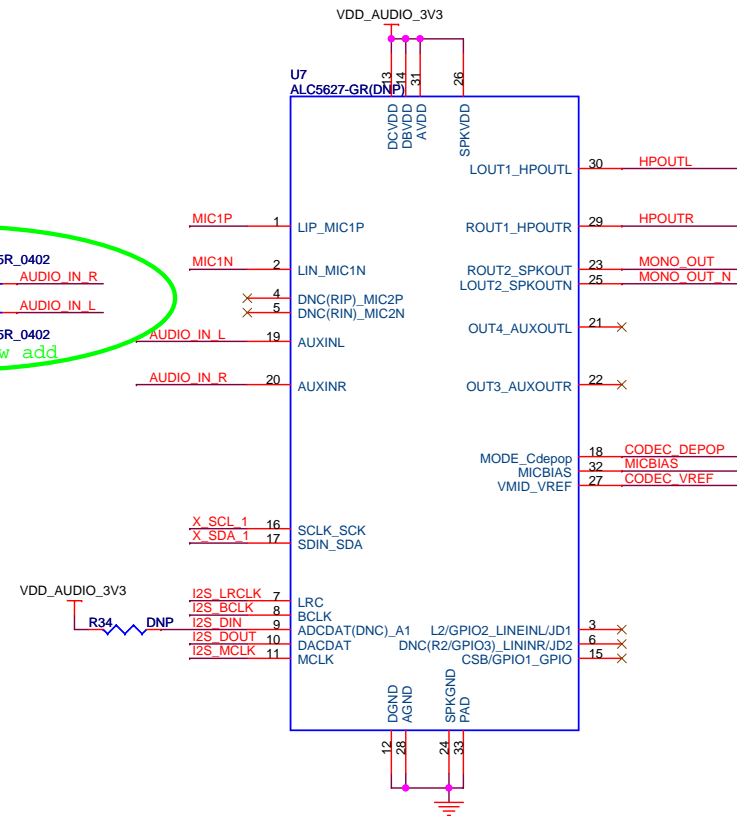
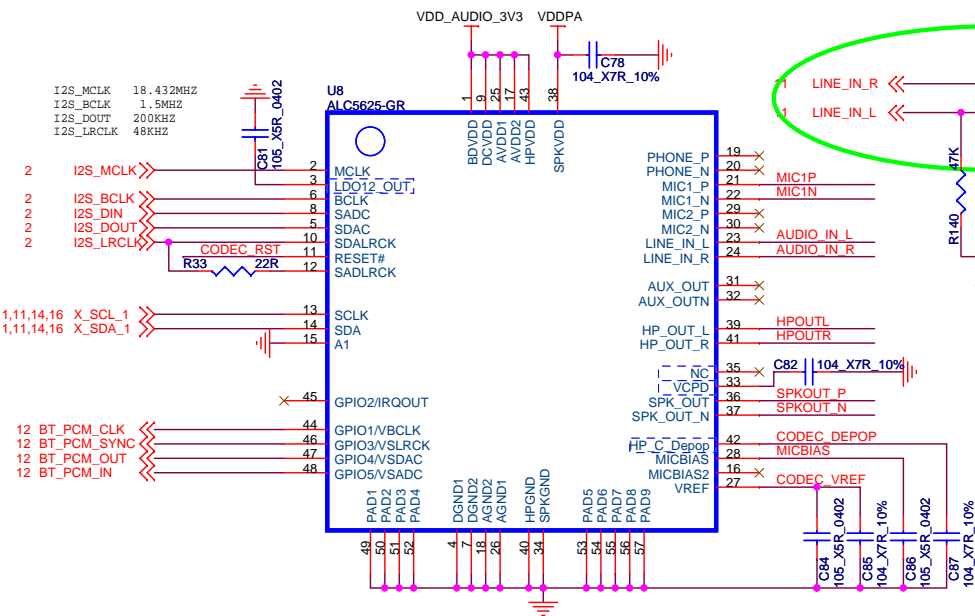
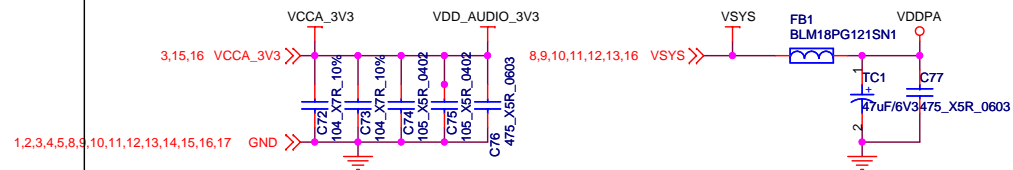


WORKING MODE X_TEST_MODE[5:4]	SUB MODE X_TEST_MODE[3:2]	FEATURE1 X_TEST_MODE[1]	FEATURE0 X_TEST_MODE[0]
2'b00: Normal 2'b01: Normal with ARM JTAG 2'b10: Function ATE	2'b00: Embedded ROM NAND boot (SLC)	1'b0: 8-bit	1'b0: 2 KB page
		1'b1: 16-bit	1'b1: 4 KB page
	2'b01: NAND boot (SLC)	1'b0: 8-bit	1'b0: 512 B page
		1'b1: 16-bit	1'b1: 2 KB page
	2'b10: Embedded ROM NAND boot (MLC)	1'b0: 8-bit	1'b0: 2 KB page
		1'b1: 16-bit	1'b1: 4 KB page
2'b11: Embedded ROM SD/MMC boot	-	-	
2'b11: Test/eFuse	2'b00: Pin scan	1'b0: Boundary scan	-
		1'b1: eFuse (X_RESET_B should be 1'b0)	-





滤波电容优先放置在U8旁边



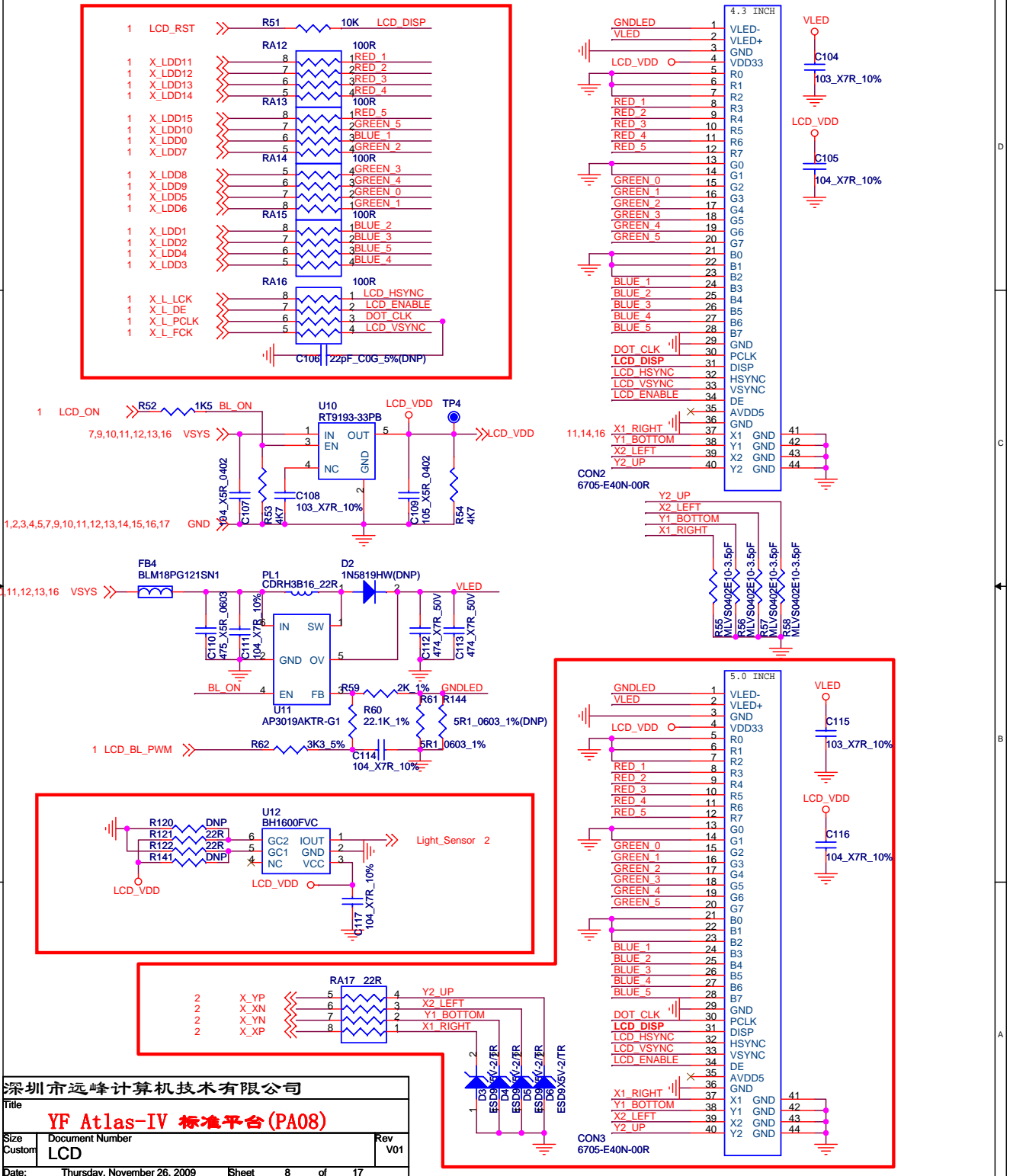
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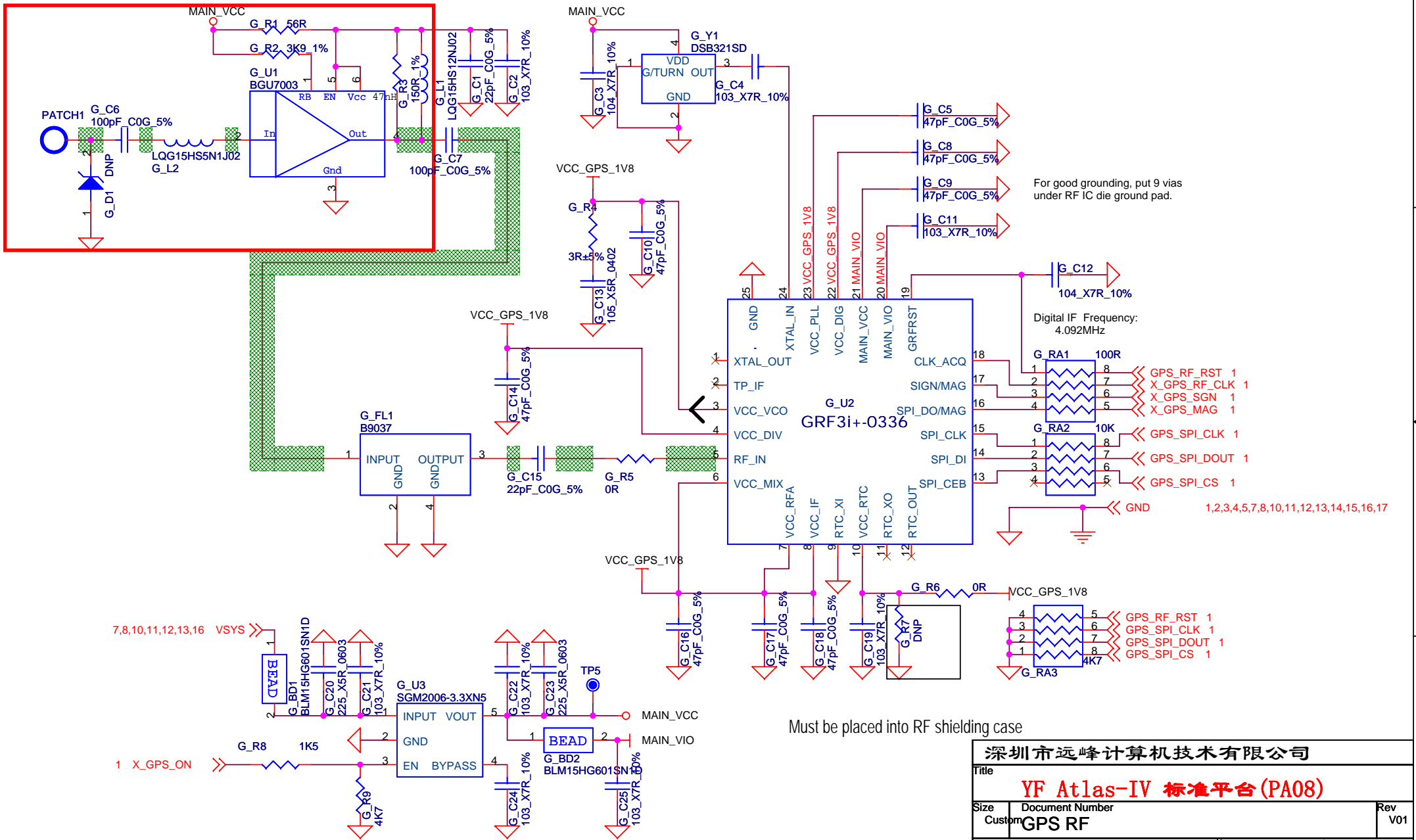
Title YF Atlas-IV 标准平台 (PA08)

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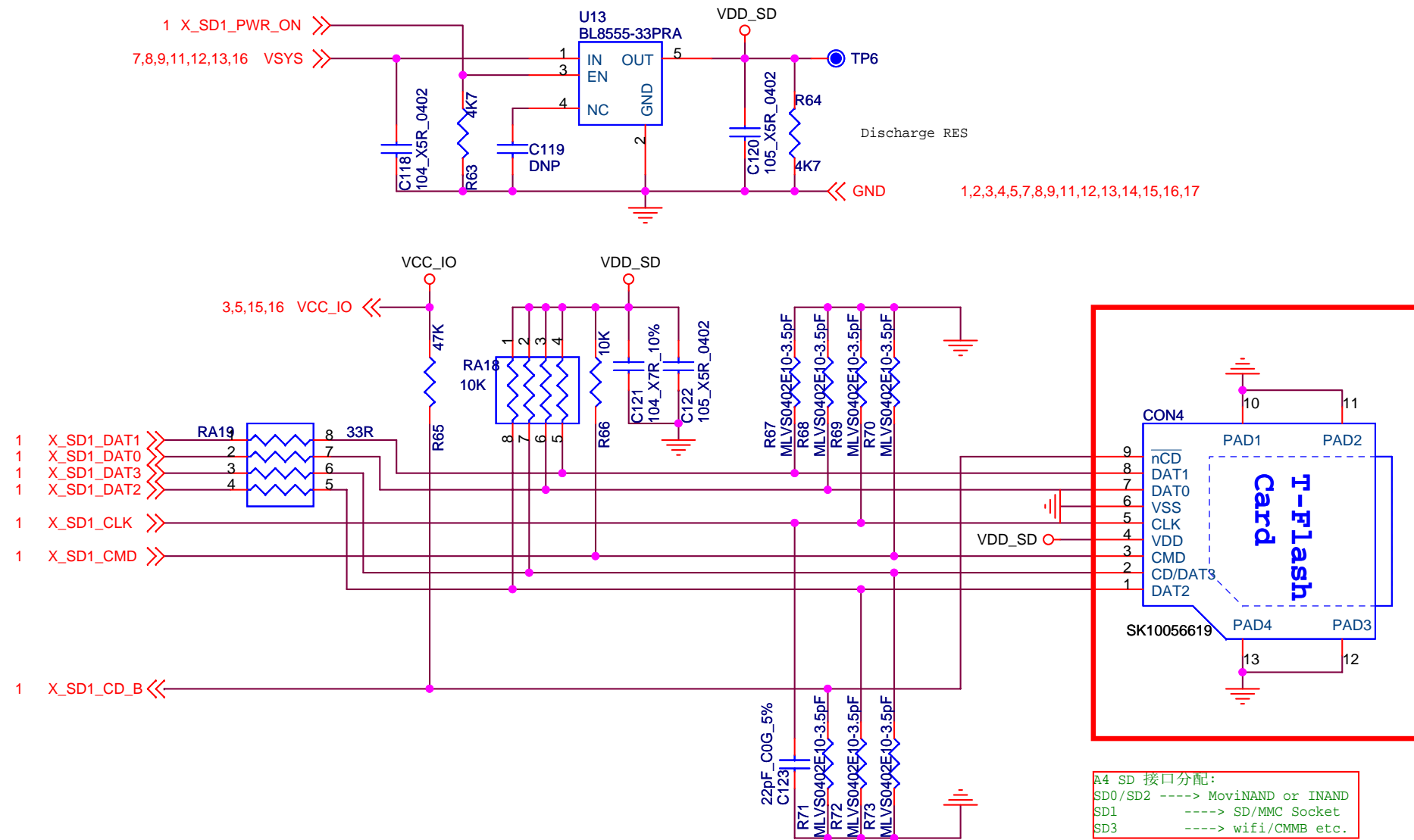
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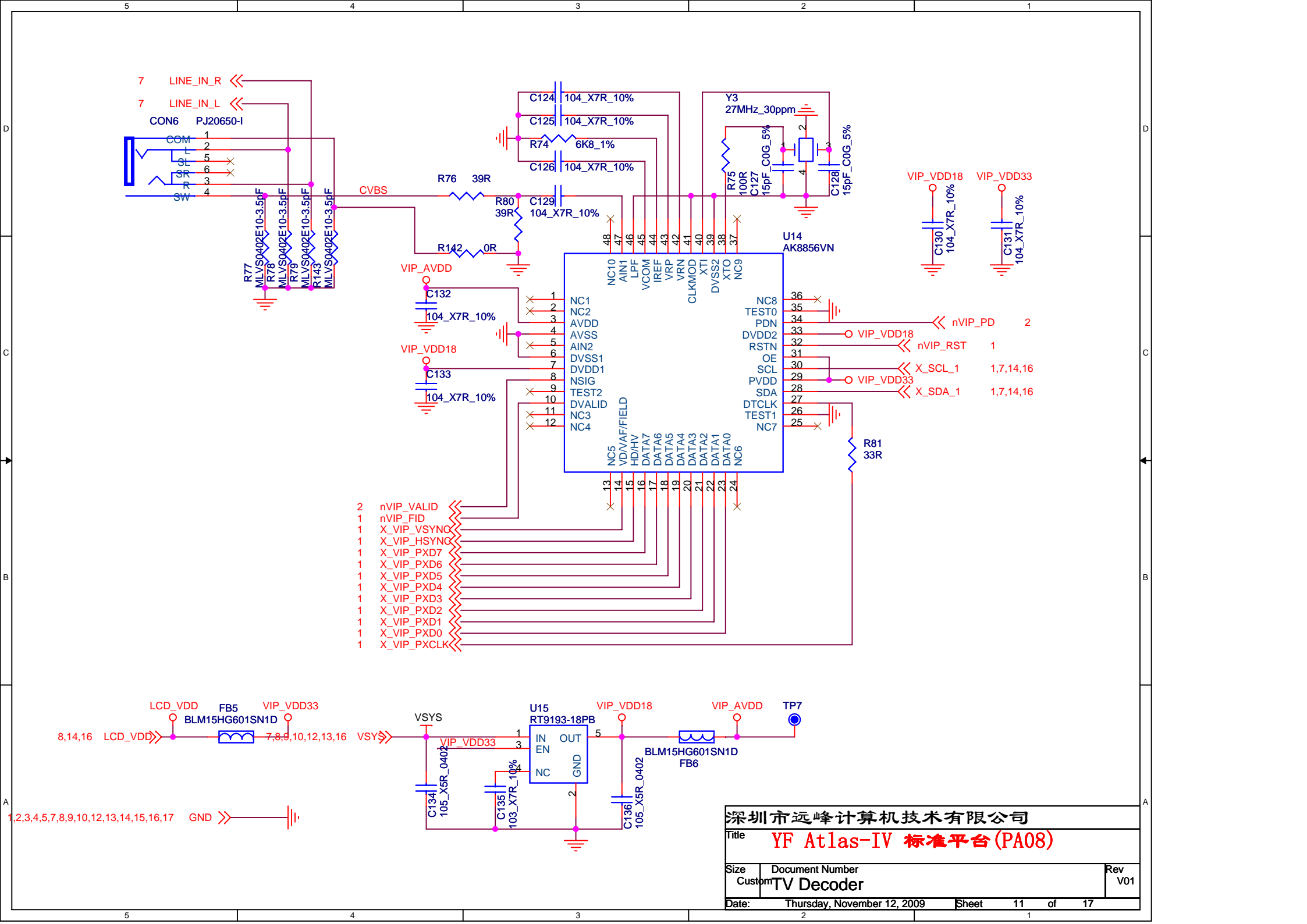
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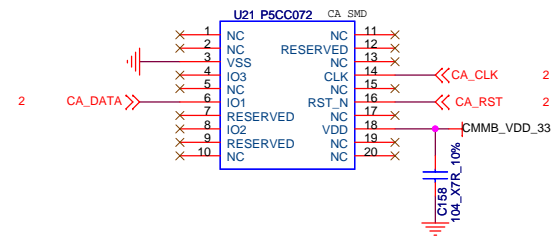
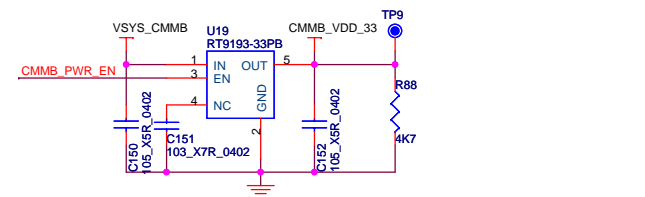
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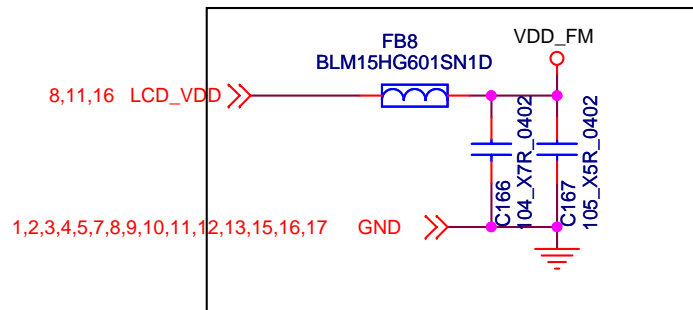
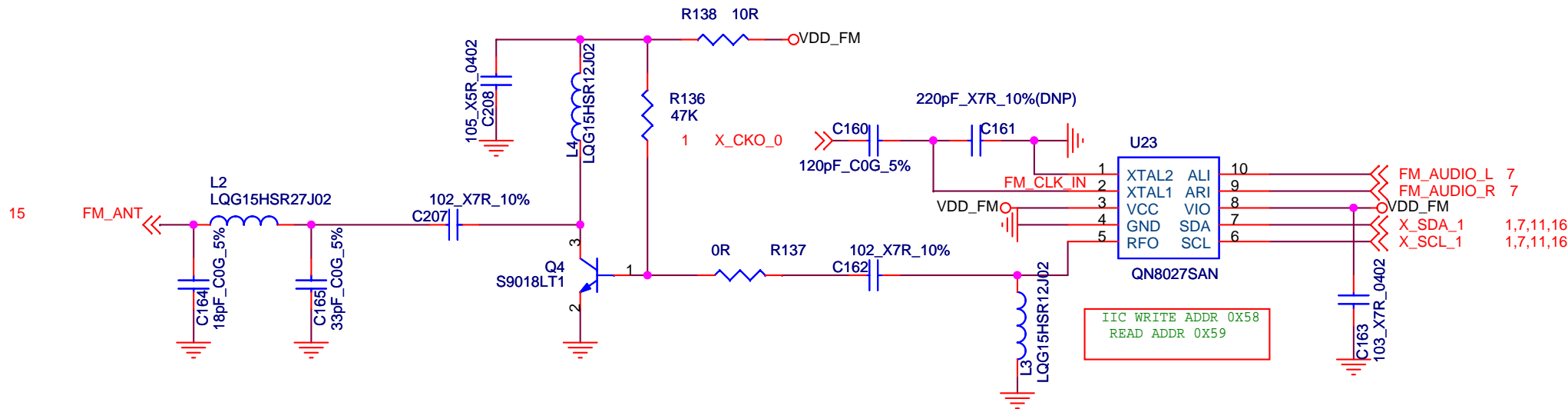
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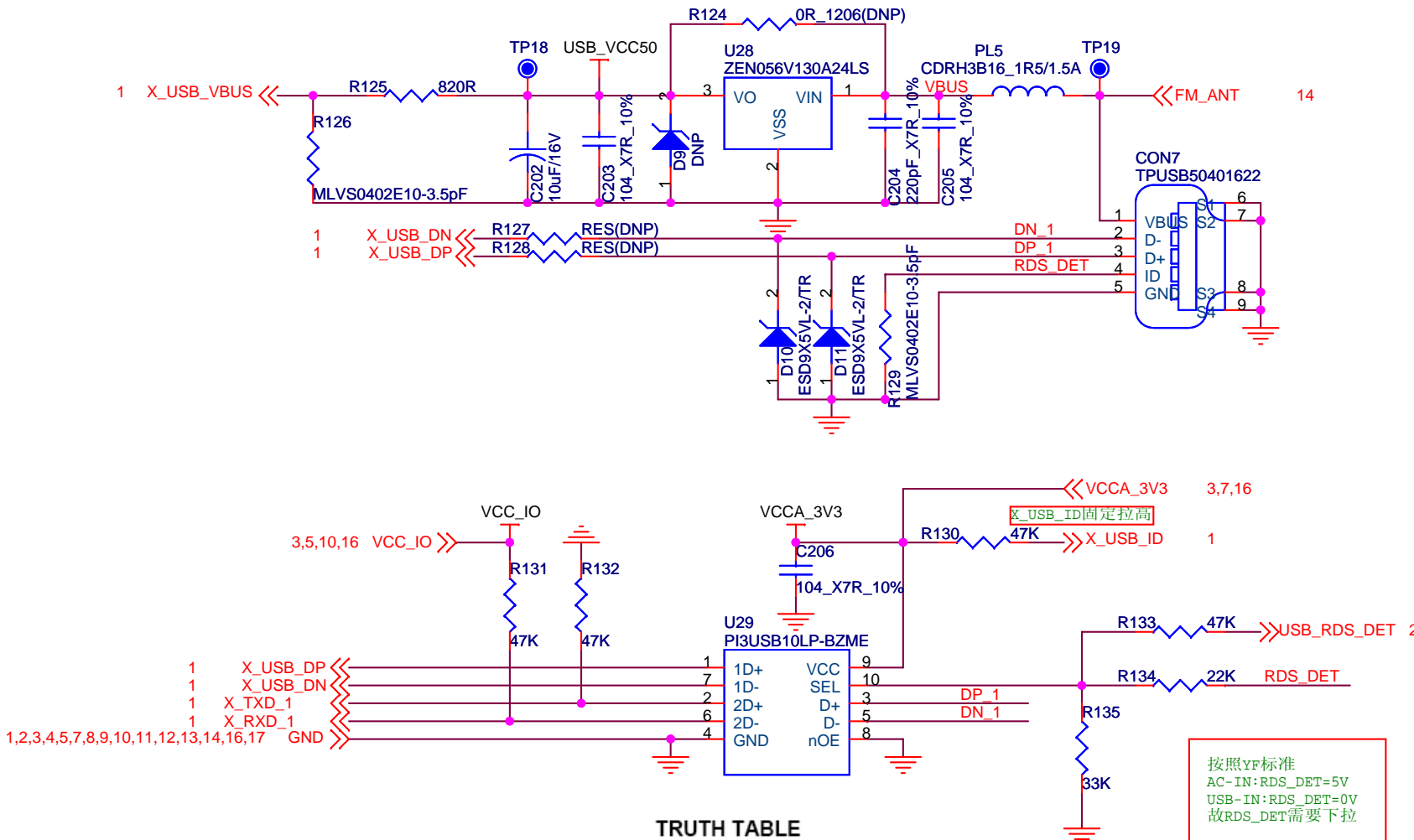
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TRUTH TABLE

S	OE	FUNCTION
X	H	Disconnect
L	L	D = 1D
H	L	D = 2D

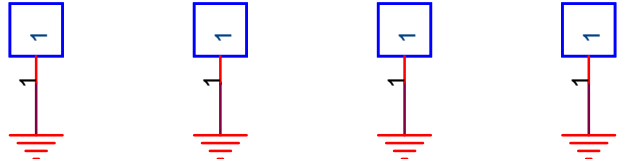
MARK1 MARK2 MARK3 MARK4
MARK(DNP) MARK(DNP) MARK(DNP) MARK(DNP)



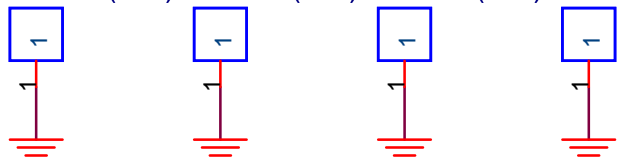
MARK5 MARK6
MARK(DNP) MARK(DNP)



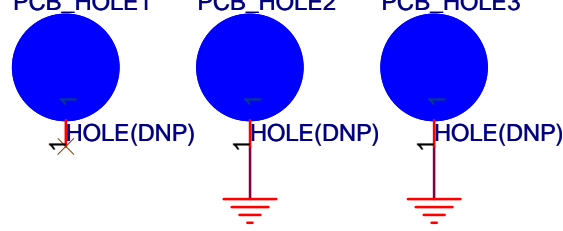
H1 H2 H3 H4
H0.5X1.5(DNP) H0.5X1.5(DNP) H0.5X1.5(DNP) H0.5X1.5(DNP)



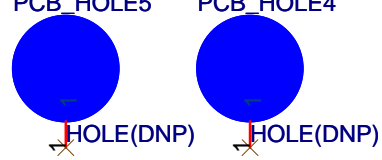
H5 H6 H7 H8
H0.5X1.5(DNP) H0.5X1.5(DNP) H0.5X1.5(DNP) H0.5X1.5(DNP)



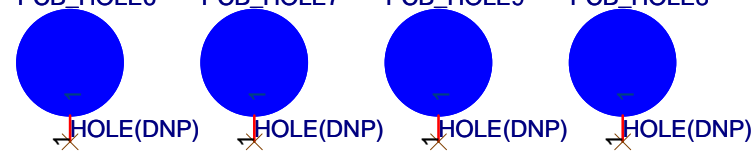
PCB_HOLE1 PCB_HOLE2 PCB_HOLE3
HOLE(DNP) HOLE(DNP) HOLE(DNP)



PCB_HOLE5 PCB_HOLE4
HOLE(DNP) HOLE(DNP)



PCB_HOLE6 PCB_HOLE7 PCB_HOLE9 PCB_HOLE8
HOLE(DNP) HOLE(DNP) HOLE(DNP) HOLE(DNP)



1,2,3,4,5,7,8,9,10,11,12,13,14,15,16 GND >>> GND

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