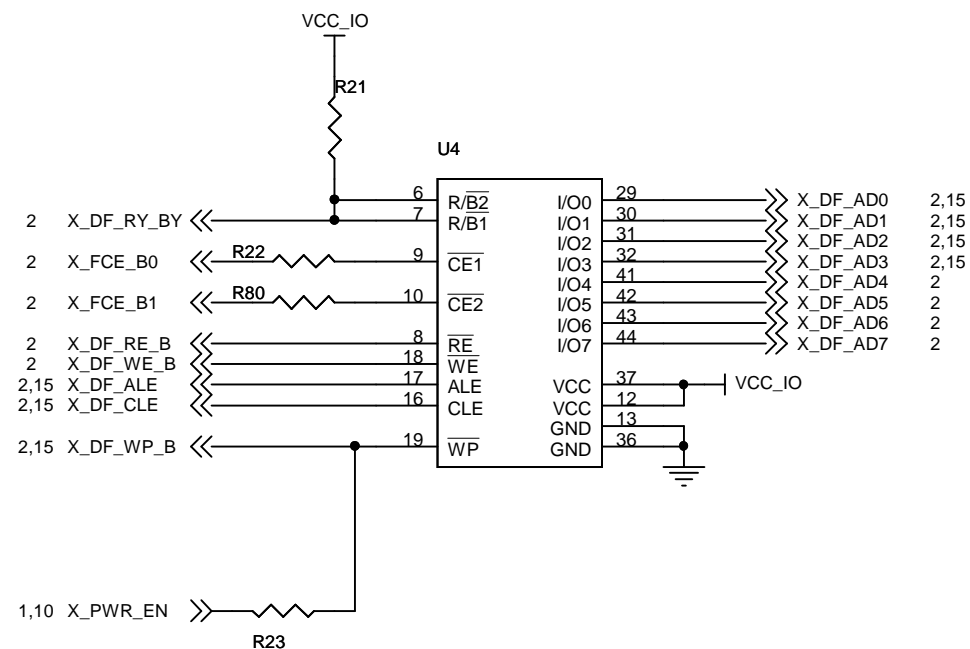


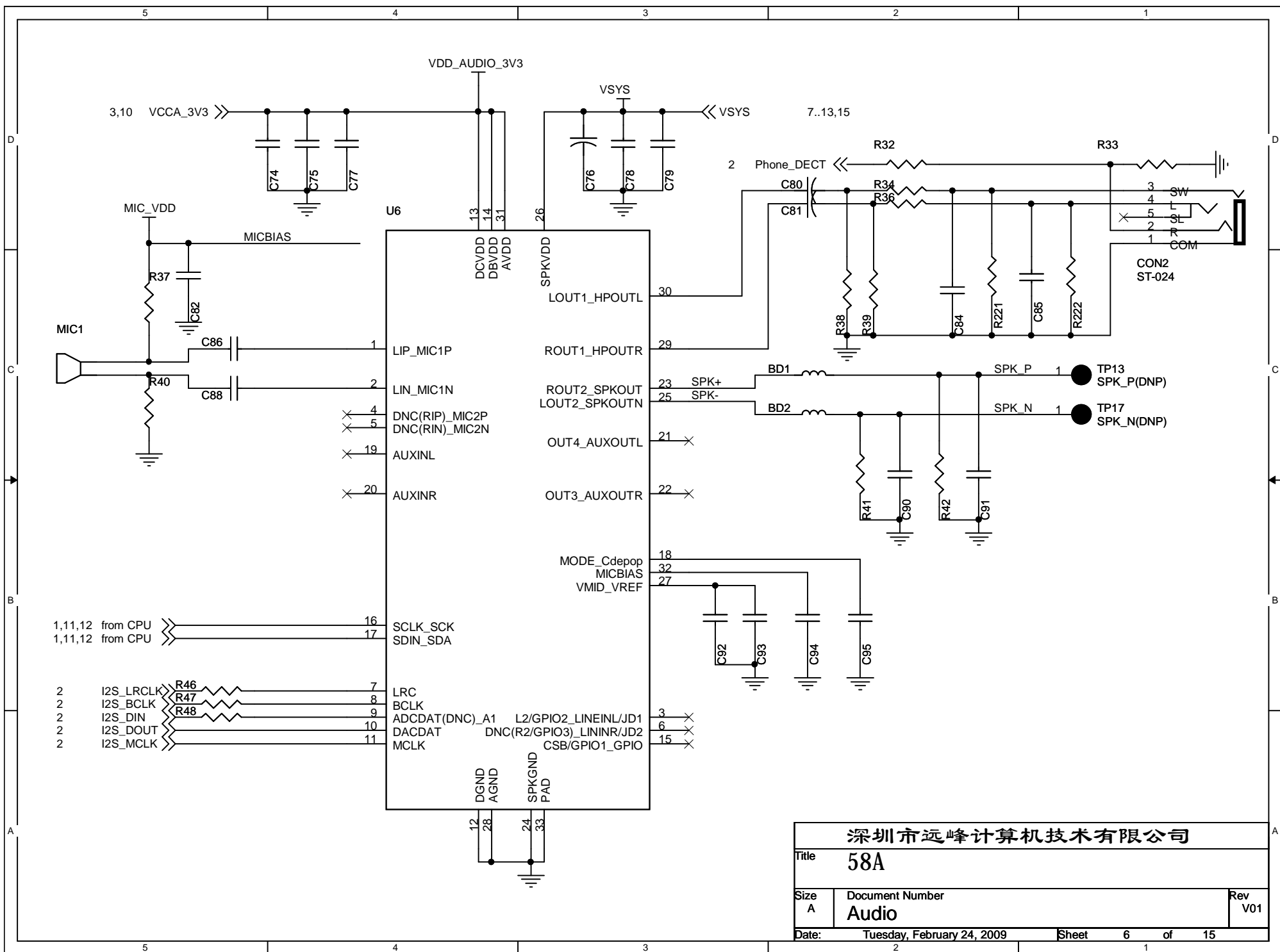
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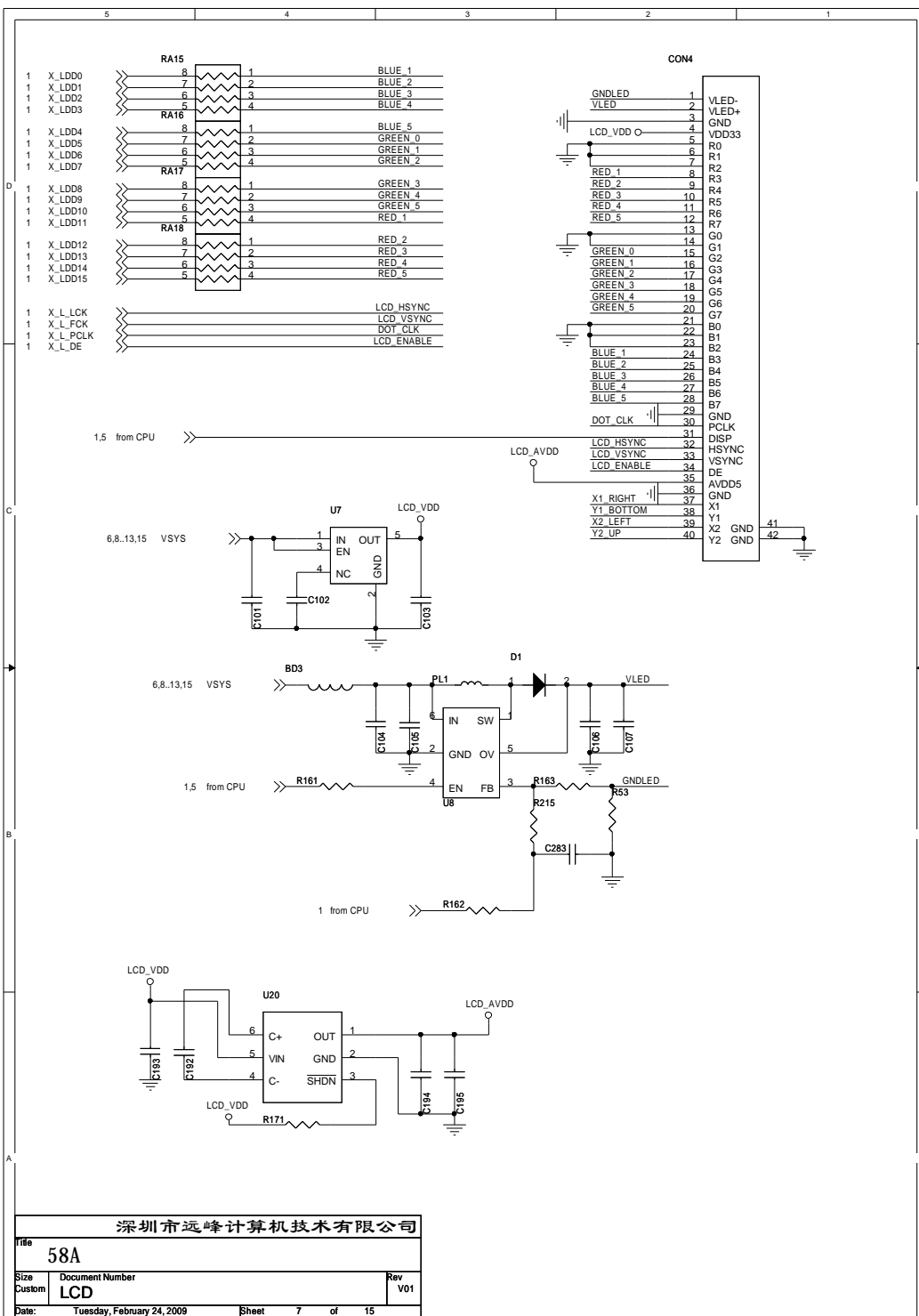


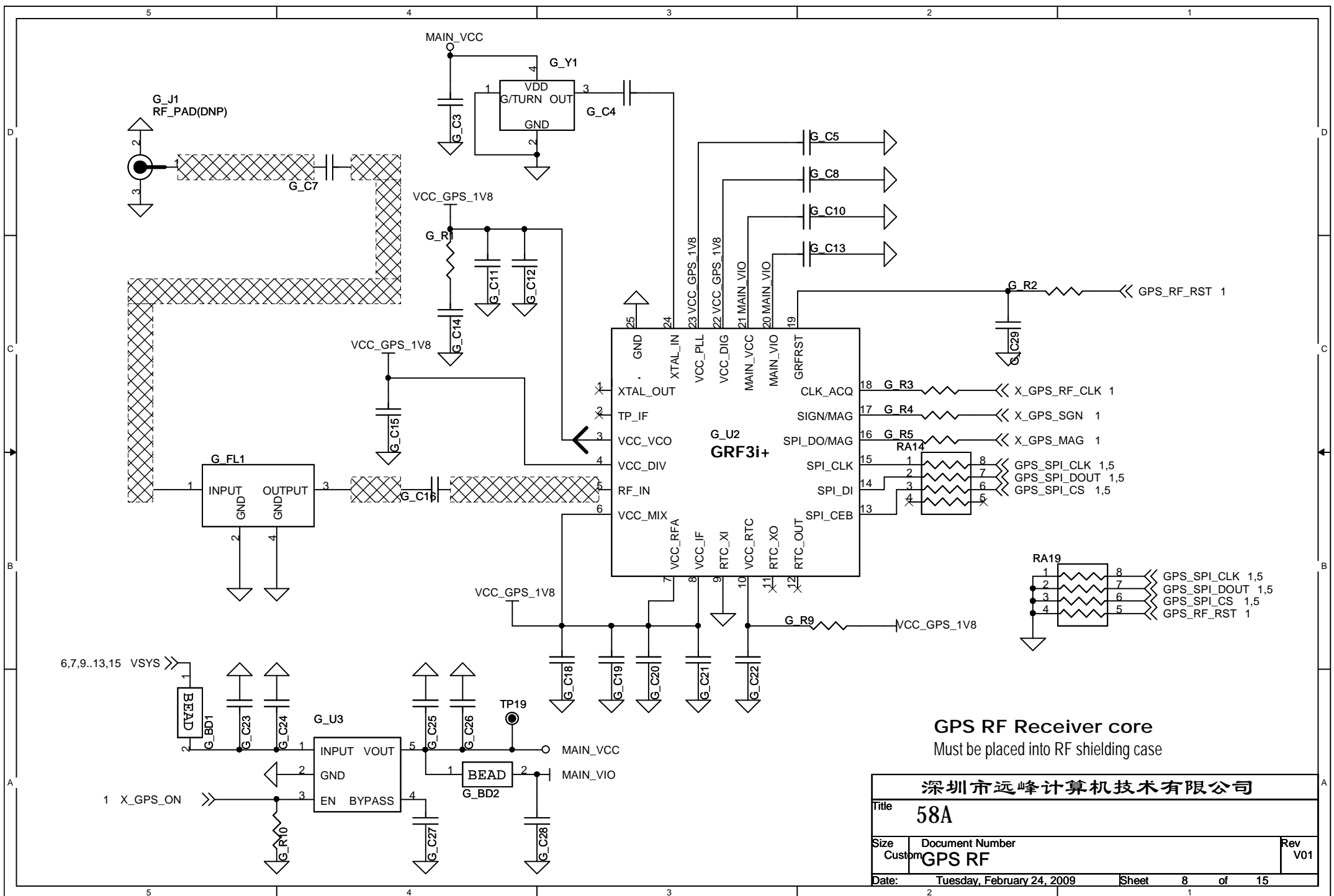


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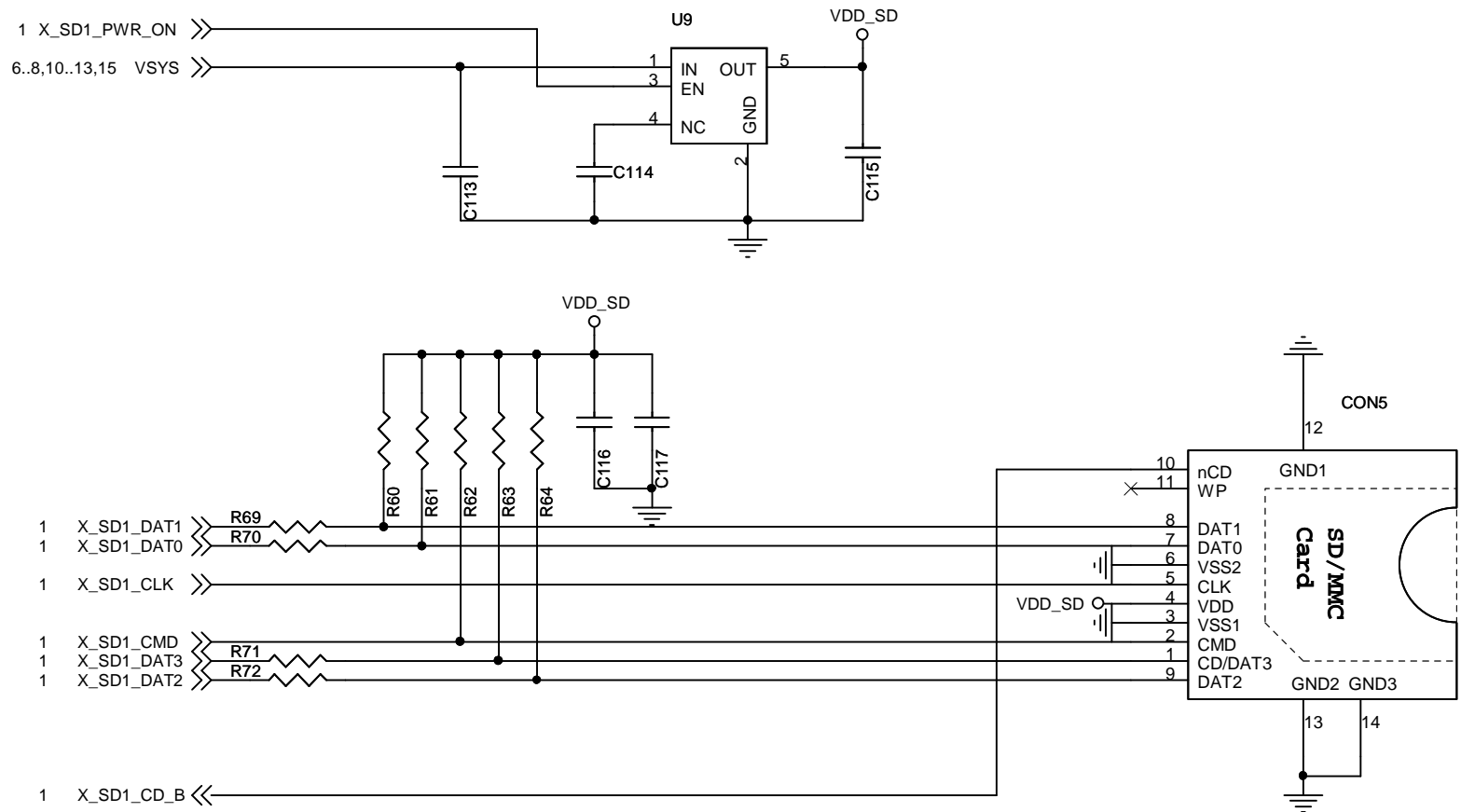


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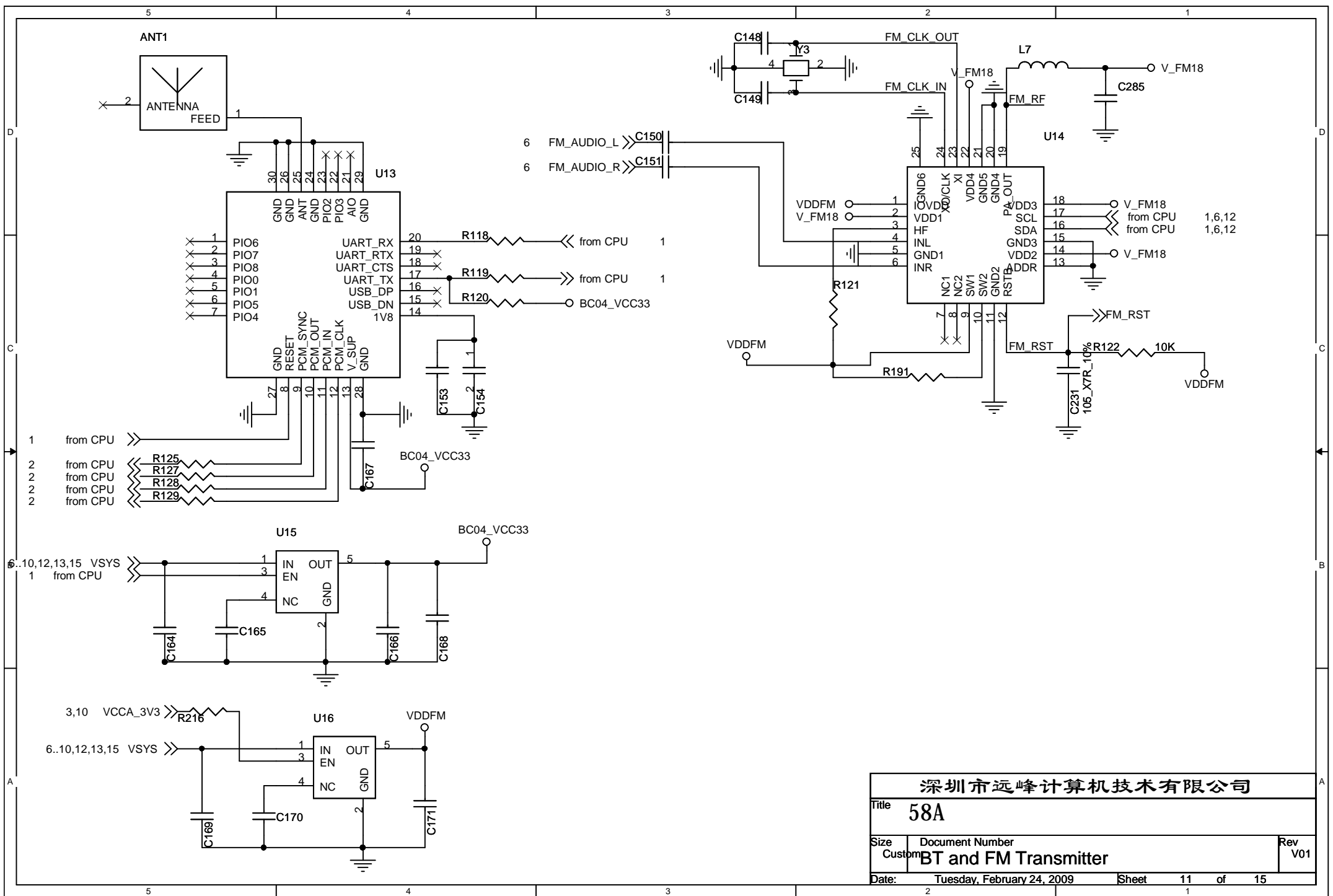




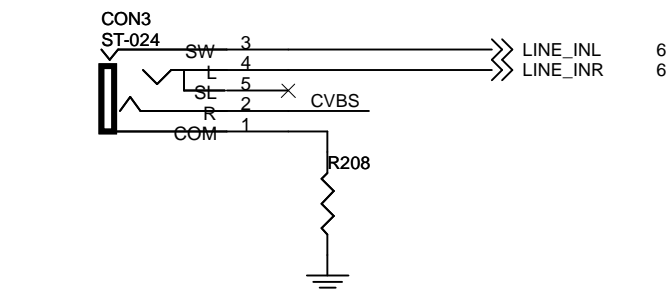


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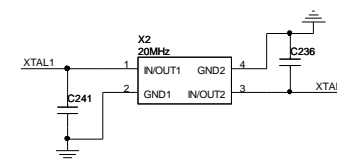
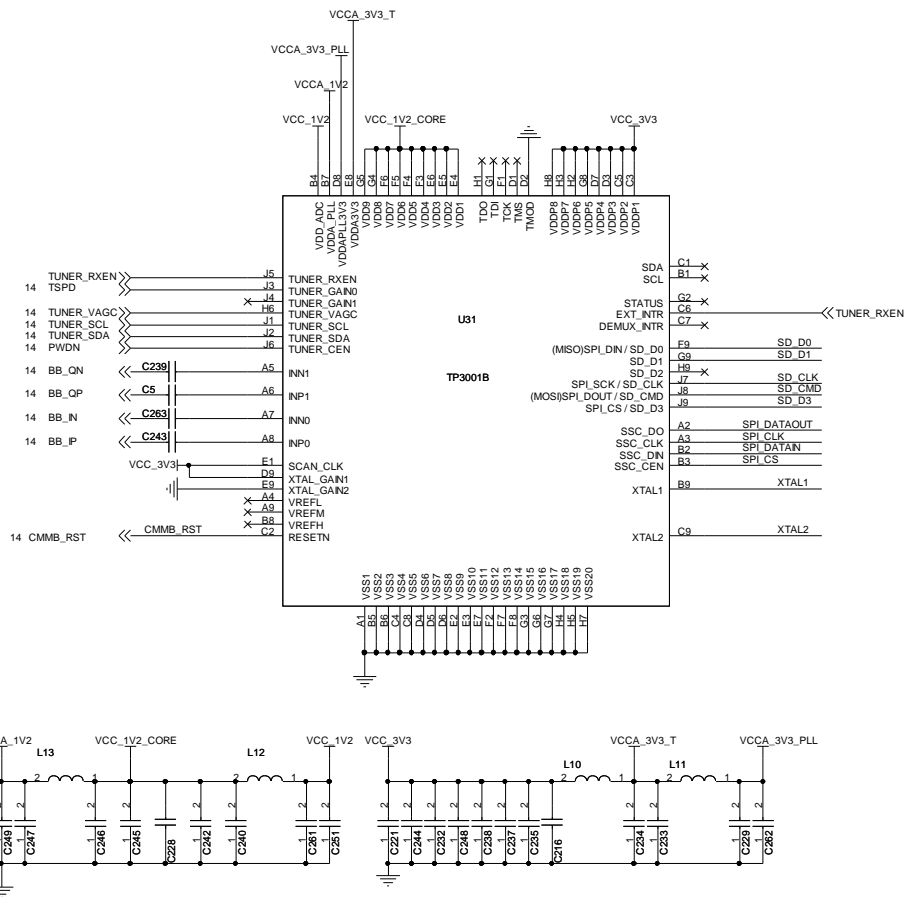
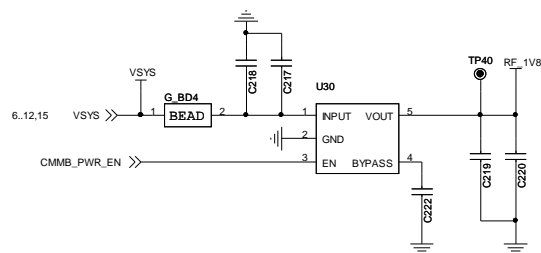
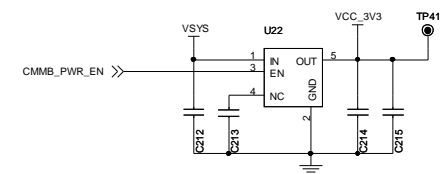
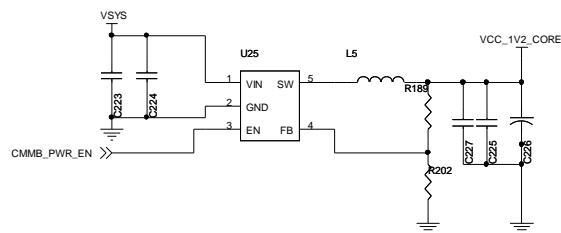


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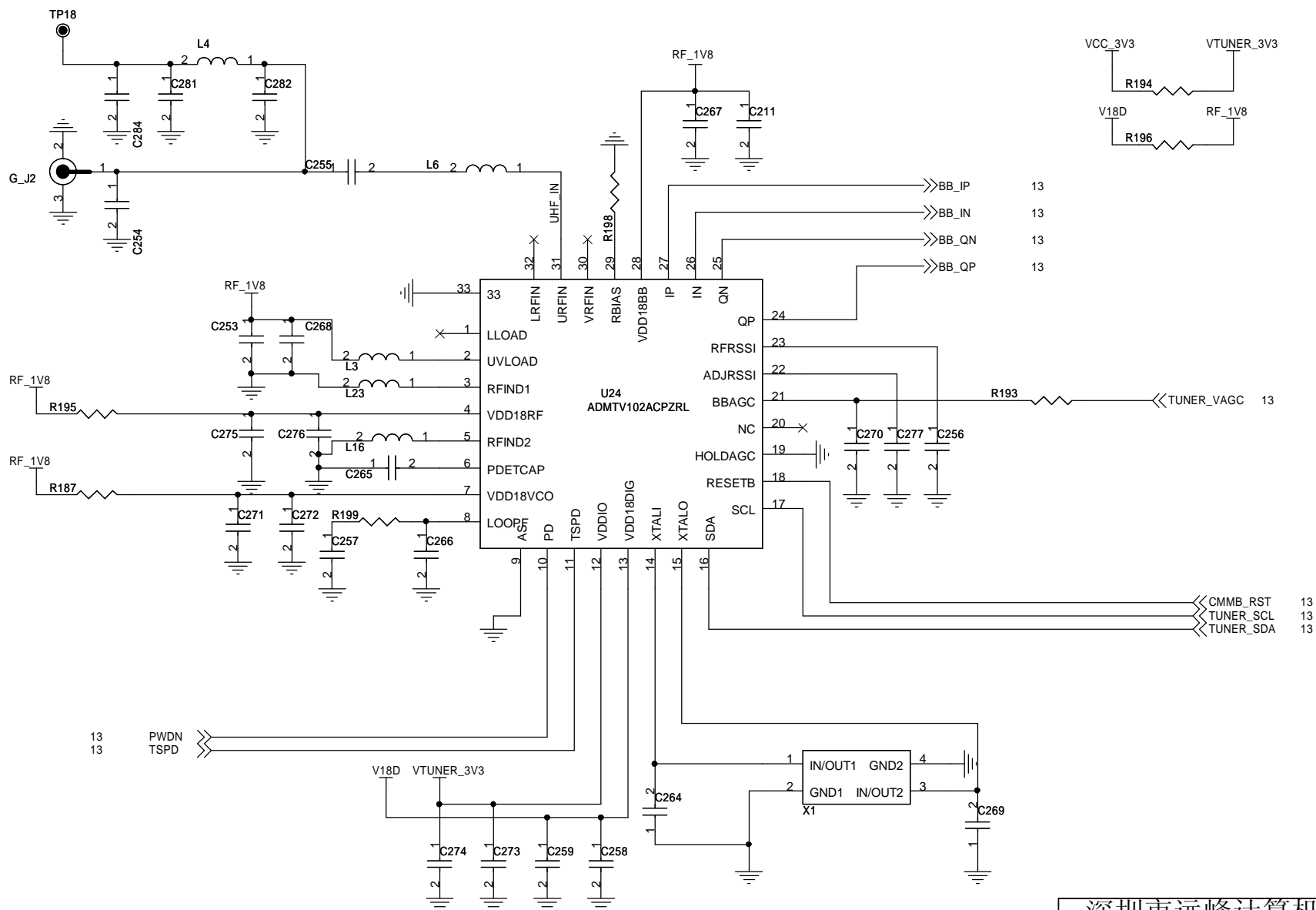


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CLK >>> CMMB\_SPL\_CLK 2  
D1 >>> CMMB\_SPL\_DOUT 2  
DO >>> CMMB\_SPL\_DIN 2



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