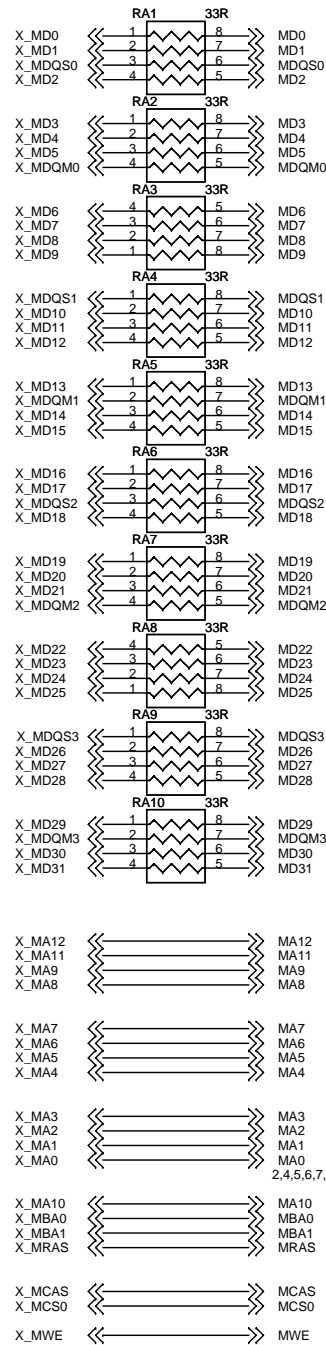
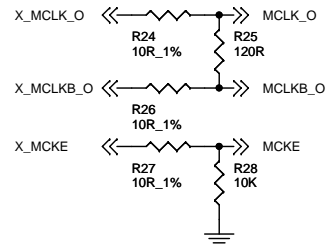
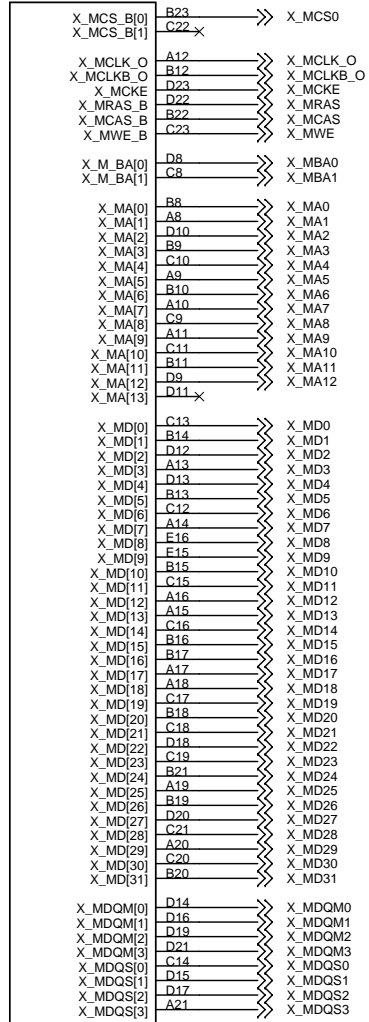
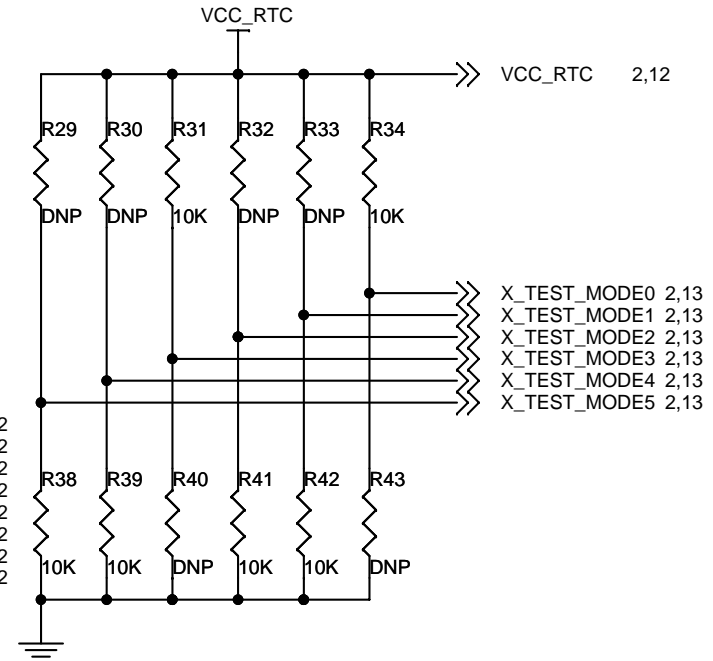
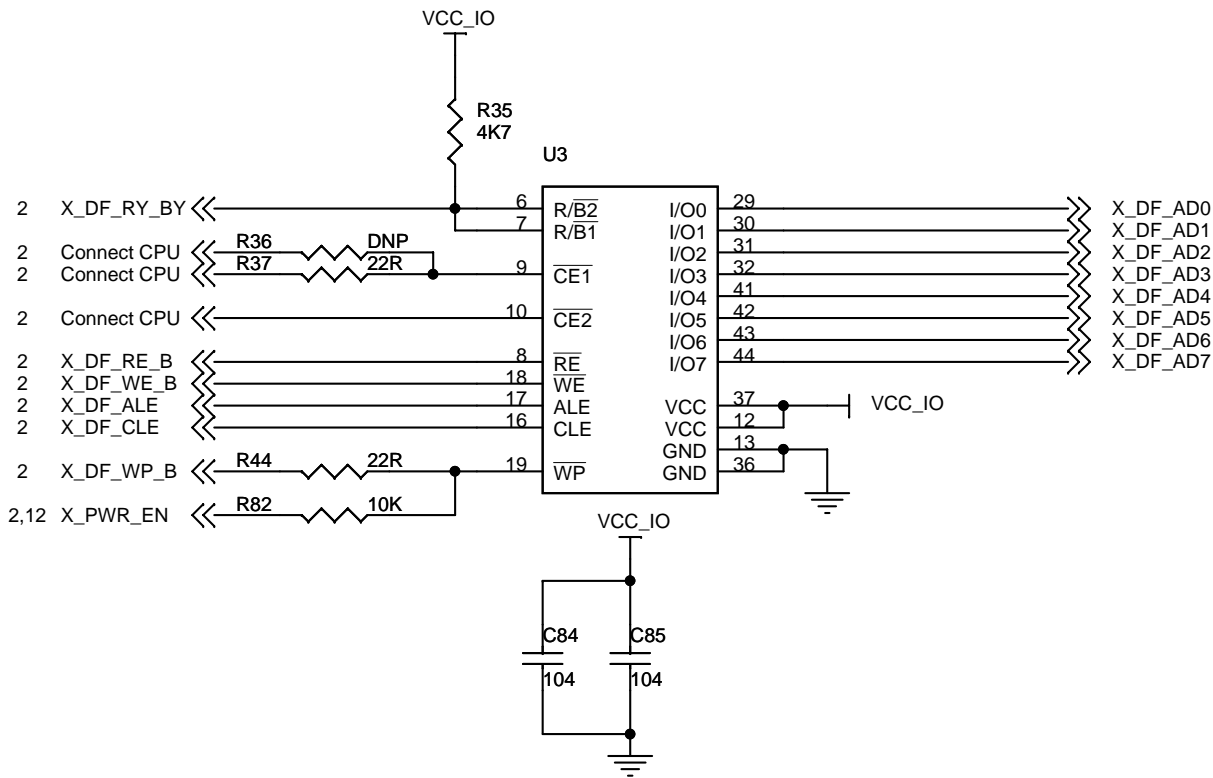




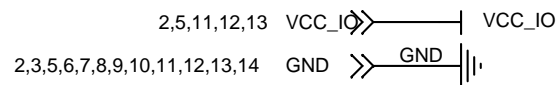
U1D





## Boot Configure for three options

Boot configuration	X_TEST_MODE[5:0]
Boundary Scan	6b110000
SLC Nand Boot 2KB Page 8 Bit	6b000101
MLC Nand Boot 2KB Page 8 Bit(Default)	6b001000
INAND Boot	6b001100
EFuse	6b110010



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A

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FLASH部分电路原理图

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V1

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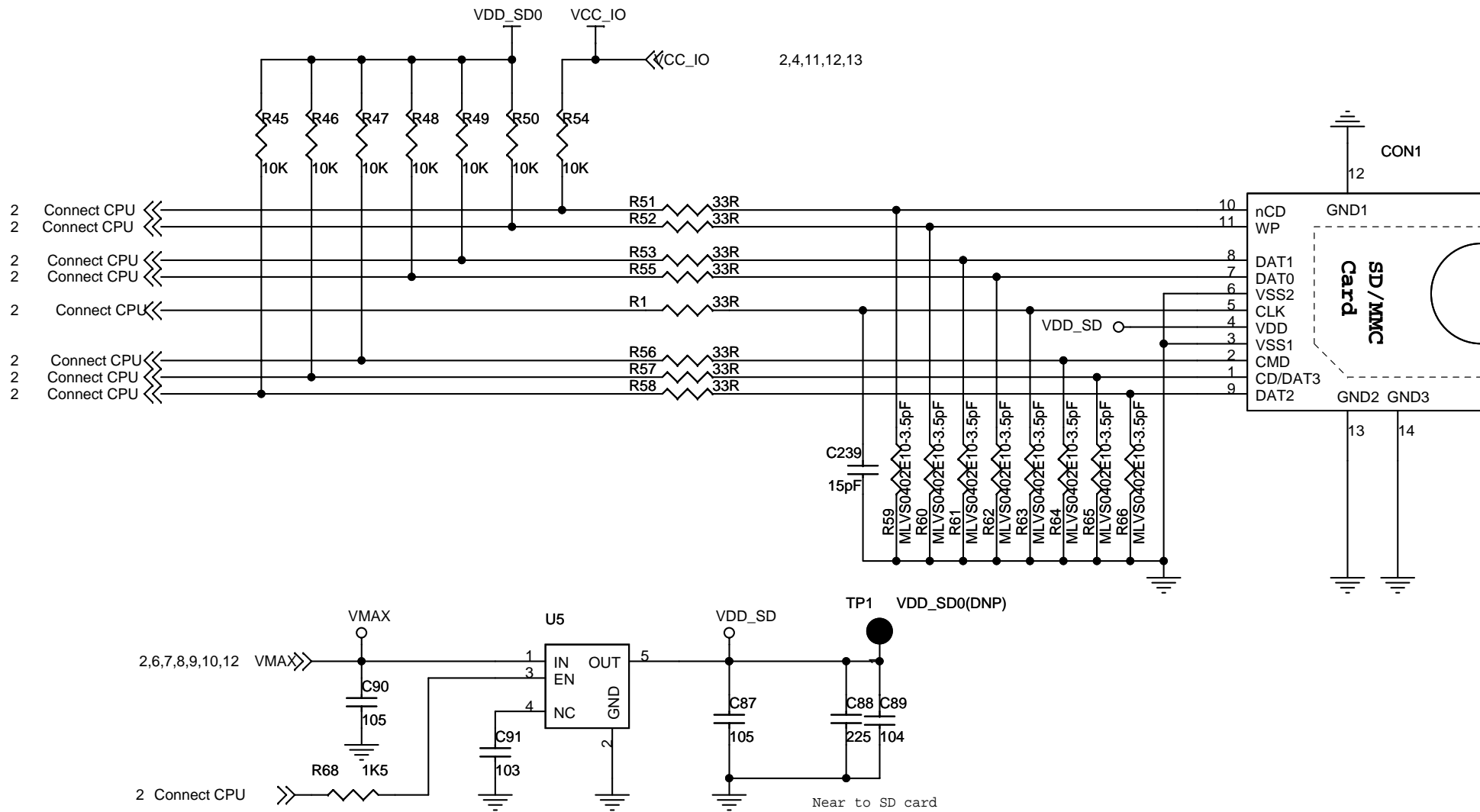
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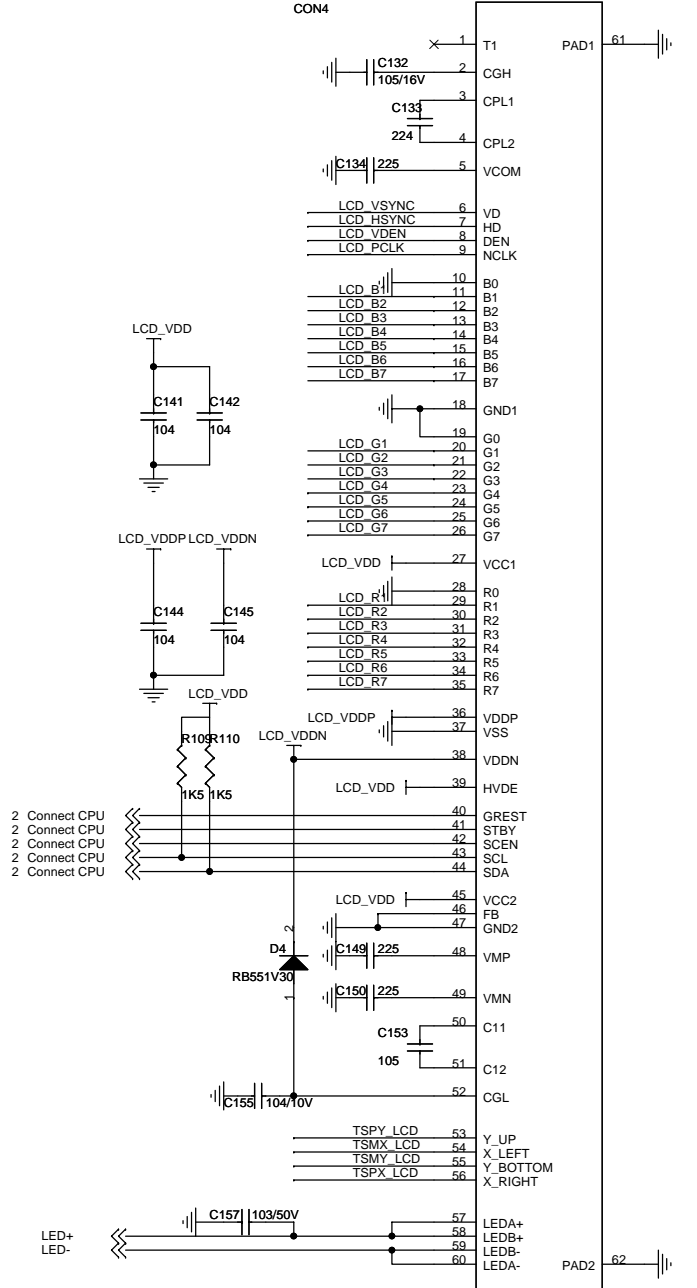
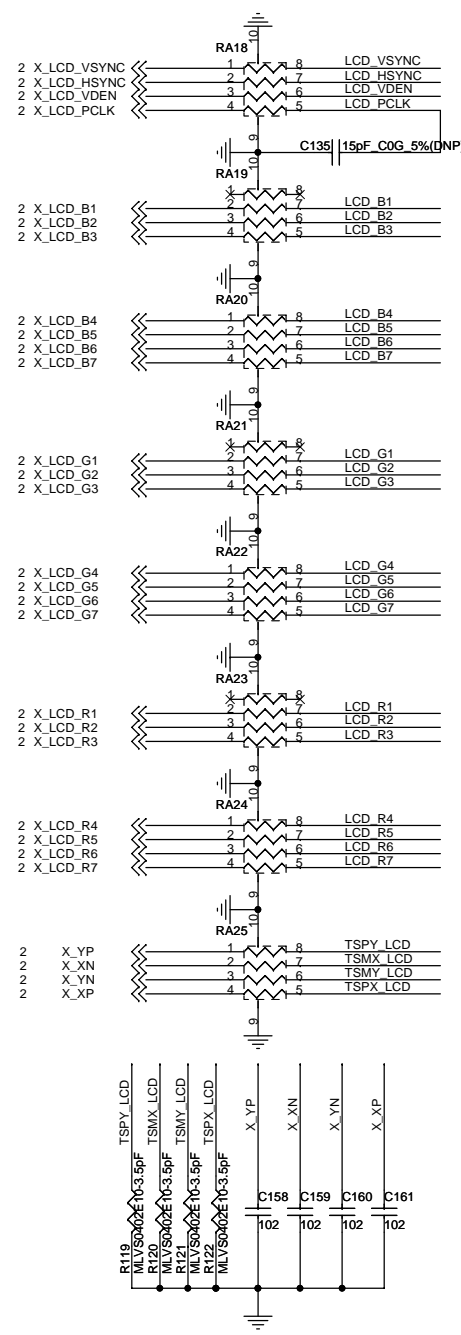
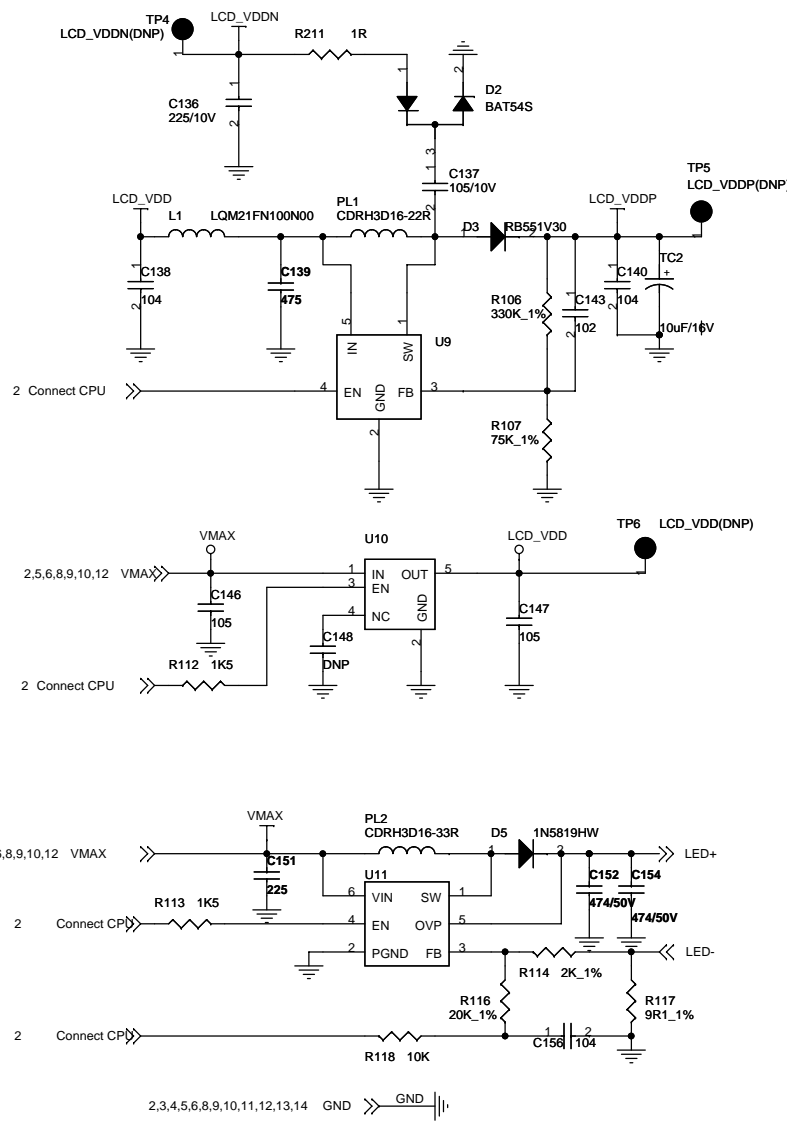
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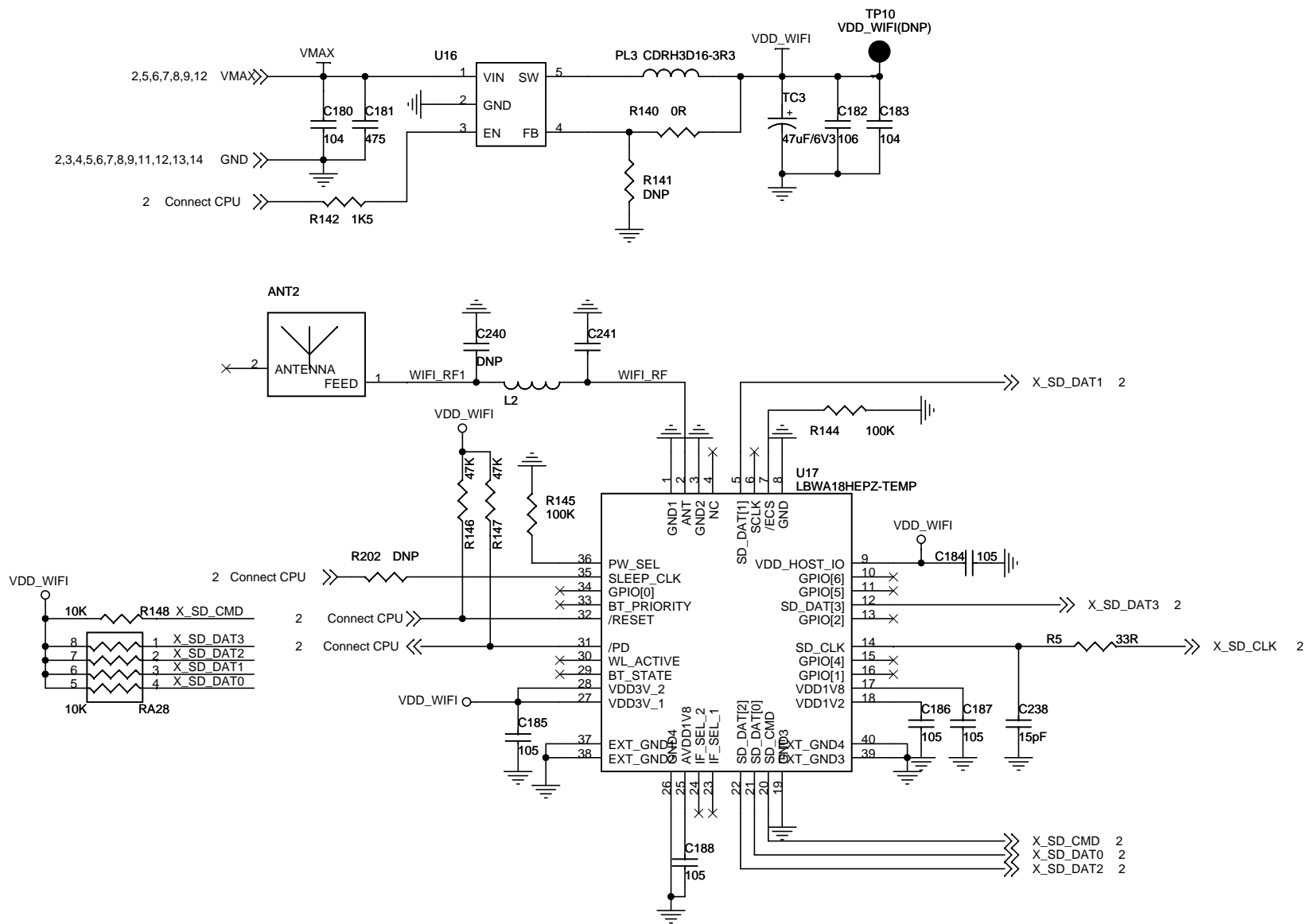












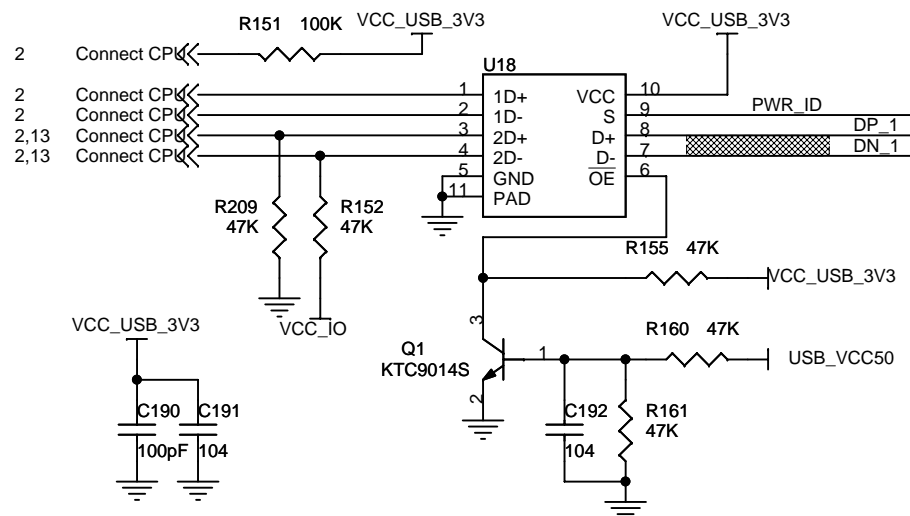
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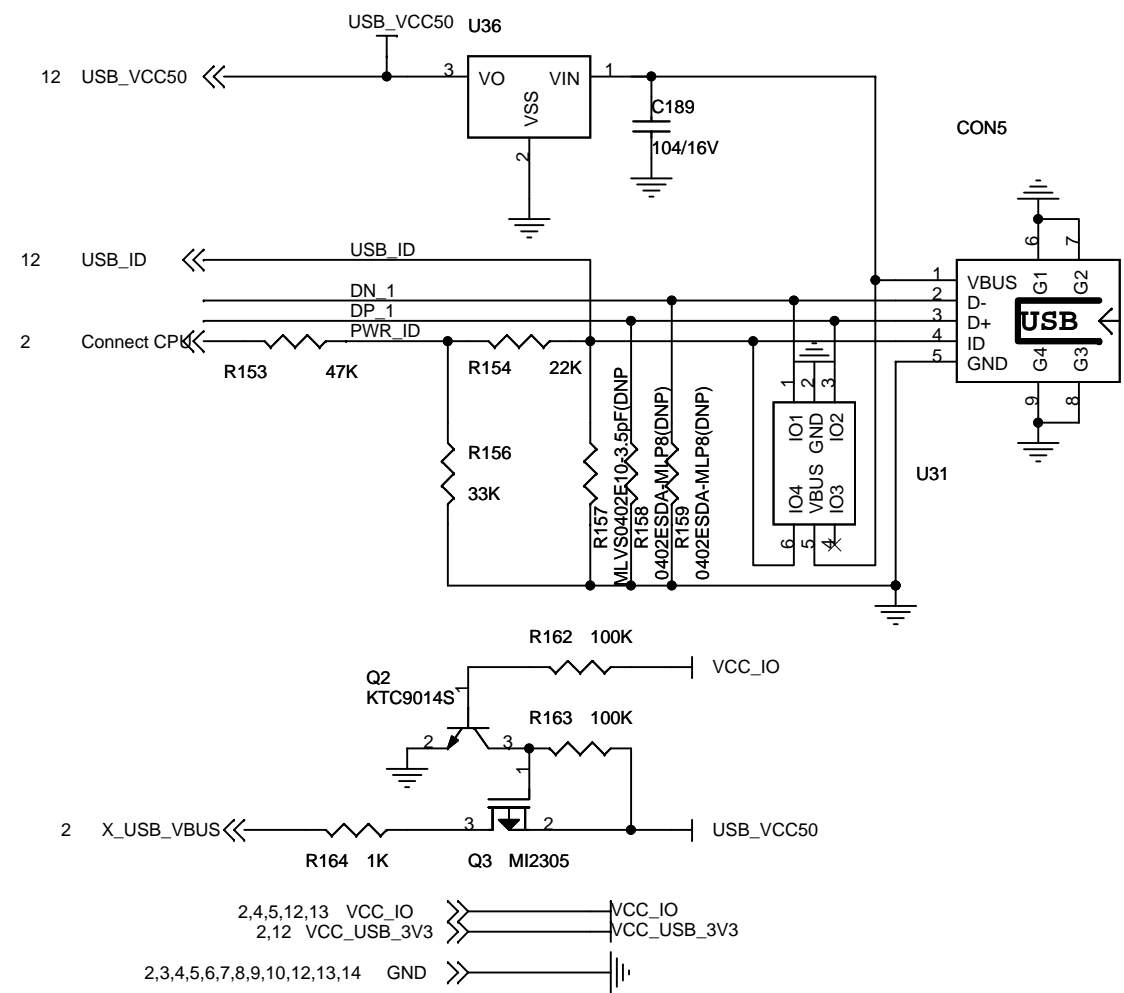
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TRUTH TABLE

S	OE	FUNCTION
X	H	Disconnect
L	L	D = 1D
H	L	D = 2D



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