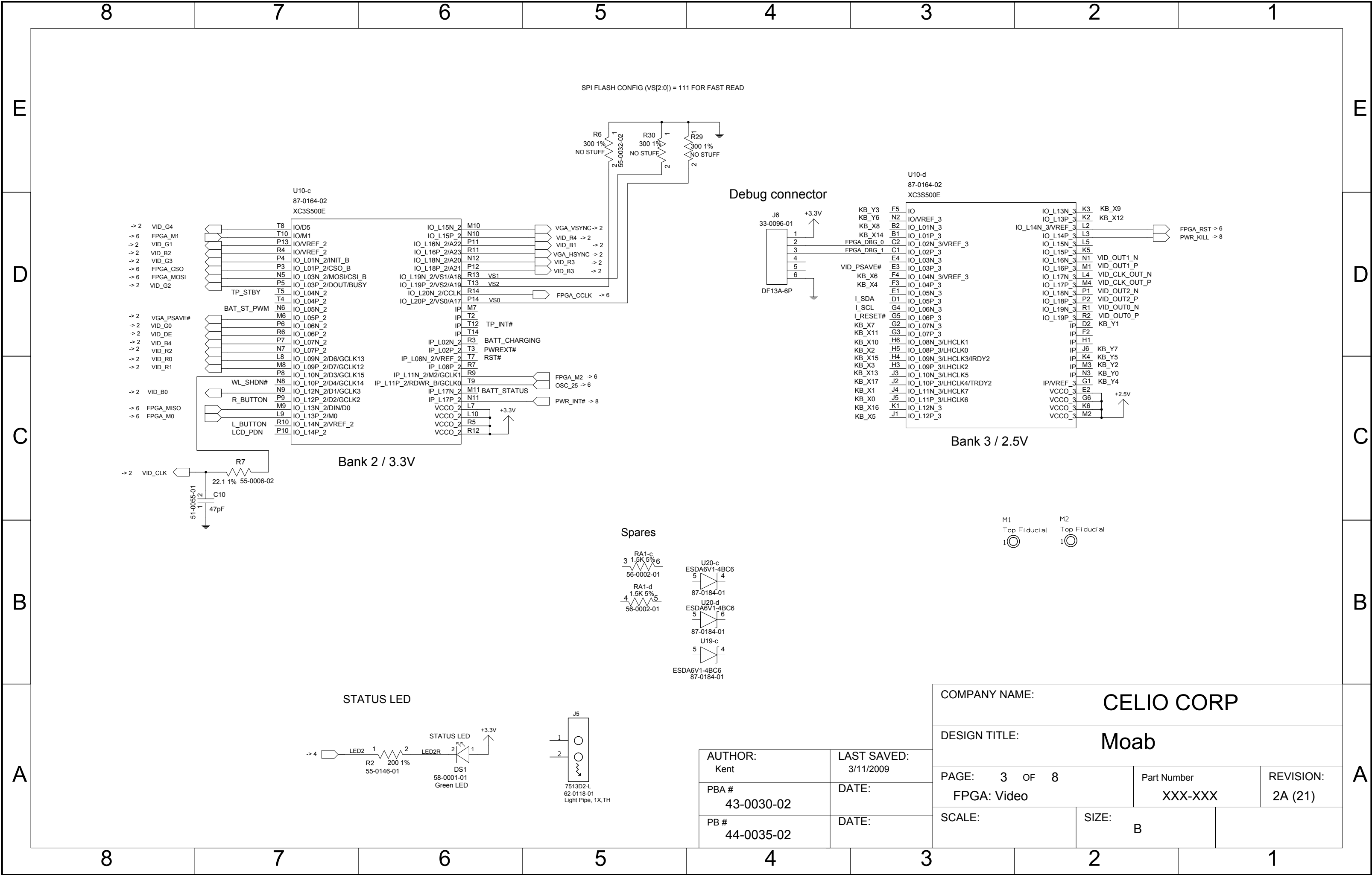
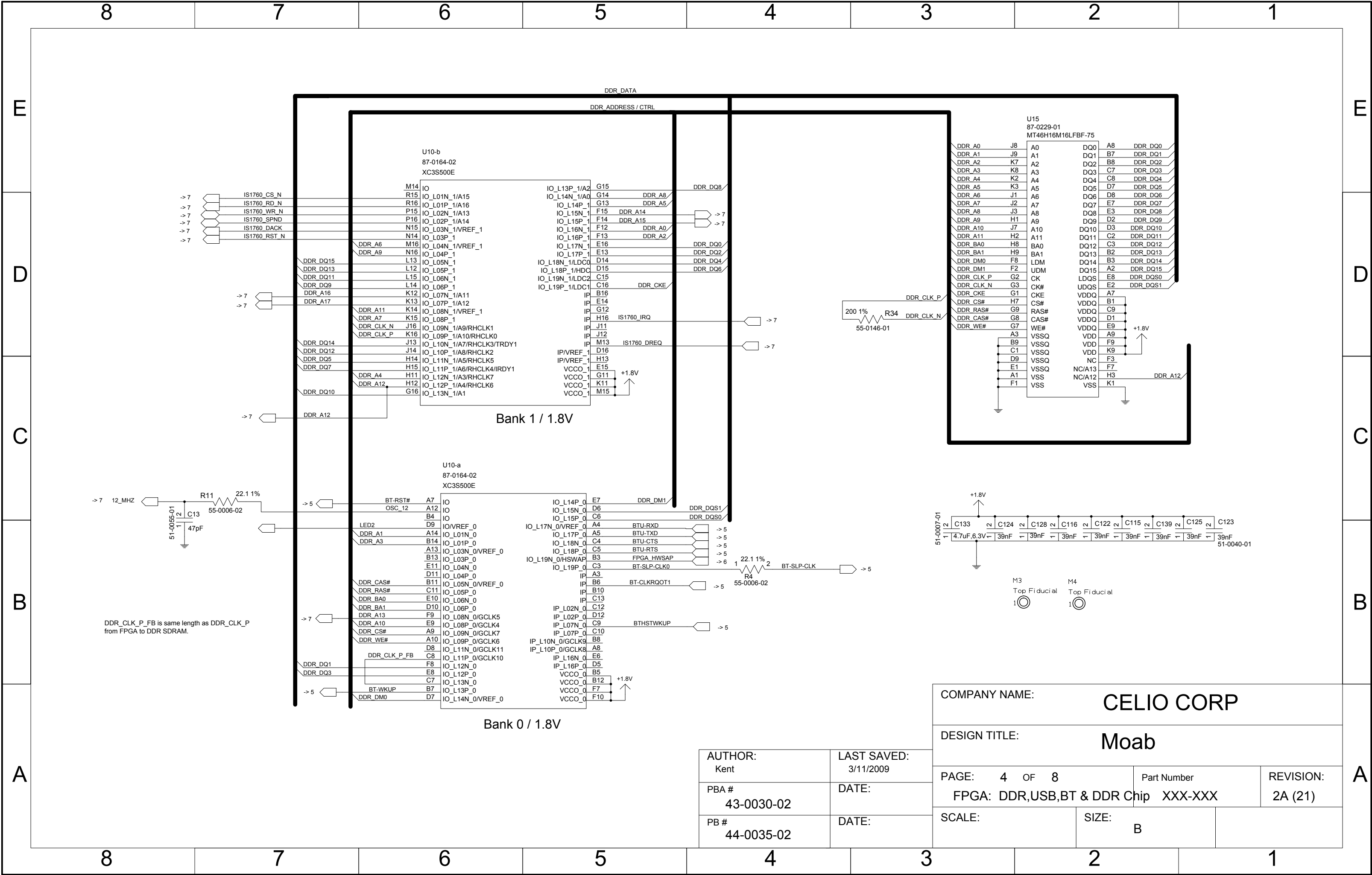


[illegible]





IS1760_CS_N
IS1760_RD_N
IS1760_WR_N
IS1760_SPND
IS1760_DACK
IS1760_RST_N

IS1760_CS_N
IS1760_RD_N
IS1760_WR_N
IS1760_SPND
IS1760_DACK
IS1760_RST_N

7
7

7

12_MHZ
R11 22.1 1%
C13 47pF
51-0055-01

DDR_CLK_P_FB is same length as DDR_CLK_P from FPGA to DDR SDRAM.

U10-b
87-0164-02
XC3S500E

IO L01N_1/A15
IO L01P_1/A16
IO L02N_1/A13
IO L02P_1/A14
IO L03N_1/VREF_1
IO L03P_1
IO L04N_1/VREF_1
IO L04P_1
IO L05N_1
IO L05P_1
IO L06N_1
IO L06P_1
IO L07N_1/A11
IO L07P_1/A12
IO L08N_1/VREF_1
IO L08P_1
IO L09N_1/A9/RHCLK1
IO L09P_1/A10/RHCLK0
IO L10N_1/A7/RHCLK3/TRDY1
IO L10P_1/A8/RHCLK2
IO L11N_1/A5/RHCLK5
IO L11P_1/A6/RHCLK4/IRDY1
IO L12N_1/A3/RHCLK7
IO L12P_1/A4/RHCLK6
IO L13N_1/A1

Bank 1 / 1.8V

U10-a
87-0164-02
XC3S500E

BT-RST#
OSC_12
LED2
DDR_A1
DDR_A3
A13
B13
E11
D11
DDR_CAS#
DDR_RAS#
DDR_BA0
DDR_BA1
DDR_A13
DDR_A10
DDR_CS#
DDR_WE#
DDR_CLK_P_FB
DDR_DQ1
DDR_DQ3
BT-WKUP
DDR_DM0

Bank 0 / 1.8V

IO L14P_0
IO L15N_0
IO L15P_0
IO L17N_0/VREF_0
IO L17P_0
IO L18N_0
IO L18P_0
IO L19N_0/HSWAP
IO L19P_0
IP A3
IP B6
IP B10
IP C13
IP C12
IP L02N_0
IP L02P_0
IP L07N_0
IP L07P_0
IP L10N_0/GCLK9
IP L10P_0/GCLK8
IP L16N_0
IP L16P_0
VCCO_0
VCCO_0
VCCO_0
VCCO_0

DDR_DM1
DDR_DQS1
DDR_DQS0
BTU-RXD
BTU-TXD
BTU-CTS
BTU-RTS
FPGA_HWSAP
BT-SLP-CLK0
BT-CLKRQOT1
BTHSTWKUP

22.1 1%
R4
BT-SLP-CLK

200 1%
R34
55-0146-01

4.7uF,6.3V
C133
C124
C128
C116
C122
C115
C139
C125
C123
51-0007-01
51-0040-01

U15
87-0229-01
MT46H16M16LFBF-75

DDR_A0
DDR_A1
DDR_A2
DDR_A3
DDR_A4
DDR_A5
DDR_A6
DDR_A7
DDR_A8
DDR_A9
DDR_A10
DDR_A11
DDR_BA0
DDR_BA1
DDR_DM0
DDR_DM1
DDR_CLK_P
DDR_CLK_N
DDR_CKE
DDR_CKE
DDR_CS#
DDR_RAS#
DDR_CAS#
DDR_WE#
A0
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10
A11
BA0
BA1
LDM
UDM
CK
CK#
CS#
RAS#
CAS#
WE#
VSSQ
VSSQ
VSSQ
VSSQ
VSSQ
VSS
VSS
DQ0
DQ1
DQ2
DQ3
DQ4
DQ5
DQ6
DQ7
DQ8
DQ9
DQ10
DQ11
DQ12
DQ13
DQ14
DQ15
LDQS
UDQS
VDDQ
VDDQ
VDDQ
VDDQ
VDDQ
VDD
VDD
VDD
VDD
NC
NC/A13
NC/A12
VSS
A8
B7
B8
C7
C8
D7
D8
E7
E3
D2
D3
C2
C3
B2
B3
A2
E8
E2
A7
B1
C9
D1
E9
A9
K9
F3
F7
H3
K1

1.8V

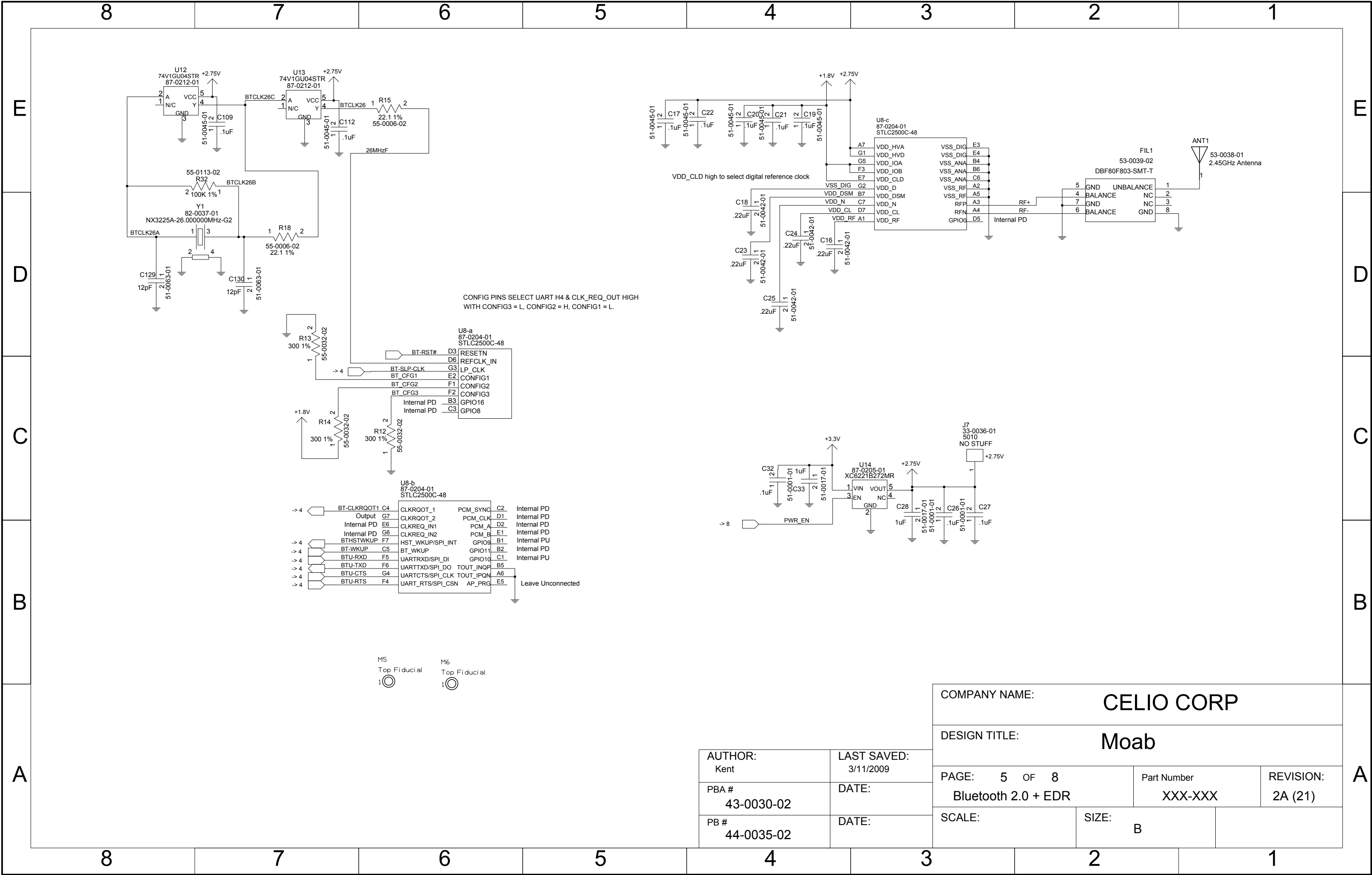
1.8V

M3
Top Fiducial
1

M4
Top Fiducial
1

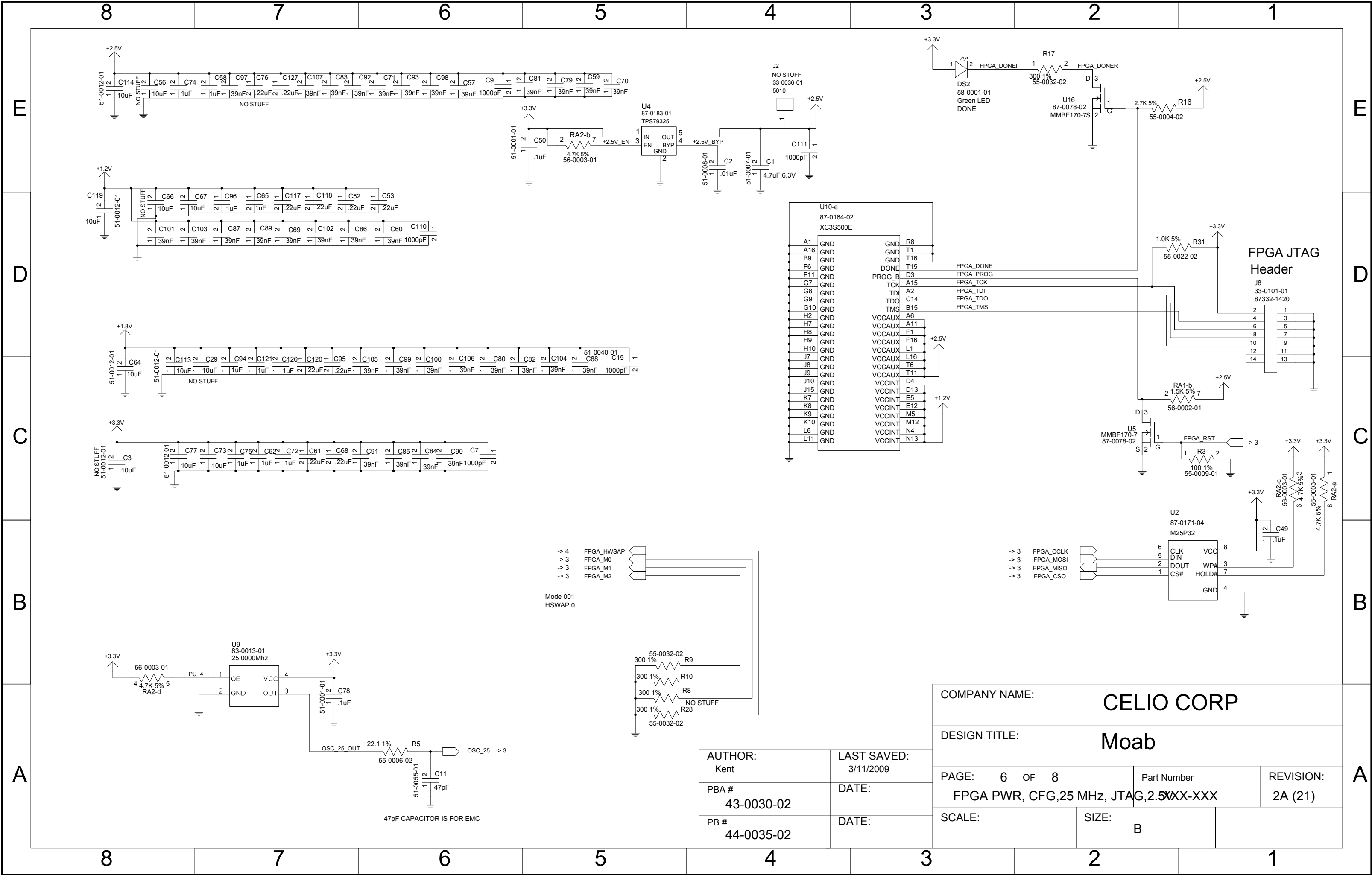
COMPANY NAME:		CELIO CORP	
DESIGN TITLE:		Moab	
PAGE: 4 OF 8	Part Number	REVISION: 2A (21)	
FPGA: DDR,USB,BT & DDR Chip		XXX-XXX	
SCALE:	SIZE: B		

AUTHOR: Kent	LAST SAVED: 3/11/2009
PBA # 43-0030-02	DATE:
PB # 44-0035-02	DATE:



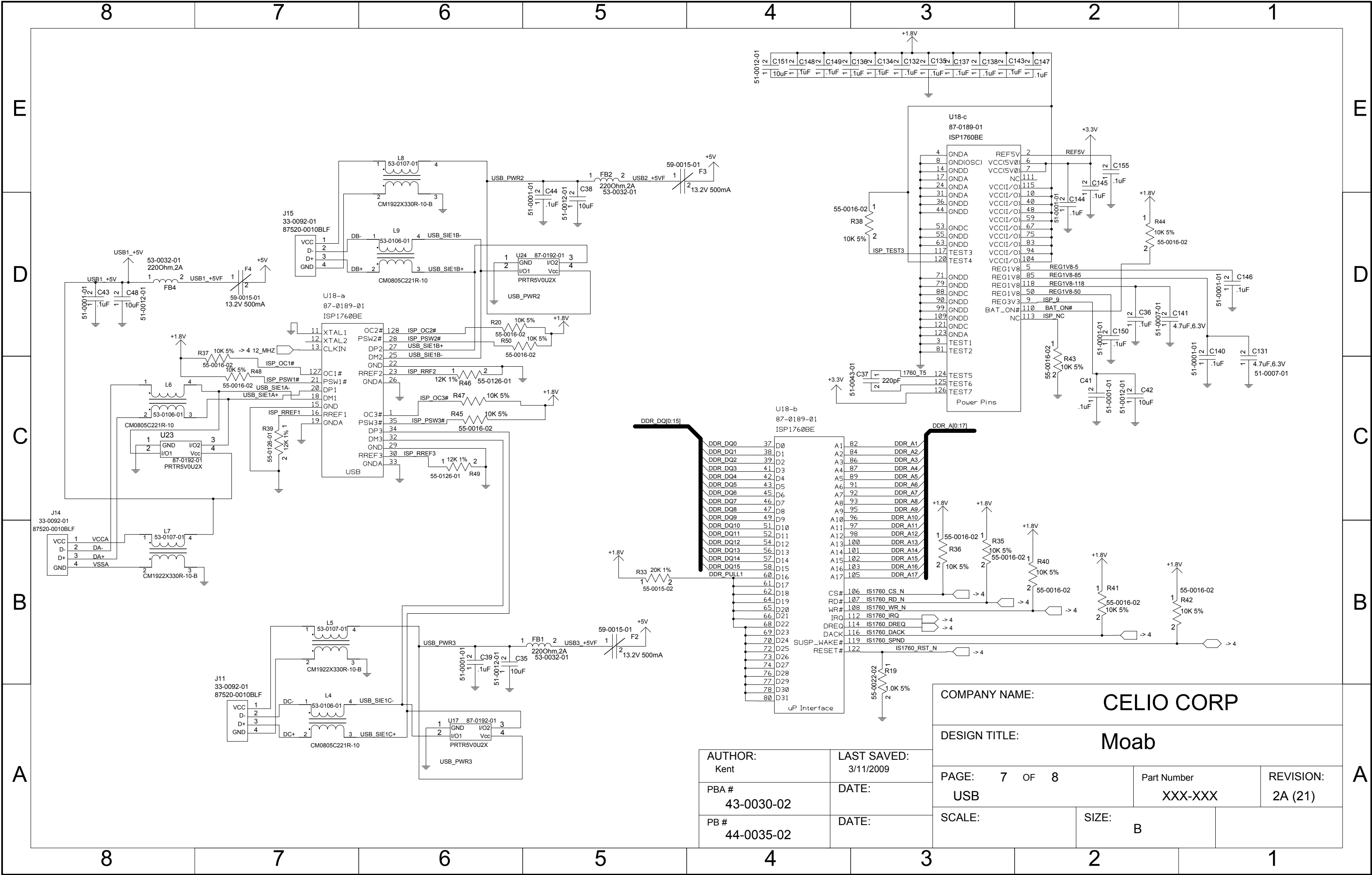
AUTHOR: Kent	LAST SAVED: 3/11/2009
PBA # 43-0030-02	DATE:
PB # 44-0035-02	DATE:

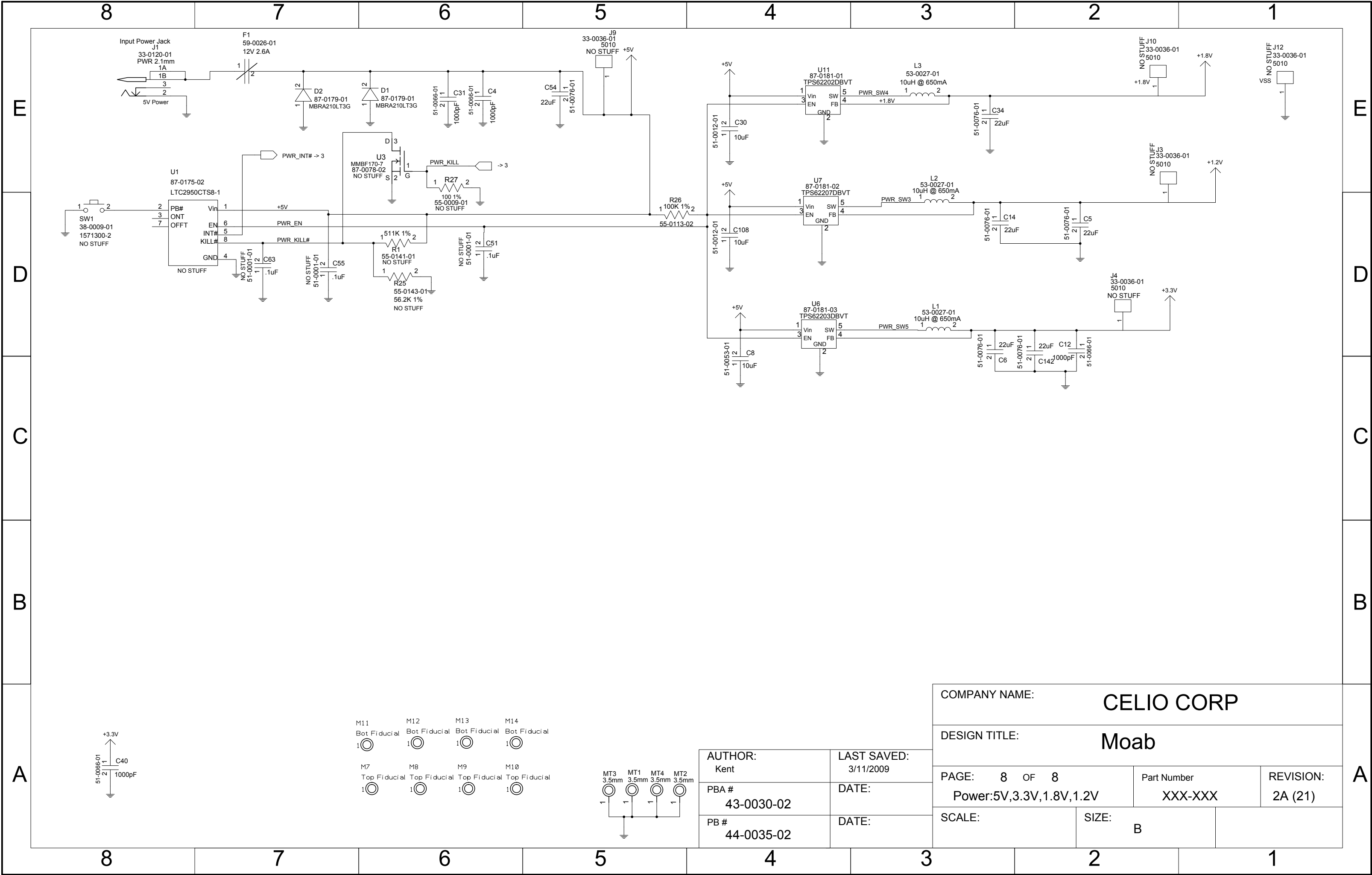
COMPANY NAME: CELIO CORP			
DESIGN TITLE: Moab			
PAGE: 5 OF 8 Bluetooth 2.0 + EDR	Part Number XXX-XXX	REVISION: 2A (21)	
SCALE:	SIZE: B		



AUTHOR: Kent	LAST SAVED: 3/11/2009
PBA # 43-0030-02	DATE:
PB # 44-0035-02	DATE:

COMPANY NAME: CELIO CORP		
DESIGN TITLE: Moab		
PAGE: 6 OF 8	Part Number	REVISION:
FPGA PWR, CFG,25 Mhz, JTAG,2.5V	XXX-XXX	2A (21)
SCALE:	SIZE: B	





COMPANY NAME:			CELIO CORP	
DESIGN TITLE:			Moab	
PAGE:	8	OF	8	Part Number
Power:5V,3.3V,1.8V,1.2V				XXX-XXX
SCALE:		SIZE:		REVISION:
		B		2A (21)

AUTHOR:	LAST SAVED:
Kent	3/11/2009
PBA #	DATE:
43-0030-02	
PB #	DATE:
44-0035-02	

M11	M12	M13	M14
Bot Fiducial	Bot Fiducial	Bot Fiducial	Bot Fiducial
1	1	1	1
M7	M8	M9	M10
Top Fiducial	Top Fiducial	Top Fiducial	Top Fiducial
1	1	1	1

