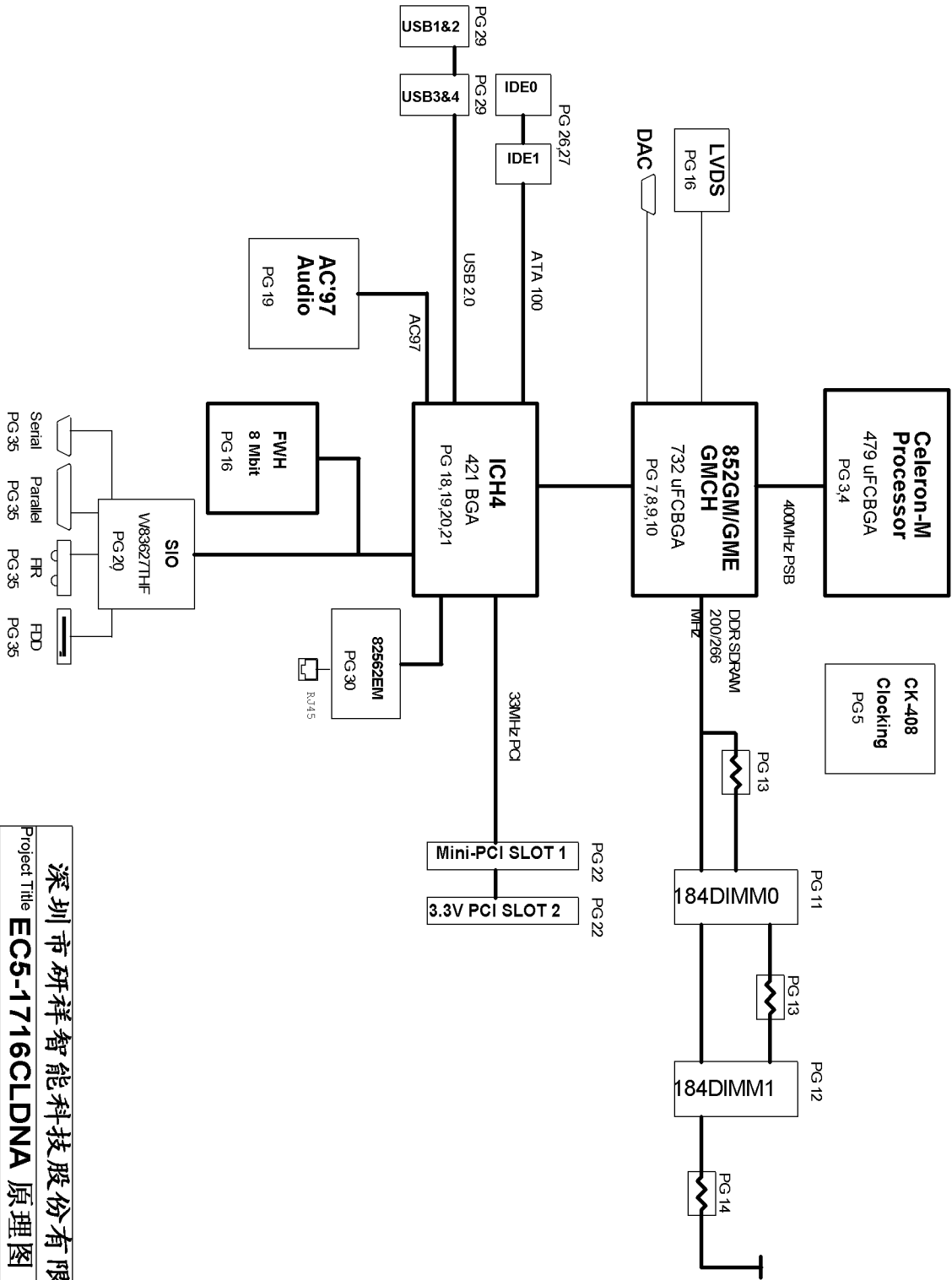


65EC5-1716CLDNA 原理图.

- 1.Celeron-M CPU + Intel852GM + ICH4;
- 2. 184 DDR DIMM 支持 DDR 内存最大到 512MB;
- 3. Intel 852GM 内建3D图形加速引擎，支持双显示/双应用；
- 4. ICH4 内建 10/100Mb以太网控制器；
- 5. ICH4内建 USB2.0 控制器 提供2个USB2.0接口；
- 6. W83627THF LPC Super I/O提供标准I/O接口和硬件监测；
- 7. (可选的)NETMOS NM9845 提供多2个COM口；
- 8. 提供PC104 Plus总线扩展PCI设备；

01) COVER SHEET
02) Title Page
03) Celeron-M Processor 1/2
04) Celeron-M Processor 2/2
05) CK-408(CS950810)
06) 852GM/E (1/3)
07) 852GM/E (2/3)
08) 852GM/E (3/3)
09) 852GM/E Compensation
10) 184 DDR DIMM
11) DDR Serial Termination
12) DDR Parallel Termination
13) ICH4 PATR 1
14) ICH4 PATR 2
15) ICH4 PATR 3
16) FWH, IDE & CF card
17) CRT,LCD Connector
18) USB Connectors
19) AC'97 Codec & Audio
20) LPC I/O W83627THF
21) Powe CONN & Power On Logic
22) VCC3,VCC2.5,VCC1_5,VCCP
23) IMVP-IV(SC1476)
24) Intel 82562ET PLC
25) PC 104 Plus BUS
26) NT9845->COM3,4
27) KEEP BLANK
28) Revision History

# Intel Celeron-M Processor With 852GM/GME BLOCK DIAGRAM



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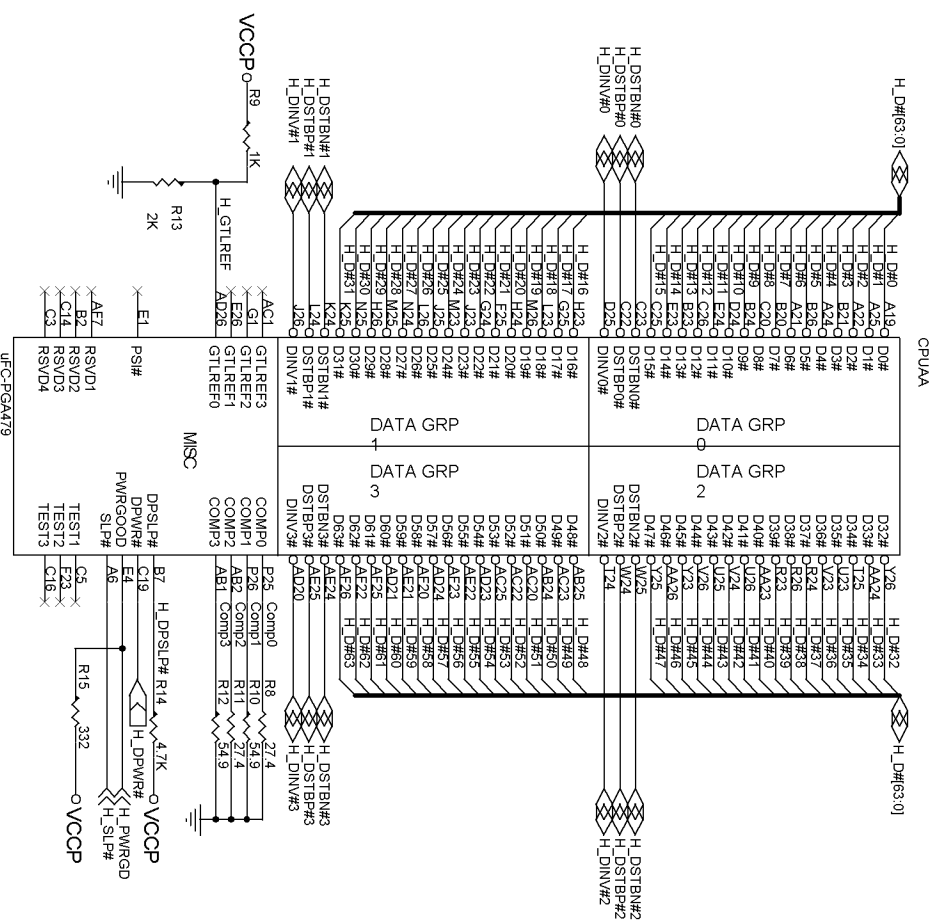
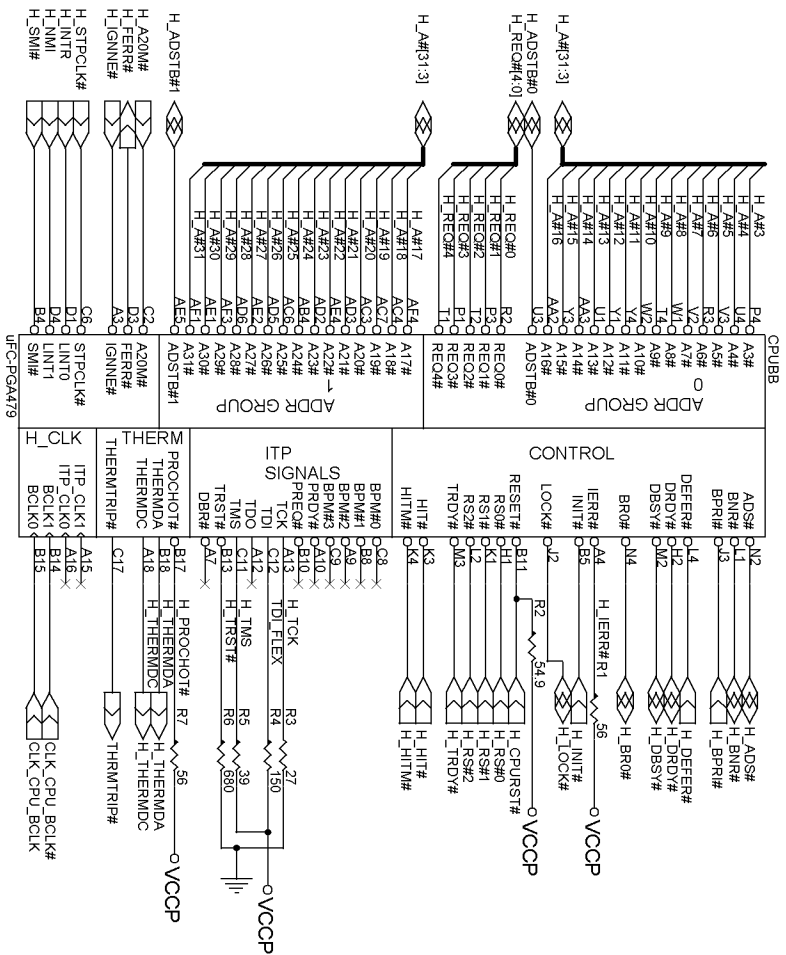
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EC5-1716CLDNA 原理图

02) Title Page

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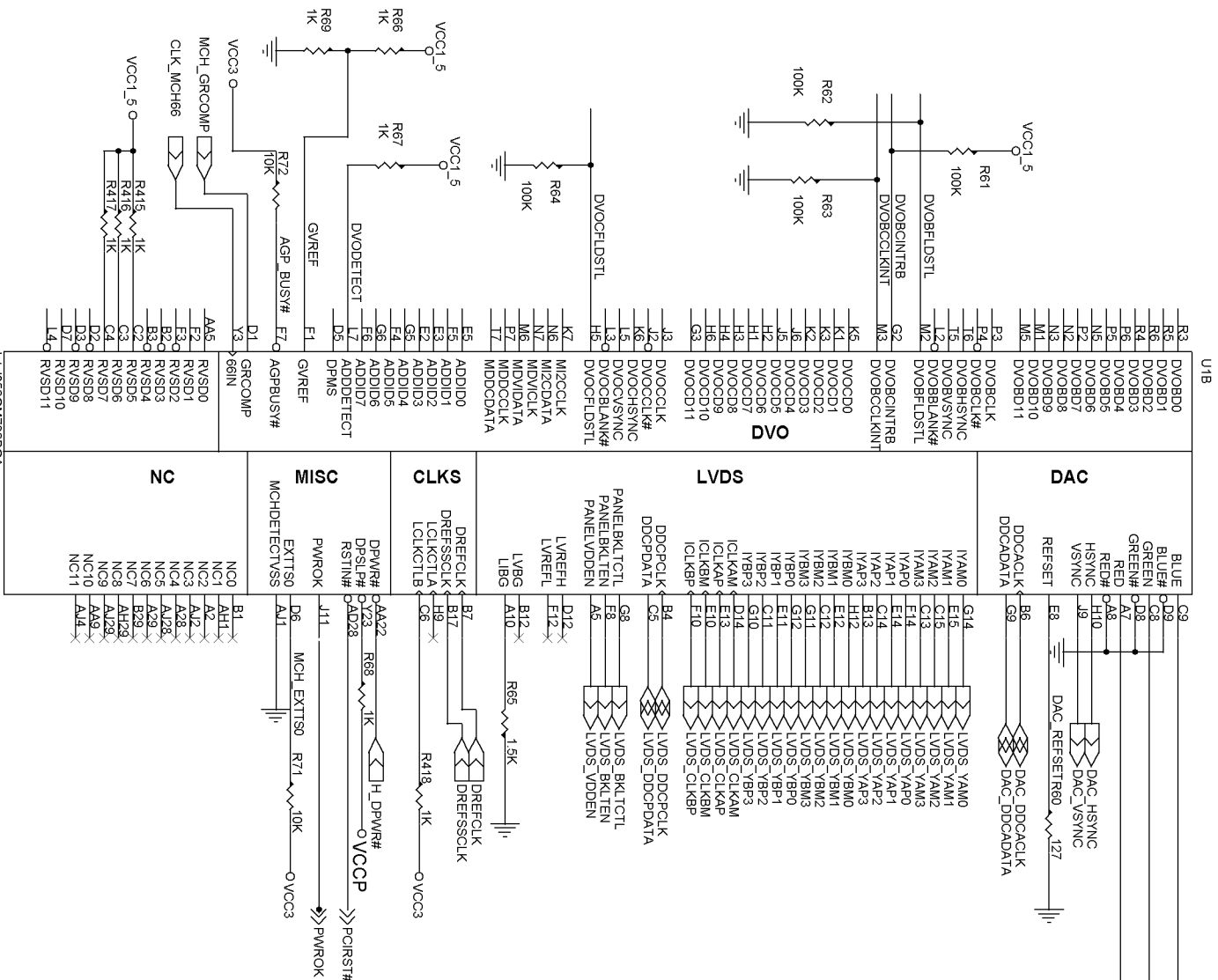
Checked by



A2	VS51	P21
A5	VS52	P24
A8	VS53	P27
A11	VS54	P30
A14	VS55	P33
A17	VS56	P36
A20	VS57	P39
A23	VS58	P42
A26	VS59	P45
A29	VS60	P48
B3	VS61	P51
B6	VS62	P54
B9	VS63	P57
B12	VS64	P60
B15	VS65	P63
B18	VS66	P66
B21	VS67	P69
B24	VS68	P72
B27	VS69	P75
B30	VS70	P78
B33	VS71	P81
B36	VS72	P84
B39	VS73	P87
B42	VS74	P90
B45	VS75	P93
B48	VS76	P96
B51	VS77	P99
B54	VS78	P102
B57	VS79	P105
B60	VS80	P108
B63	VS81	P111
B66	VS82	P114
B69	VS83	P117
B72	VS84	P120
B75	VS85	P123
B78	VS86	P126
B81	VS87	P129
B84	VS88	P132
B87	VS89	P135
B90	VS90	P138
B93	VS91	P141
B96	VS92	P144
B99	VS93	P147
C2	VS94	P150
C5	VS95	P153
C8	VS96	P156
C11	VS97	P159
C14	VS98	P162
C17	VS99	P165
C20	VS100	P168
C23	VS101	P171
C26	VS102	P174
C29	VS103	P177
C32	VS104	P180
C35	VS105	P183
C38	VS106	P186
C41	VS107	P189
C44	VS108	P192
C47	VS109	P195
C50	VS110	P198
C53	VS111	P201
C56	VS112	P204
C59	VS113	P207
C62	VS114	P210
C65	VS115	P213
C68	VS116	P216
C71	VS117	P219
C74	VS118	P222
C77	VS119	P225
C80	VS120	P228
C83	VS121	P231
C86	VS122	P234
C89	VS123	P237
C92	VS124	P240
C95	VS125	P243
C98	VS126	P246
C101	VS127	P249
C104	VS128	P252
C107	VS129	P255
C110	VS130	P258
C113	VS131	P261
C116	VS132	P264
C119	VS133	P267
C122	VS134	P270
C125	VS135	P273
C128	VS136	P276
C131	VS137	P279
C134	VS138	P282
C137	VS139	P285
C140	VS140	P288
C143	VS141	P291
C146	VS142	P294
C149	VS143	P297
C152	VS144	P300
C155	VS145	P303
C158	VS146	P306
C161	VS147	P309
C164	VS148	P312
C167	VS149	P315
C170	VS150	P318
C173	VS151	P321
C176	VS152	P324
C179	VS153	P327
C182	VS154	P330
C185	VS155	P333
C188	VS156	P336
C191	VS157	P339
C194	VS158	P342
C197	VS159	P345
C200	VS160	P348
C203	VS161	P351
C206	VS162	P354
C209	VS163	P357
C212	VS164	P360
C215	VS165	P363
C218	VS166	P366
C221	VS167	P369
C224	VS168	P372
C227	VS169	P375
C230	VS170	P378
C233	VS171	P381
C236	VS172	P384
C239	VS173	P387
C242	VS174	P390
C245	VS175	P393
C248	VS176	P396
C251	VS177	P399
C254	VS178	P402
C257	VS179	P405
C260	VS180	P408
C263	VS181	P411
C266	VS182	P414
C269	VS183	P417
C272	VS184	P420
C275	VS185	P423
C278	VS186	P426
C281	VS187	P429
C284	VS188	P432
C287	VS189	P435
C290	VS190	P438
C293	VS191	P441
C296	VS192	P444
C299	VS193	P447
C302	VS194	P450
C305	VS195	P453
C308	VS196	P456
C311	VS197	P459
C314	VS198	P462
C317	VS199	P465
C320	VS200	P468
C323	VS201	P471
C326	VS202	P474
C329	VS203	P477
C332	VS204	P480
C335	VS205	P483
C338	VS206	P486
C341	VS207	P489
C344	VS208	P492
C347	VS209	P495
C350	VS210	P498
C353	VS211	P501
C356	VS212	P504
C359	VS213	P507
C362	VS214	P510
C365	VS215	P513
C368	VS216	P516
C371	VS217	P519
C374	VS218	P522
C377	VS219	P525
C380	VS220	P528
C383	VS221	P531
C386	VS222	P534
C389	VS223	P537
C392	VS224	P540
C395	VS225	P543
C398	VS226	P546
C401	VS227	P549
C404	VS228	P552
C407	VS229	P555
C410	VS230	P558
C413	VS231	P561
C416	VS232	P564
C419	VS233	P567
C422	VS234	P570
C425	VS235	P573
C428	VS236	P576
C431	VS237	P579
C434	VS238	P582
C437	VS239	P585
C440	VS240	P588
C443	VS241	P591
C446	VS242	P594
C449	VS243	P597
C452	VS244	P600
C455	VS245	P603
C458	VS246	P606
C461	VS247	P609
C464	VS248	P612
C467	VS249	P615
C470	VS250	P618
C473	VS251	P621
C476	VS252	P624
C479	VS253	P627
C482	VS254	P630
C485	VS255	P633
C488	VS256	P636
C491	VS257	P639
C494	VS258	P642
C497	VS259	P645
C500	VS260	P648
C503	VS261	P651
C506	VS262	P654
C509	VS263	P657
C512	VS264	P660
C515	VS265	P663
C518	VS266	P666
C521	VS267	P669
C524	VS268	P672
C527	VS269	P675
C530	VS270	P678
C533	VS271	P681
C536	VS272	P684
C539	VS273	P687
C542	VS274	P690
C545	VS275	P693
C548	VS276	P696
C551	VS277	P699
C554	VS278	P702
C557	VS279	P705
C560	VS280	P708
C563	VS281	P711
C566	VS282	P714
C569	VS283	P717
C572	VS284	P720
C575	VS285	P723
C578	VS286	P726
C581	VS287	P729
C584	VS288	P732
C587	VS289	P735
C590	VS290	P738
C593	VS291	P741
C596	VS292	P744
C599	VS293	P747
C602	VS294	P750
C605	VS295	P753
C608	VS296	P756
C611	VS297	P759
C614	VS298	P762
C617	VS299	P765
C620	VS300	P768
C623	VS301	P771
C626	VS302	P774
C629	VS303	P777
C632	VS304	P780
C635	VS305	P783
C638	VS306	P786
C641	VS307	P789
C644	VS308	P792
C647	VS309	P795
C650	VS310	P798
C653	VS311	P801
C656	VS312	P804
C659	VS313	P807
C662	VS314	P810
C665	VS315	P813
C668	VS316	P816
C671	VS317	P819
C674	VS318	P822
C677	VS319	P825
C680	VS320	P828
C683	VS321	P831
C686	VS322	P834
C689	VS323	P837
C692	VS324	P840
C695	VS325	P843
C698	VS326	P846
C701	VS327	P849
C704	VS328	P852
C707	VS329	P855
C710	VS330	P858
C713	VS331	P861
C716	VS332	P864
C719	VS333	P867
C722	VS334	P870
C725	VS335	P873
C728	VS336	P876
C731	VS337	P879
C734	VS338	P882
C737	VS339	P885
C740	VS340	P888
C743	VS341	P891
C746	VS342	P894
C749	VS343	P897
C752	VS344	P900
C755	VS345	P903
C758	VS346	P906
C761	VS347	P909
C764	VS348	P912
C767	VS349	P915
C770	VS350	P918
C773	VS351	P921
C776	VS352	P924
C779	VS353	P927
C782	VS354	P930
C785	VS355	P933
C788	VS356	P936
C791	VS357	P939
C794	VS358	P942
C797	VS359	P945
C800	VS360	P948
C803	VS361	P951
C806	VS362	P954
C809	VS363	P957
C812	VS364	P960
C815	VS365	P963
C818	VS366	P966
C821	VS367	P969
C824	VS368	P972
C827	VS369	P975
C830	VS370	P978
C833	VS371	P981
C836	VS372	P984
C839	VS373	P987
C842	VS374	P990
C845	VS375	P993
C848	VS376	P996
C851	VS377	P999
C854	VS378	P1002
C857	VS379	P1005
C860	VS380	P1008
C863	VS381	P1011
C866	VS382	P1014
C869	VS383	P1017
C872	VS384	P1020
C875	VS385	P1023
C878	VS386	P1026
C881	VS387	P1029
C884	VS388	P1032
C887	VS389	P1035
C890	VS390	P1038
C893	VS391	P1041
C896	VS392	P1044
C899	VS393	P1047
C902	VS394	P1050
C905	VS395	P1053
C908	VS396	P1056
C911	VS397	P1059
C914	VS398	P1062
C917	VS399	P1065
C920	VS400	P1068
C923	VS401	P1071
C926	VS402	P1074
C929	VS403	P1077
C932	VS404	P1080
C935	VS405	P1083
C938	VS406	P1086
C941	VS407	P1089
C944	VS408	P1092
C947	VS409	P1095
C950	VS410	P1098
C953	VS411	P1101
C956	VS412	P1104
C959	VS413	P1107
C962	VS414	P1110
C965	VS415	P1113
C968	VS416	P1116
C971	VS417	P1119
C974	VS418	P1122
C977	VS419	P1125
C980	VS420	P1128
C983	VS421	P1131
C986	VS422	P1134
C989	VS423	P1137
C992	VS424	P1140
C995	VS425	P1143
C998	VS426	P1146
C1001	VS427	P1149
C1004	VS428	P1152
C1007	VS429	P1155
C1010	VS430	P1158
C1013	VS431	P1161
C1016	VS432	P1164
C1019	VS433	P1167
C1022	VS434	P1170
C1025	VS435	P1173
C1028	VS436	P1176
C1031	VS437	P1179
C1034	VS438	P1182
C1037	VS439	P1185
C1040	VS440	P1188
C1043	VS441	P1191
C1046	VS442	P1194
C1049	VS443	P1197
C1052	VS444	P1200
C1055	VS445	P1203
C1058	VS446	P1206
C1061	VS447	P1209
C1064	VS448	P1212
C1067	VS449	P1215
C1070	VS450	P1218
C1073	VS451	P1221
C1076	VS452	P1224
C1079	VS453	P1227
C1082	VS454	P1230
C1085	VS455	P1233
C1088	VS456	P1236
C1091	VS457	P1239
C1094	VS458	P1242
C1097	VS459	P1245
C1100	VS460	P1248
C1103	VS461	P1251
C1106	VS462	P1254
C1109	VS463	P1257

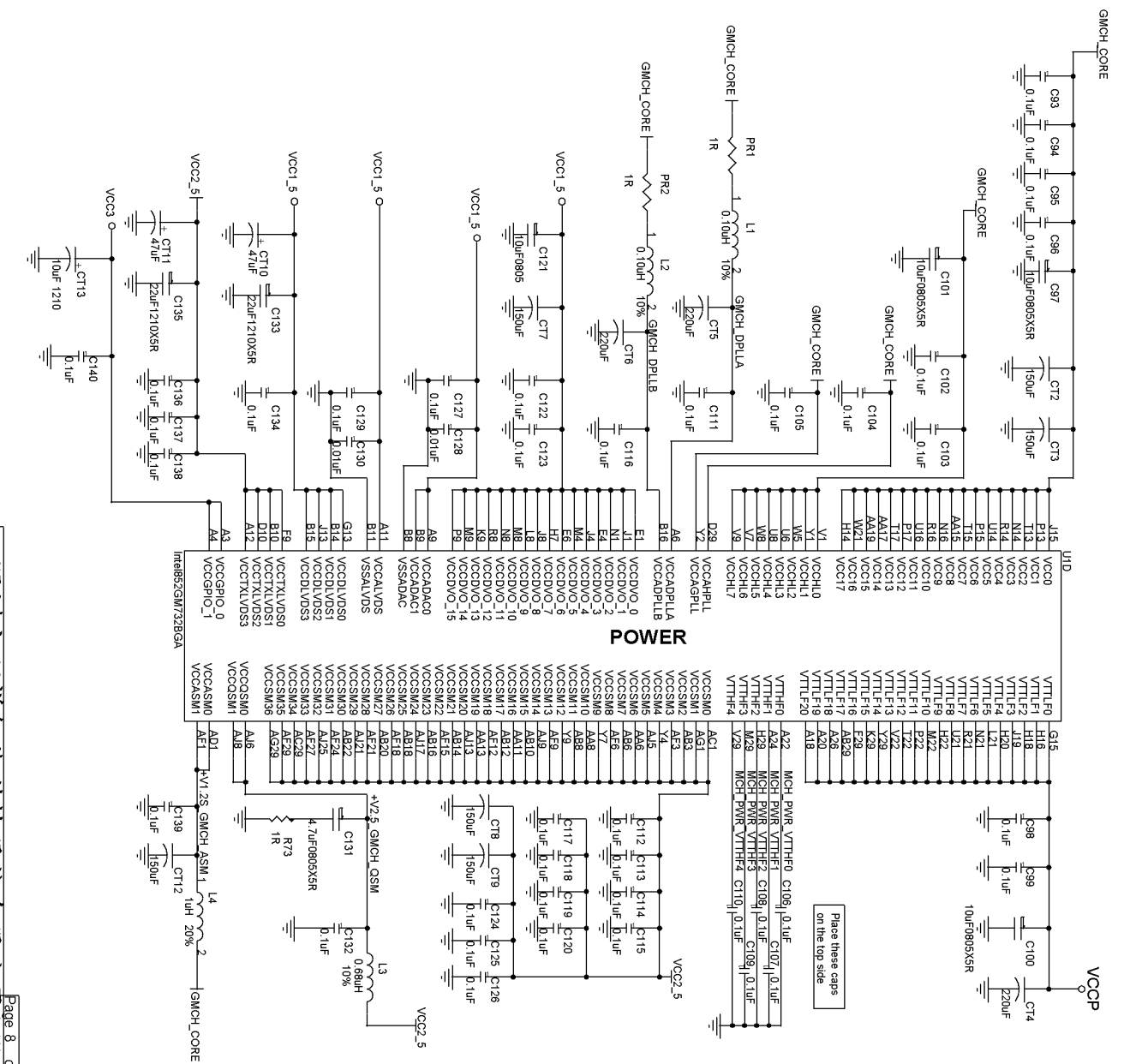




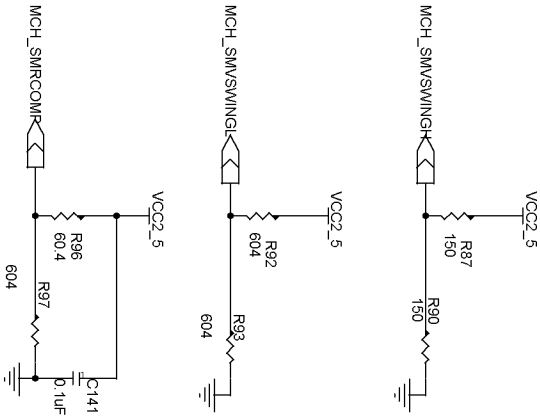
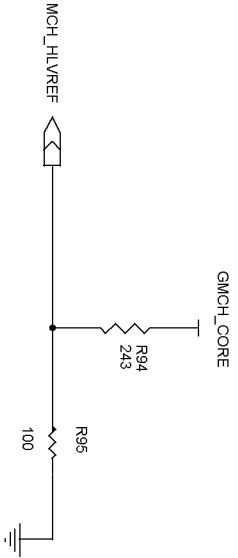
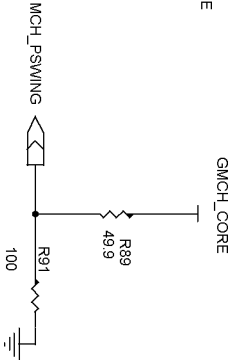
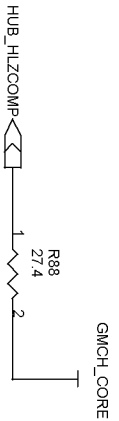
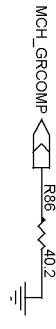
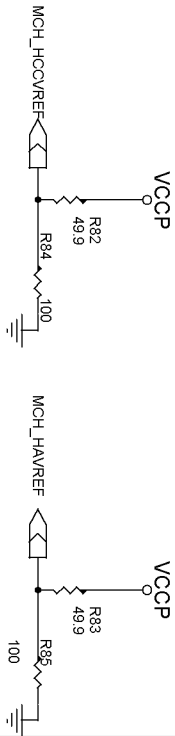
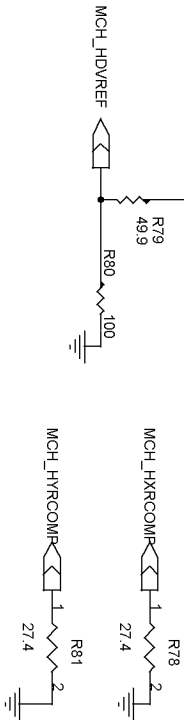
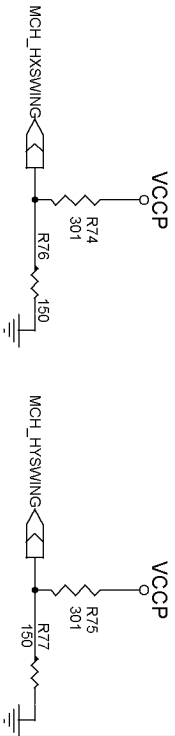


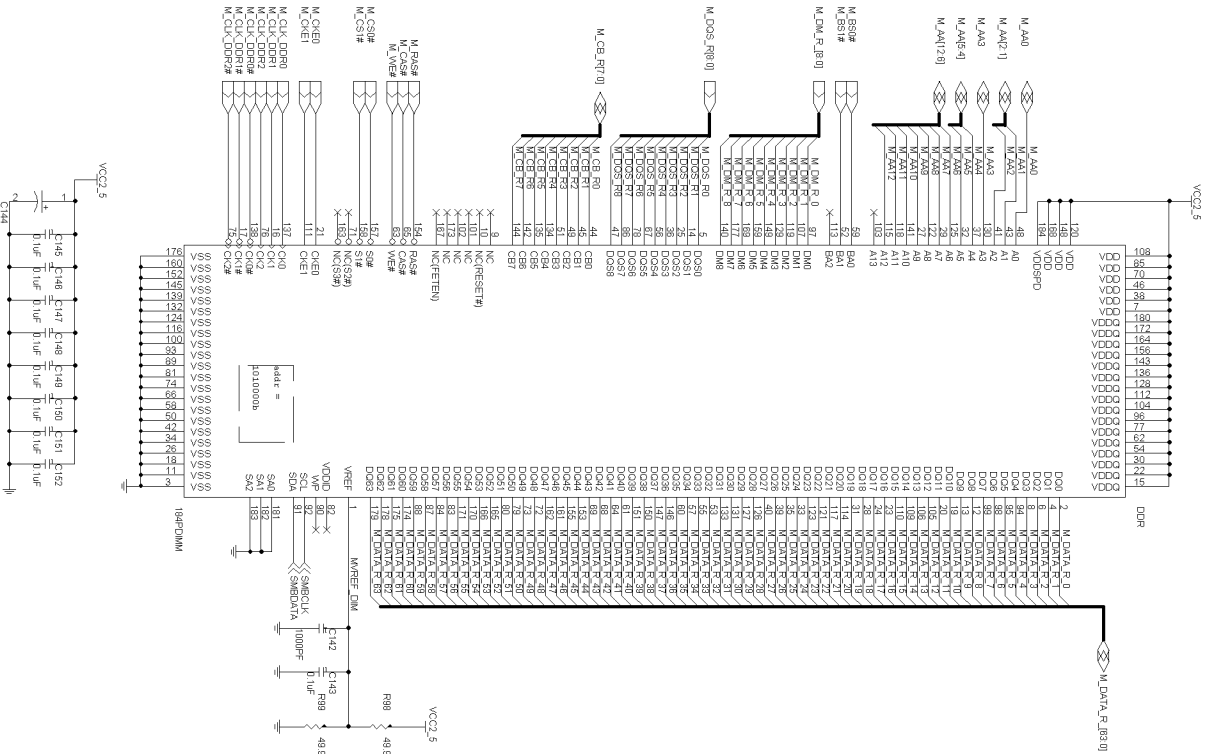
Intel852GM/732BGA

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M\_DATA[63:0]  M\_DATA\_R\_[63:0] 

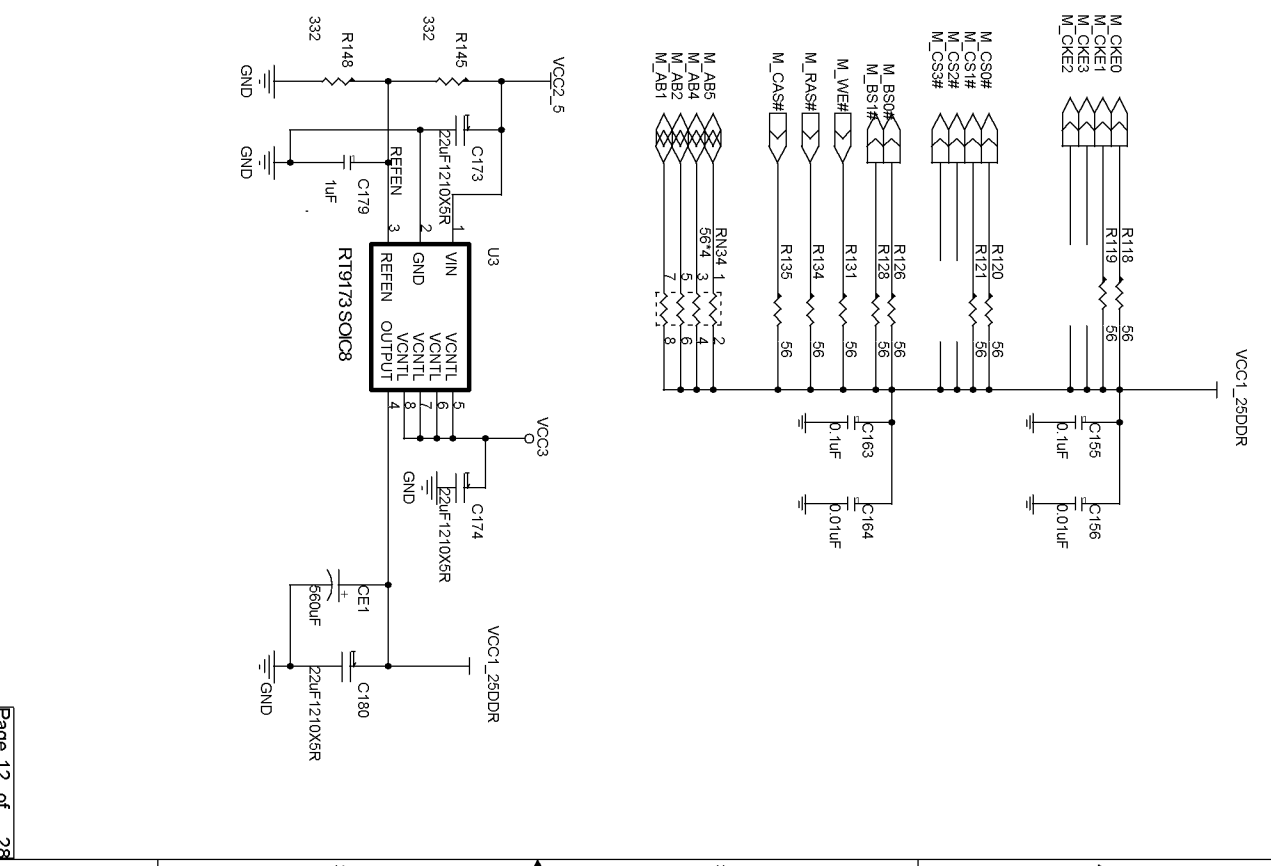
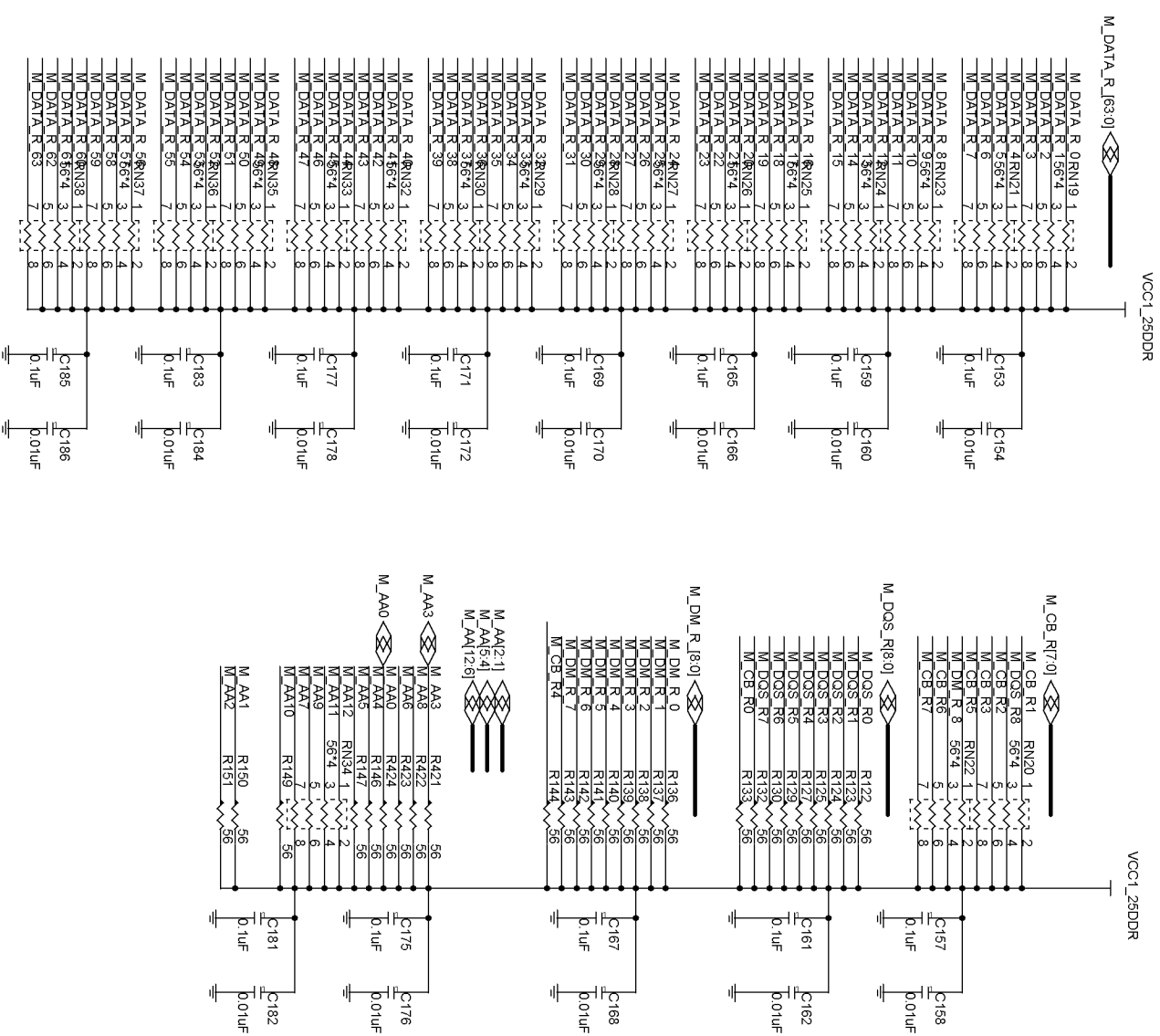
M_DATA0	M_DATA1	M_DATA2	M_DATA3	M_DATA4	M_DATA5	M_DATA6	M_DATA7	M_DATA8	M_DATA9	M_DATA10	M_DATA11	M_DATA12	M_DATA13	M_DATA14	M_DATA15	M_DATA16	M_DATA17	M_DATA18	M_DATA19	M_DATA20	M_DATA21	M_DATA22	M_DATA23	M_DATA24	M_DATA25	M_DATA26	M_DATA27	M_DATA28	M_DATA29	M_DATA30	M_DATA31	M_DATA32	M_DATA33	M_DATA34	M_DATA35	M_DATA36	M_DATA37	M_DATA38	M_DATA39	M_DATA40	M_DATA41	M_DATA42	M_DATA43	M_DATA44	M_DATA45	M_DATA46	M_DATA47	M_DATA48	M_DATA49	M_DATA50	M_DATA51	M_DATA52	M_DATA53	M_DATA54	M_DATA55	M_DATA56	M_DATA57	M_DATA58	M_DATA59	M_DATA60	M_DATA61	M_DATA62	M_DATA63																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																
1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2	1	5	2	2

M_DOS[0]				M_DOS_R[0]
M_DOS0	R102	10		M_DOS_R0
M_DOS1	R103	10		M_DOS_R1
M_DOS2	R104	10		M_DOS_R2
M_DOS3	R105	10		M_DOS_R3
M_DOS4	R106	10		M_DOS_R4
M_DOS5	R107	10		M_DOS_R5
M_DOS6	R108	10		M_DOS_R6
M_DOS7	R109	10		M_DOS_R7

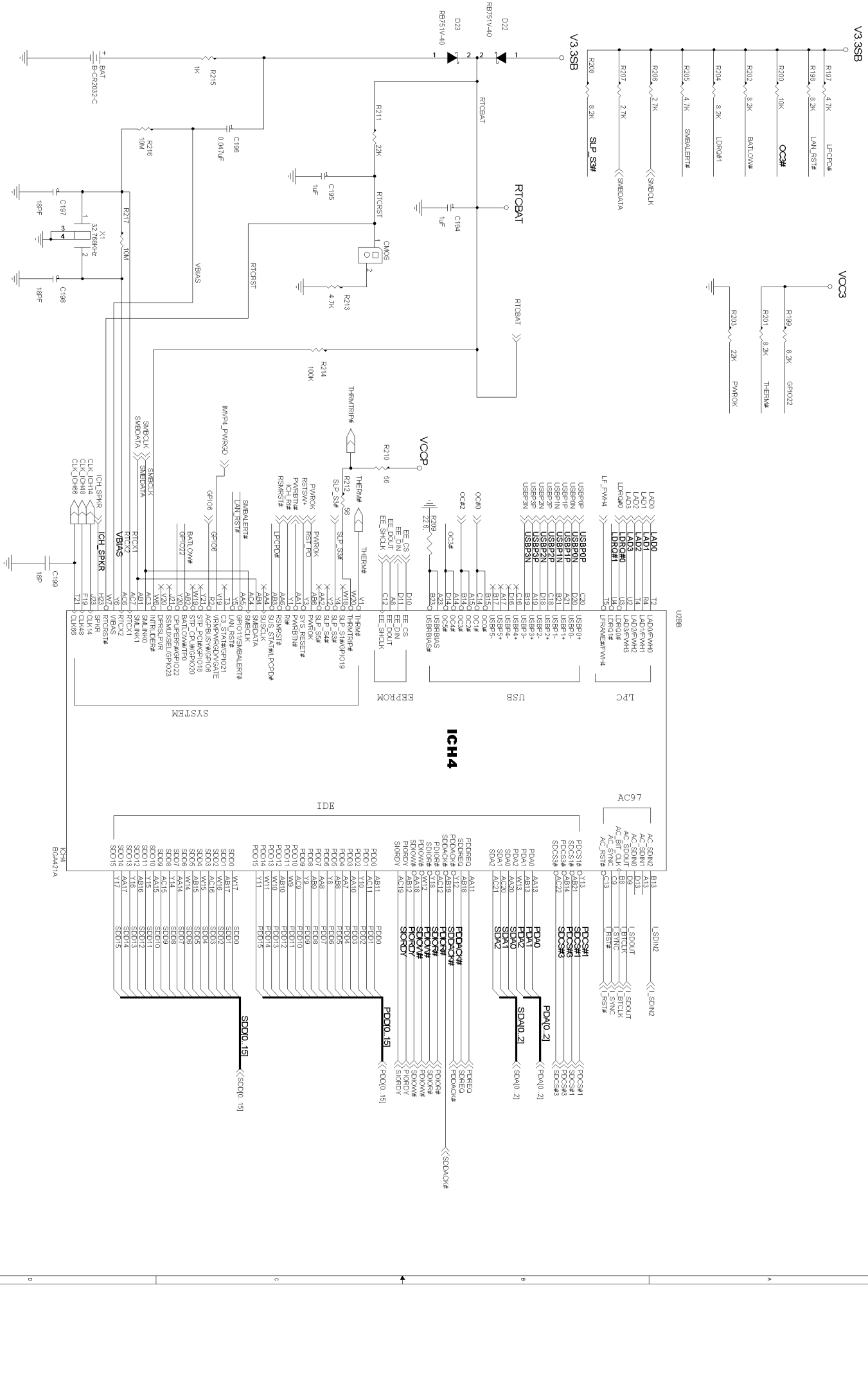
M_DM[0] 	M_DM_R_0 		
M_DM0	R110	10	M_DM_R_0
M_DM1	R111	10	M_DM_R_1
M_DM2	R112	10	M_DM_R_2
M_DM3	R113	10	M_DM_R_3
M_DM4	R114	10	M_DM_R_4
M_DM5	R115	10	M_DM_R_5
M_DM6	R116	10	M_DM_R_6
M_DM7	R117	10	M_DM_R_7

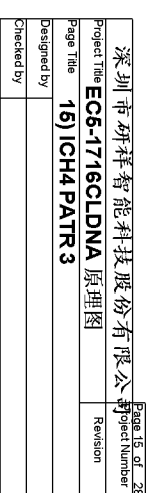
M\_CB[7:0]  M\_CB\_R[7:0] 

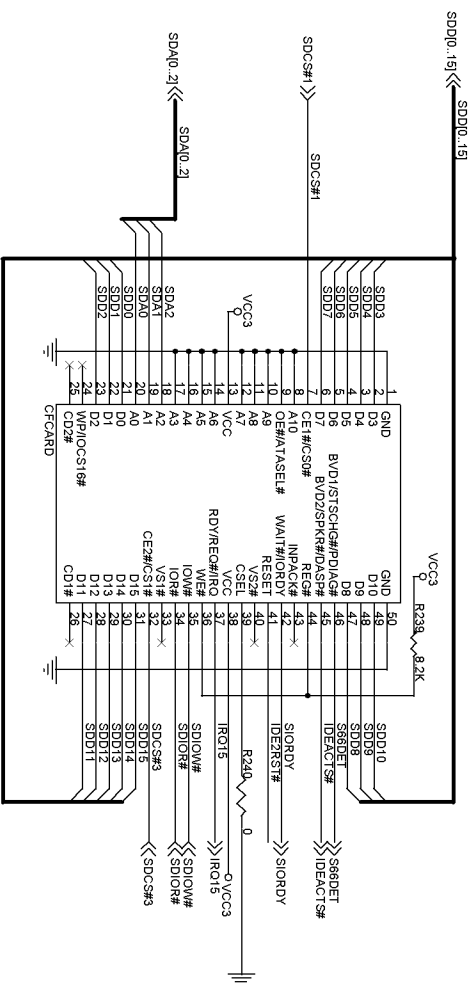
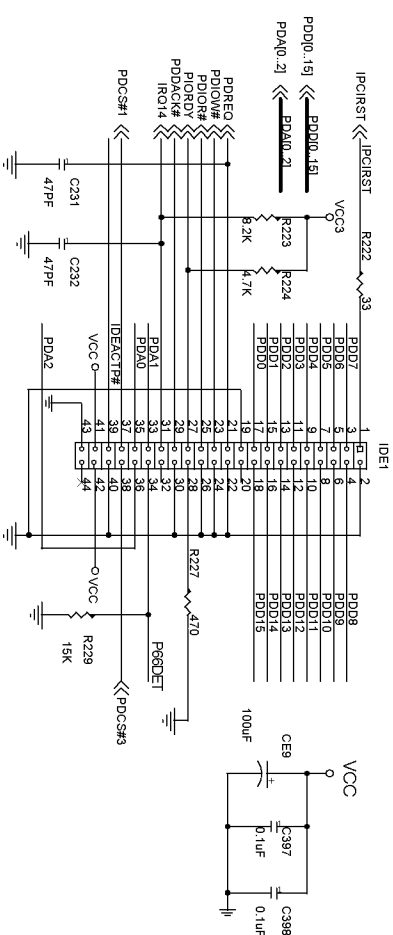
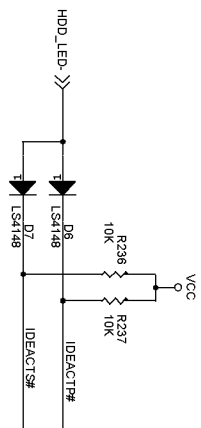
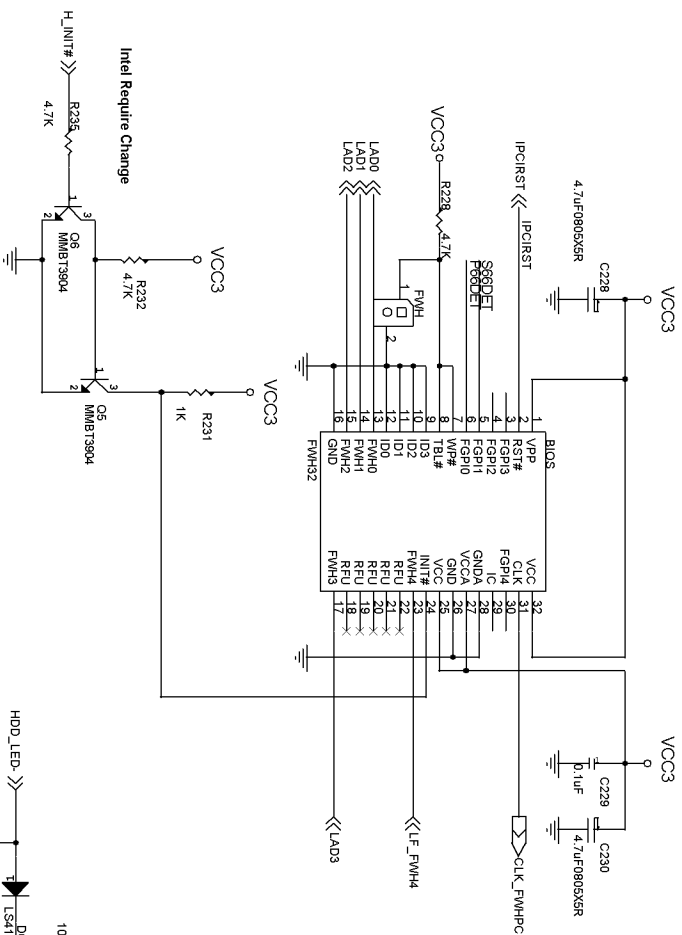
M CB0	RN5	1	5	2	2	M CB_R0
M CB1	10	4	3	1	4	M CB_R1
M DOS8	5	1	6	8		M DOS_R8
M CB2	RN7	1	5	2	2	M CB_R2
M CB4	10	4	3	1	4	M CB_R4
M CB5	5	1	6	8		M CB_R5
M DM8	7	1	2	8		M DM_R8
M CB6	R109	10				M CB_R6
M CB7	10					M CB_R7
	R101	10				M CB_R3
						M CB_R7



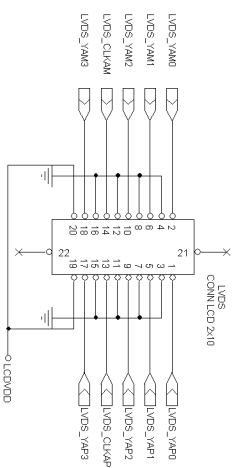
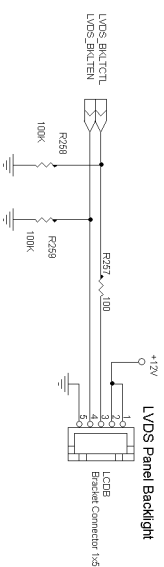
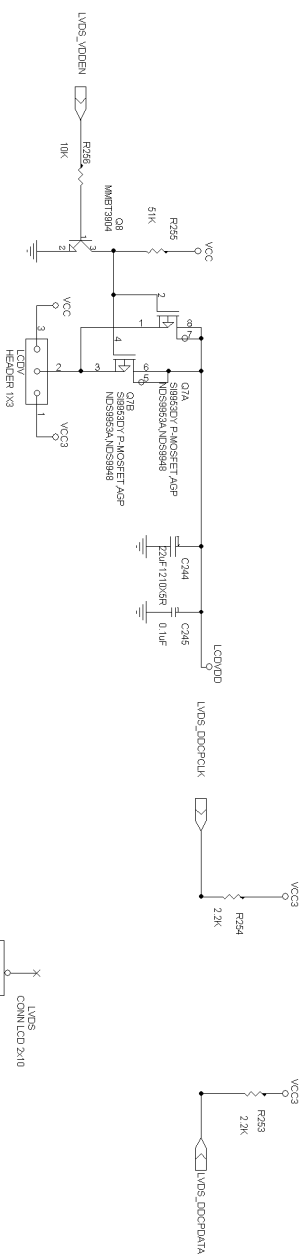
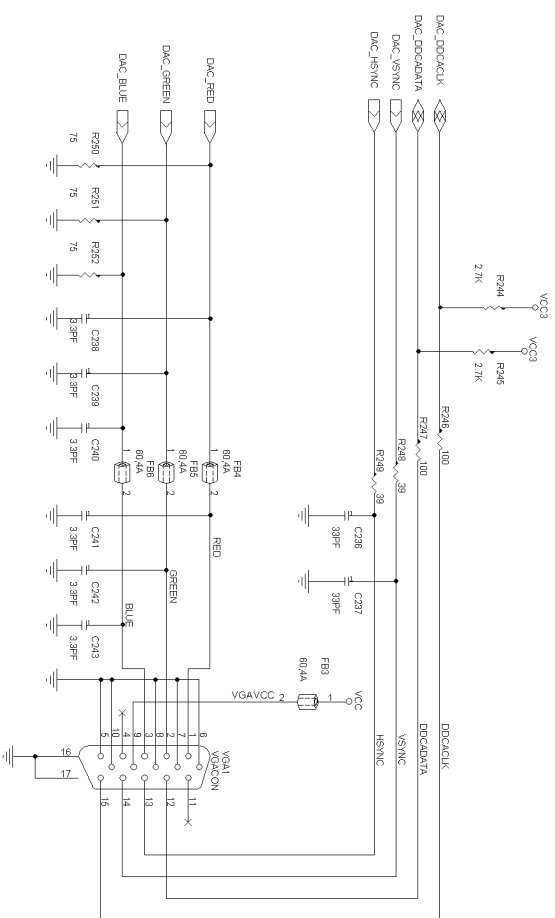




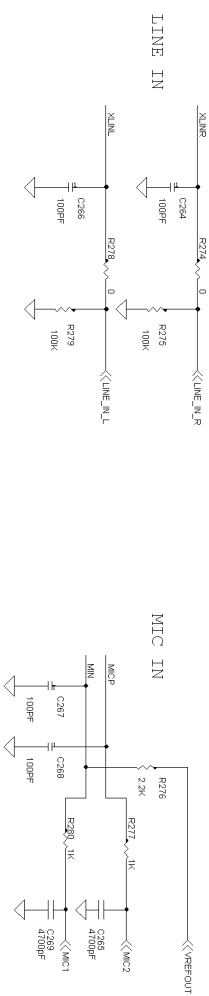
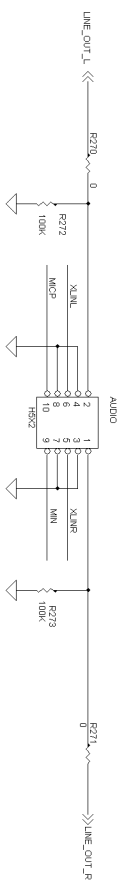
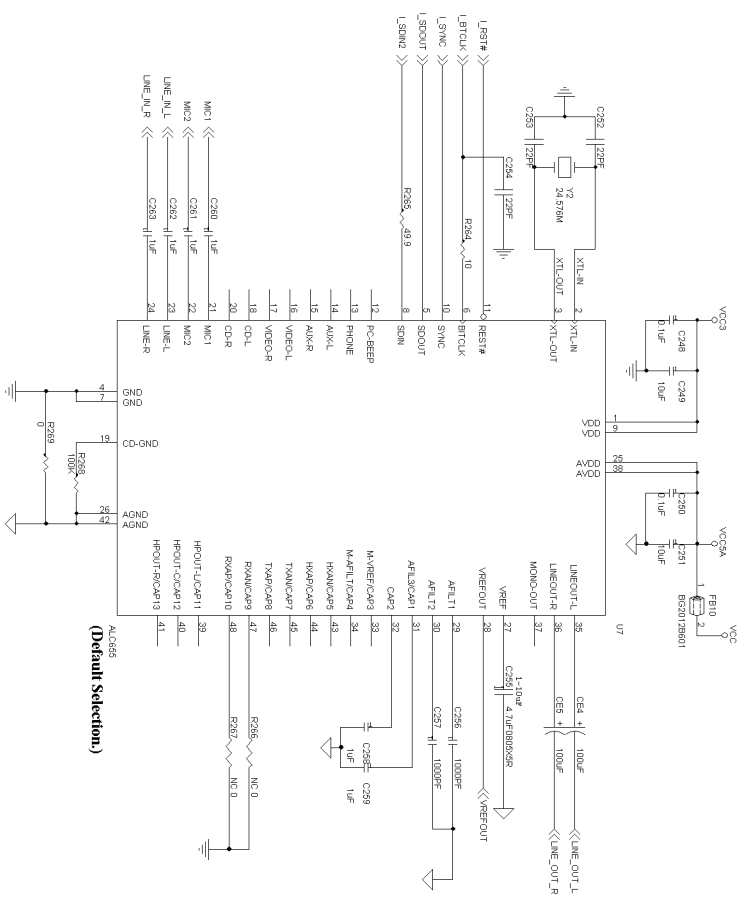




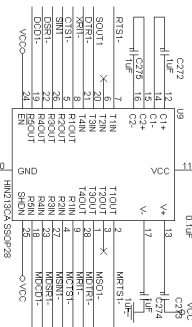




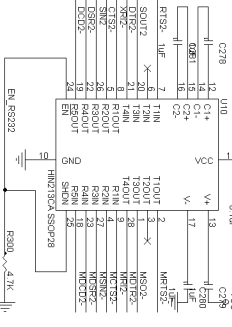




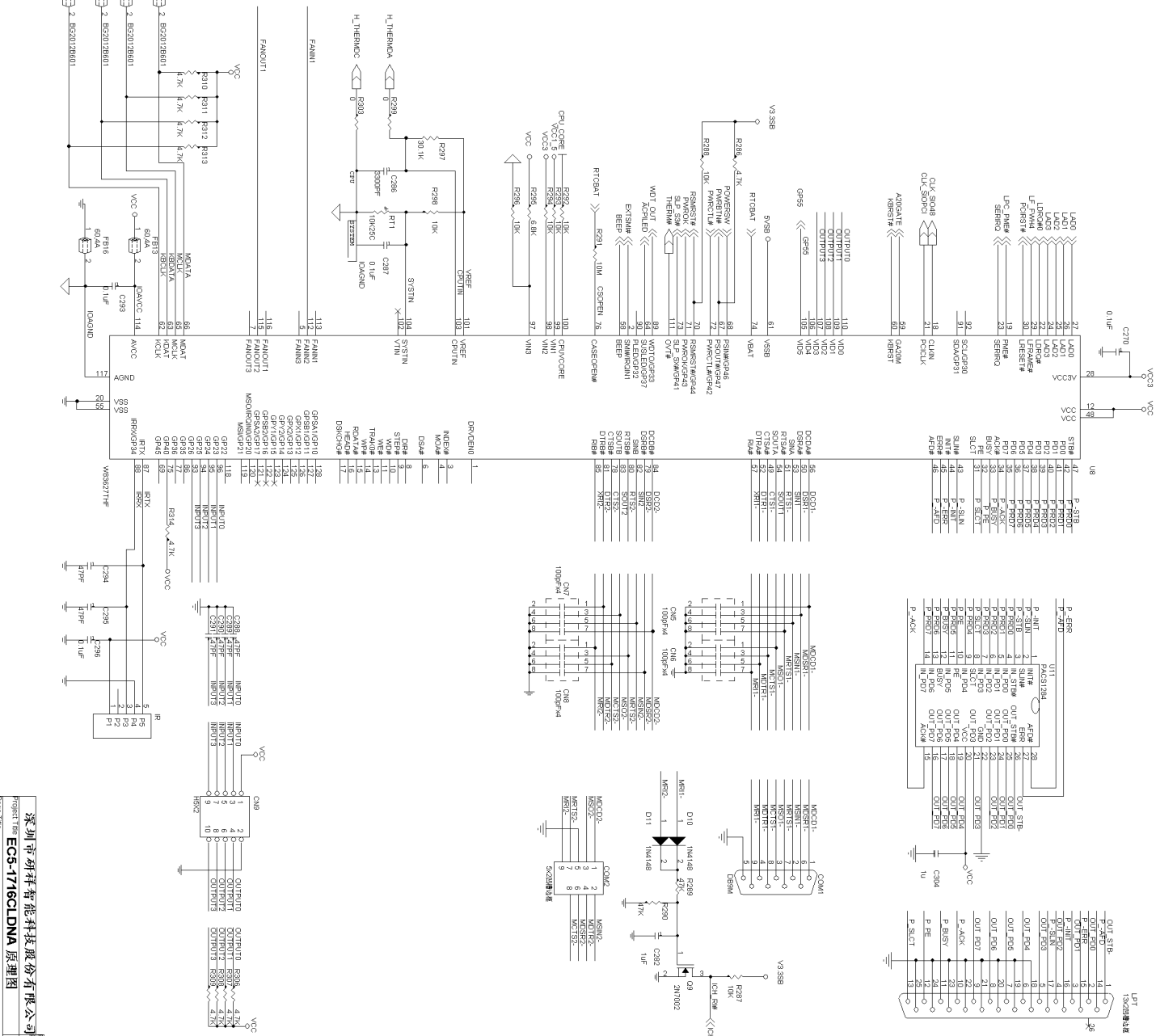
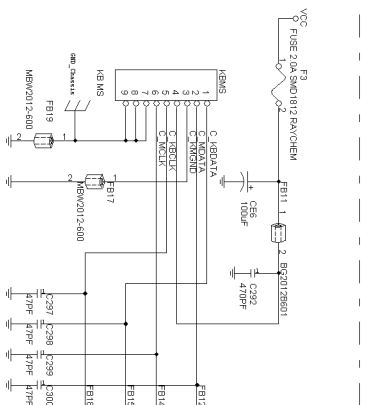
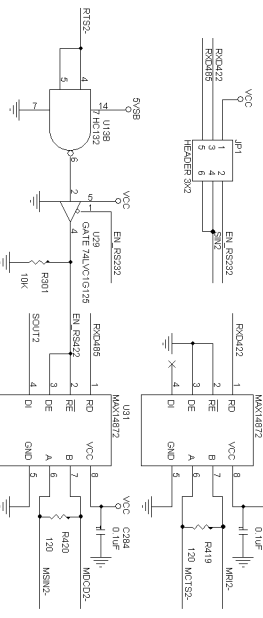
COM1

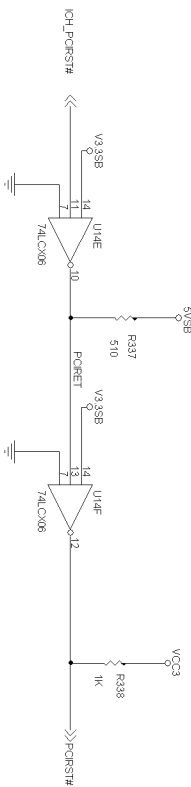
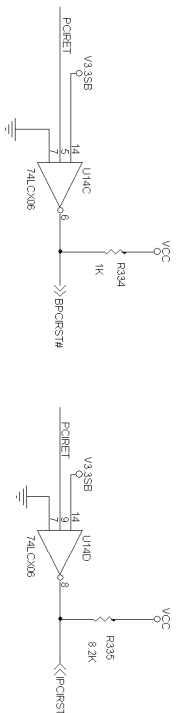
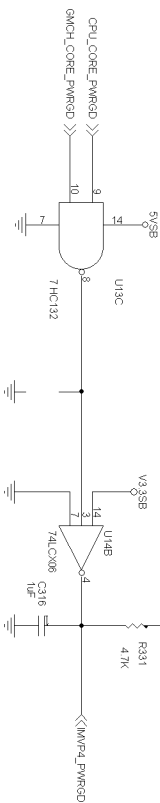
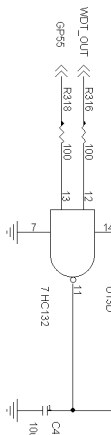
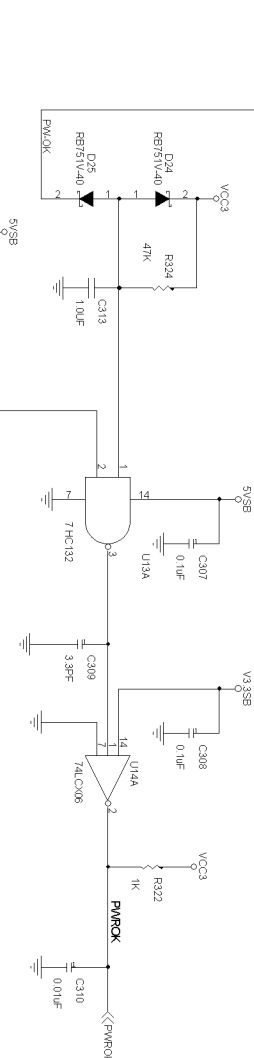
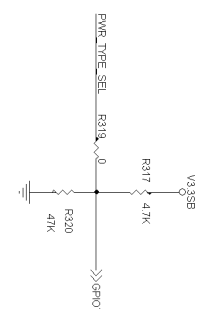
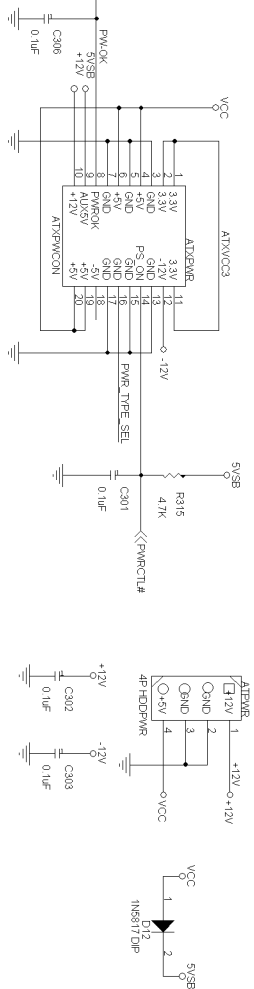


COM2



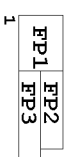
COM2	RS422	RS485
PIN-1	D-	TX-
PIN-2	D+	TX+
PIN-6	CTS	RX+
PIN-9	R1	RX-
U2	1-2	3-4
		5-6



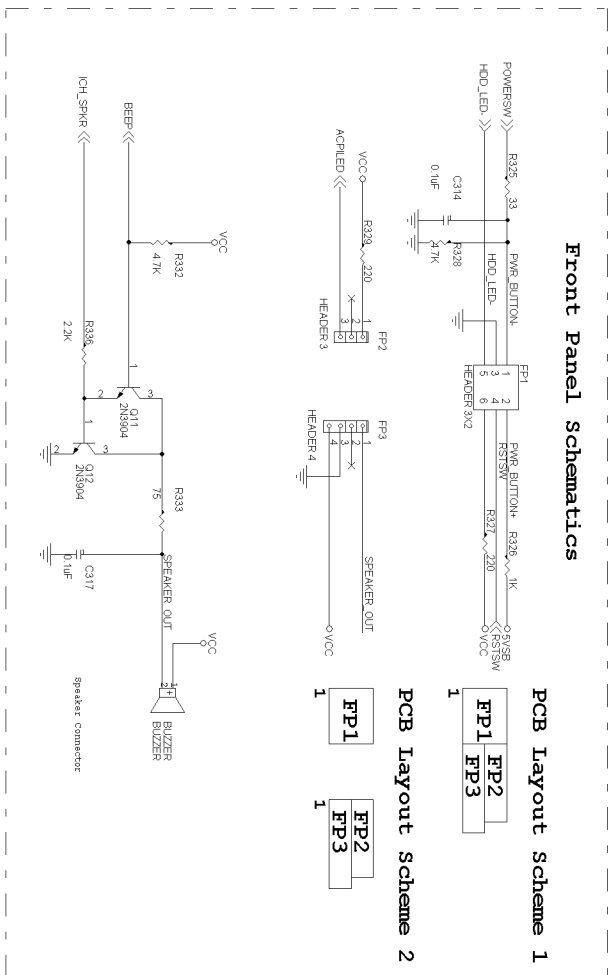
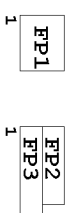


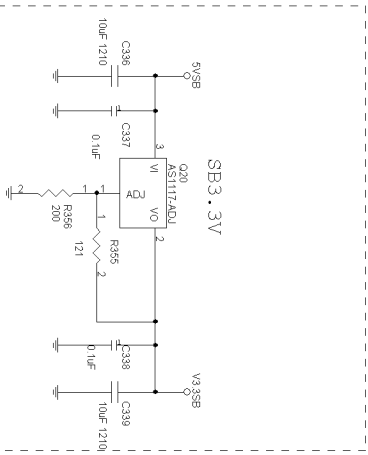
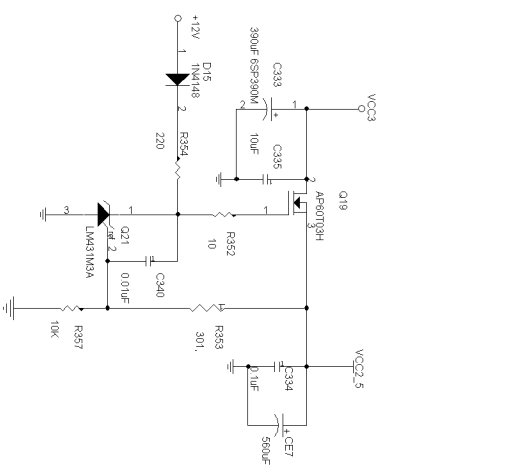
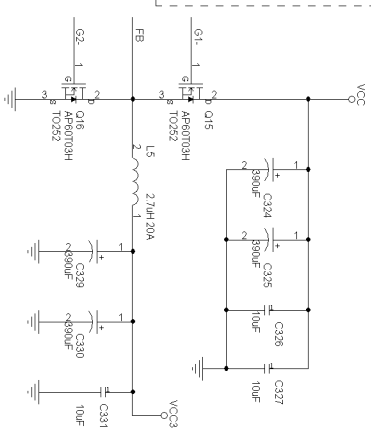
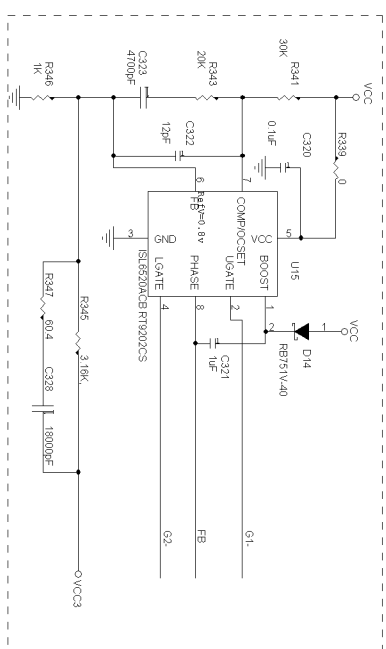
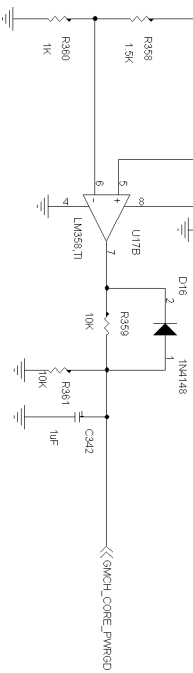
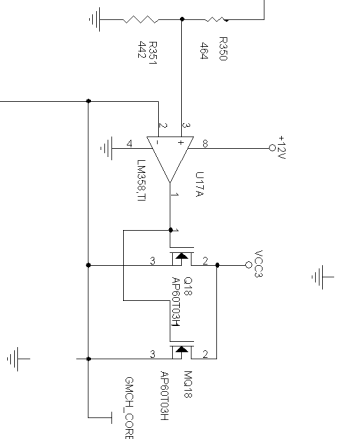
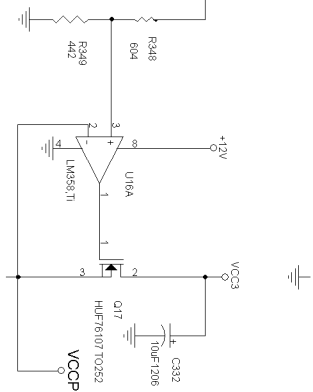
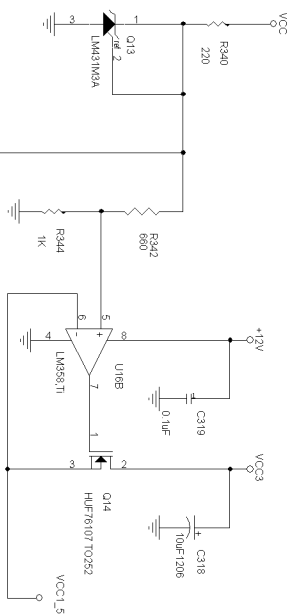
## Front Panel Schematics

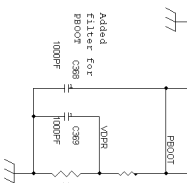
### PCB Layout scheme 1

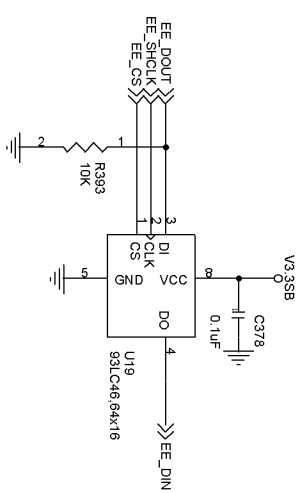
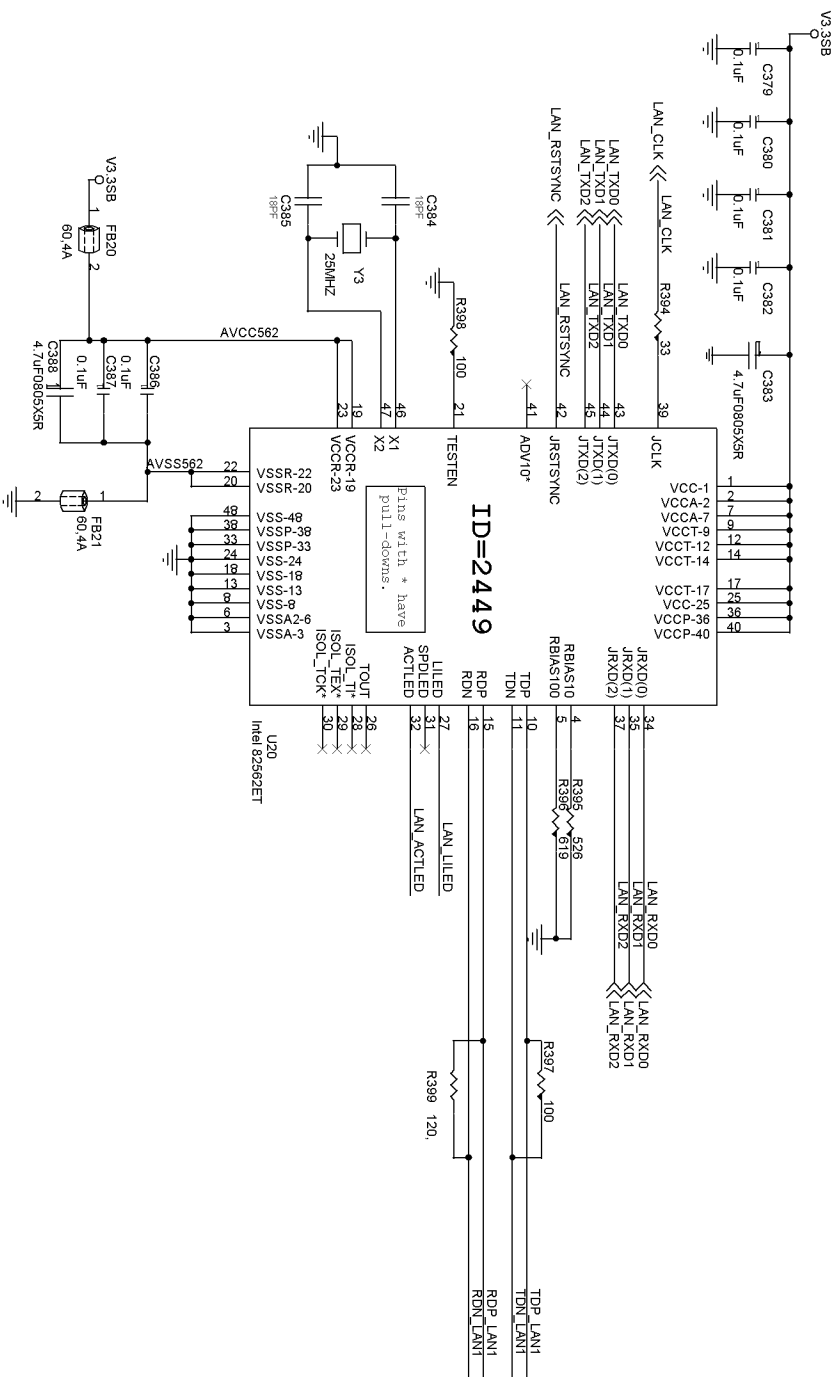


### PCB Layout scheme 2



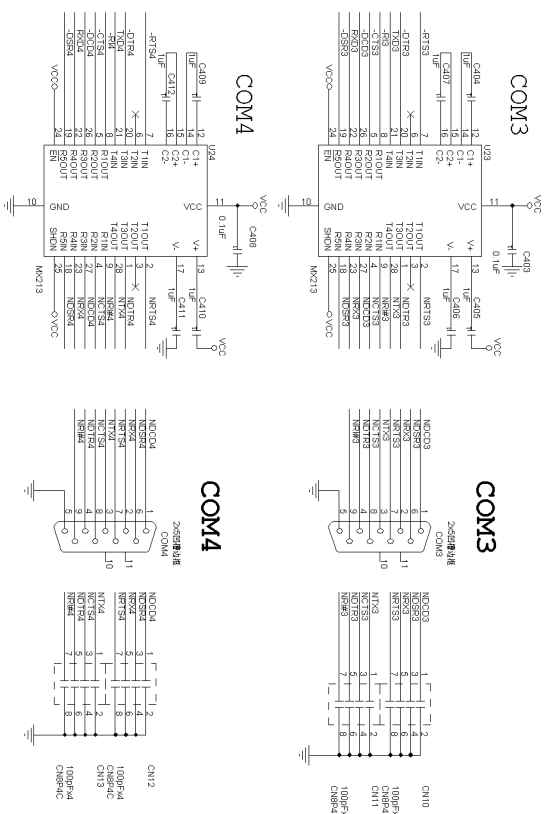












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Checked by				

Version Description				Date	
A0					
A1					
A2					
Project Title				Revision History	
Designed by					
Checked by					