Product Description

T800 is a low cost mobile terminal that uses Atmel ARM9 SoC 32 bit RISC processor which has cache and MMU which is appropriate for any OS. The CPU features 16K instruction and data cache. The CPU has built-in PLL so that the clock rate can be changed to suit different application and maximum operating frequency can be 240MHz. Since the CPU has rich IOs and peripheral controllers, minimum external logic devices are needed.

RF Description

The Wireless Microprocessor is a powerful central processing unit with an ARM9 32bit core,integrated cellular voice,data and wireless Internet connectivity,and unparalleled external peripherals & companion chipset management.

Wireless Microprocessor features VariPower & VariSpeed,which allow for programmable power consumption,in an extremely compact BGA576 package,and has been designed to meet the demand for embedded applications with memory protection and security features.

By combining a memory and required application peripherals with an architecture that makes efficient use of existing resources,the Wireless Microprocessor achieves superior performance while eliminating external components,easing system design and manufacture,and reducing cost compared to alternative solutions.

The ARM clock is programmable up to 104MHz. The DSP clock is at 78MHz. The External Bus Interface clock is set-up to 26MHz.