## AMM240WTD Block Diagram Rx/Tx **RS-232** I/O GF9450 U-COM POWER **POWER** POWER 24LC02 27 MHz **VGA** Ę **POWER** AD9884 R/G/B 24bit 0 A/D Converter H/V CLK 0 DDR2 DDR2 DDR2 DDR2 Analog R/G/B 30 bit PORT A TMDS Data/Clk DVI R/G/B 24bit SIL1161CT 30 bit PORT B DS90C387 LVDS Dual **CLK** R/G/B 48bit 24LC02 **TMDS Receiver** $\bigcirc$ Data/Clk 10 MHz = **GF9450** LVDS PORT LCD Transmitter **PANEL** $\bigcirc$ R/Pr 28.636363 CPLD ô36ა√ MHz OUTPUT G/Y Y/Cb/Cr 30bit $\bigcirc$ G/Y ADV7403 → Rx/Tx CONTROL Component/RGB $\bigcirc$ B/Pb 16bit $\bigcirc$ HS/CS FLASH Y/C 8bit S29GL128N90 VPX3226 $\bigcirc$ VS = Video Decoder CLK **Back Light** GSPI Control 20.25 MHz Composite Key Control (front knob) Dimming **VIDEO** $\bigcirc$ Control 20.25 MHz = Y/C 8bit VPX3226 S-VIDEO Y-C Video Decoder Photo CLK **INVERTER** Sensor U-COM Rx/Tx **Key Control** GS1574 SH7145 Y/C 10bit (backside serial) Temp SD/HD SDI GS1559 Sensor CLK Rx/Tx GS1574 11.0592 MHz **OUT** IN 128Kx8 SRAM GO1528 VCO SRAM GS1578