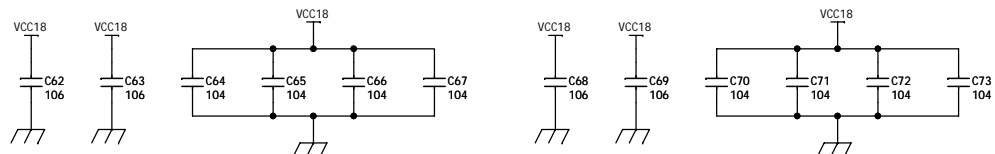
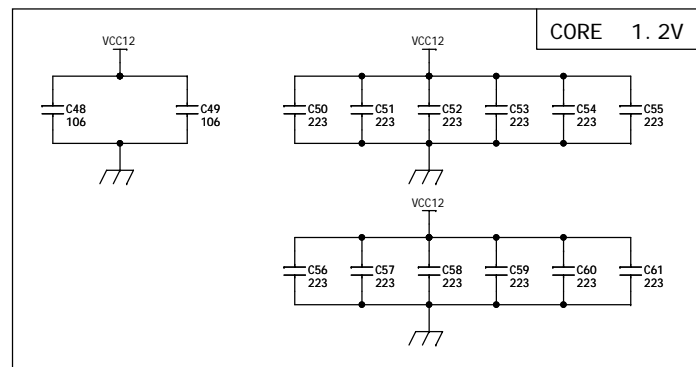
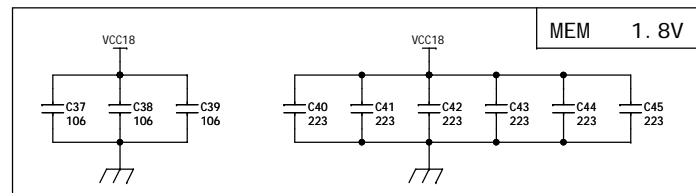
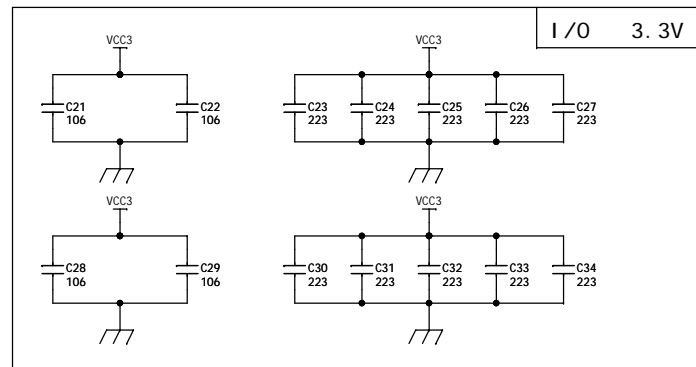
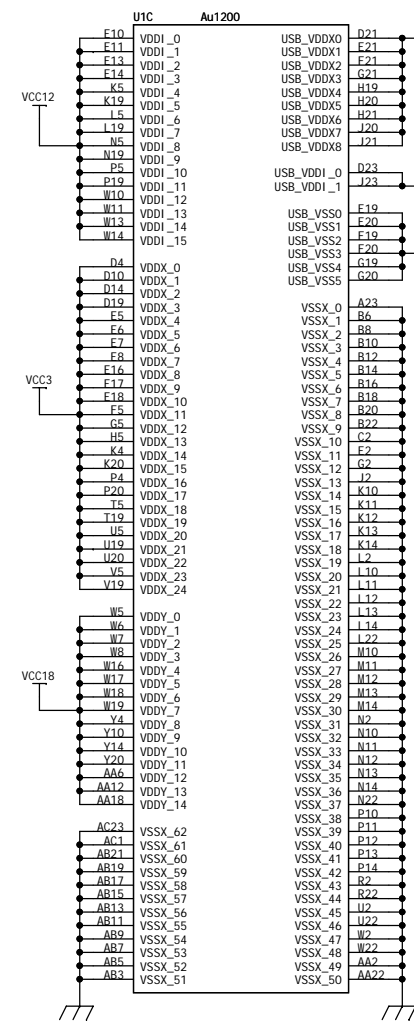
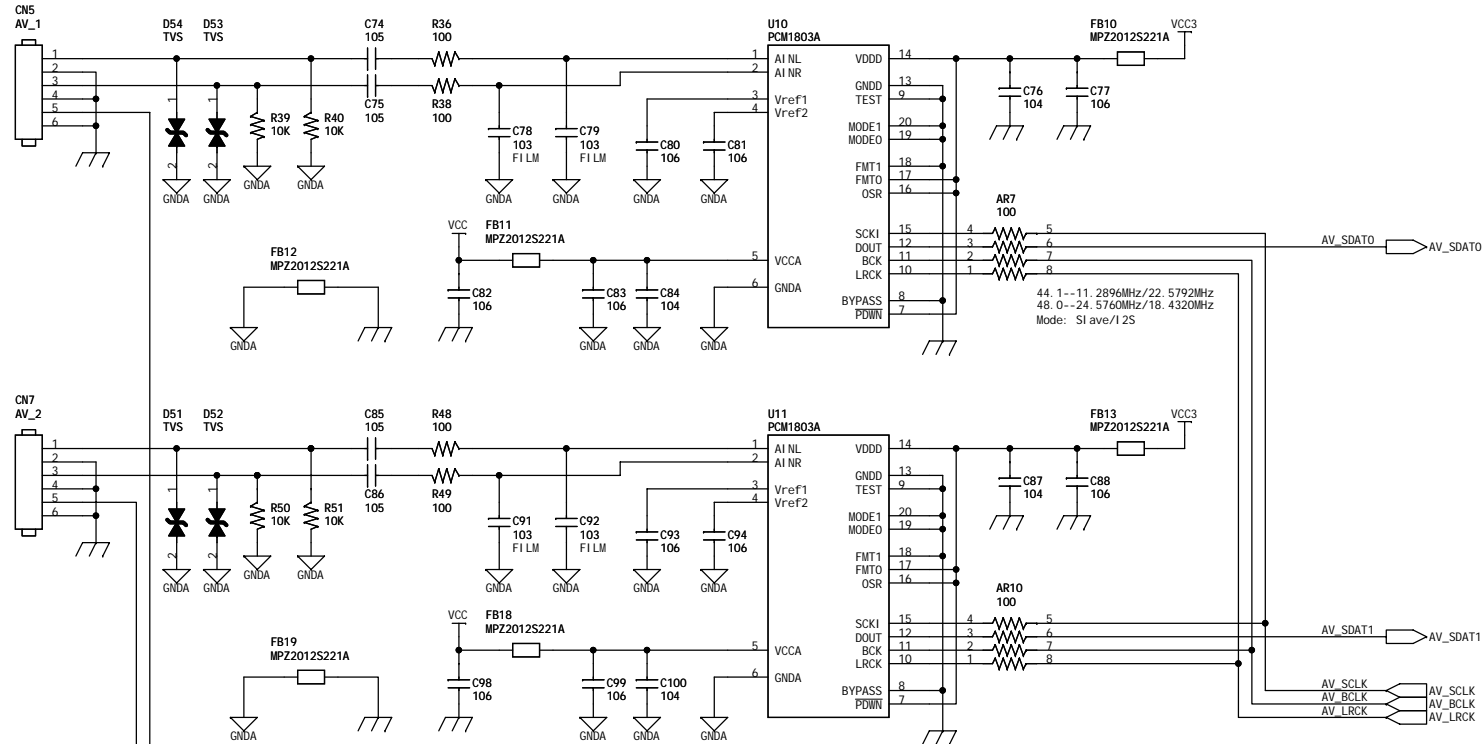


to_FPGA

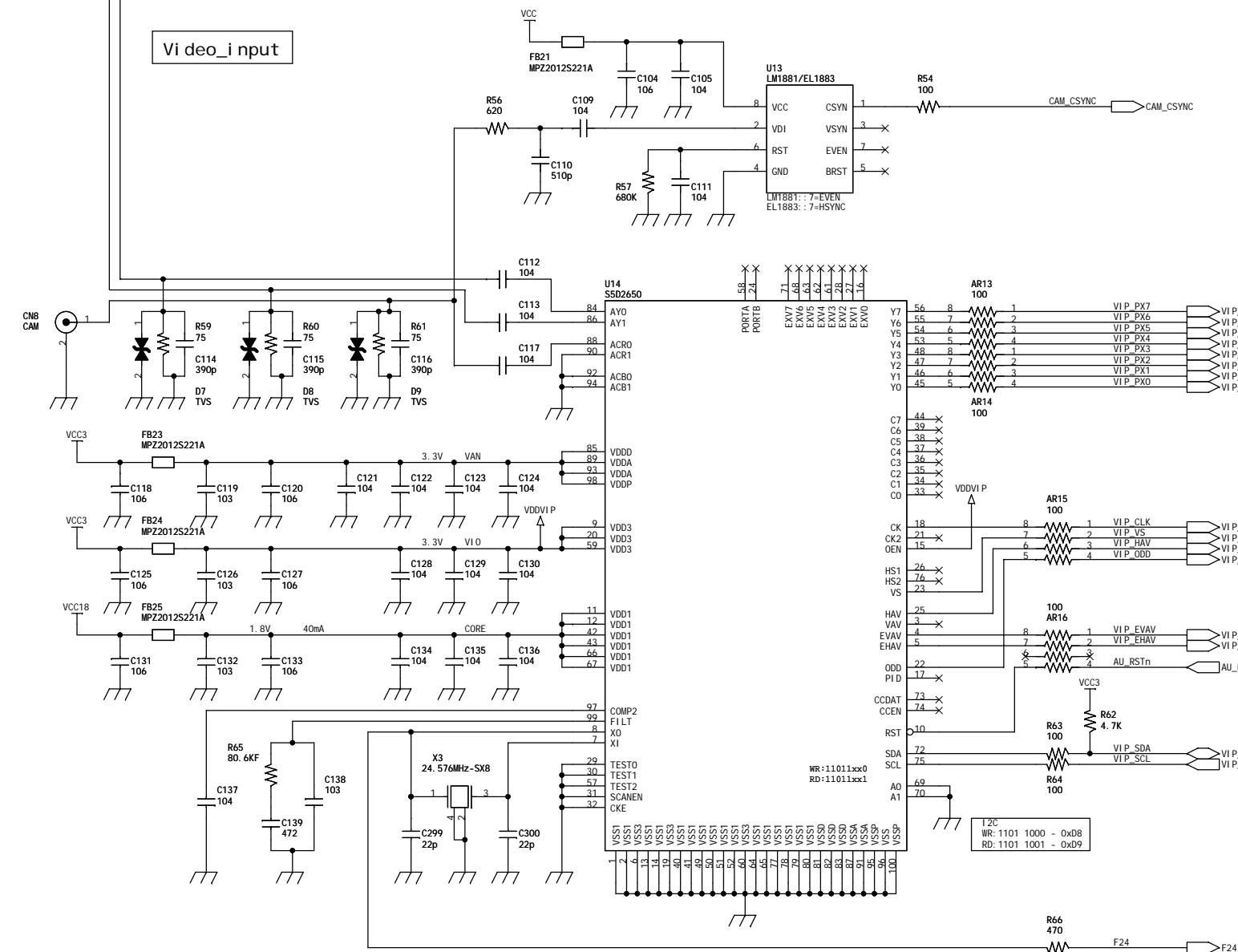
Au1200_PWR_Consumption	DDR2-533_PWR_Consumption
VDDI (1.2V) = 549mA(max)	VDD (1.8V) = 240mA(max)
VDDX (3.3V) = 077mA(max)	
VDDY (1.8V) = 138mA(max)	DDR2x2 = 480mA(max)
VDDY (2.5V) = 209mA(max)	



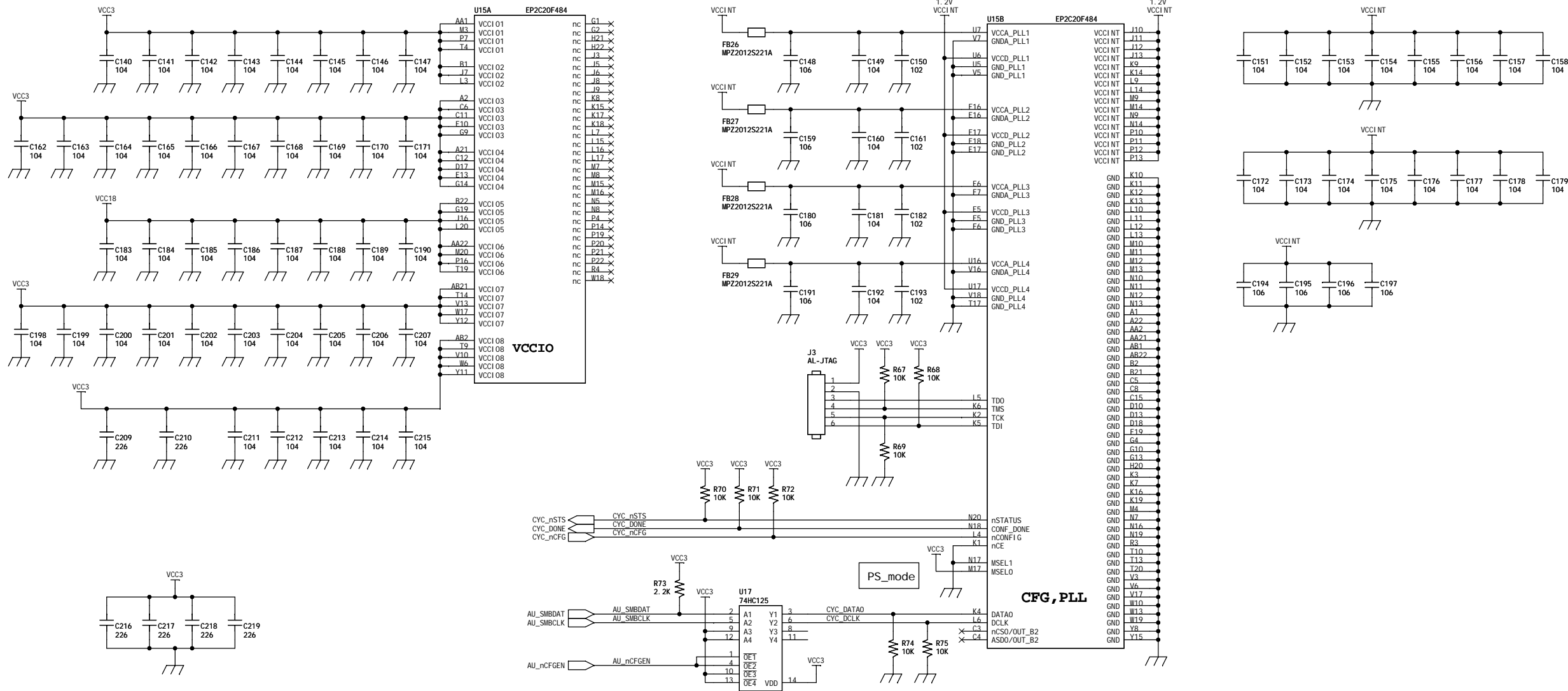
Audi o_i nput

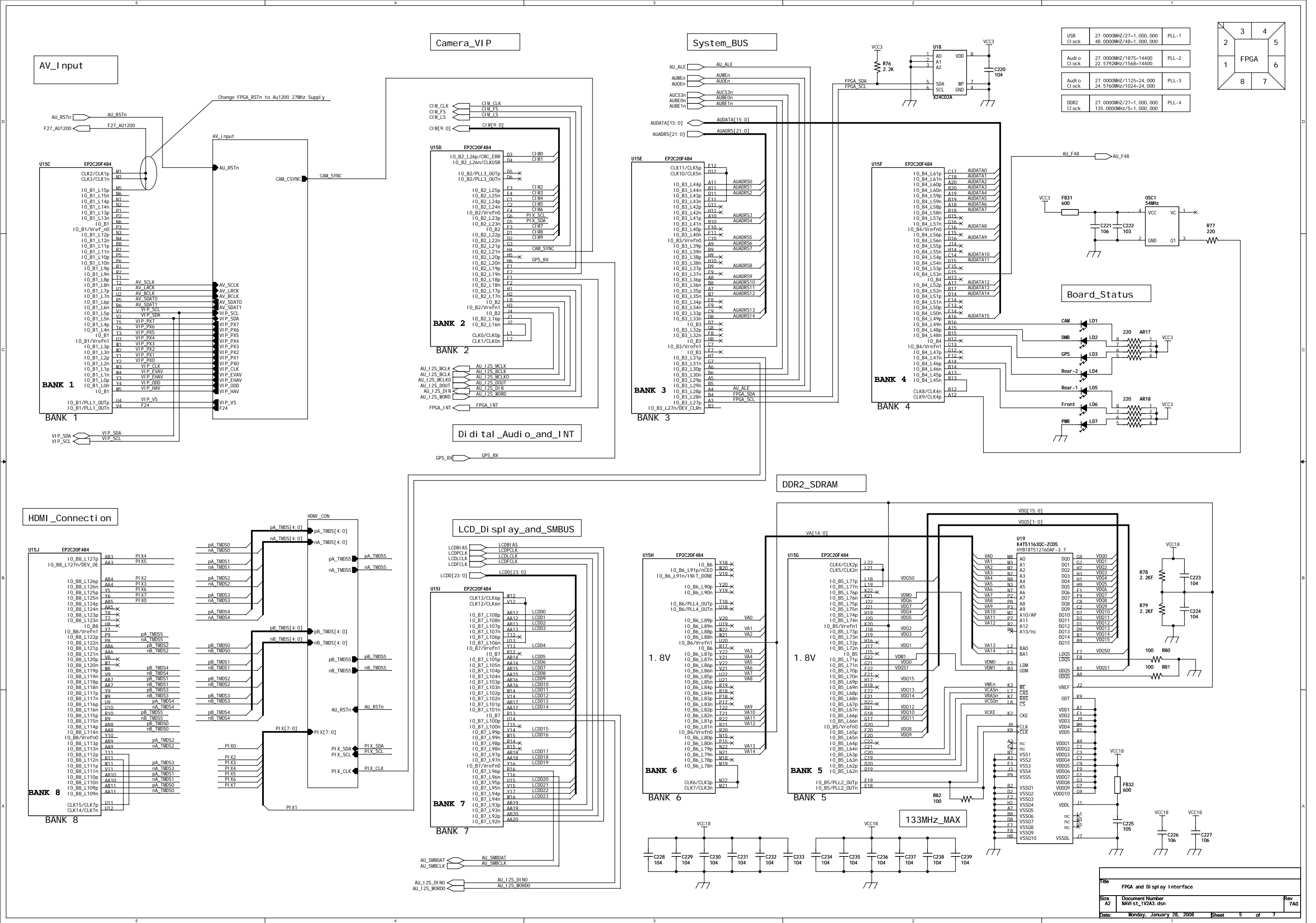


Vi deo_i nput

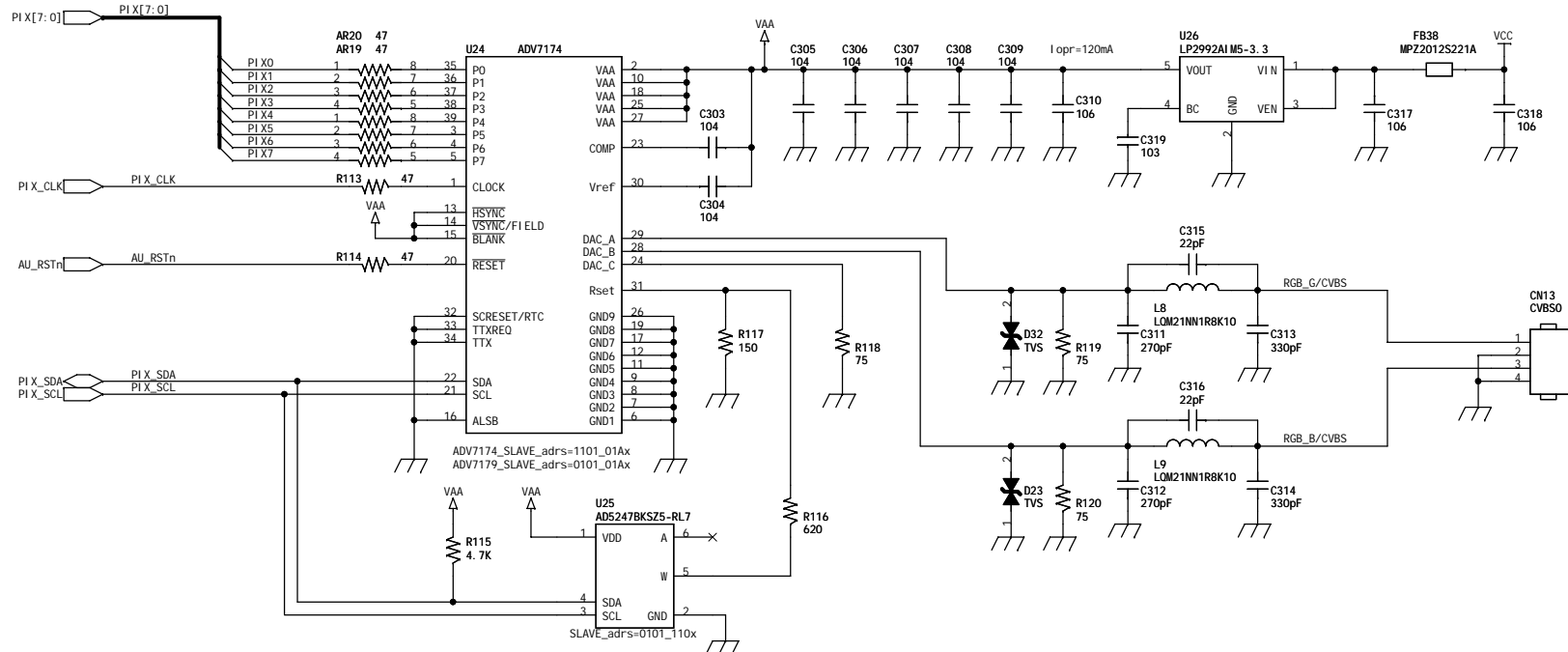
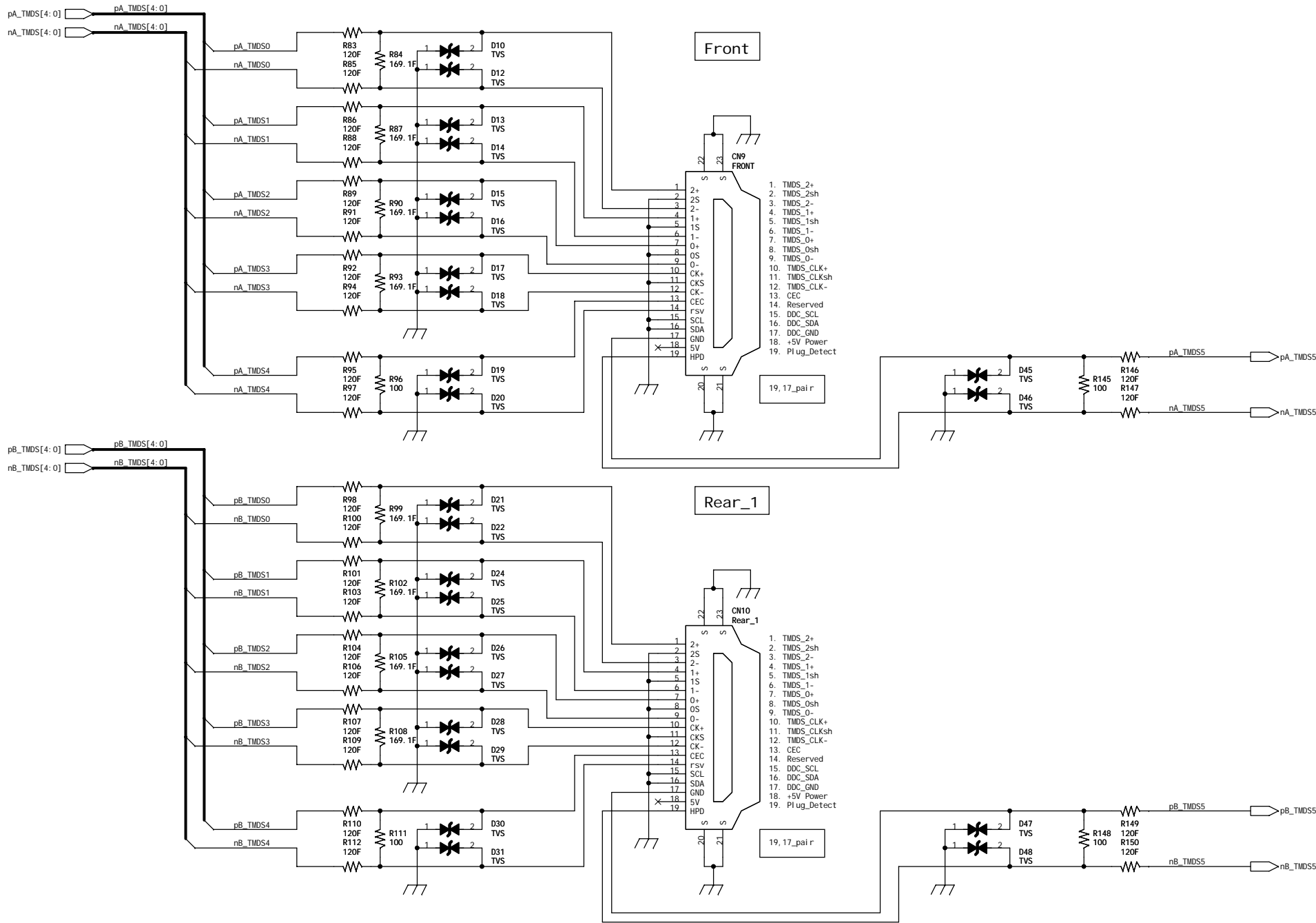


Title				
Audio, Video Input				
Size A2	Document Number NAVI st_1v2A3.dsn			Rev 7A0
Date:	Monday, January 28, 2008	Sheet	3	of 7

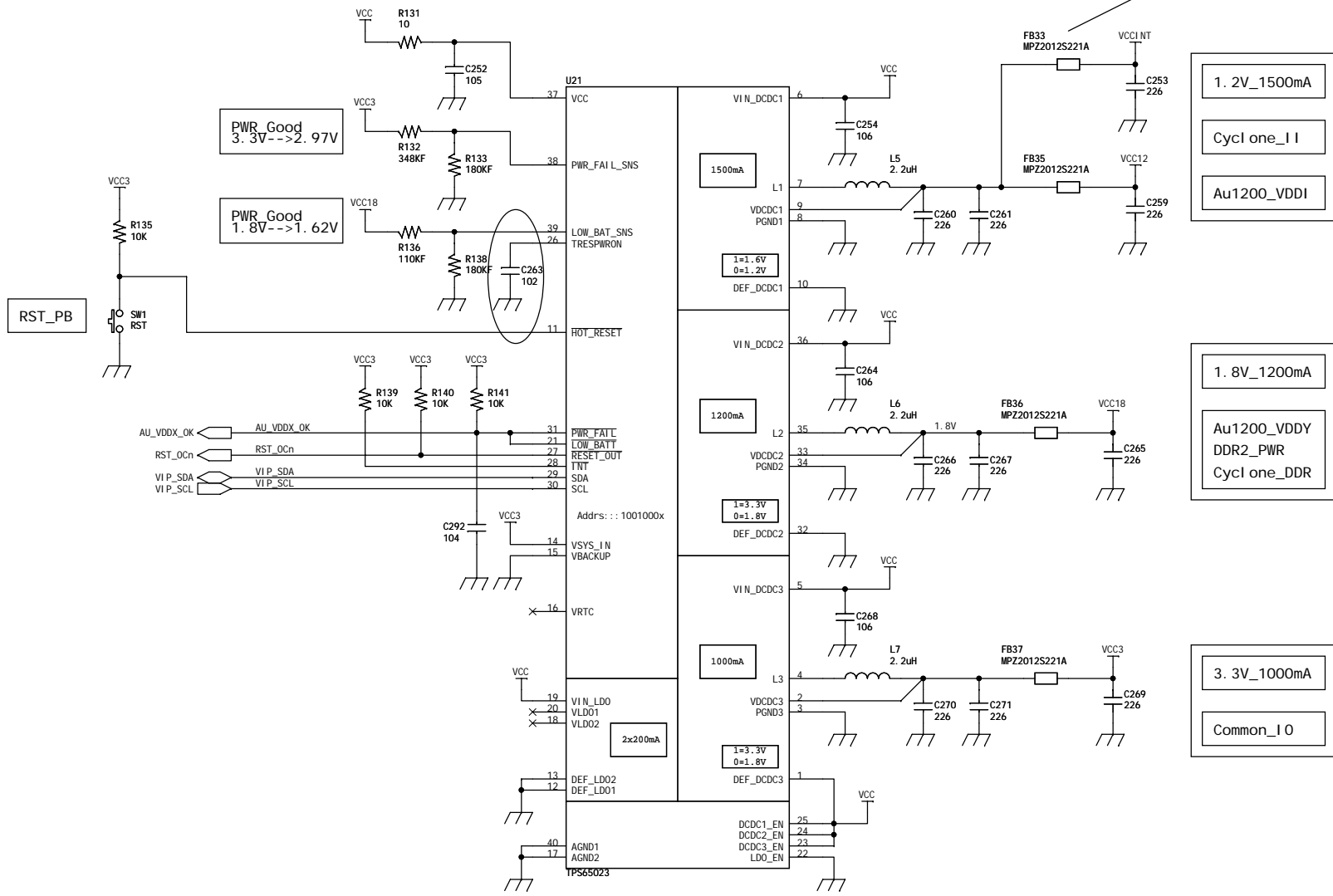
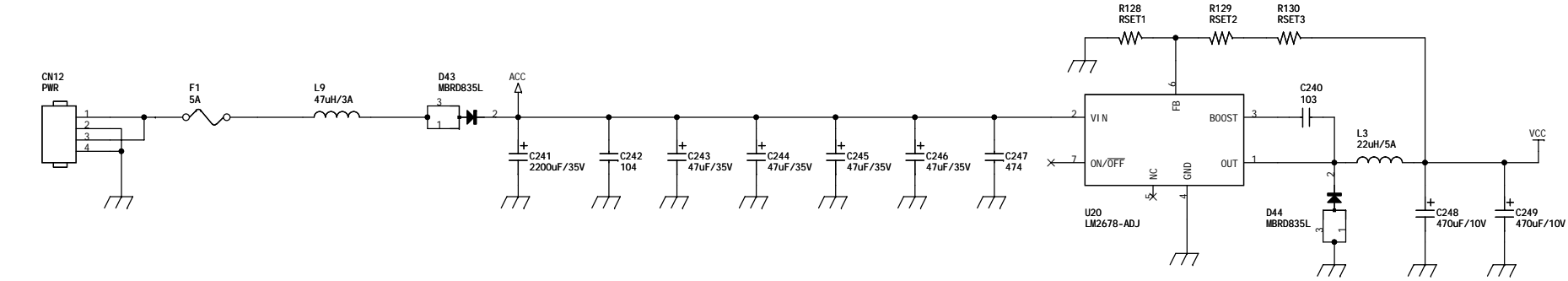




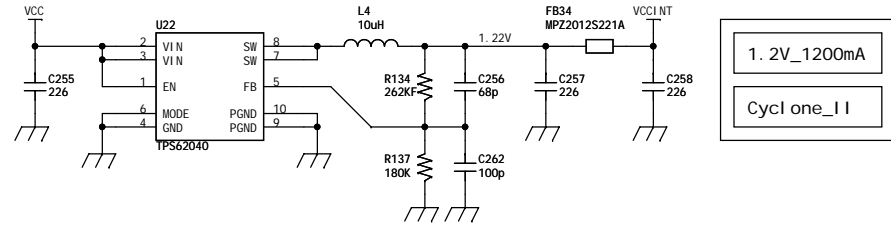
HDMI_Connector



	RSET1	RSET2	RSET3
ADJ	2200	5600	1500
3.3V	NC	3.3K	330



Optional_Insert



Au1200_PWR_Consumption VDDI (1.2V)=549mA(max)_CORE VDDY(1.8V)=138mA(max)_DDR10 VDDX(3.3V)=077mA(max)_I0	DDR2-533_PWR_Consumption Au1200_DDRx2(1.8V)=480mA(max) Total_1.8V=138+480=618mA
FPGA_Power_Consumption VCCINT(1.2V)= VCCIO(1.8V)= VCCIO(2.5V)= VCCIO(3.3V)= DDRx1(1.8V)=240mA(max)	AV_Sheet_PWR_Consumption VIP_Supply_Current(5V)=200mA DMB_PWR_Current(5V)=140mA Audi_o_ADC_Current(5V)=30mA
GPS_USB_PWR_Consumption GPS_Peak_Supply_Current=95mA GPS_Antenna_Supply_Current=20mA USB_Current(5V)=500mA ETC_Current(5V)=500mA Total_Current(5V)=1,115mA	