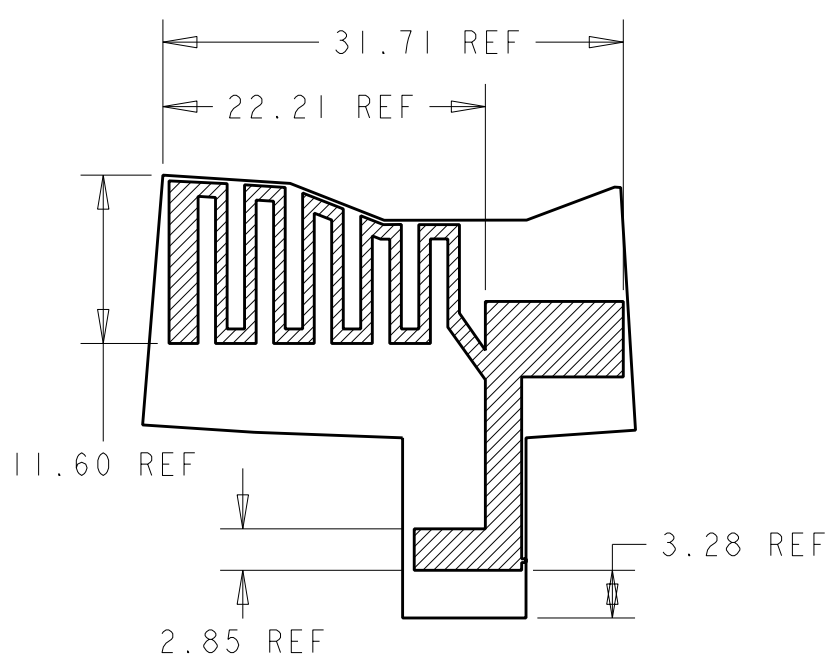
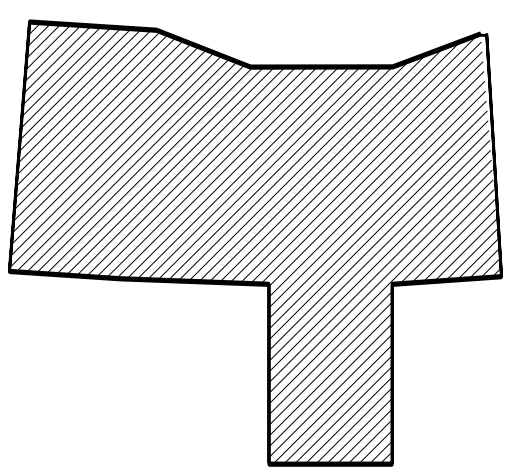


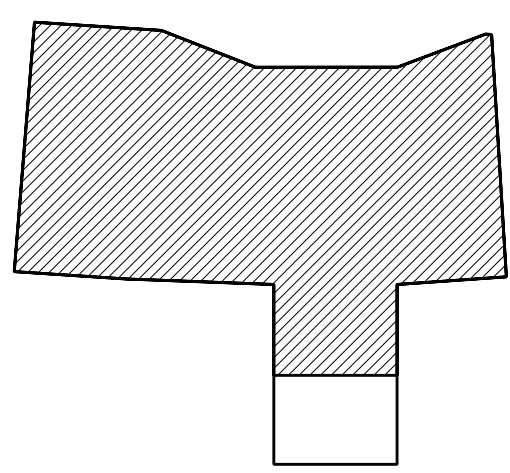
1513815-1 AS SHOWN



TOP CIRCUIT LAYOUT
REFERENCE ONLY



BOTTOM ADHESIVE LAYER
WITH BACKING
REFERENCE ONLY



TOP SOLDERMASK LAYER
REFERENCE ONLY

NOTES: UNLESS OTHERWISE SPECIFIED.


1. PART MARKING: (SILKSCREEN LAYER):
TYCO ELECTRONICS LOGO, PN AND
REVISION APPROXIMATELY WHERE
SHOWN WITH WHITE EPOXY INK.
VENDOR LOGO, DATE CODE, AND UL
CODE APPROXIMATELY WHERE SHOWN
WITH WHITE EPOXY INK.
2. MATERIAL: AP 8525 2 MIL PI WITH
0.5 OZ. COPPER.
BACKING: 3M VHB
ENGINEERING APPROVED EQUIVALENT.
3. OVERPLATING:
PLATING ON EXPOSED COPPER AREAS
ENIG
OR ENGINEERING APPROVED EQUIVALENT.
4. GENERAL TOLERANCES: (UNLESS OTHERWISE SPECIFIED)
COPPER PATTERN: $\pm 0.05\text{mm}$
BOARD OUTLINE: $\pm 0.20\text{mm}$
BOARD OUTLINE TO COPPER PATTERN: $\pm 0.125\text{mm}$
BOARD OUTLINE TO SOLDERMASK: $\pm 0.20\text{mm}$
VENDOR TO COMPENSATE FOR OVER/UNDER ETCH FACTOR
5. PART IS RoHS COMPLIANT PER TYCO ELECTRONICS
STANDARD 230-702.
6. ALL DIMENSIONS ACCORDING TO GERBER FILE:
1513815-1RevA1_gbr.zip.
7. PARTS MUST BE MANUFACTURED IN THE USA.

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LOC
HQ
DIST
00

	AI	Cosmetic updated to gbr. files	09JUL2008	DJL	MAH
	A	Release to production	23JUN2008	DJL	MAH
	3	Note 4 added	26SEP2007	DJL	MAH
P	LTR	REVISION RECORD	DATE	DWN	APVD

DIMENSIONS: mm	TOLERANCES UNLESS OTHERWISE SPECIFIED:
	0 PLC ± 0.50
	1 PLC ± 0.25
	2 PLC ± 0.10
	3 PLC ± 0.05
	4 PLC \pm
ANGLES $\pm 0.5^\circ$	
SURFACE TEXTURE	

DWN DJL	26SEP2007	MATERIAL -	HEAT TREAT -	
CHK -	-	-	-	
APVD -	-	 Tyco Electronics Tyco Electronics Rochester, New York 14623		
NAME US Dual Band FPC				
SCALE -	SIZE A3	DRAWING NO 1513815	SHEET 1	OF 1
			REV A1	