







[4,5,8,11,12,13,16,17] VPH_PWR _____ VPH_PWR [4,5,8,11,12,13,16,17] VBATT_TEMP [8] [8] KYPD_LED_DRV_N [3] R_LED_EN 1 WT -----[3] B_LED_EN VREG MSMP [1,2,3,4,5,8,12,13,14,15,17] [3] G_LED_EN ____ ______ [8] VREG_SD -C519 ___ C520 ___ [5] VCC_CARD1 ____ \circ \circ \circ [3] SDC1_DATA2 [3] SDC1_DATA3 [3] SDC1_CMD [3] SDC1_DATA0
[3] SDC1_DATA1 [1,2,3,4,5,8,12,13,14,15,17] R516 [5] DATA_CARD1 ____ [3] SDC1_DET CLK_CARD1 RST_CARD1 -----VPH_PWR [4,5,8,11,12,13,16,17] VPH_PWR [4,5,8,11,12,13,16,17] ______ CLK_CARD1 [5] [5,8] MSM_SIM1_CLK VCC_CARD1 [5] _ C522 __ C521 R532 VREG_GP3 [5,8] MSM_SIM2_DATA [5,8] DATA_CARD1 [5] [3,5] UIM_SIM_SW MSM_USB_DN ____ [5] VCC_CARD2 ____ UIM_SIM_SW [3,5] U506 [5,8] MSM_SIM2_CLK ____ 11 RST_CARD1 [5]
10 UIM_SIM_SW [3,5] [3,5] UIM_SIM_SW C531 ___ C535 [5,8] VREG_GP3 -C523 _____ __ C524 [5] DATA_CARD2 ____ [5,8] MSM_SIM1_DATA CLK_CARD2 RST_CARD2 [5,8] MSM_SIM1_RST [5,8] MSM_SIM2_RST ____

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