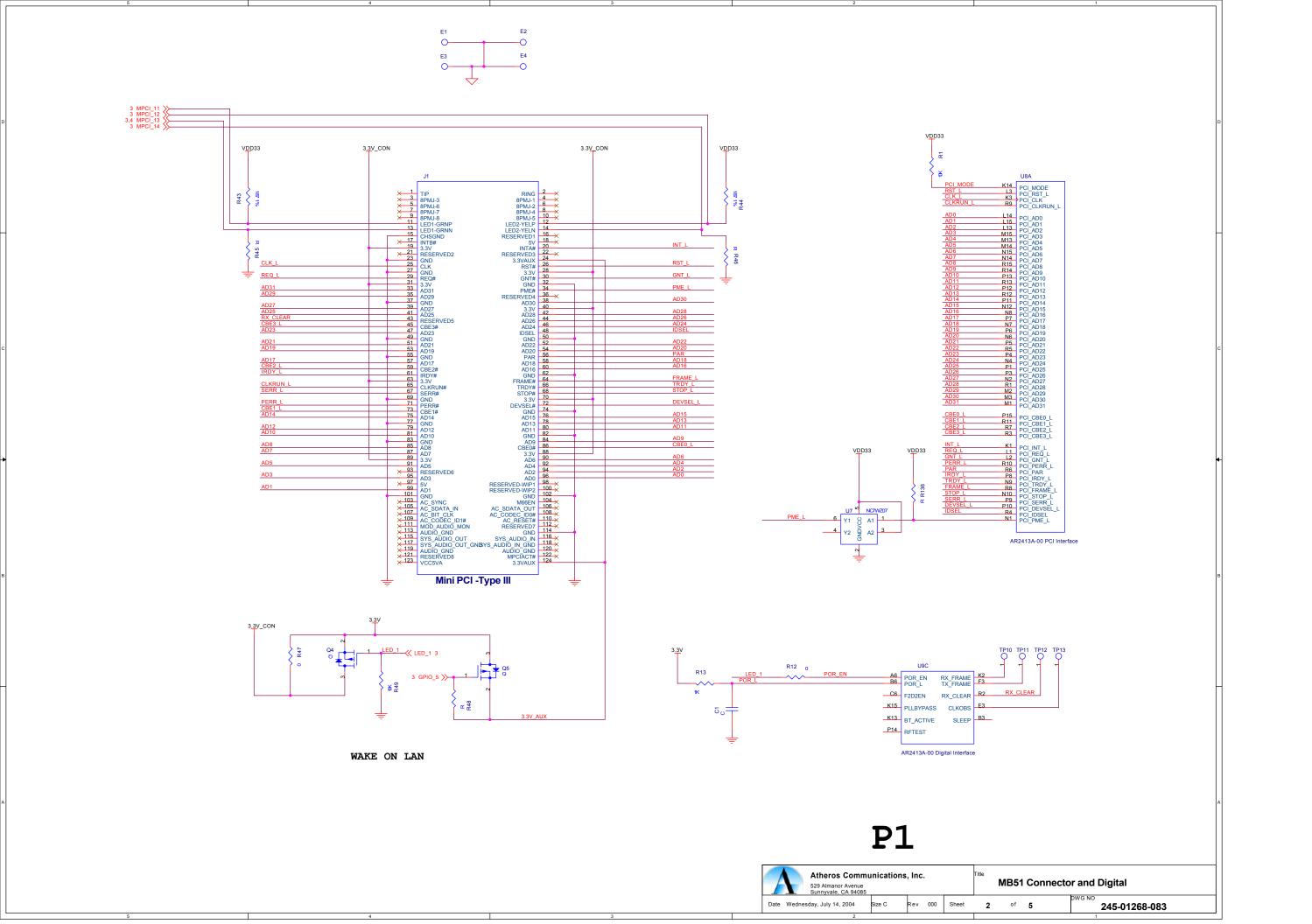
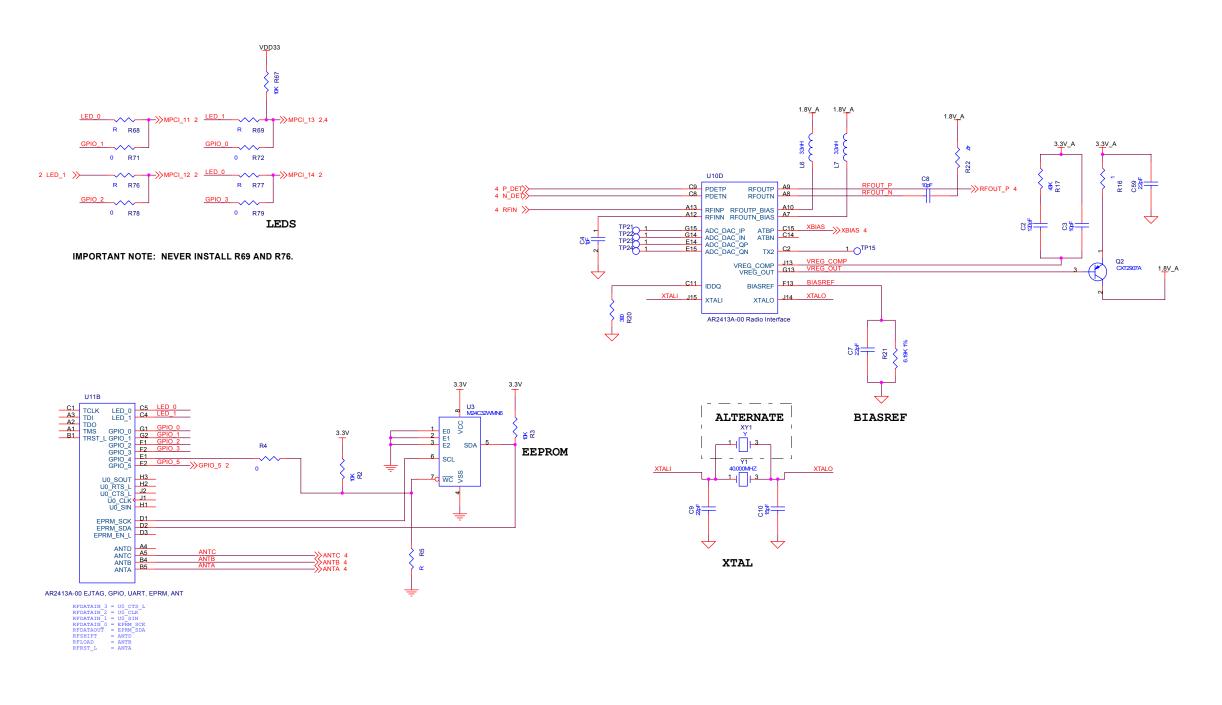
## MB51 - AR5005G Based 802.11b/g Type IIIb Mini PCI

**ATHEROS** 

1/8/04   245-01268-010, Rev 000   CL	BATE	REVISION NUMBER	INITIALS	1) Change C22 to R131. Change C24 to R134. Change C29 to R133. Change C42 to L13. Change L10 to C147. Delete C23, C25. And C143, 1uF, 0603 to input of VR3.
Change R53 from 2.2k to 1.1k. Change R52 from 1.5k to 750   3] XLNA input match: Change C45 from 10pf to 15pF Change C47 fom 1pf to 1.5pf   4] XTAL: Change C9 from 24pf to 22pf. Change C10 from 10pf to 15pF   5] XTAL: Change C9 from 24pf to 22pf. Change C14 from 10pf to 15pF   6] XTAL: Change C9 from 24pf to 22pf. Change C14 from 3.3pf DNL Change C3 from 10pf to 15pC DNL Change R31 from 10 to 0. Change R32 from 22 to 0. Change R129 from 0 to 10p. Change R310 from 10 to 150   3] XPA Supply: Change R130 from 10 to 150   3] XPA Supply: Change C77 from 10pf to 15pf. Change C16 from 4.7uf to DNL Change VR3 from 40 from 4.7uf to DNL Change VR3 from 40 from 4.7uf to DNL Change VR3 from 40 from 4.7uf to DNL Change C47 from 10pf to 15pf. Change C47 from 4.7uf to DNL Change C47 from 10pf to 15pf. Change C47 from 4.7uf to DNL Change C47 from 50 from 50 from 50 from 4.7uf to DNL Change C47 from 4.7uf to DNL Change C47 from 50 from 50 from 50 from 50 from 50 from 4.7uf to DNL Change C47 from 4.7uf to 15pf to 12pf 4 (2pf 4)	1/8/04	245-01268-010, Rev 000	CL	Add RF Kill Circuit R135,0402, Q7, 2N3906.
1.5pF 4) XTAL: Change C9 from 24pF to 22pF. Change C10 from 10pF to 15pF 3/17/04 245-01268-030, Rev 000 CL 1) RF_IN Match: Change L13 from 1.6nH to C149 2.2pF. Change C147 from 3.3pF DNI. Change C78 from 10pF to R137 0.5hm 2) XPA Blas: Change R111 from 10 to 10. Change R31 from 22 to 0. Change R129 from 0.5 to 10p. Change R120 from 0.5 to 10p. Change C140 from 10p. to 5.8pF 4) 1.5x Regulator: Change C3 from DNI to MMBT2907A. Change C140 from 4.7x to 10p. Change VR3 from AME886MBFTto DNI 9) RPGUT_BIAS: Change L8 supply from 3.3v_A to 1.8v_A. Change L7 supply from 3.3v_A to 1.8v_A. 6) PME_L: Add R13s 10k (0402) pullup 7) XLNA Blass: Change C3 from 0.01uF to DNI 8) XPA: Change U8 from XS552E to 6F1214  245-01268-031 CL 1) XLNA: Change C41 from 10pF 14 to 10pF 5% 2) RF_IN Match: Change C41 from 10pF 10 7.2pF 3) Bypsassing: Change C59 from 10pF to 7.2pF 4) External Detector: Change R61 and R84 from 1.1k to 5.1k 5) XPA Blass: Change R127 from 10 to 100 (again) 4/1/104 245-01268-032 CL 1) Mode Pins: Change R17, R3 from 10k 11 kto 10k 5% 2) RF_KIII: Change R67 from DNI to 10k 3) PME: Change R135 from 10k to DNI (10k) 4/27/04 245-01268-050 JW Changed R7 fro 0402. Disconnect J1, p15. Delete R2 and siege net. Sway U P8.P8. Remove R50. Remove Q6. Change R48 to R between 3.3v_AUX and GPIO_3. Changed R73 from 10k to DNI (10k) 4/27/04 245-01268-050 JW Changed R71 to 0402. Disconnect J1, p15. Change C8 from 10pF. Changed C77, C25, C38, C43, C44, C74 to 3.0pF. Changed C77, C25, C38, C43, C44, C74 to 3.0pF. Changed C77, C25, C38, C43, C44, C74 to 3.0pF. Changed C77, C25, C38, C43, C44, C74 to 3.0pF. Changed C77, C25, C38, C43, C44, C74 to 3.0pF. Changed C77, C25, C38, C43, C44, C74 to 3.0pF. Changed C77, C25, C38, C43, C44, C74 to 3.0pF. Changed C77, C25, C38, C43, C44, C74 to 3.0pF. Changed C77, C25, C38, C43, C44, C74 to 3.0pF. Changed C78 to 11, S74, Change C78 to T0pN. to 11, S74, Change C78 to 10pN. to 11, S74				Change R53 from 2.2k to 1.1k. Change R52 from 1.1k to 750
245-01288-030, Rev 000				
DNI. Change C78 from 10pF to R137 0 ohm 2   XPA Blass: Change R112 from 10 to 10. Change R31 from 22 to 0. Change R129 from 0 to 100. Change R313 from 10 to 15. 3   XPA Blass: Change R315 from 10 to 15. 4   1.8V Regulator: Change Q2 from DNI to MMBT2907A. Change C140 from 4.7uf to 10Ni. Change V87 from AMESSOMEFT to NI. 5   RFOUT_BIAS: Change L6 supply from 3.3V_A to 1.8V_A. Change L7 supply from 3.3V_A to 1.8V_A. 6   PME_L. Add R136 10K (1042) pullup 7   XLNA Blass: Change C39 from DNI to 10Ni 8   XPA: Change C39 from 10 to 10 to NNi 8   XPA: Change C39 from 10pF to 2.2pF 4   External Detector: Change R31 and R34 from 1.1k to 5.1k 5   XPA Blass: Change C39 from 10pF to 1pF 3   Bypassing: Change C59 from 10pF to 1pF 4   External Detector: Change R31 and R34 from 1.1k to 5.1k 6   XPA Blass: Change R128 from 10 to 100 (again) 4   XIII				
2) XPA Bias: Change R111 from 10 to 0. Change R131 from 22 to 0. Change R129 from 0 to 100. Change R132 from 0 to 105 (3) XPA Supply: Change C77 from 10pF to 5.6pF. Change C76 from 10pF to 5.6pF.	3/17/04	245-01268-030, Rev 000	CL	1) RF_IN Match: Change L13 from 1.6nH to C149 2.2pF. Change C147 from 3.3pF to
3) XPA Supply: Change C77 from 10pF to 5.6pF   4) 1.8V Regulator: Change Q27 from DNI to MMST2097A. Change C74 from 10pF to 5.6pF   4) 1.8V Regulator: Change Q27 from DNI to MMST2097A. Change C14 from 7.1V to DNI. Change VR3 from AME8805MEFT to DNI   5) RFOUT_BIAS: Change L6 supply from 3.3V_A to 1.8V_A. Change L7 supply from 4.3V_A. Change L7 supply from 4.3V_A. Change L7 supply from 5.2pC supply from				
4  1.8% Regulator: Change Q2 from DNI to MMBT2907A. Change C140 from 4.7ut to DNI. Change VR3 from AMBES6MEFTED DNI. Shange VR3 from Masses VR3 from Change VR3 from AMBES6MEFTED DNI. Shange VR3 from Change VR3 from Masses VR3 from 3.3V A to 1.8V_A. Change L7 supply from 3.3V_A to 1.8V_A. Change L7 supply from 3.3V_A to 1.8V_A. Change L7 supply from 3.3V_A to 1.8V_A. Change VR3 from 0.01ut fo DNI. Shange VR3 from 1.0t for 1.0t fo				
5) RFOUT_BIAS: Change L6 supply from 3.3" A to 1.8" A. Change L7 supply from 3.3" A to 1.8" A. 6) PME_L: Add R136 10K (0402) pullup 7) XLNA Bias: Change G3 from 0.01tl fo DNI 8) XPA: Change U6 from LX5512B to GP1214  3/36/04 245-01268-031				4) 1.8V Regulator: Change Q2 from DNI to MMBT2907A. Change C140 from 4.7uF
3.3V A to 1.8V A   6   1.8V A				to DNI. Change VR3 from AME8805MEFTto DNI 5) RFOUT_BIAS: Change L6 supply from 3.3V_A to 1.8V_A. Change L7 supply from
77 XLNĀ Blasc Change C39 from 0.01uF to DNI 8 XPA: Change U6 from L051uF to DNI 8 XPA: Change U6 from L051uF to DNI 8 XPA: Change U6 from L051uF to DNI 8 XPA: Change C41 from 10pF to 10pF 5% 2 RF_INI Match: Change C41 from 10pF to 10pF 5% 2 RF_INI Match: Change C41 from 10pF to 10pF 5% 3 Bypassing: Change C59 from 10pF to 2.2pF 4 External Detector: Change R81 and 8from 1.1K to 5.1K 5 XPA Blas: Change R129 from 10 to 100 (again) 4/1/104 245-01268-032 CL 1) Mode Pins: Change R81 R3 from 10K 1% to 10K 5% 2) RF Kill: Change R87 from DNI to 10K 4/6/04 245-01268-040 JW Change C6, C14 to 0402. Disconnect J1,p16. Delete R2 and sleep net. Swap U P8,P9. Remove R50. Remove Q6. Change R48 to R between 3.3V_AUX and GPIO_3. Change R87 from E00 to 10pF. Change R88 to R between EED_1 and GND. 4/27/04 245-01268-050 JW Change R87 to 0805 package. Change R48 to R between 3.3V_AUX and CPIO_3. Change R87 to 0805 package. Change R88 to R between C8 to 10pF. Changed C27, C28,C38,C43,C44,C74 to 3.0pF. 4/27/04 245-01268-051 CL Change R133, R134 from 220 5% to 221 1%. Changed C27, C28,C38,C43,C44,C74 to 3.3pF. 5/6/04 245-01268-060 JW Change R12,R13 from DNI to 10k. Change C1 from DNI to 0.1uF 5/28/04 245-01268-060 JW Change R12,R13 from DNI to 10k. Change C1 from DNI to 0.1uF 6/4/04 245-01268-070 JW Move C140 to 1.8V_A. Change R48 and R49 to DNI. Remove VR3,C57. Move C148 from 3.3V. 6/4/04 245-01268-080 VN Fixed pin out of Q2. 6/11/04 245-01268-081 CL Excel BOM hand-edited. EPICOM PA added as Alternate Vendor. PCB part numbrup-rev'd as well as header and footer information. 7/6/04 245-01268-082 CL Changed R130 from 150 to 200.				
3/38/04				7) XLNA Bias: Change C39 from 0.01uF to DNI
2   RF, IN Match: Change C4 from 10pF to 1pF   3) Bypassing: Change C59 from 10pF to 2.2pF   4) External Detector: Change R81 and R84 from 1.1K to 5.1K   5) XPA Blas: Change R12 from 10 to 100 (again)   4/1/104   245-01268-032   CL   1) Mode Pins: Change R1, 37 from 10k 1% to 10k 5%   2) RF Kill: Change R67 from DNI to 10k (10k)   245-01268-040   JW   Change R67 from DNI to 10k to DNI (10k)   Change R136 from 100k to DNI (10k)   Change R67 from DNI to 10k (10k)   Change R67 from DNI to 18v (10k)   Change R67 from DNI to 10k (10k)   C				8) XPA: Change U6 from LX5512B to GP1214
3] Bypassing: Change C59 from 10pF to 2.2pF	3/30/04	245-01268-031	CL	1) XLNA: Change C41 from 10pF 1% to 10pF 5%
4) External Detector: Change R81 and R84 from 1.1K to 5.1K 5) XPA Bias: Change R129 from 10 to 100 (again) 4/1/04 245-01268-032 CL 1) Mode Pins: Change R1, from 10K to 10K 5% 2) RF Kill: Change R67 from DNI to 10K 3) PME: Change R136 from 100K to DNI (10K) 4/6/04 245-01268-040 JW Change R6, C14 to 0.402. Disconnect J1, p16. Delete R2 and sleep net. Swap U P8, P9. Remove R50. Remove GC change R48 to R between 3.3V_AUX and GPIO_3. Change R49 to R between LED_1 and GND. 4/27/04 245-01268-050 JW Changed R47 to 0.805 package. Changed R131 to 22 ohms. Changed R133,R134 to 220 ohms. Changed R27 (25.638,643,644,674 to 3.0pF. 4/27/04 245-01268-051 CL Change R133, R134 from 220 5% to 221 1%. Changed C27, C28,C38,C43,C44,C74 to 3.3pF. 5/6/04 245-01268-052 CL POR: Change R12,R13 from DNI to 10k. Change C1 from DNI to 0.1uF 5/28/04 245-01268-060 JW Changed R12 to 0 ohm. Changed C1 to DNI. R12 is now connected to LED_1 instead of 3.3V. 6/4/04 245-01268-070 JW Move C140 to 1.8V_A. Change R48 and R49 to DNI. Remove VR3,C57. Move C148 from 3.3V to 1.8V. Change C1 to U1pE2. Added R4, 0 OHM, to U1 pE1 and U3 p7. Added pullup R2, 10 OHM to U3 p7. Added R5 DNI between GND and U3 p7. Change R16 to 0.603. 6/9/04 245-01268-080 VN Fixed pin out of Q2. Changed R13 to 1K. Disconnected U1pD3 6/9/04 245-01268-081 CL Excel BOM hand-edited. EPICOM PA added as Alternate Vendor. PCB part numbrup-rev'd as well as header and footer information.				
Alt/104				4) External Detector: Change R81 and R84 from 1.1K to 5.1K
2   RF Kill: Change R67 from DNI to 10K   3   PME: Change R67 from DNI to 10K   3   PME: Change R136 from 100K to DNI (10K)				5) XPA Bias: Change R129 from 10 to 100 (again)
3) PME: Change R136 from 100K to DNI (10K)   4/6/04	4/1/04	245-01268-032	CL	1) Mode Pins: Change R1, R3 from 10K 1% to 10K 5%
A/6/04				
P8,P9. Remove R50. Remove Q6. Change R48 to R between 3.3V_AUX and GPIO_3. Change R49 to R between LED_1 and GND.  4/27/04 245-01268-050 JW Changed R47 to 0805 package. Changed R131 to 22 ohms. Changed R133,R134 to 220 ohms. Changed R22 pin 2 from GND to 1.8V_A. Changed C8 to 10pF.  Changed C27, C28,C38,C43,C44,C74 to 3.0pF.  4/27/04 245-01268-051 CL Change R133, R134 from 220 5% to 221 1%.  Changed C27, C28,C38,C43,C44,C74 to 3.3pF.  5/6/04 245-01268-052 CL POR: Change R12,R13 from DNI to 10k. Change C1 from DNI to 0.1uF  5/28/04 245-01268-060 JW Changed R12 to 0 ohm. Changed C1 to DNI. R12 is now connected to LED_1 instead of 3.3V.  6/4/04 245-01268-070 JW Move C140 to 1.8V_A. Change R48 and R49 to DNI. Remove VR3,C57. Move C148 from 3.3V to 1.8V. Change Q1 to S0T89 CXT2907A. Changed Q5p1 to GPIO_5 and connected to U1pE2. Added R4, 0 OHM, to U1 pE1 and U3 p7. Added pullup R2, 10 OHM to U3 p7. Added p2 ohm to U3 p7. Added pullup R2, 10 OHM to U3 p7. Added pullup R2, 10 OHM to U3 p7. Added pullup R2, 10 OHM to U3 p7. Added pallup R2, 10 OHM to U3 p7. Added pullup R2, 10 OHM to U3 p7. Added pallup R2, 10 OHM to	4/6/04	245 04269 040	130/	
4/27/04       245-01268-050       JW       Changed R47 to 0805 package. Changed R131 to 22 ohms. Changed R133,R134 to 220 ohms. Changed R22 pin 2 from GND to 1.8V_A. Changed C8 to 10pF.         4/27/04       245-01268-051       CL       Change R133, R134 from 220 5% to 221 1%. Changed C27, C28,C38,C43,C44,C74 to 3.3pF.         5/6/04       245-01268-052       CL       POR: Change R12,R13 from DNI to 10k. Change C1 from DNI to 0.1uF         5/28/04       245-01268-060       JW       Changed R12 to 0 ohm. Changed C1 to DNI. R12 is now connected to LED_1 instead of 3.3V.         6/4/04       245-01268-060       JW       Move C140 to 1.8V_A. Change R48 and R49 to DNI. Remove VR3,C57. Move C148 from 3.3V to 1.8V. Change Q2 to S0T89 CXT2907A. Changed Q5p1 to GPIO_5 and connected to U1pE2. Added R4, 0 OHM, to U1 pE1 and U3 p7. Added pullup R2, 10 OHM, to U1 pE1 and U3 p7. Added pullup R2, 10 OHM, to U1 pE1 and U1 pE1 and U1 pE1. Changed R13 to 1K. Disconnected U1pD3         6/9/04       245-01268-080       VN       Fixed pin out of Q2.         6/11/04       245-01268-081       CL       Excel BOM hand-edited. EPICOM PA added as Alternate Vendor. PCB part number up-rev'd as well as header and footer information.         7/6/04       245-01268-082       CL       Changed R130 from 150 to 200.         7/12/04       245-01268-083       CL       Add OEpic as alternate XPA following regulatory certification and ATMEL as	4/6/04	245-01200-040	3 44	P8,P9. Remove R50. Remove Q6. Change R48 to R between 3.3V_AUX and
220 ohms. Changed R22 pin 2 from GND to 1.8V_A. Changed C8 to 10pF. Changed C27, C28,C38,C43,C44,C74 to 3.0pF.  4/27/04 245-01268-051 CL Change R133, R134 from 220 5% to 221 1%. Changed C27, C28,C38,C43,C44,C74 to 3.3pF.  5/6/04 245-01268-052 CL POR: Change R12,R13 from DNI to 10k. Change C1 from DNI to 0.1uF  5/28/04 245-01268-060 JW Changed R12 to 0 ohm. Changed C1 to DNI. R12 is now connected to LED_1 instead of 3.3v.  6/4/04 245-01268-070 JW Move C140 to 1.8V_A. Change R48 and R49 to DNI. Remove VR3,C57. Move C148 from 3.3v to 1.8v. Change Q2 to SOT89 CXT2907A. Changed Q5p1 to GPIO_5 and connected to U1pE2. Added R4, 0 OHM, to U1 pE1 and U3 p7. Added pullup R2, 1t OHM to U3 p7. Added R5 DNI between GND and U3 p7. Change R16 to 0603. Changed R13 to 1K. Disconnected U1pD3  6/9/04 245-01268-080 VN Fixed pin out of Q2.  6/11/04 245-01268-081 CL Excel BOM hand-edited. EPICOM PA added as Alternate Vendor. PCB part numbrup-rev'd as well as header and footer information.  7/6/04 245-01268-082 CL Changed R130 from 150 to 200.				GPIO_3. Change R49 to R between LED_1 and GND.
Changed C27, C28,C38,C43,C44,C74 to 3.0pF.	4/27/04	245-01268-050	JW	Changed R47 to 0805 package. Changed R131 to 22 ohms. Changed R133,R134 to
Changed C27, C28,C38,C43,C44,C74 to 3.3pF.				
Changed C27, C28,C38,C43,C44,C74 to 3.3pF.	4/27/04	245-01268-051	CL	Change R133, R134 from 220 5% to 221 1%.
5/28/04   245-01268-060   JW   Changed R12 to 0 ohm. Changed C1 to DNI. R12 is now connected to LED_1 instead of 3.3V.				
instead of 3.3V.  JW Move C140 to 1.8V_A. Change R48 and R49 to DNI. Remove VR3,C57. Move C148 from 3.3V to 1.8V. Change Q2 to SOT89 CXT2907A. Changed Q5p1 to GPIO_5 and connected to U1pE2. Added R4, 0 OHM, to U1 pE1 and U3 p7. Added pullup R2, 10 OHM to U3 p7. Added R5 DNI between GND and U3 p7. Change R16 to 0603. Changed R13 to 1K. Disconnected U1pD3  6/9/04 245-01268-080 VN Fixed pin out of Q2.  CL Excel BOM hand-edited. EPICOM PA added as Alternate Vendor. PCB part number up-rev'd as well as header and footer information.  7/6/04 245-01268-082 CL Changed R130 from 150 to 200.  Add OEpic as alternate XPA following regulatory certification and ATMEL as	5/6/04	245-01268-052	CL	POR: Change R12,R13 from DNI to 10k. Change C1 from DNI to 0.1uF
6/4/04 245-01268-070 JW Move C140 to 1.8V_A. Change R48 and R49 to DNI. Remove VR3,C57. Move C148 from 3.3V to 1.8V. Change Q2 to SOT89 CXT2907A. Changed Q5p1 to GPIO_5 and connected to U1pE2. Added R4, 0 OHM, to U1 pE1 and U3 p7. Added pullup R2, 10 OHM to U3 p7. Added R5 DNI between GND and U3 p7. Change R16 to 0603. Changed R13 to 1K. Disconnected U1pD3 6/9/04 245-01268-080 VN Fixed pin out of Q2. 6/11/04 245-01268-081 CL Excel BOM hand-edited. EPICOM PA added as Alternate Vendor. PCB part number up-rev'd as well as header and footer information. 7/6/04 245-01268-082 CL Changed R130 from 150 to 200. 7/12/04 245-01268-083 CL Add OEpic as alternate XPA following regulatory certification and ATMEL as	5/28/04	245-01268-060	JW	
from 3.3V to 1.8V. Change Q2 to SOT89 CXT2907A. Changed Q5p1 to GPIO_5 and connected to U1pE2. Added R4, 0 OHM, to U1 pE1 and U3 p7. Added pullup R2, 10 OHM to U3 p7. Added R5 DNI between GND and U3 p7. Change R16 to 0603.  Changed R13 to 1K. Disconnected U1pD3  6/9/04  245-01268-080  VN  Fixed pin out of Q2.  Excel BOM hand-edited. EPICOM PA added as Alternate Vendor. PCB part number up-rev'd as well as header and footer information.  7/6/04  245-01268-082  CL  Changed R130 from 150 to 200.  7/12/04  245-01268-083  CL  Add OEpic as alternate XPA following regulatory certification and ATMEL as	0/4/04			
OHM to U3 p7. Added R5 DNI between GND and U3 p7. Change R16 to 0603. Changed R13 to 1K. Disconnected U1pD3  6/9/04  245-01268-080  VN Fixed pin out of Q2.  6/11/04  245-01268-081  CL Excel BOM hand-edited. EPICOM PA added as Alternate Vendor. PCB part number up-rev'd as well as header and footer information.  7/6/04  245-01268-082  CL Changed R130 from 150 to 200.  7/12/04  245-01268-083  CL Add OEpic as alternate XPA following regulatory certification and ATMEL as	6/4/04	245-01266-070	3 44	from 3.3V to 1.8V. Change Q2 to SOT89 CXT2907A. Changed Q5p1 to GPIO_5 and
Changed R13 to 1K. Disconnected U1pD3  6/9/04 245-01268-080 VN Fixed pin out of Q2.  6/11/04 245-01268-081 CL Excel BOM hand-edited. EPICOM PA added as Alternate Vendor. PCB part number up-rev'd as well as header and footer information.  7/6/04 245-01268-082 CL Changed R130 from 150 to 200.  7/12/04 245-01268-083 CL Add OEpic as alternate XPA following regulatory certification and ATMEL as				connected to U1pE2. Added R4, 0 OHM, to U1 pE1 and U3 p7. Added pullup R2, 10K
6/11/04 245-01268-081 CL Excel BOM hand-edited. EPICOM PA added as Alternate Vendor. PCB part number up-rev'd as well as header and footer information.  7/6/04 245-01268-082 CL Changed R130 from 150 to 200.  7/12/04 245-01268-083 CL Add OEpic as alternate XPA following regulatory certification and ATMEL as				
up-rev'd as well as header and footer information.  7/6/04 245-01268-082 CL Changed R130 from 150 to 200.  7/12/04 245-01268-083 CL Add OEpic as alternate XPA following regulatory certification and ATMEL as	6/9/04	245-01268-080	VN	Fixed pin out of Q2.
up-rev'd as well as header and footer information.  7/6/04 245-01268-082 CL Changed R130 from 150 to 200.  7/12/04 245-01268-083 CL Add OEpic as alternate XPA following regulatory certification and ATMEL as				
7/12/04 245-01268-083 CL Add OEpic as alternate XPA following regulatory certification and ATMEL as	6/11/04	245-01268-081	CL	
7/12/04 245-01268-083 CL Add OEpic as alternate XPA following regulatory certification and ATMEL as	7/0/04	245 04200 002	01	Channel B420 from 450 to 200
	7/6/04	245-01268-082	CL	Changed R130 from 150 to 200.
alternate EEPROM.	7/12/04	245-01268-083	CL	
				alternate EEPROM.

1	Atheros Communications, Inc. 529 Almanor Avenue Sunnyvale, CA 94085							Title MB51				
	Date Wednes	sday, July 14, 2004	Size C	Rev	000	Sheet	1	of	5	245-01268-083		

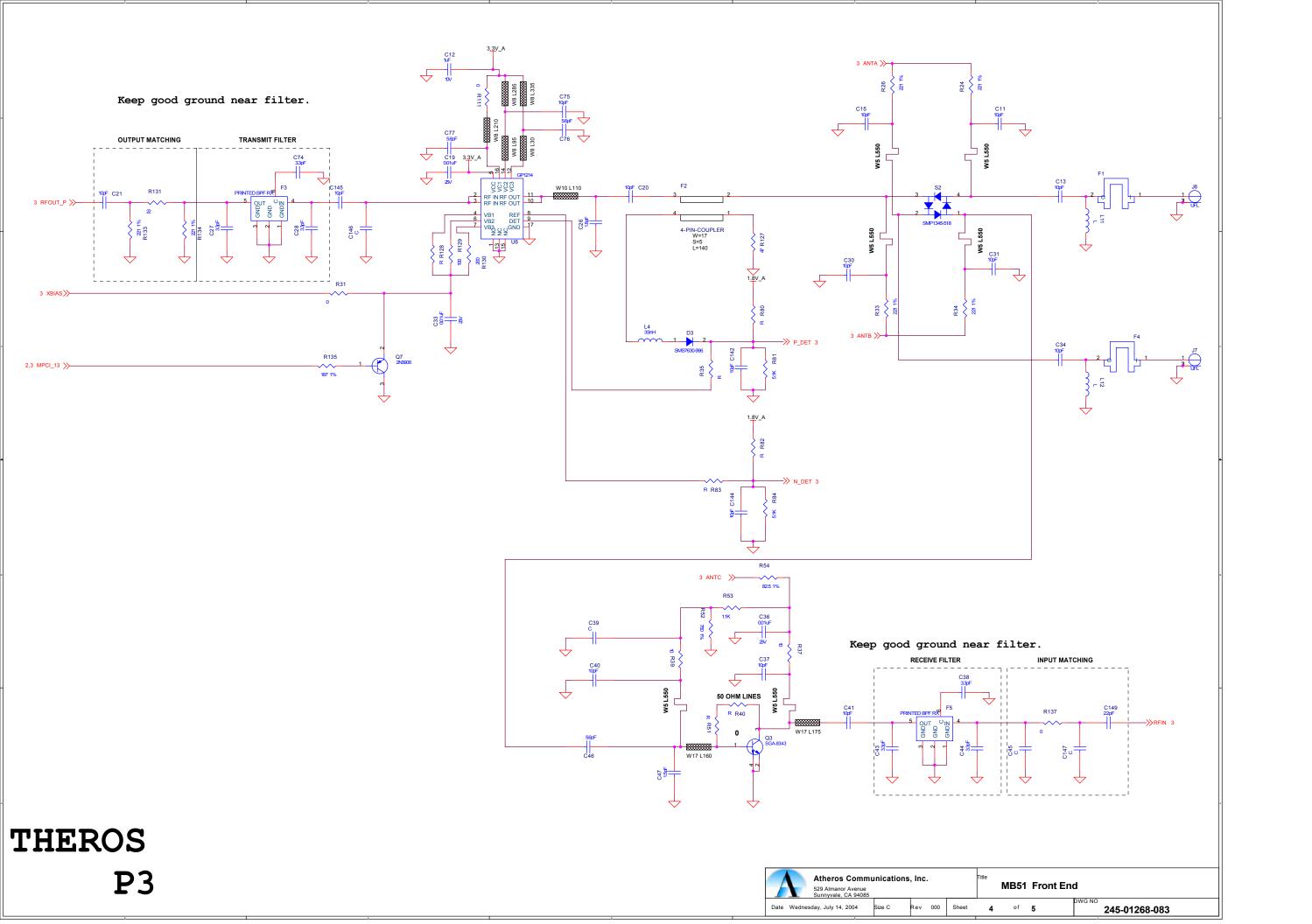




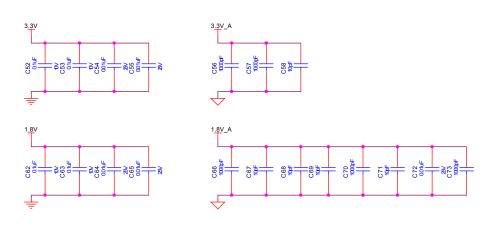
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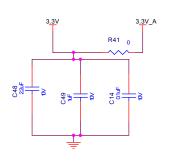
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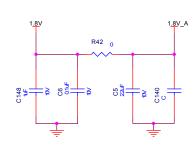
1	Atheros Communications, Inc. 529 Almanor Avenue Sunnyvale, CA 94085								Title MB51 Radio Interface				
	Date Wednesday, July 14, 2004 Size C			Size C	Rev	000	Sheet	3	of	5	245-01268-083		

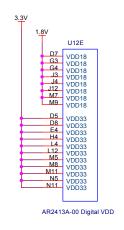


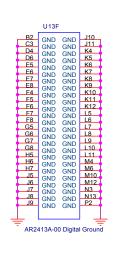
## ATHEROS P4

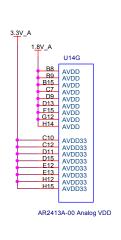


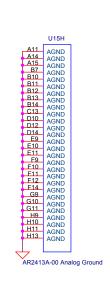


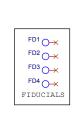




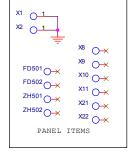


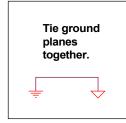


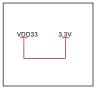




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