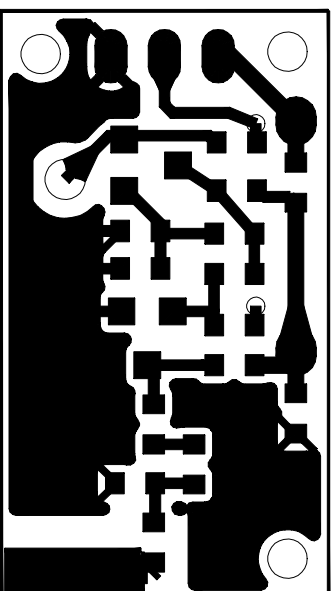
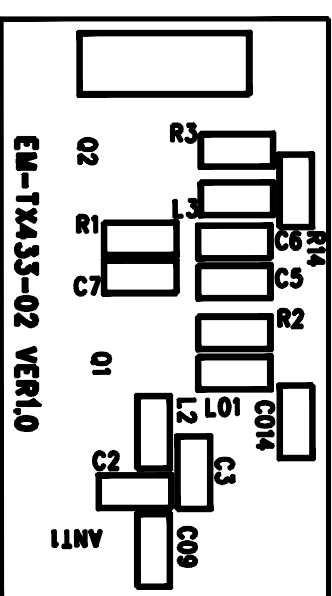


**Top Layer of  
Tx PCB**



**Top Layer Layout  
Tx PCB**



**Top Silkscreen  
Tx PCB**