

Figure 1 - EUT

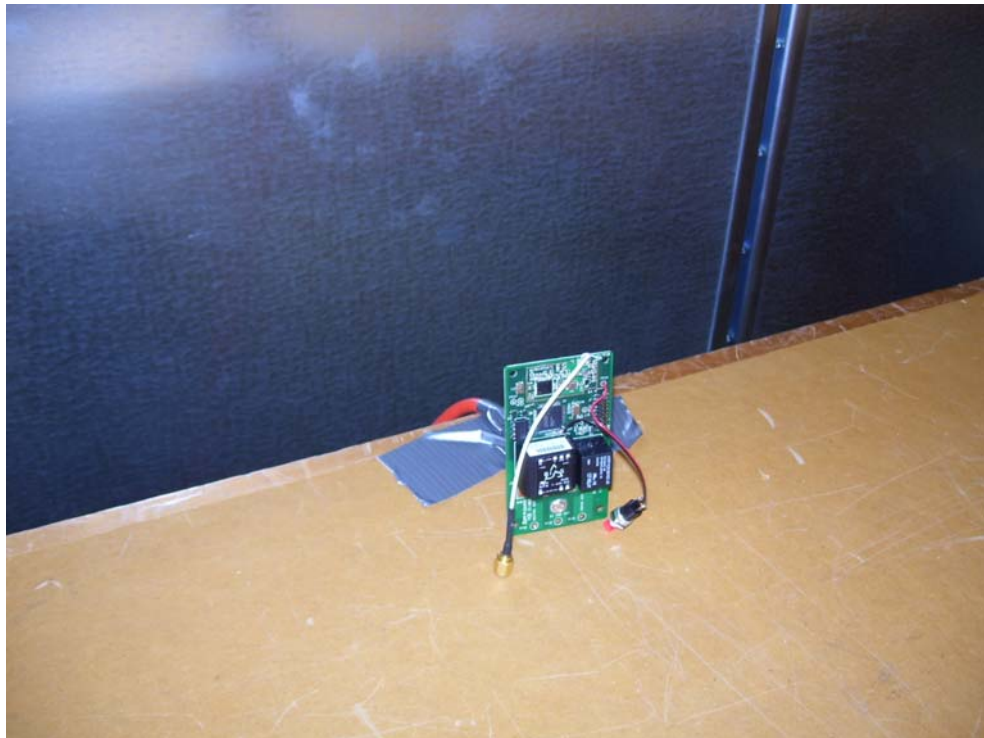


Figure 2 - Conducted Emissions Test Setup



Figure 3 - Radiated Emissions Test Setup

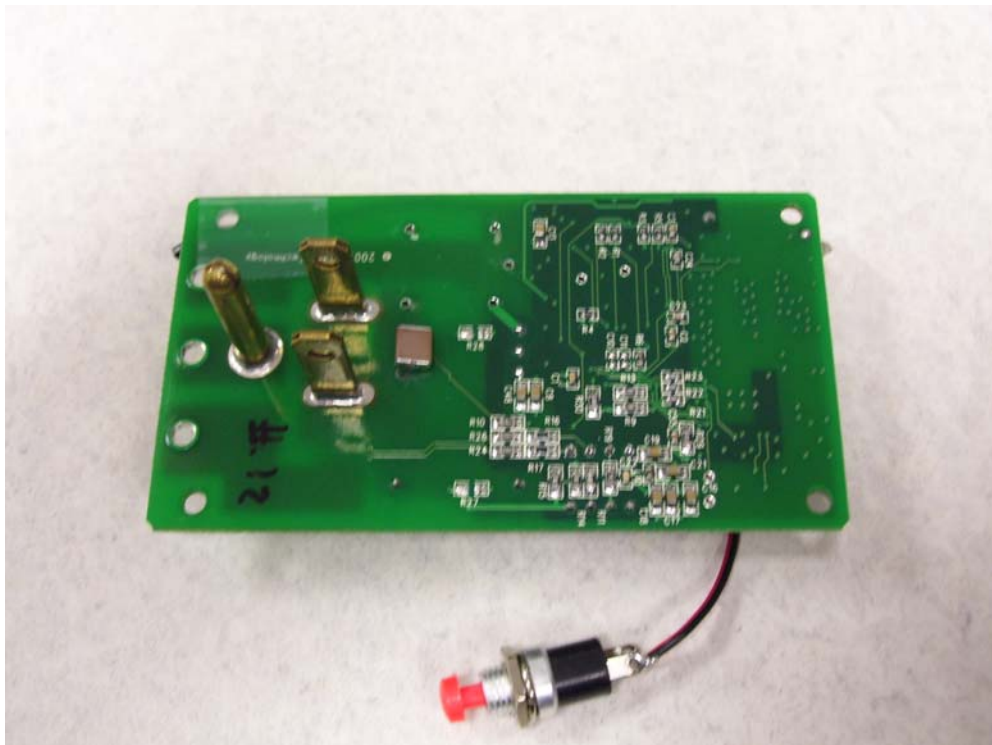


Figure 4 - Back Side of PCB

