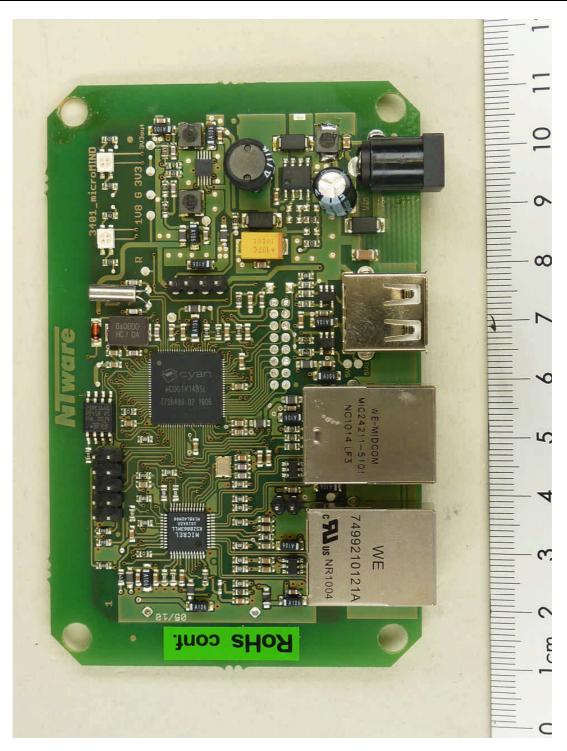


Annex B of test report F102069E2 2nd version

EUT: microMind



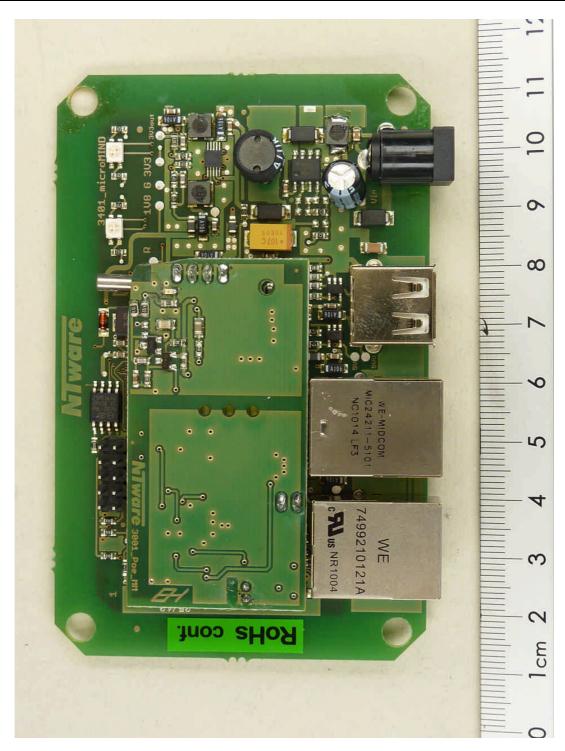
102069_5.jpg

Main PCB, top view (PoE-PCB inserted)



Annex B of test report F102069E2 2nd version

EUT: microMind

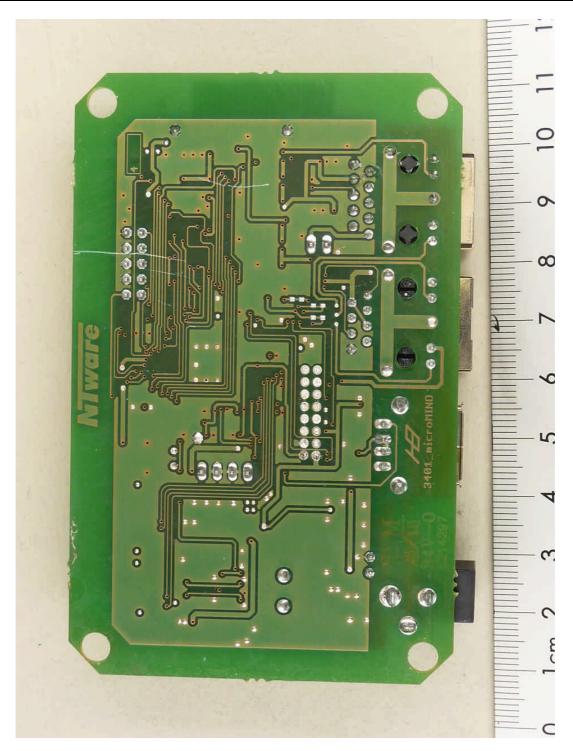


102069_6.jpg

Main PCB, top view (No PoE-PCB inserted)



Annex B of test report F102069E2 2nd version EUT: microMind

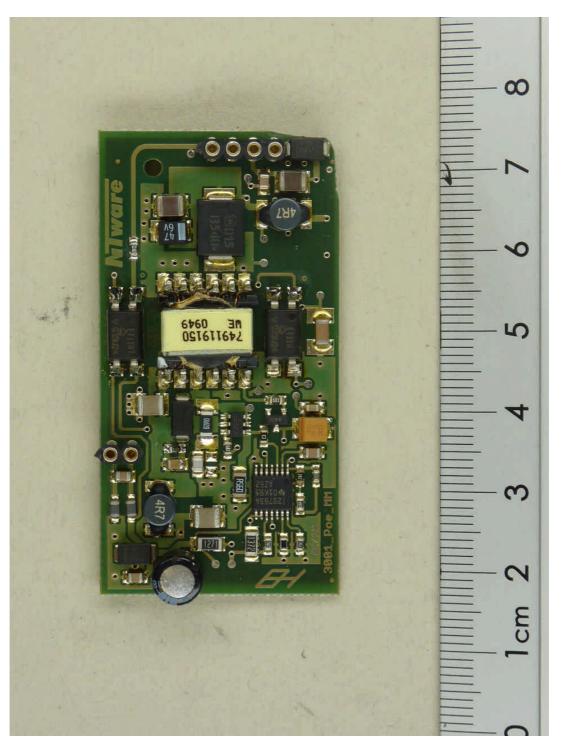


102069_7.jpg

Main PCB, bottom view



Annex B of test report F102069E2 2nd version EUT: microMind

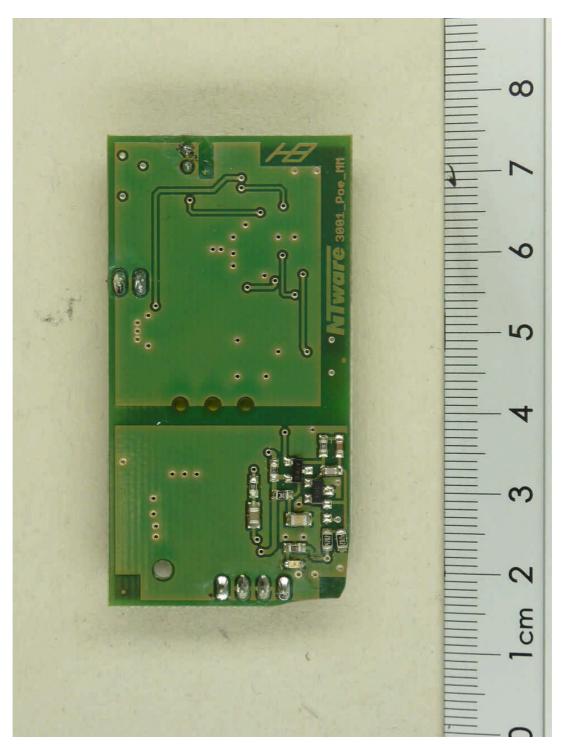


102069_8.jpg

PoE-PCB, top view



Annex B of test report F102069E2 2nd version EUT: microMind



102069_9.jpg

PoE-PCB, bottom view