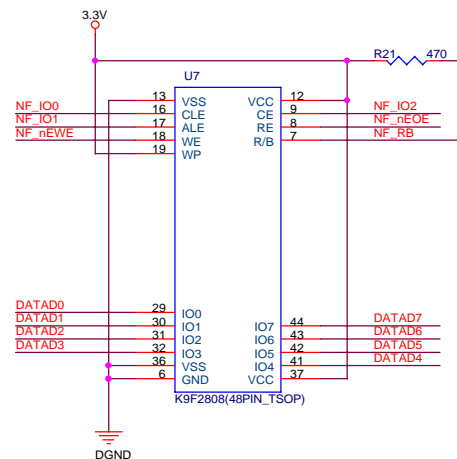
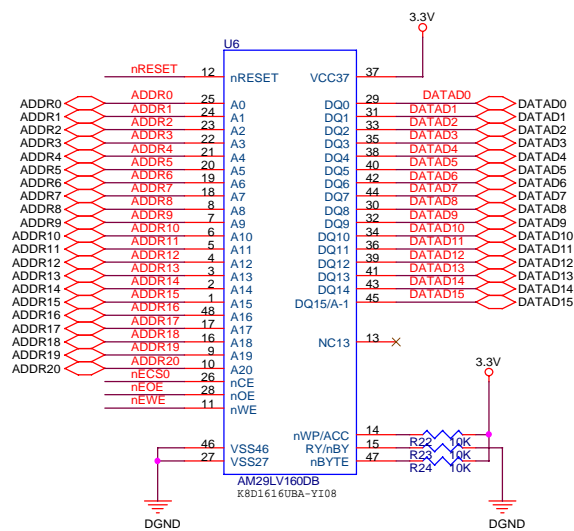
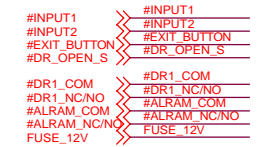
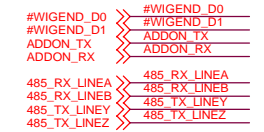
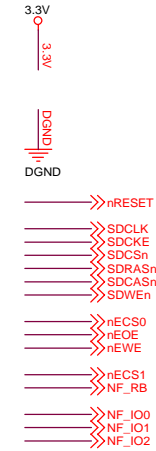
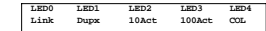


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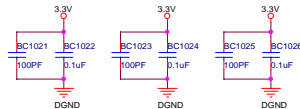
Hardwire Configuration network:

1. This configuration shows

Enable: Auto negotiation, Full duplex, 100Mbps,
Link Down Power Saving, MII interface

Disable: Isolate, Repeater mode

2. These seven configuration pins could be connected to VDD or GND directly.



GROUND RING을 넣어 주세요.

TX+와 TX-의 패턴 길이를 가능한 맞추어야 함

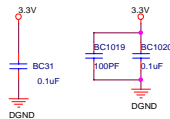
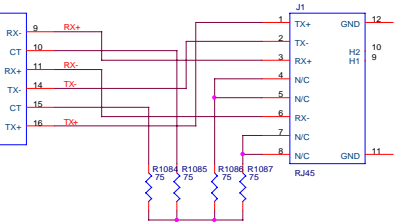
RX+와 RX-의 패턴 길이를 가능한 맞추어야 함

PY_+ 와 PY_- 의 합산 값으로 배선되어야 합니다.

TX+와 TX-의 항상 쌍으로 배선되어야 합니다.

GROUND RING을 넣어 주세요.

TX+와 TX-의 패턴 길이를 가능한 맞추어야 함



(CONNECT TO CHASSIS GND)

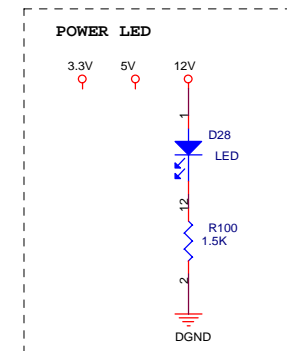
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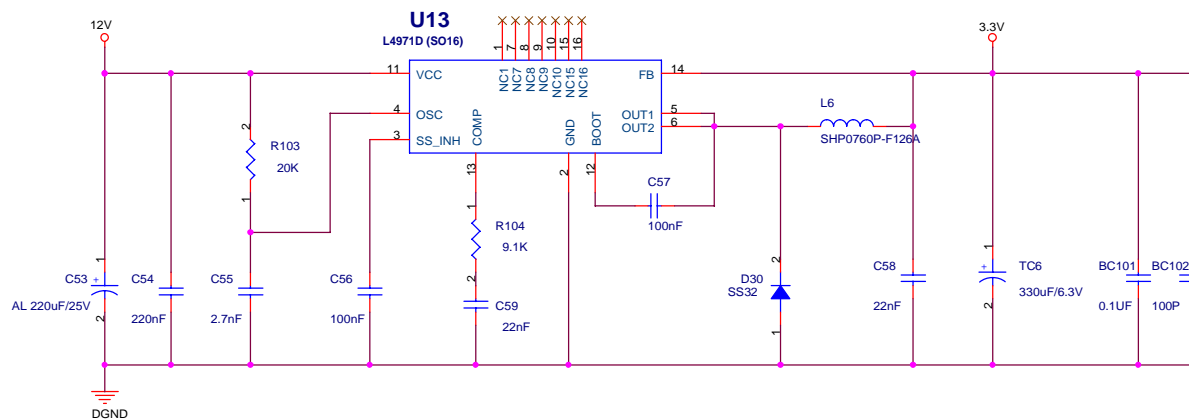
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#WIGEND_D1	>>>	#WIGEND_D1
ADDON_TX	>>>	ADDON_TX
ADDON_RX	>>>	ADDON_RX
485_RX_LINEA	>>>	485_RX_LINEA
485_RX_LINEB	>>>	485_RX_LINEB
485_TX_LINEY	>>>	485_TX_LINEY
485_TX_LINEZ	>>>	485_TX_LINEZ

SP_AGND >> SP_AGND

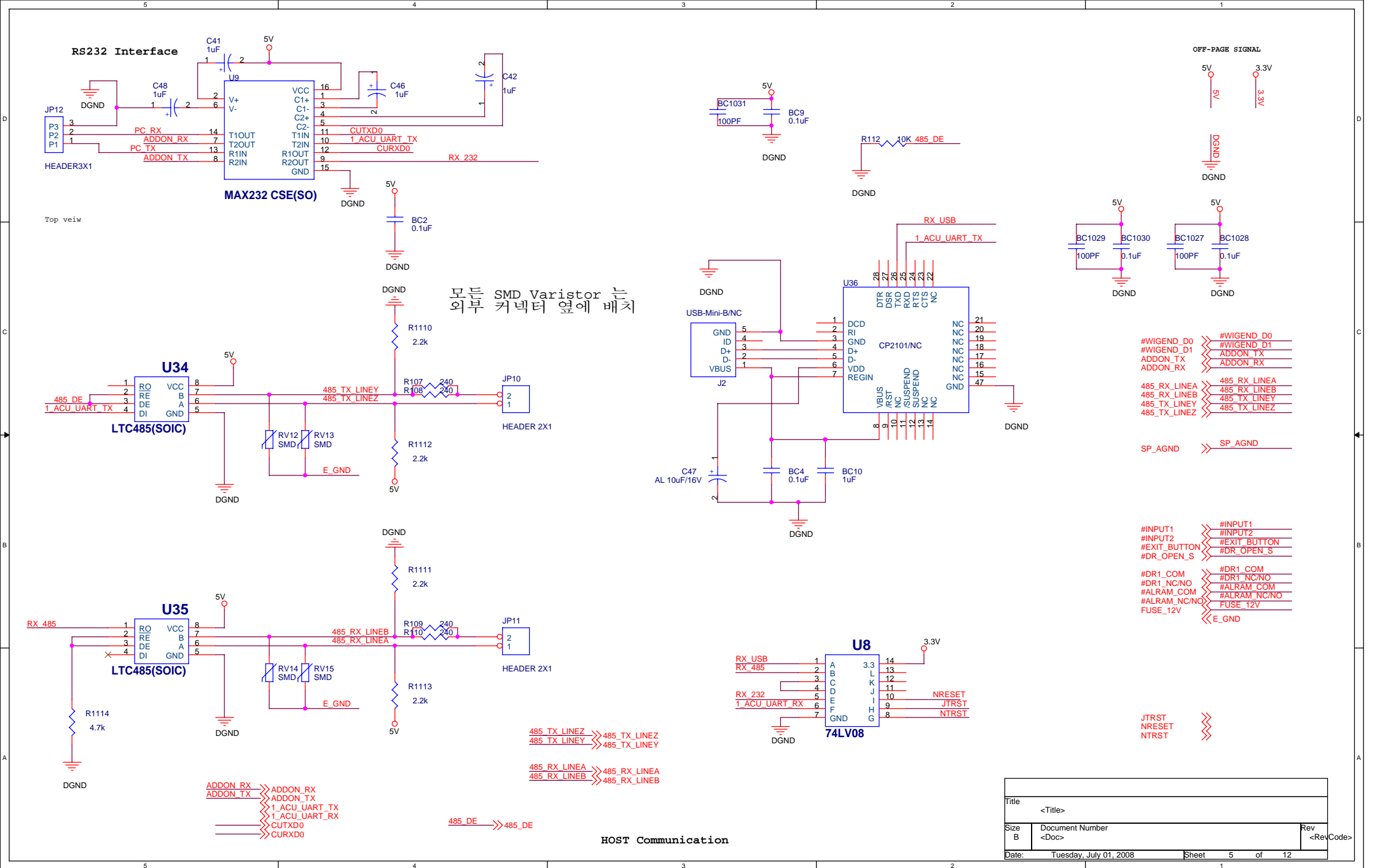
#INPUT1	↔	#INPUT1
#INPUT2	↔	#INPUT2
#EXIT_BUTTON	↔	#EXIT_BUTTON
#DR_OPEN_S	↔	#DR_OPEN_S
#DR1_COM	↔	#DR1_COM
#DR1_NC/NO	↔	#DR1_NC/NO
#ALRAM_COM	↔	#ALRAM_COM
#ALRAM_NC/NO	↔	#ALRAM_NC/NO
FUSE_12V	↔	FUSE_12V



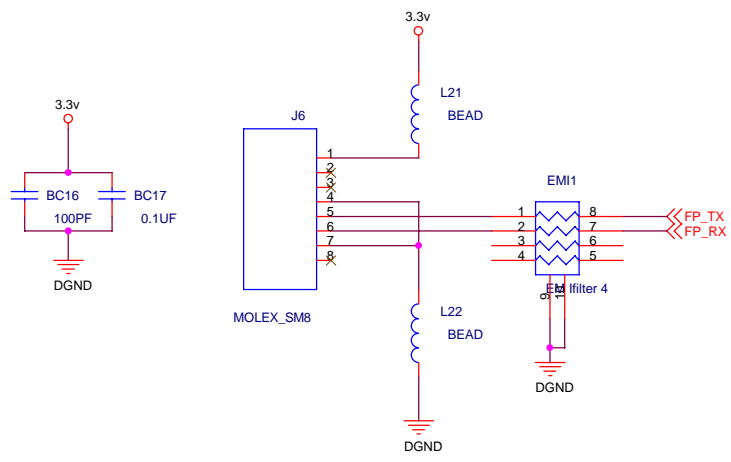
#INPUT1	#INPUT1
#INPUT2	#INPUT2
#EXIT_BUTTON	#EXIT_BUTTON
#DR_OPEN_S	#DR_OPEN_S
E_GND	E_GND
#DR1_COM	#DR1_COM
#DR1_NC/NO	#DR1_NC/NO
#ALRAM_COM	#ALRAM_COM
#ALRAM_NC/NO	#ALRAM_NC/NO
FUSE_12V	FUSE_12V



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POWER			
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#WIGEND_D0 >> #WIGEND_D0
#WIGEND_D1 >> #WIGEND_D1
ADDON_TX >> ADDON_TX
ADDON_RX >> ADDON_RX

485_RX_LINEA >> 485_RX_LINEA
485_RX_LINEB >> 485_RX_LINEB
485_TX_LINEY >> 485_TX_LINEY
485_TX_LINEZ >> 485_TX_LINEZ

SP_AGND >> SP_AGND

#INPUT1 >> #INPUT1
#INPUT2 >> #INPUT2
#EXIT_BUTTON >> #EXIT_BUTTON
#DR_OPEN_S >> #DR_OPEN_S

#DR1_COM >> #DR1_COM
#DR1_NC/NO >> #DR1_NC/NO
#ALRAM_COM >> #ALRAM_COM
#ALRAM_NC/NO >> #ALRAM_NC/NO
FUSE_12V >> FUSE_12V

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Finger Print Connector		
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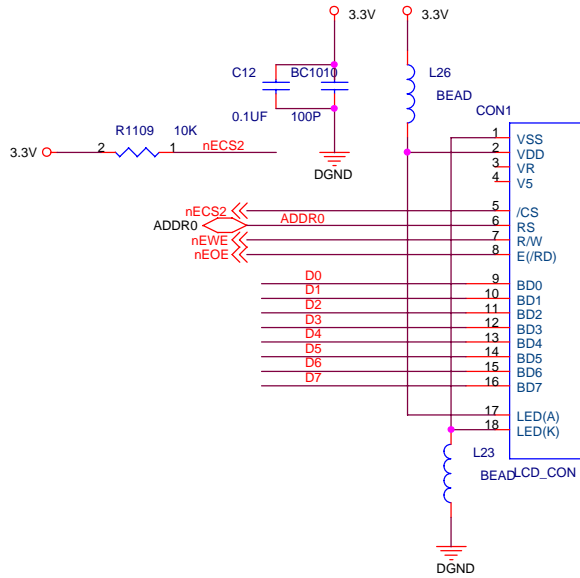
#WIGEND_D0 >> #WIGEND_D0
#WIGEND_D1 >> #WIGEND_D1
ADDON_TX >> ADDON_TX
ADDON_RX >> ADDON_RX

485_RX_LINEA >> 485_RX_LINEA
485_RX_LINEB >> 485_RX_LINEB
485_TX_LINEY >> 485_TX_LINEY
485_TX_LINEZ >> 485_TX_LINEZ

SP_AGND >> SP_AGND

#INPUT1 >> #INPUT1
#INPUT2 >> #INPUT2
#EXIT_BUTTON >> #EXIT_BUTTON
#DR_OPEN_S >> #DR_OPEN_S

#DR1_COM >> #DR1_COM
#DR1_NC/NO >> #DR1_NC/NO
#ALRAM_COM >> #ALRAM_COM
#ALRAM_NC/NO >> #ALRAM_NC/NO
FUSE_12V >> FUSE_12V

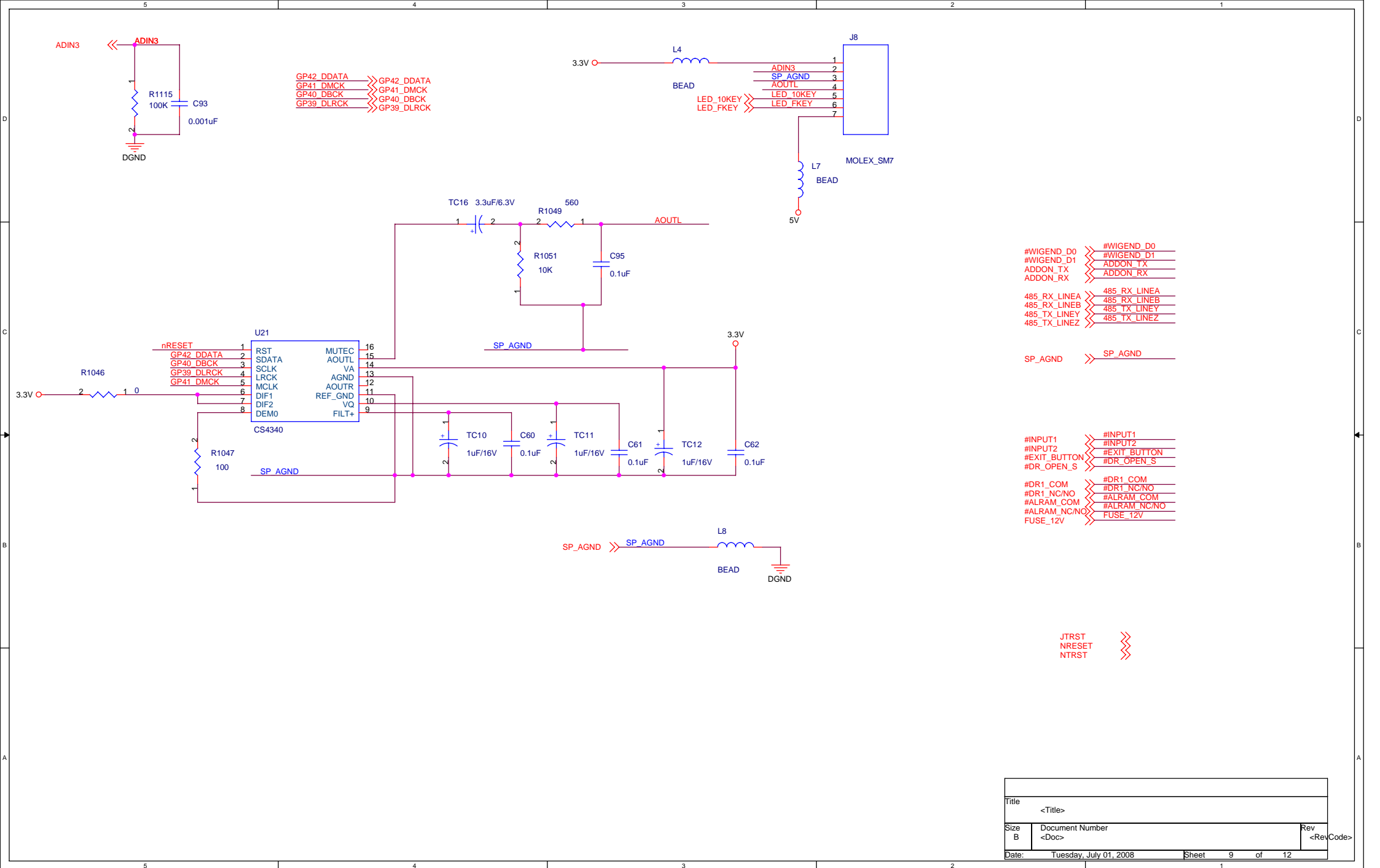


DATAD0 >> DATAD0 D0
DATAD1 >> DATAD1 D1
DATAD2 >> DATAD2 D2
DATAD3 >> DATAD3 D3

DATAD4 >> DATAD4 D4
DATAD5 >> DATAD5 D5
DATAD6 >> DATAD6 D6
DATAD7 >> DATAD7 D7

>> nECS2
>> nEOE
>> nEWE

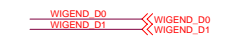
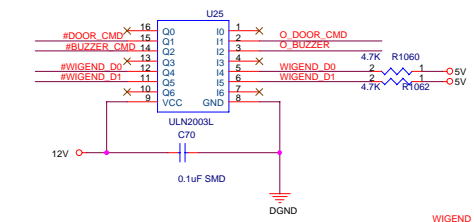
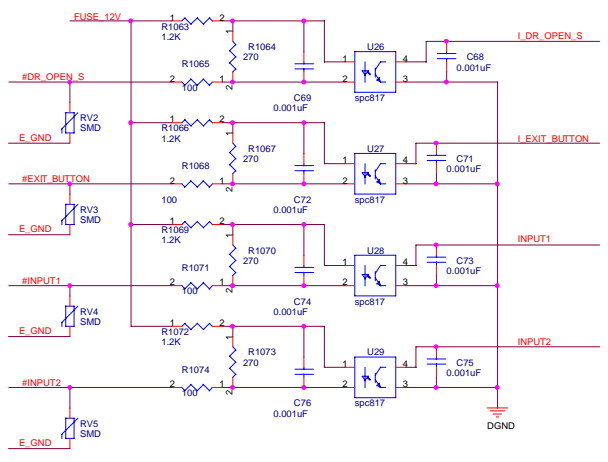
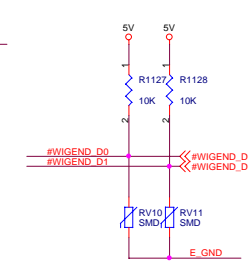
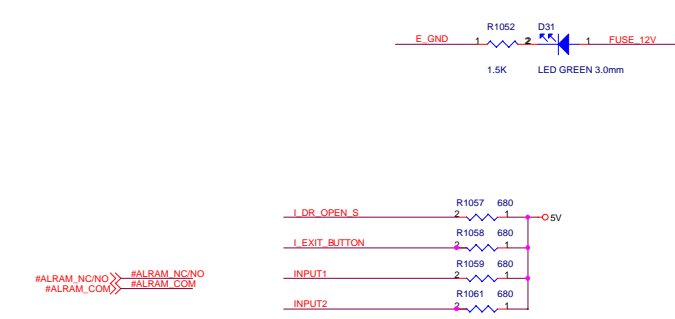
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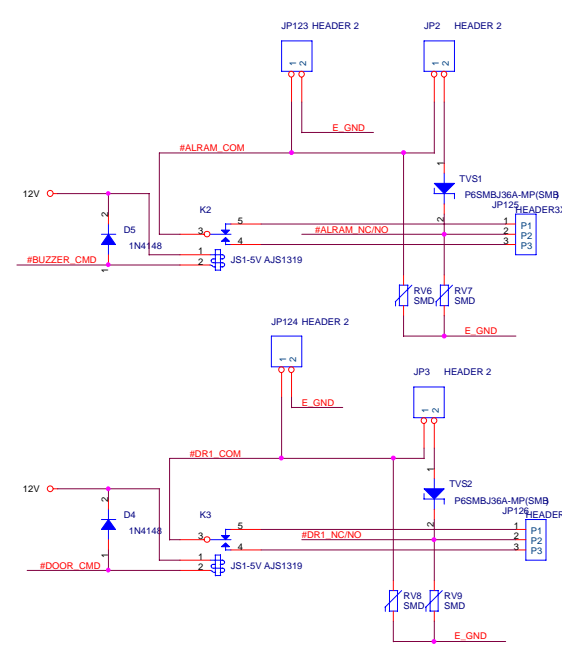
#WIGEND_D0 >> #WIGEND_D0
#WIGEND_D1 >> #WIGEND_D1
ADDON_TX >> ADDON_TX
ADDON_RX >> ADDON_RX
485_RX_LINEA >> 485_RX_LINEA
485_RX_LINEB >> 485_RX_LINEB
485_TX_LINEA >> 485_TX_LINEA
485_TX_LINEB >> 485_TX_LINEB
485_TX_LINEZ >> 485_TX_LINEZ
SP_AGND >> SP_AGND
#INPUT1 >> #INPUT1
#INPUT2 >> #INPUT2
#EXIT_BUTTON >> #EXIT_BUTTON
#DR_OPEN_S >> #DR_OPEN_S
#DR1_COM >> #DR1_COM
#DR1_NC/NO >> #DR1_NC/NO
#ALRAM_COM >> #ALRAM_COM
#ALRAM_NC/NO >> #ALRAM_NC/NO
FUSE_12V >> FUSE_12V

JTRST >>
NRESET >>
NTRST >>

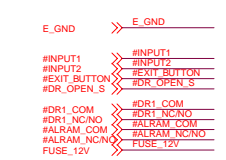
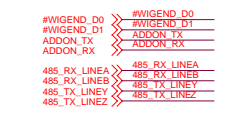
Title		
<Title>		
Size	Document Number	Rev
B	<Doc>	<RevCode>
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SMD Varistor 는 1005 1608 2012 등 다양한 사이즈가 있으나 가용하면 1608로 사용요망
모든 SMD Varistor 는 외부 커넥터 옆에 배치



모든 SMD Varistor 와 TVS DIODE 는 외부 커넥터 옆에 배치



TVS DIODE 는 이완수자장/김정옥사장 쪽에 부품 선정하여 FootPrint 선정
핀번호 주의 A(+)/K(-)

File			<Title>
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C			<Doc>
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Rev			<Rev Code>

#WIGEND_D0 >> #WIGEND_D0
#WIGEND_D1 >> #WIGEND_D1
ADDON_TX >> ADDON_TX
ADDON_RX >> ADDON_RX

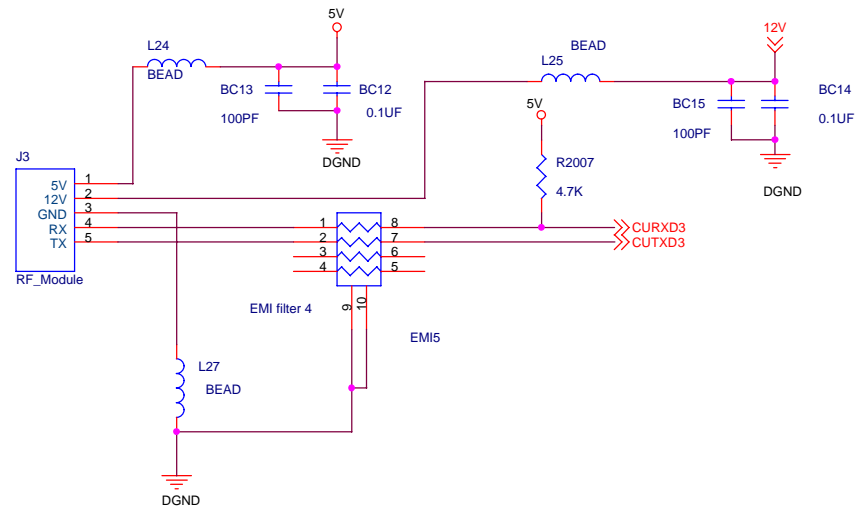
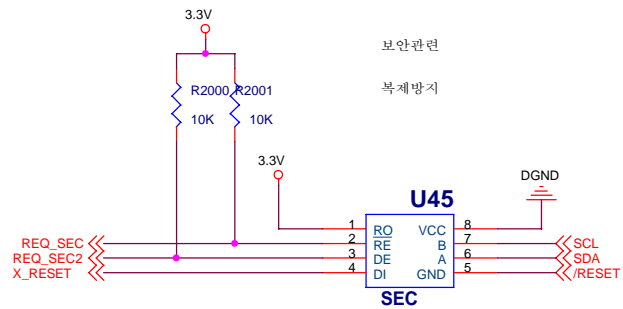
485_RX_LINEA >> 485_RX_LINEA
485_RX_LINEB >> 485_RX_LINEB
485_TX_LINEY >> 485_TX_LINEY
485_TX_LINEZ >> 485_TX_LINEZ

SP_AGND >> SP_AGND

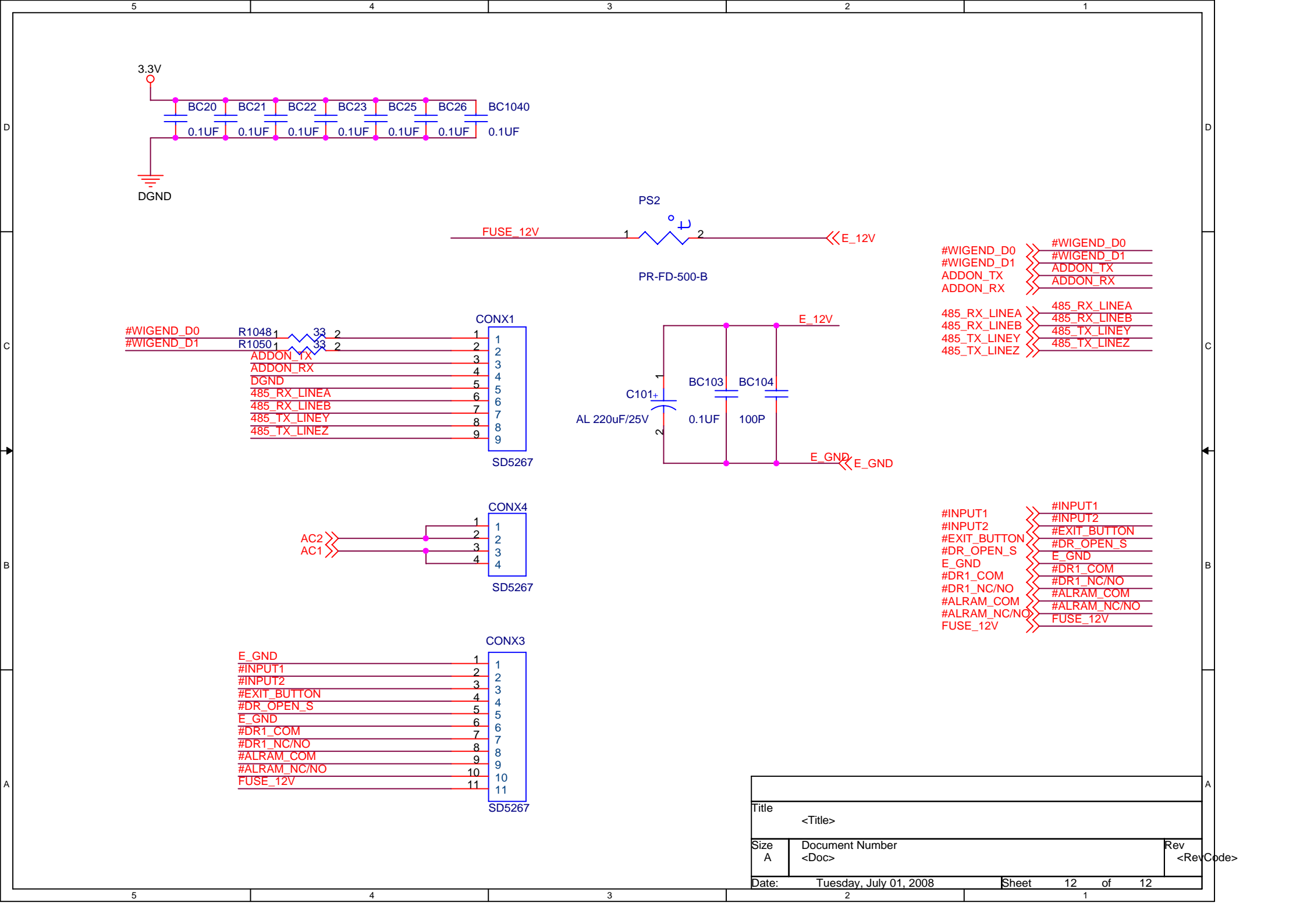
#INPUT1 >> #INPUT1
#INPUT2 >> #INPUT2
#EXIT_BUTTON >> #EXIT_BUTTON
#DR_OPEN_S >> #DR_OPEN_S

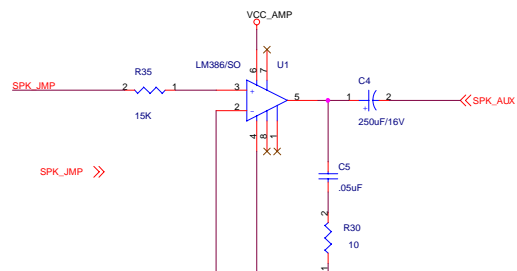
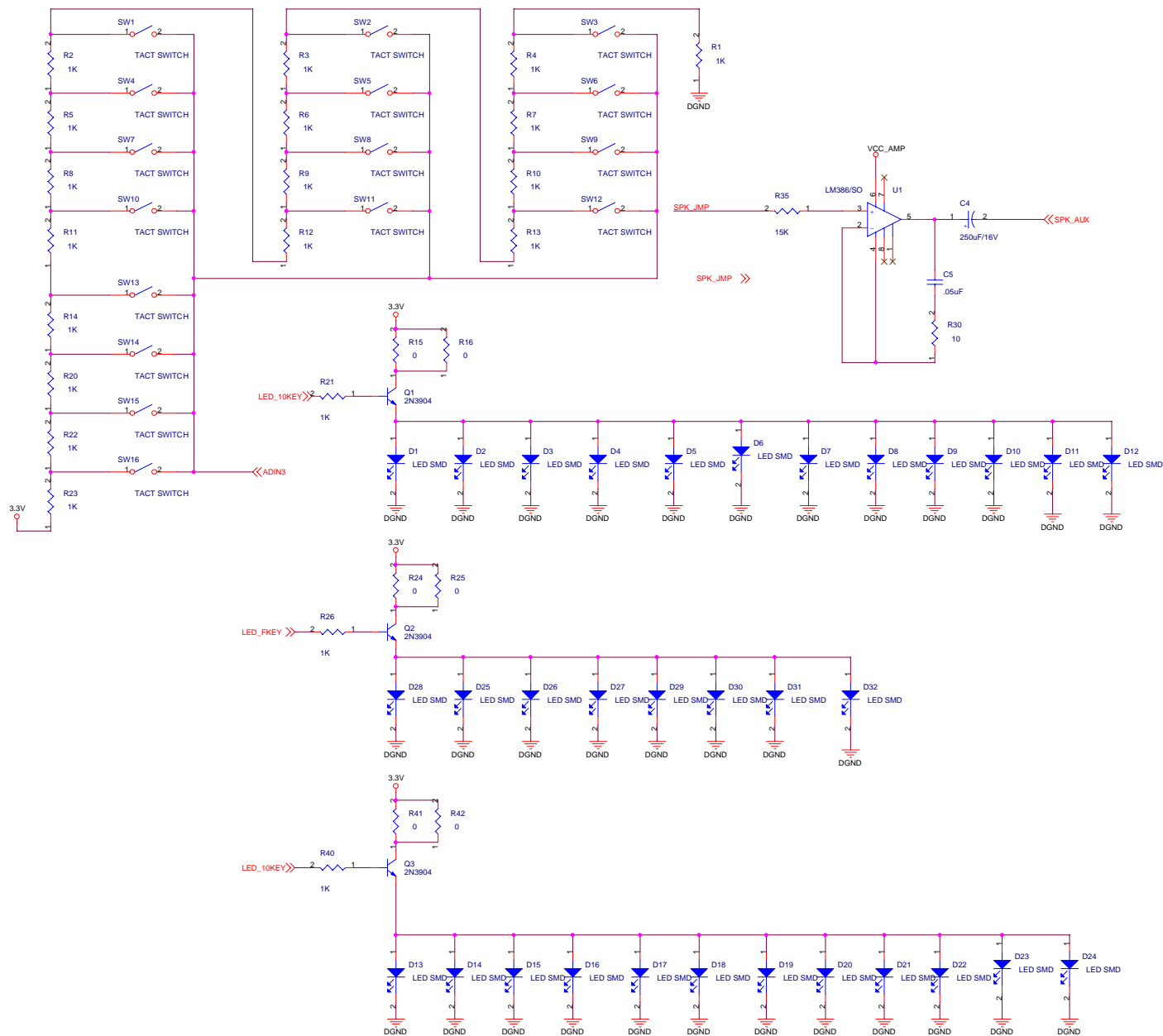
#DR1_COM >> #DR1_COM
#DR1_NC/NO >> #DR1_NC/NO
#ALRAM_COM >> #ALRAM_COM
#ALRAM_NC/NO >> #ALRAM_NC/NO

/RESET >>
X_RESET >>

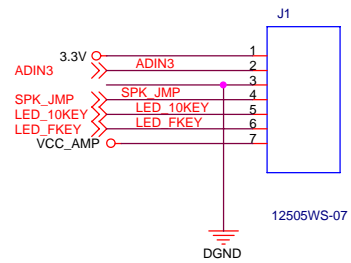
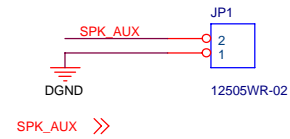
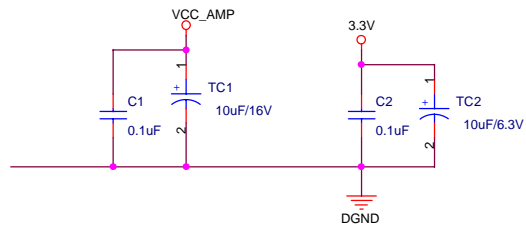


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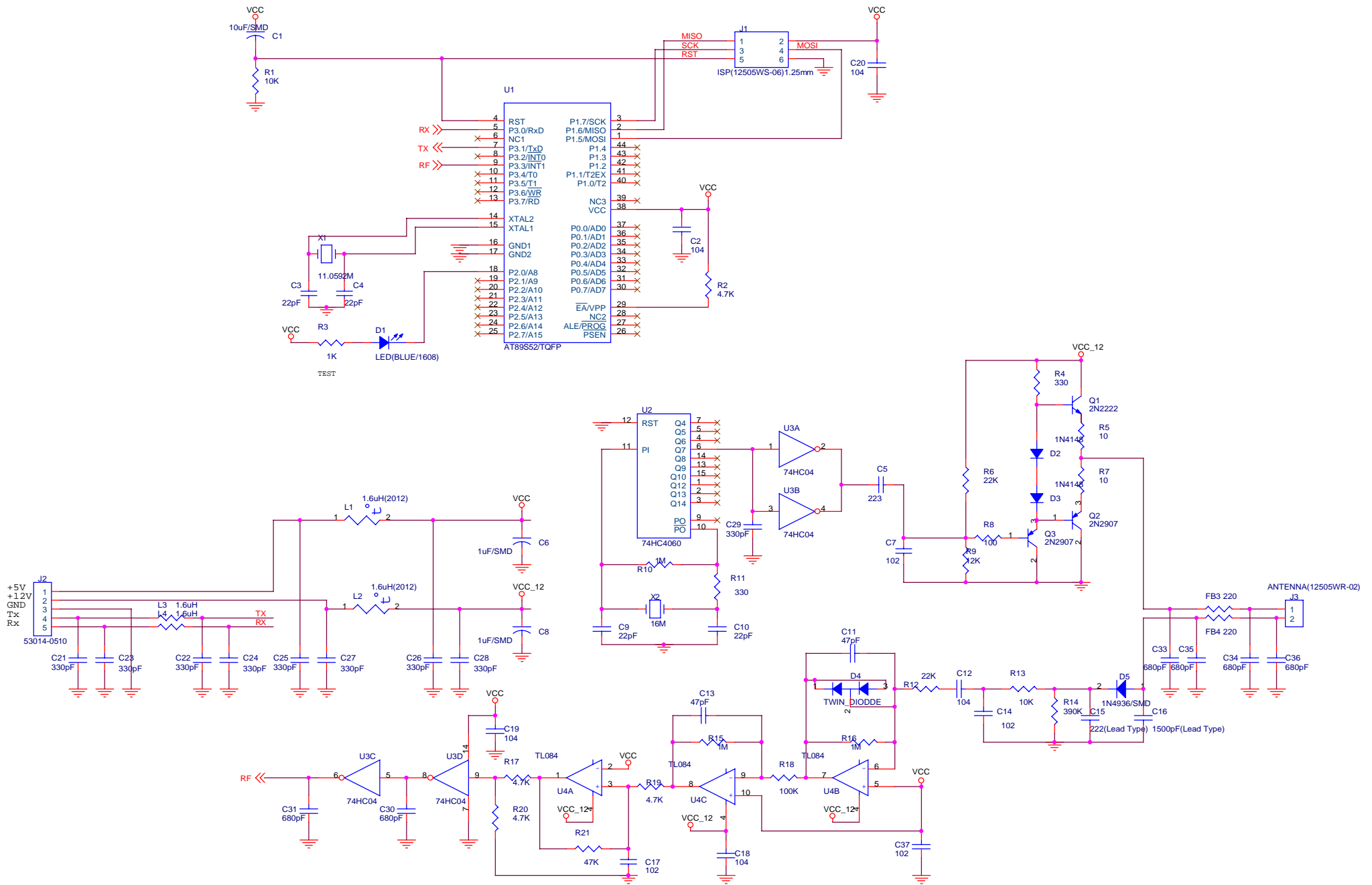




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Size	Document Number	Rev
C	KEY & LED	1.4
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Title		
F_KEYPAD(080526)		
Size	Document Number	Rev
B	I/O	1.4
Date:	Monday, June 30, 2008	Sheet 2 of 2



ALL CHIP R,C 1608 Type

Title		
PR526(080527)		
Size	Document Number	Rev
A3	MAIN	2.2
Date:	Tuesday, July 01, 2008	Sheet 1 of 1