

SCH080327-01-VIDEO-MIRROR_V1.3		
Title		
<Title> <b>ALTERA - CLOCK_CONFIG</b>		
Size	Document Number	Rev
A	<Doc>	1.3
Date:	Tuesday, April 15, 2008	Sheet 3 of 5



2007년 11월 10일 수정사항

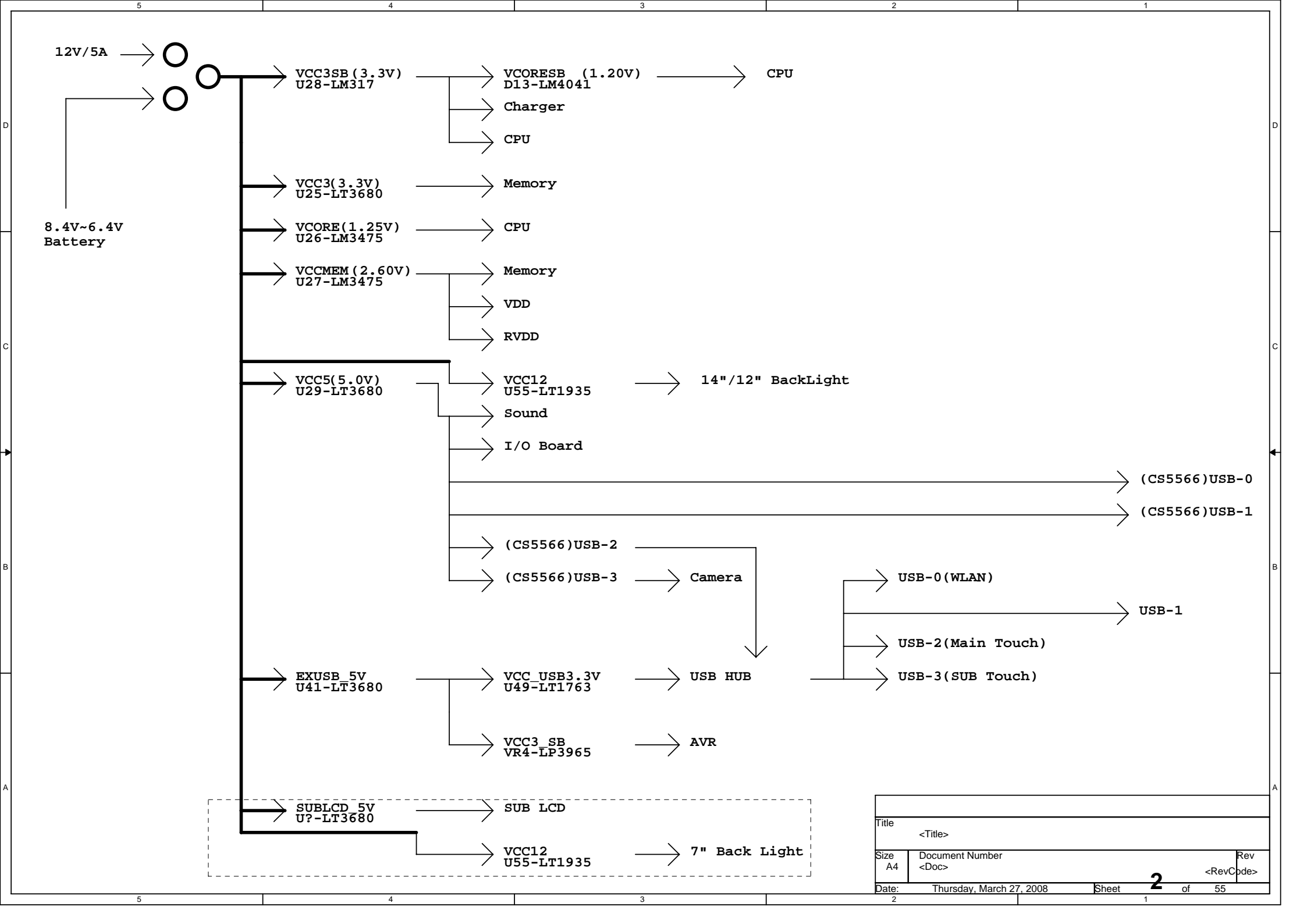
PAGE3: 저항들 R20부터 R26 1608 SIZE로 변경. D5제거  
PAGE5: R49추가 U9 PIN25번 VCC연결  
PAGE10: ENP1#제거 후 VCC5연결  
PAGE15: J49, PIN 40,41 GND제거 후 VCC5로 변경  
PAGE18: U25,U29,U41 IC 변경 VR4 EXUSB\_5V로 변경 R494제거.  
모든 저항을 1608 SIZE로 변경  
PAGE22: R275,R270을 10K를 1K 로 변경  
PAGE24: JP7에 11번을 GND 수정  
PAGE26: J42에 1,2번을 VCC5를 EXUSB\_5V로 변경  
C363,364 VCC5를 EXUSB\_5V로 변경  
PAGE28: U51에 4번핀 ENP1# 한개를 삭제

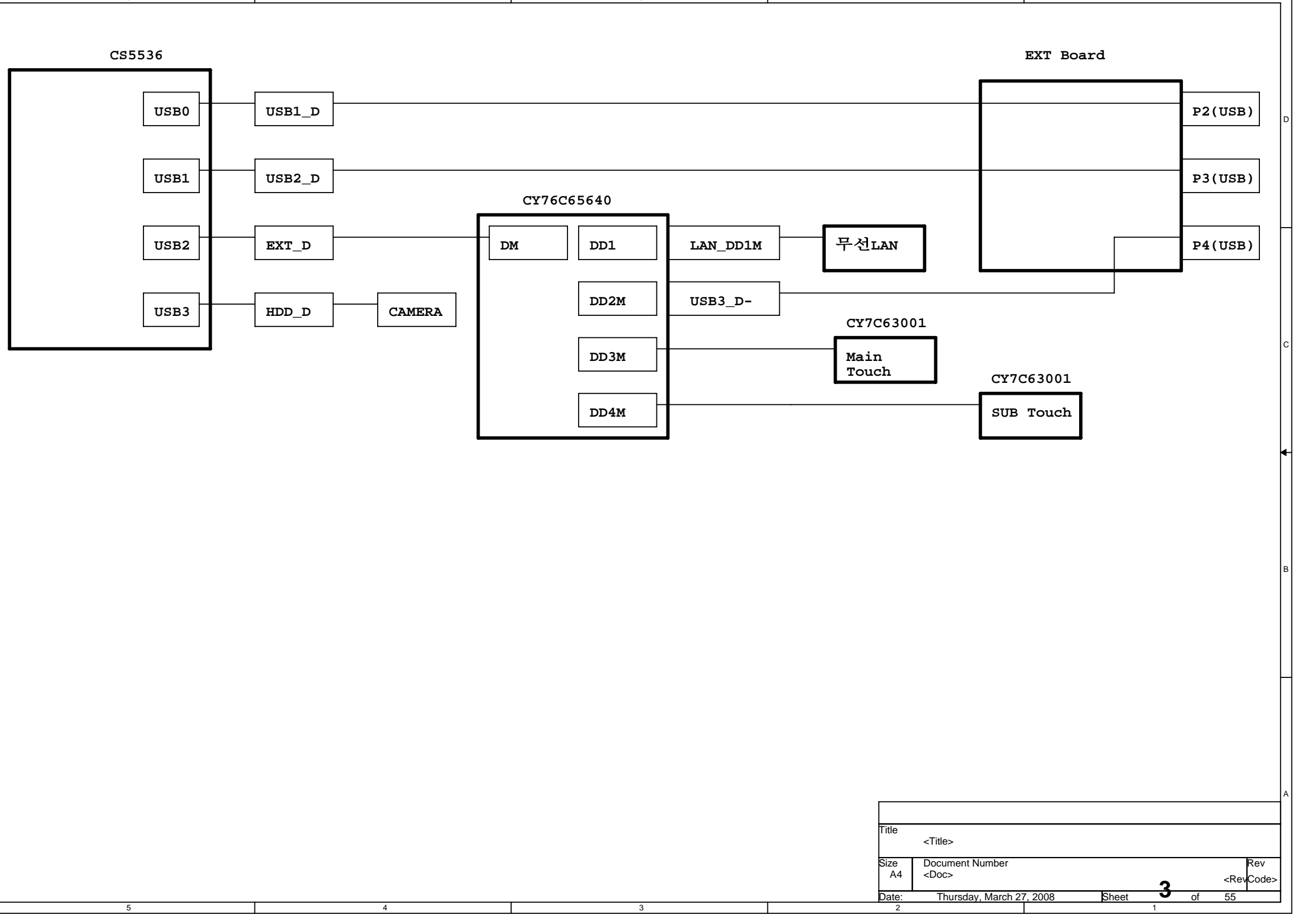
2007년 9월 12일 수정사항

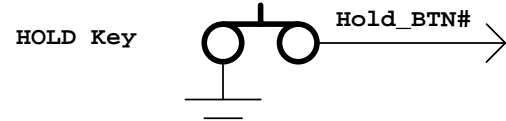
R25 32.4K ==> 18K 온도 센서 제어  
R26 309K ==> 510K 충전 시간 제어  
Q10 Del. ==> Charge LED 제어  
R42 Del. ==> Charge LED 제어  
U4 Del. ==> Camera USB Mux 제거  
R44 Del. ==> Camera USB Mux 제거  
Q105 Del. ==> Camera 전원 관리  
R458 Del. ==> Camera 전원 관리  
Q106 Del. ==> WLAN 전원 관리  
R459 Del. ==> WLAN 전원 관리

U25,U29 Pin7 Open  
J12 삭제, LCD BackLight Inverter EXT  
Board 아래로 이동

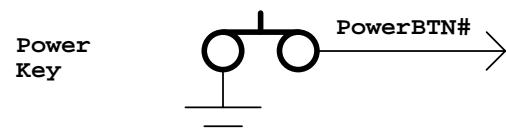
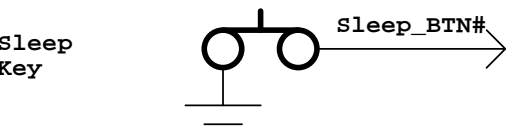
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History		
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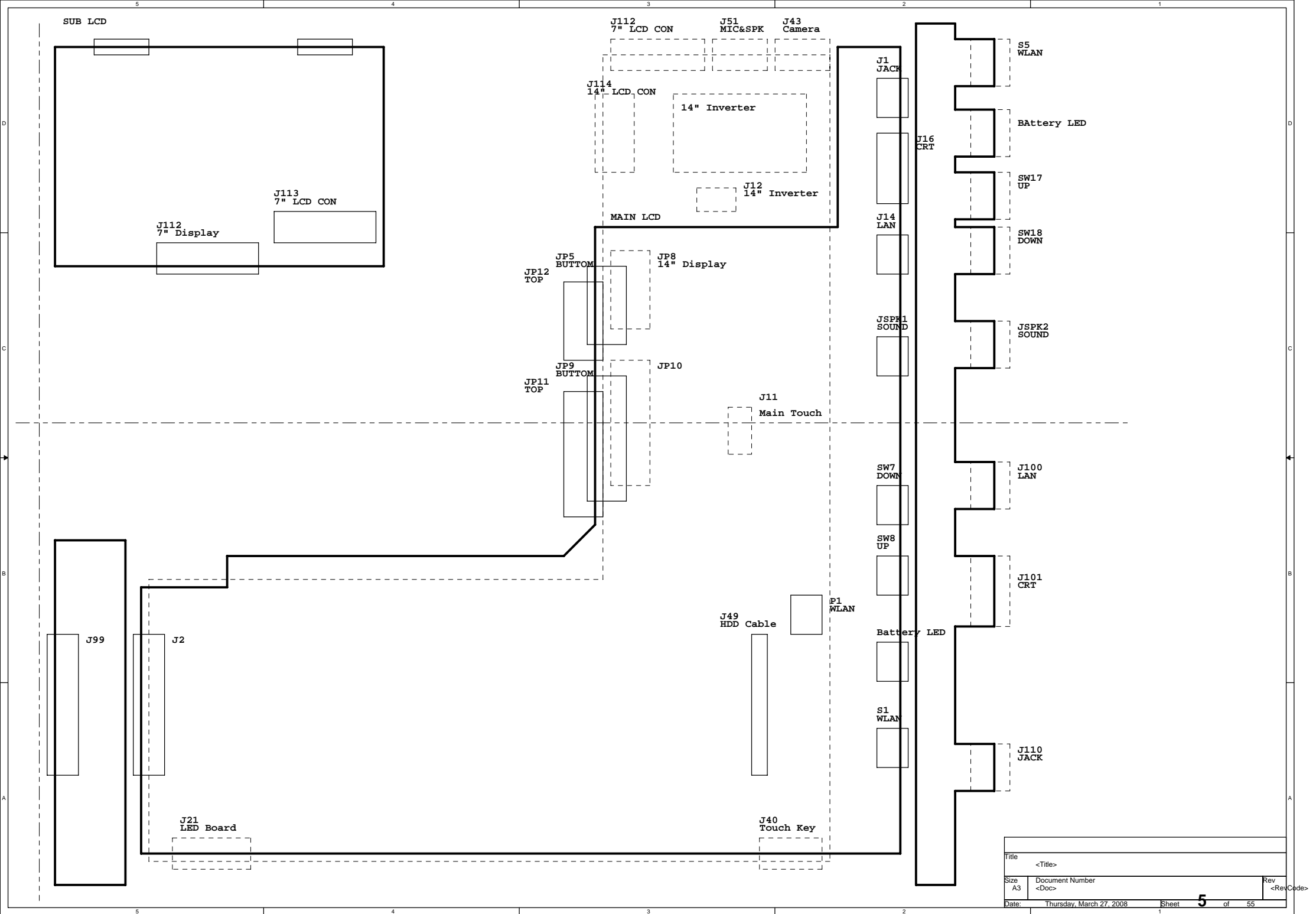


HOLD Key가 동작 할 경우  
USB전원을 끈다



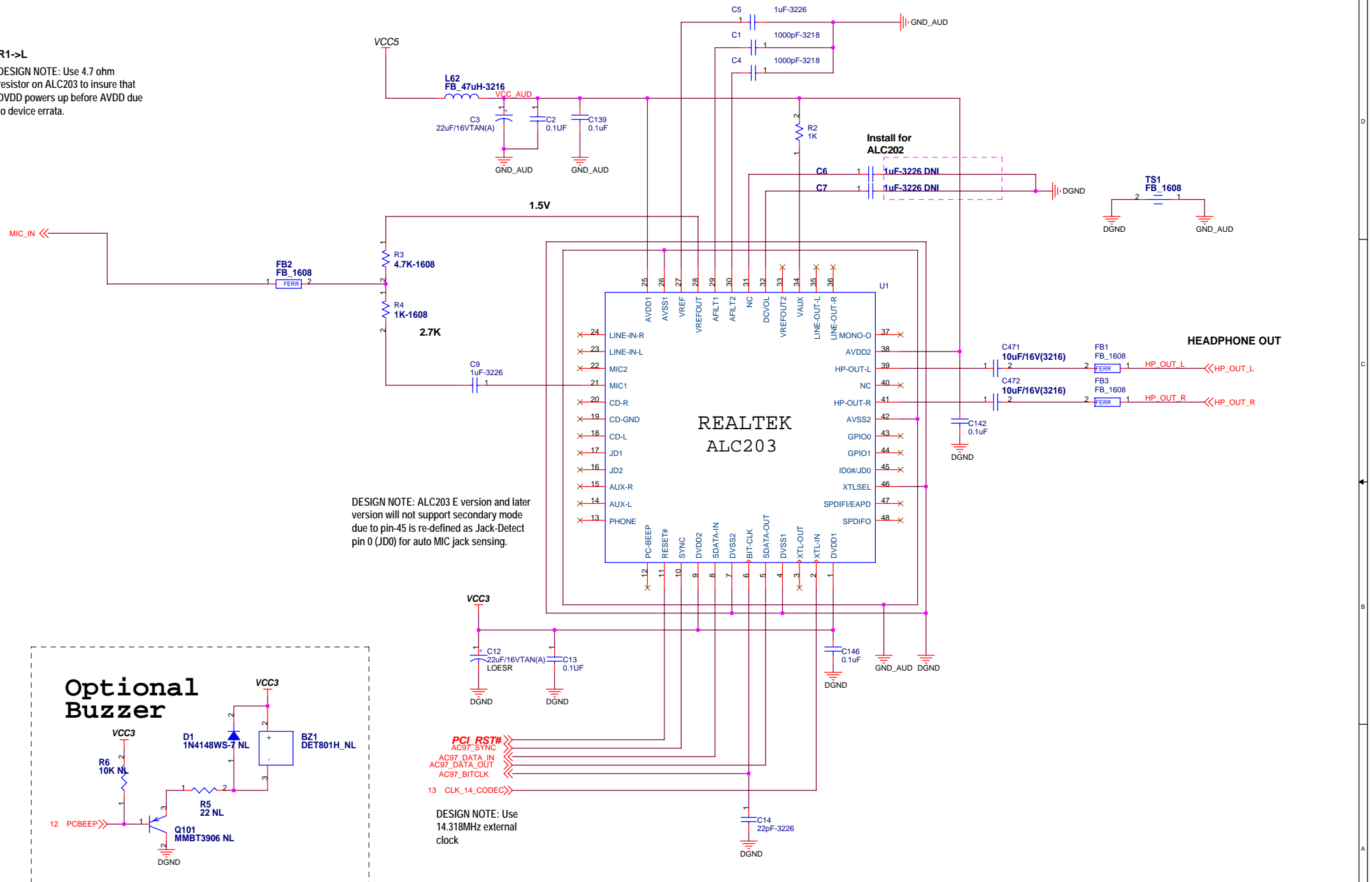
HOLD Key가 Sleep Key와  
Power Key가 동작한다



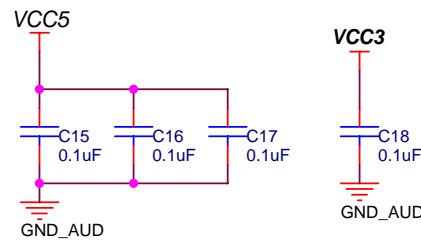
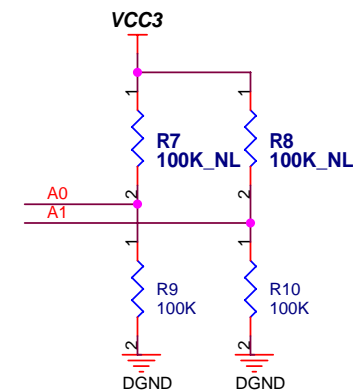
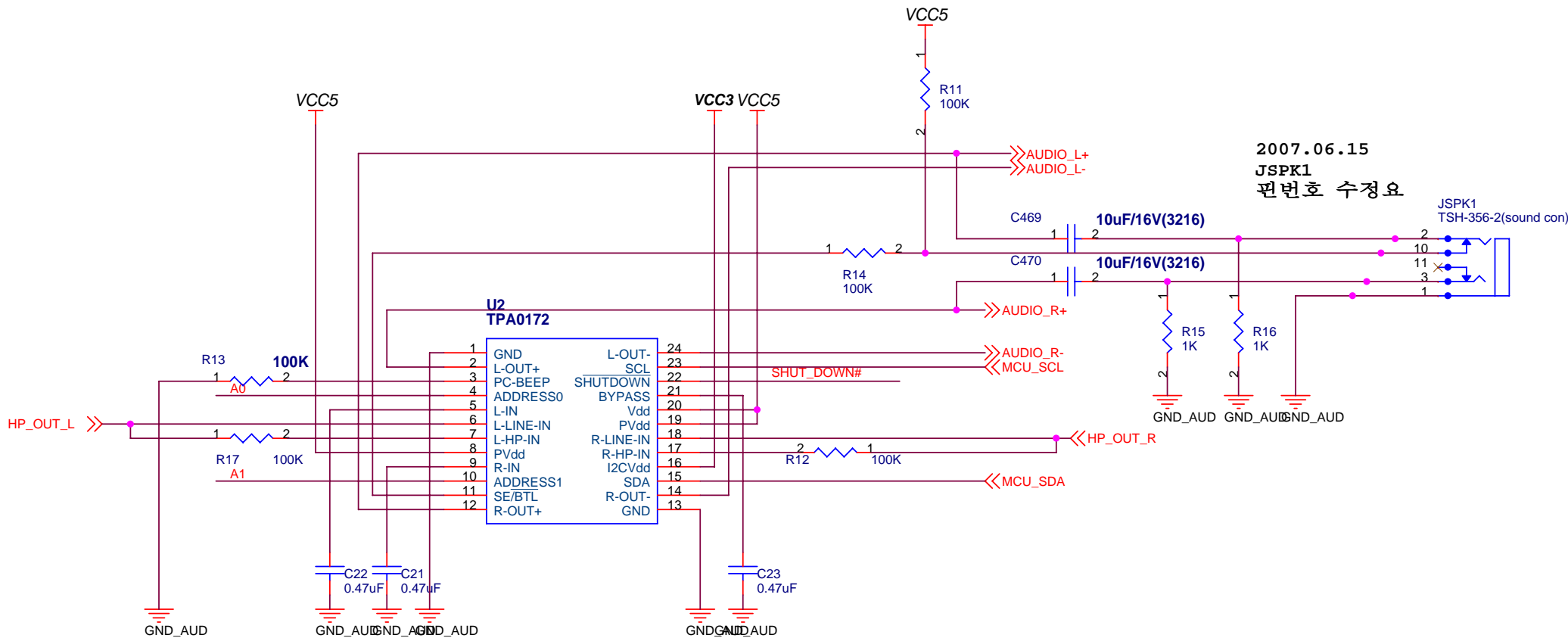


R1->L

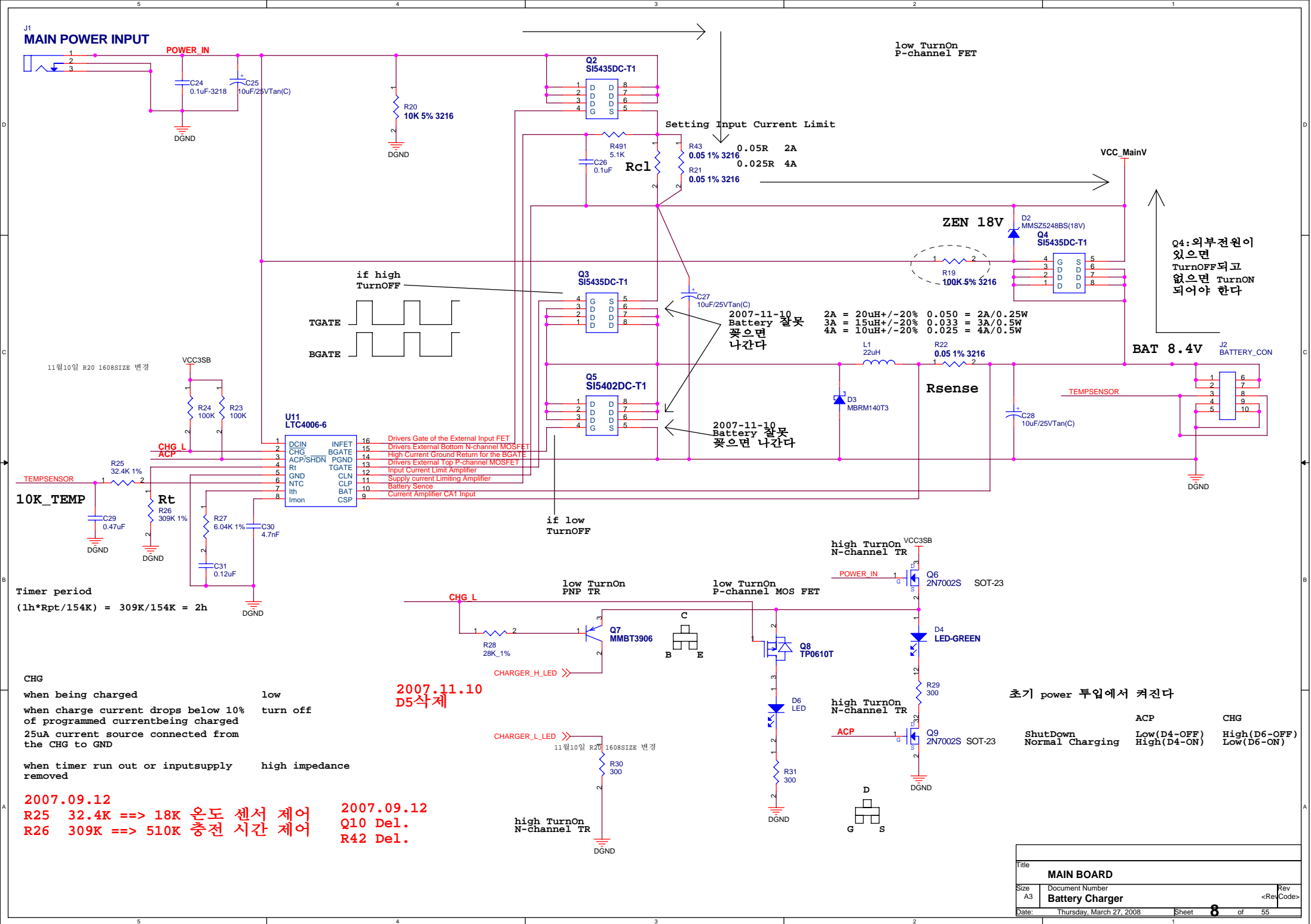
DESIGN NOTE: Use 4.7 ohm resistor on ALC203 to insure that DVDD powers up before AVDD due to device errata.

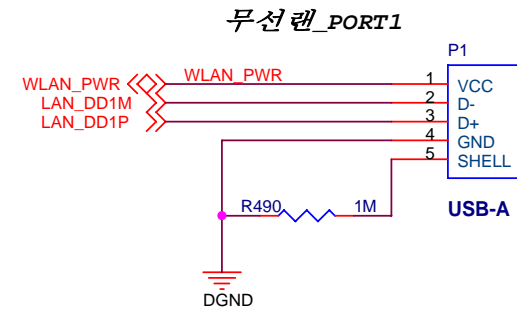


Title			MAIN BOARD
Size	Document Number	Rev	
A3	Audio CODEC	<RevCode>	
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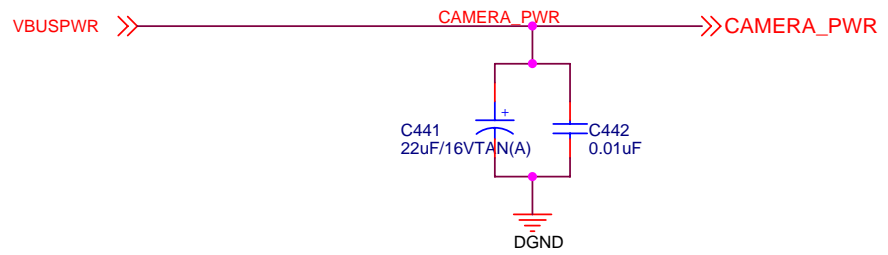


Title			
MAIN BOARD			
Size	Document Number	Rev	
A4	Audio AMP	<Rev Code>	
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2007.09.12  
U4 Del.  
R44 Del.



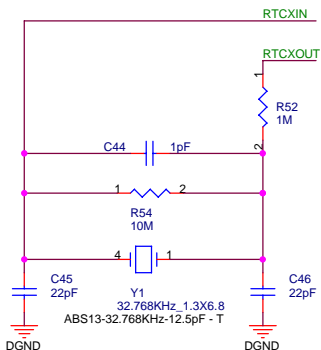
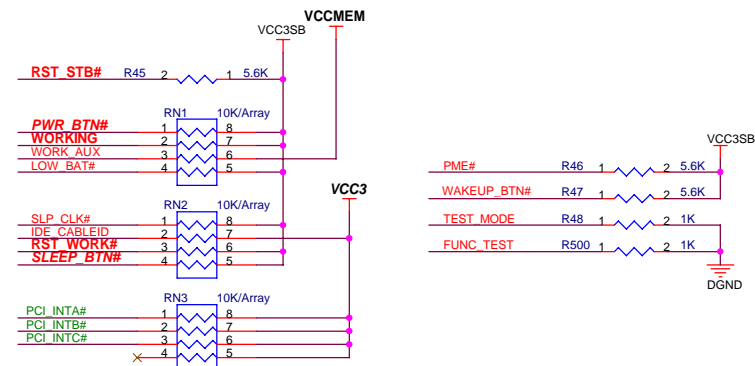
2007.09.12  
Q105 Del.  
R458 Del.

2007.09.12  
Q106 Del.  
R459 Del.

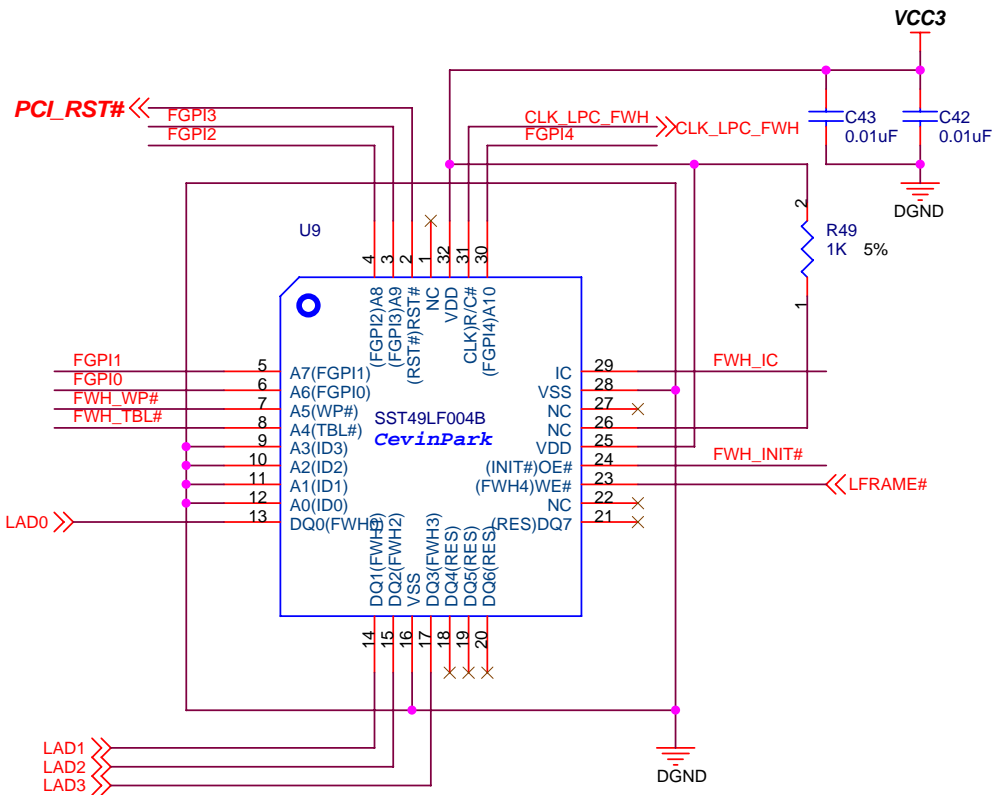
Title			
MAIN BOARD			
Size	Document Number		Rev
A4	CAM & WLAN Connector		<Rev Code>
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DESIGN NOTE: Any GPIO can be used as an IRQx or INTx.  
There are significant bootloader configuration changes required if selection is different than shown. Changes are highly discouraged.



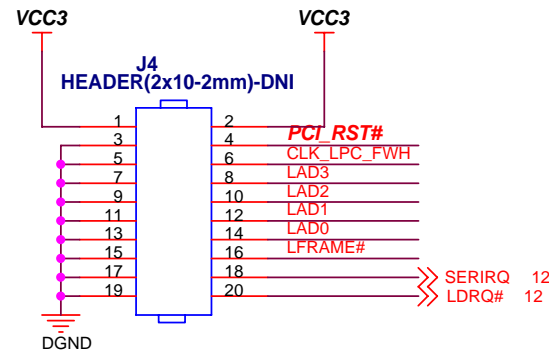
Title			
MAIN BOARD			
Size	Document Number	Rev	
A3	CS55436 PCI & System	<RevCode>	
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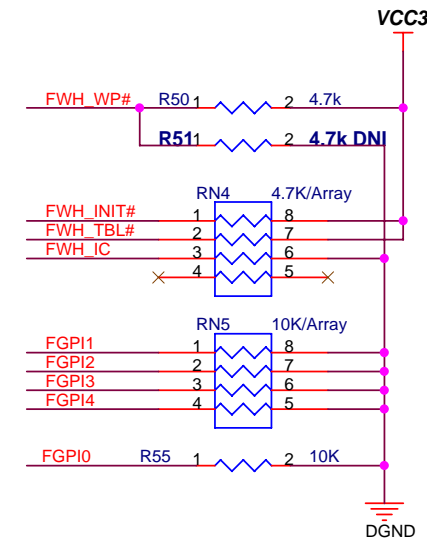
SST FWH: 49LF002B(2MB)/003B(3MB)/004B(4MB) DEFAULT  
 SST LPC ROM:49LF030A(3MB)/040A(4MB)/80A(8MB)  
 WINBOND LPCROM W49V002/003/004

## LPC Header

2007.06.15  
 J4  
 NL



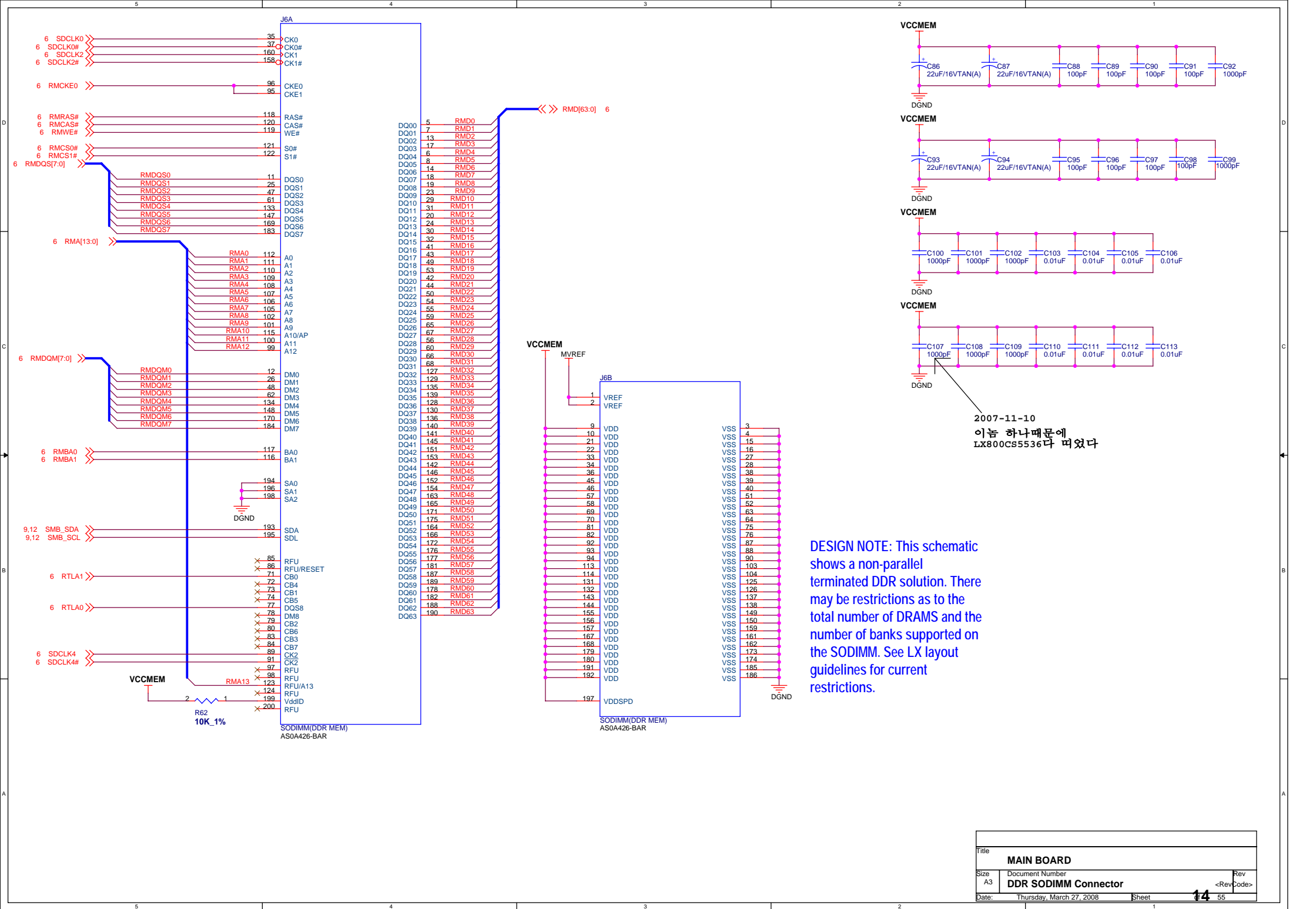
DESIGN NOTE: LPC Header is optional but recommended for debug support.

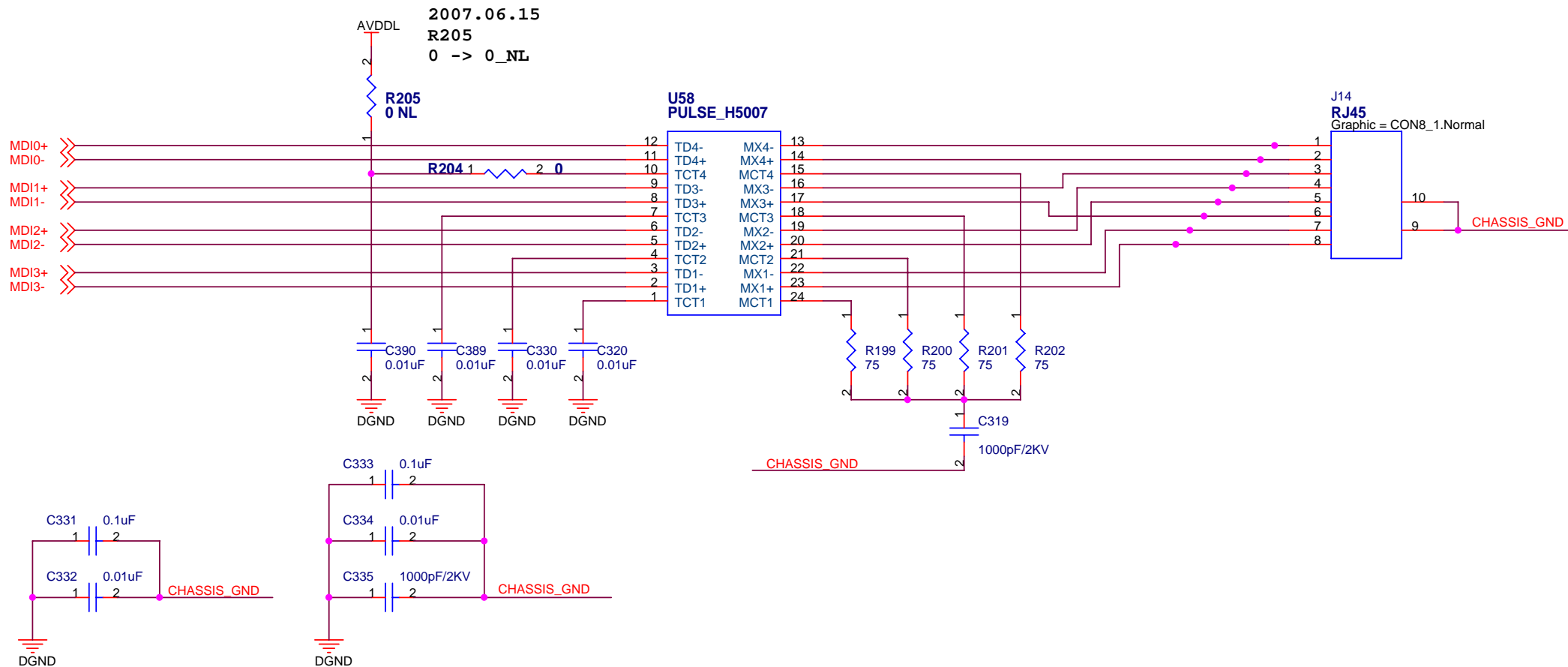




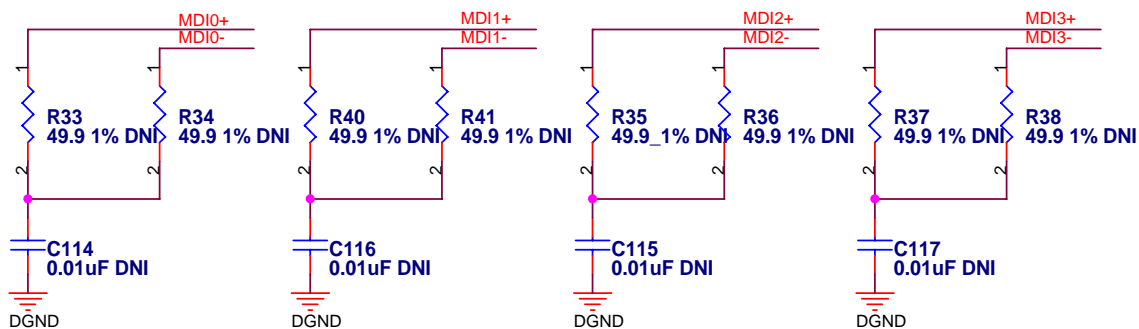




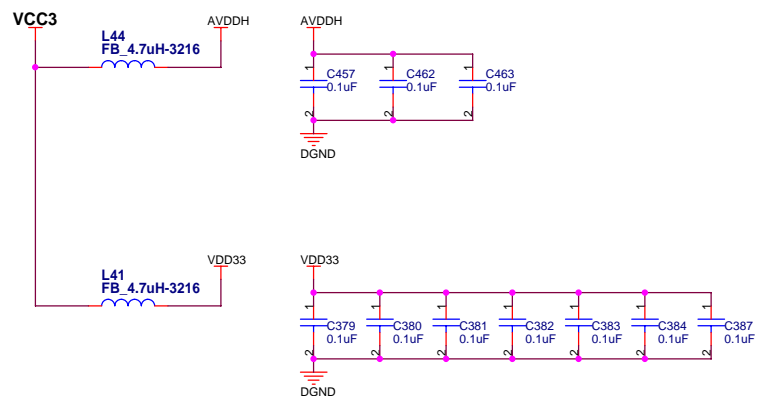
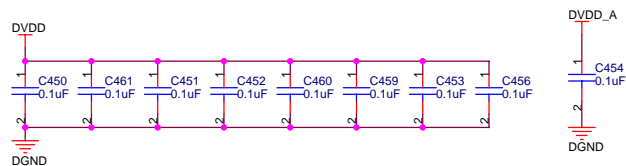
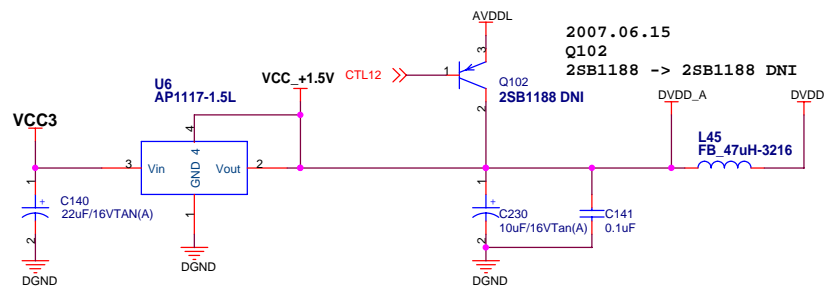
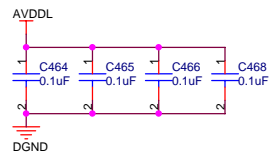
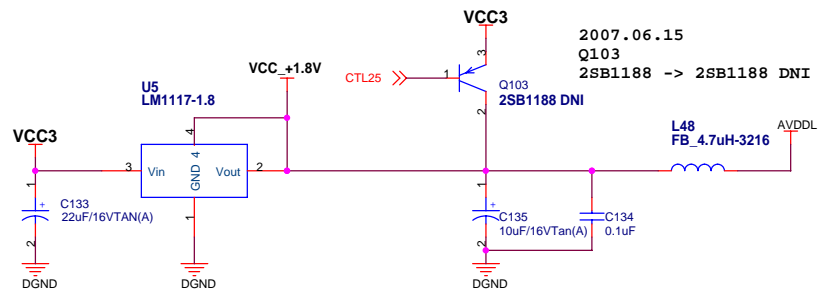


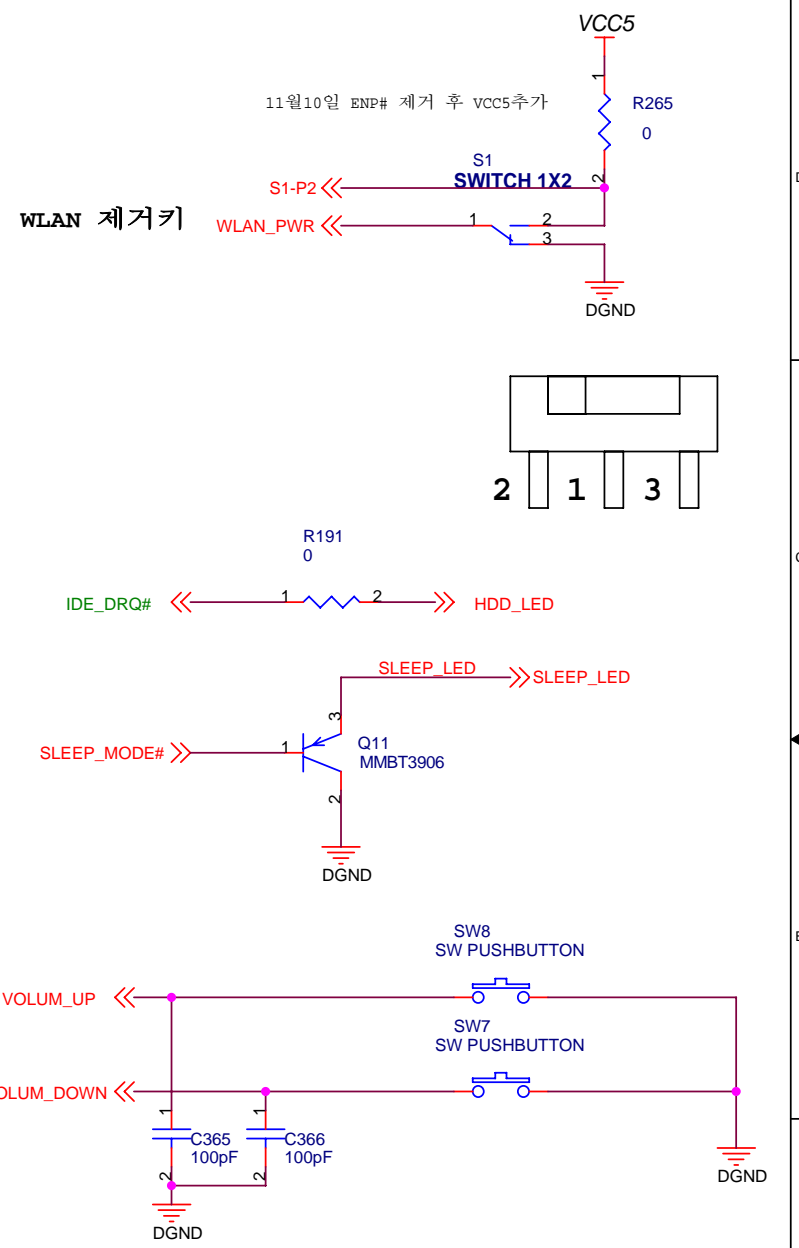
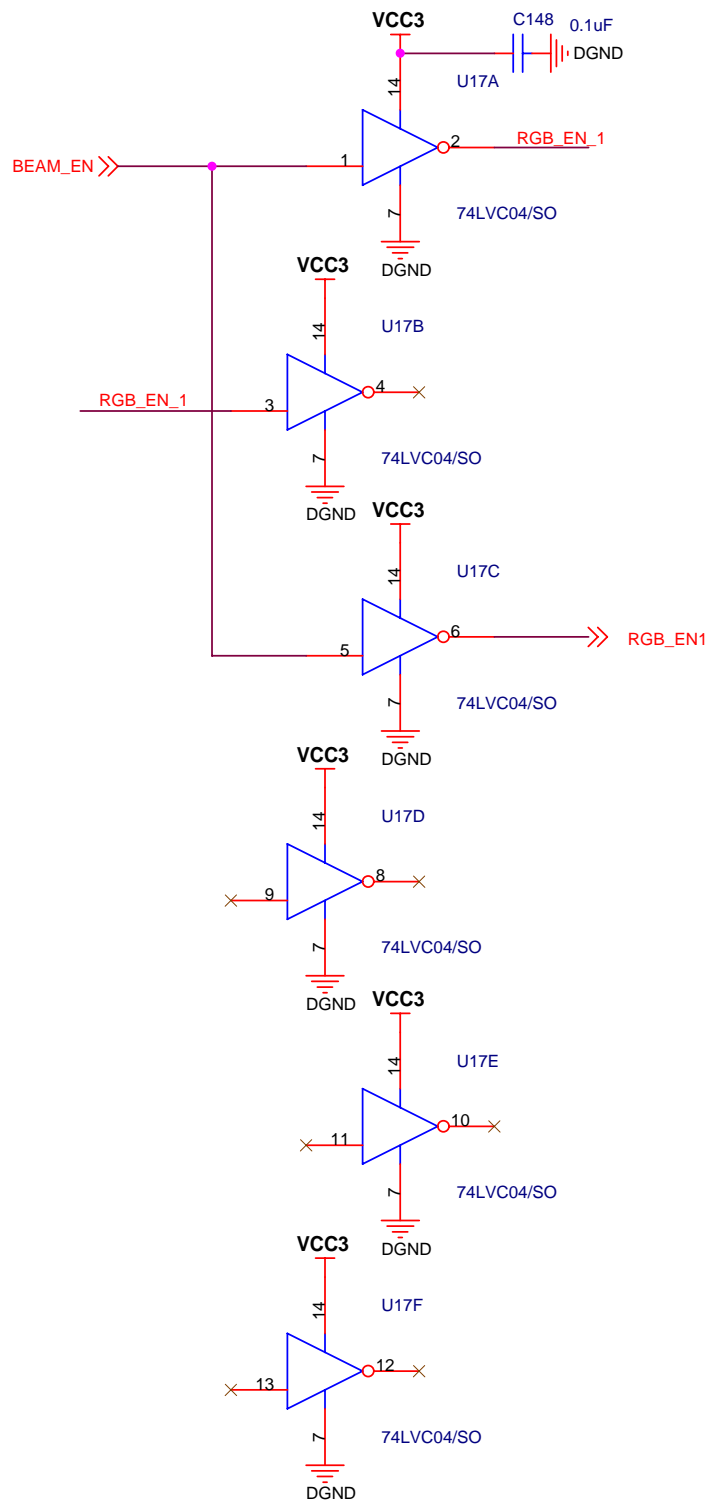


2007.06.15 상자 속 소자 모두 NL

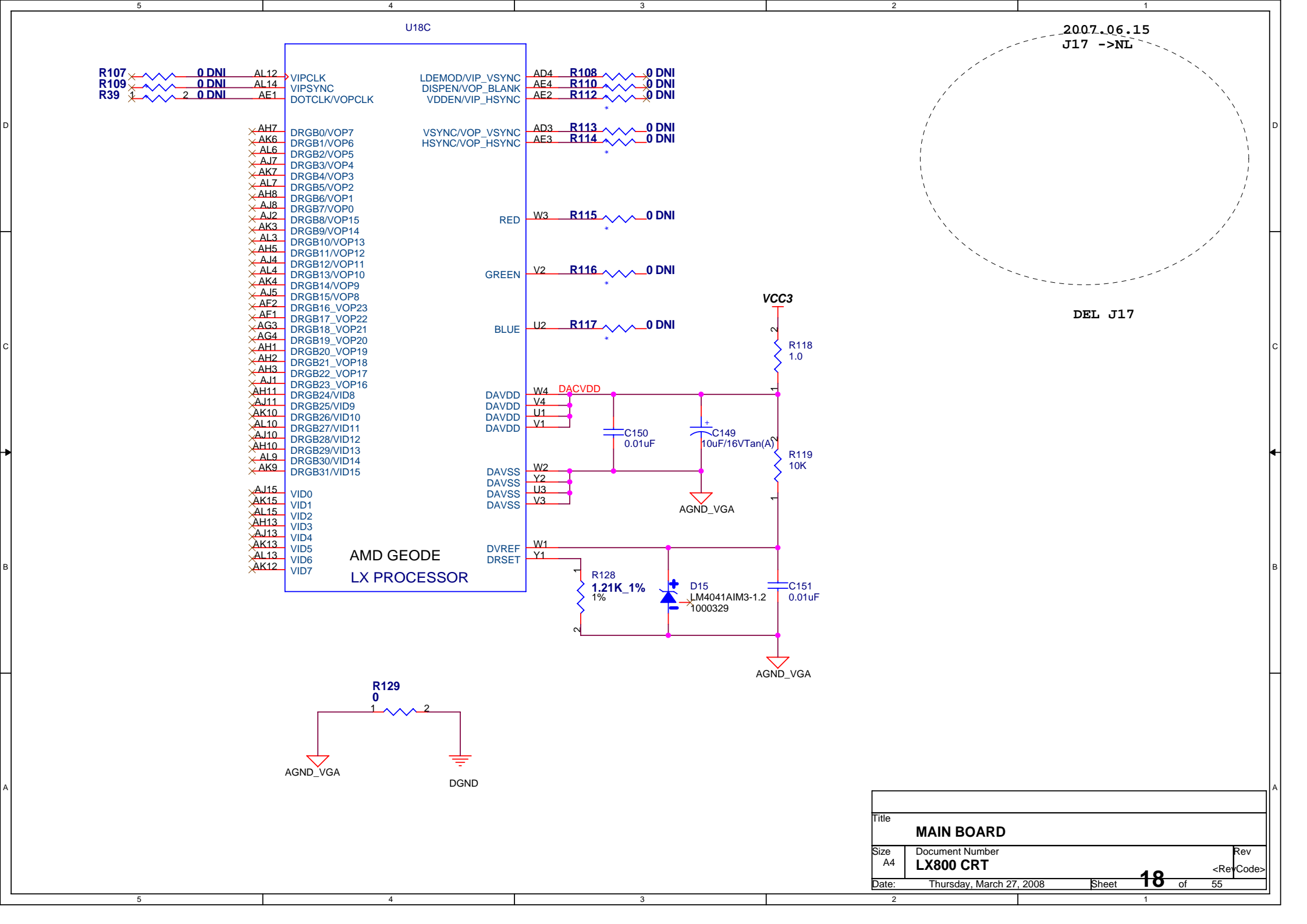


Title		
MAIN BOARD		
Size	Document Number	Rev
A4	Ethernet Transformer	<RevCode>
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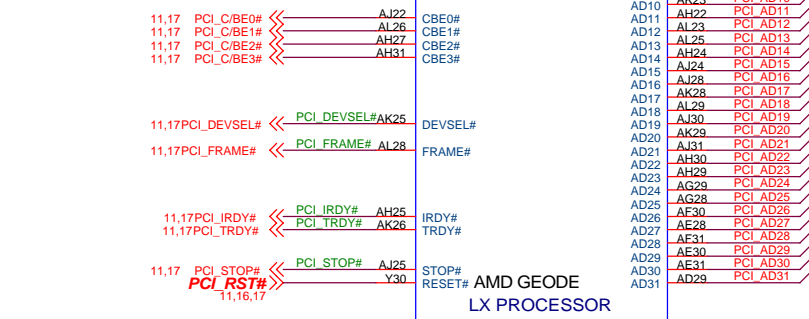


Title		
MAIN BOARD		
Size A4	Document Number Front LED & Key	Rev <RevCode>
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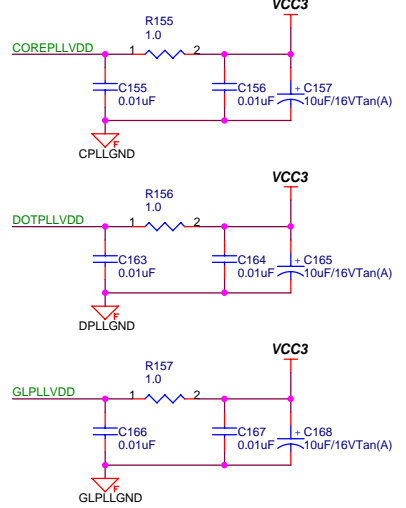
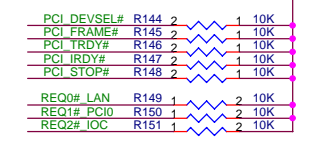
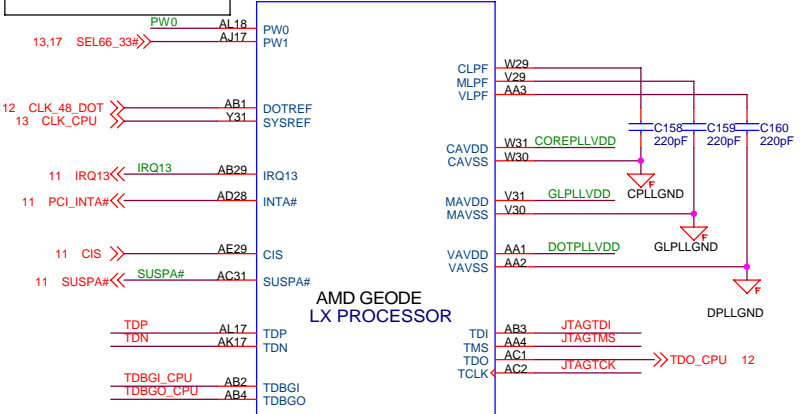




DESIGN NOTE: REQ1 & GNT1 are not used in the base schematic. Can be used for PCI slot or another PCI device.



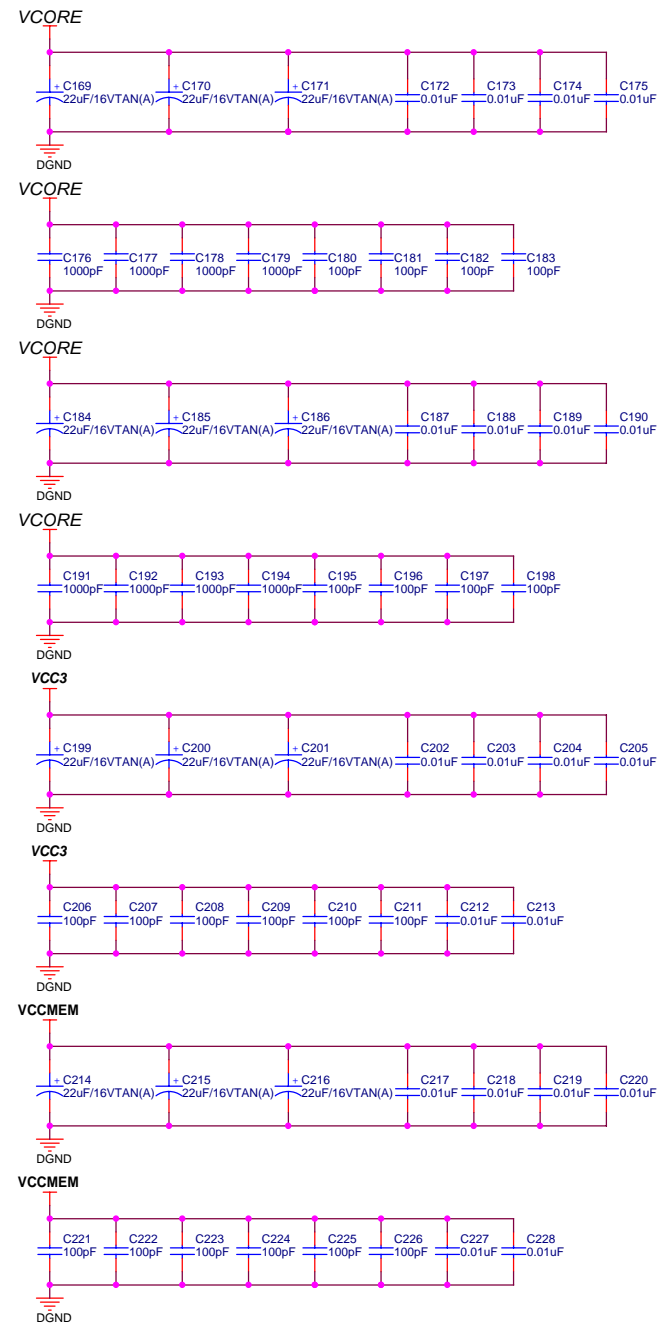
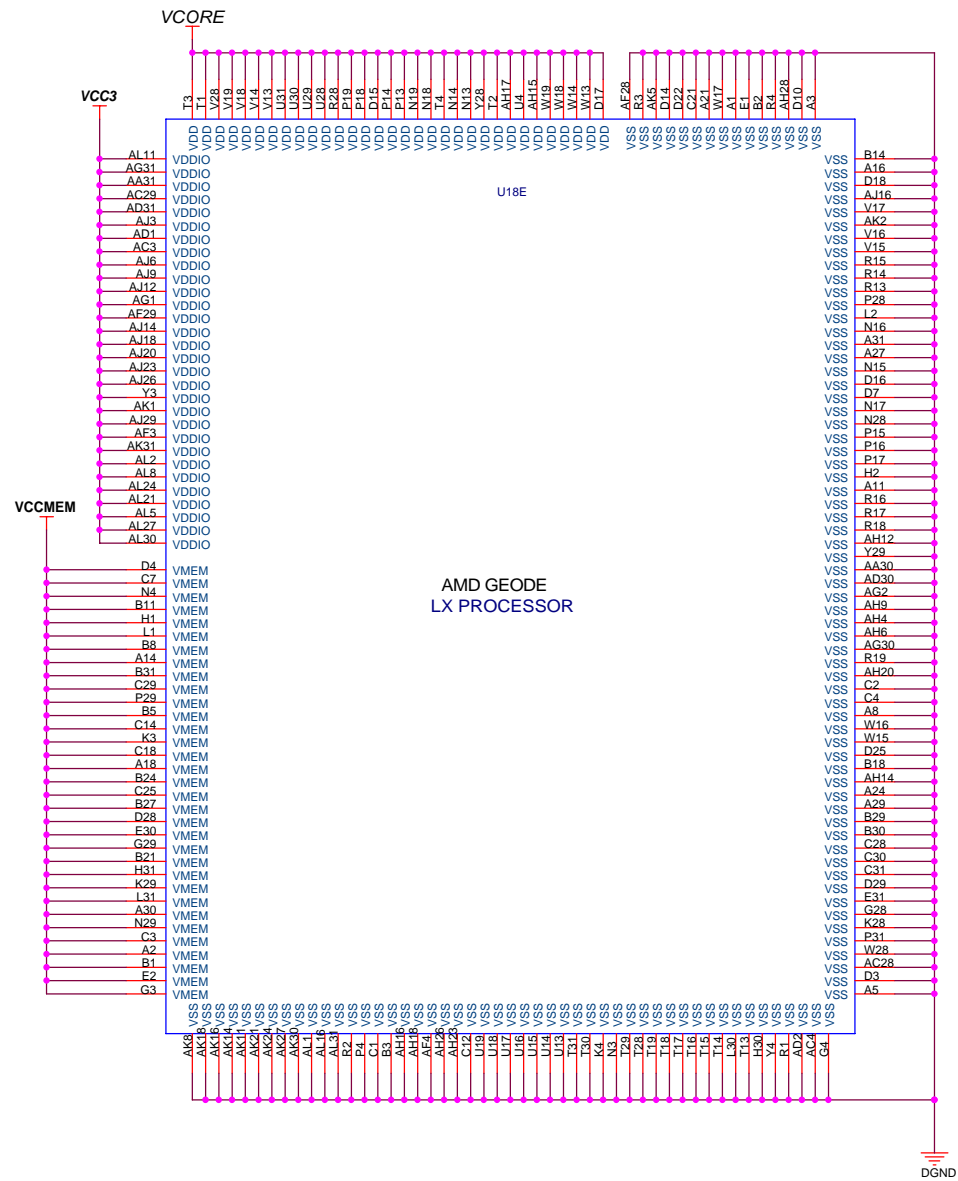
DESIGN NOTE: CLK\_48\_DOT source can be from an external TV encoder if that is the desired display. This allows for solid Genlock operation.



ACCESS.BUS ADDRESS=98 FOR LM86  
**TEMPERATURE SENSOR**

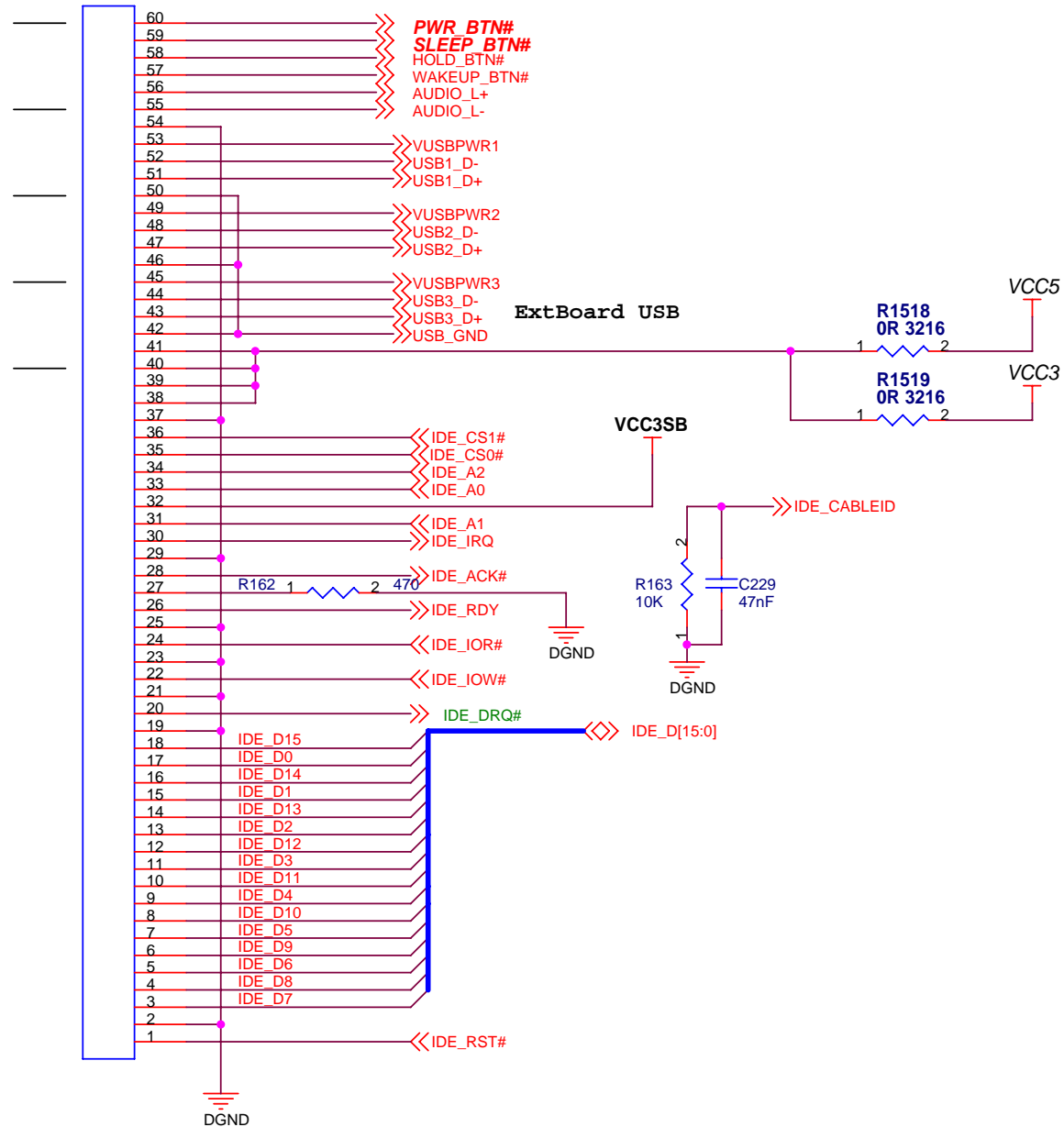
Title			
MAIN BOARD			
Size	A3	Document Number	Rev
		LM800 PCI System	<RevCode>
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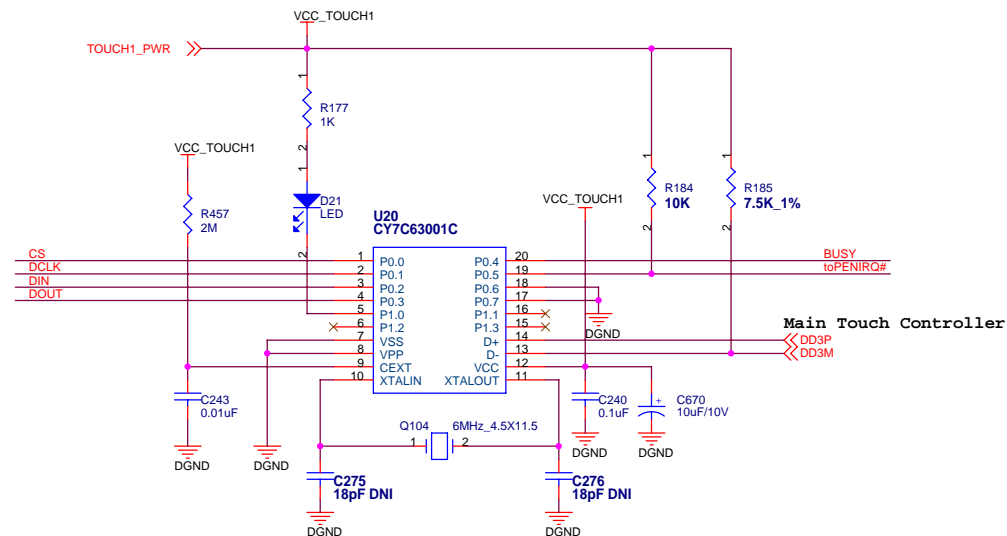
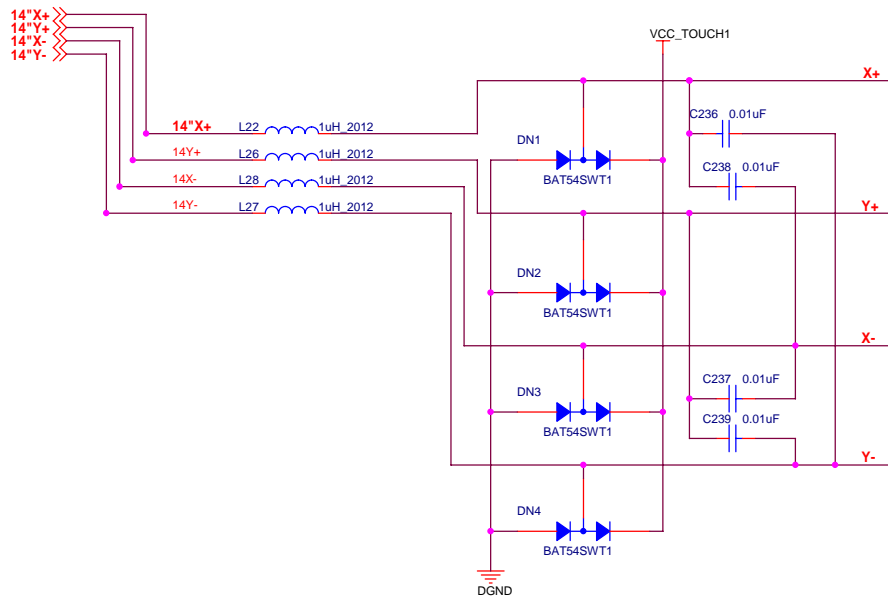


Title			
MAIN BOARD			
Size	Document Number	Rev	
A3	LX800 Power	<RevCode>	
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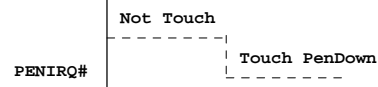
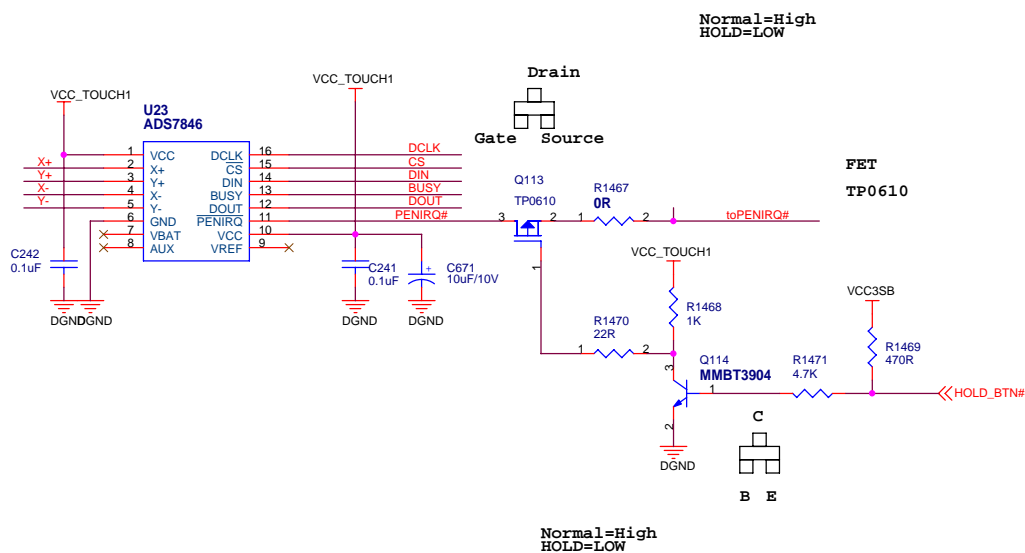
J49  
05002HR-60J02-L



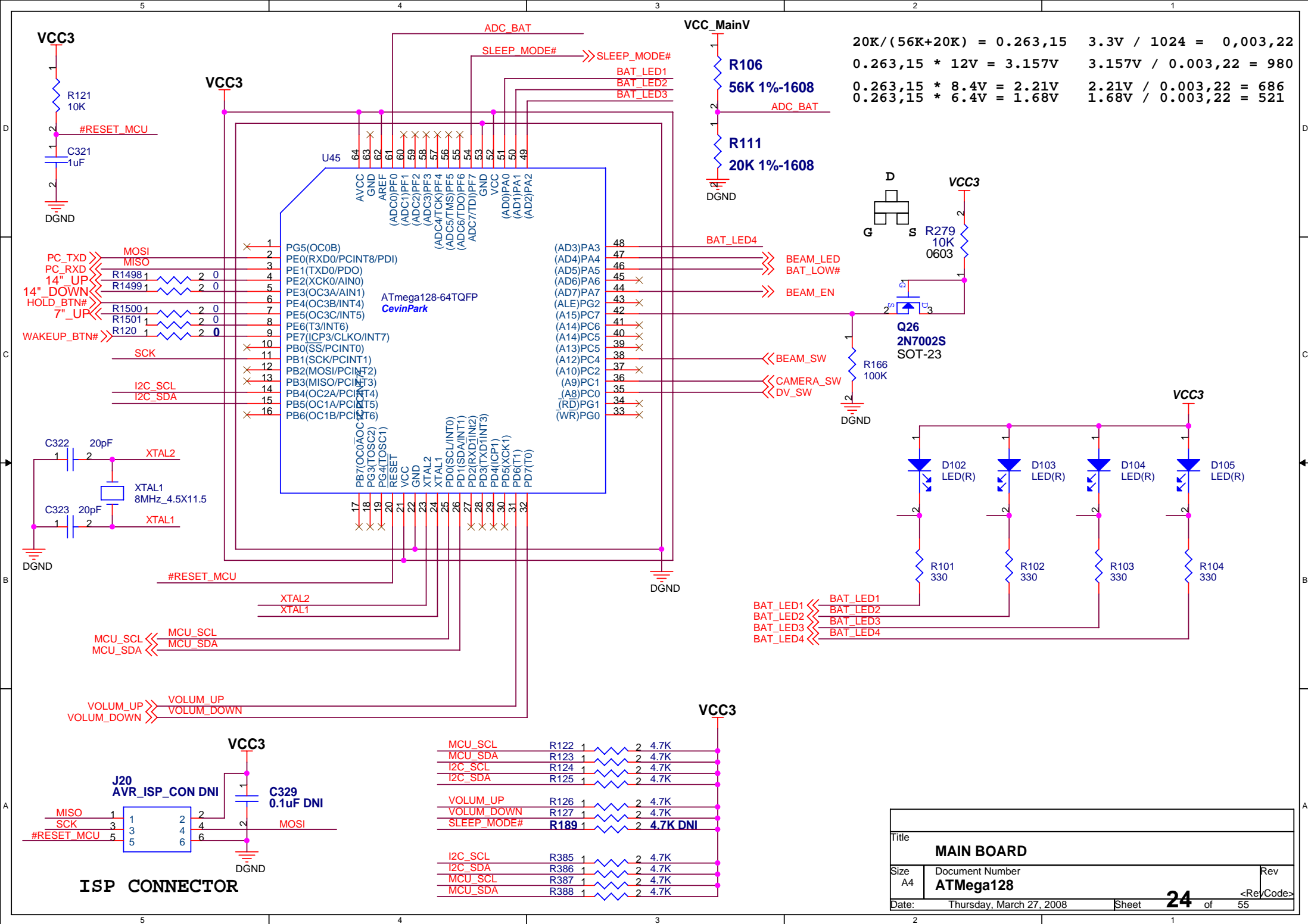
Title			
MAIN BOARD			
Size	Document Number	Rev	
A4	EXT Board Connector	<RevCode>	
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2007.06.15  
C275,C276  
18pF -> 18pF\_NL



Title		
MAIN BOARD		
Size	Document Number	Rev
A3	Main Touch	<RevCode>
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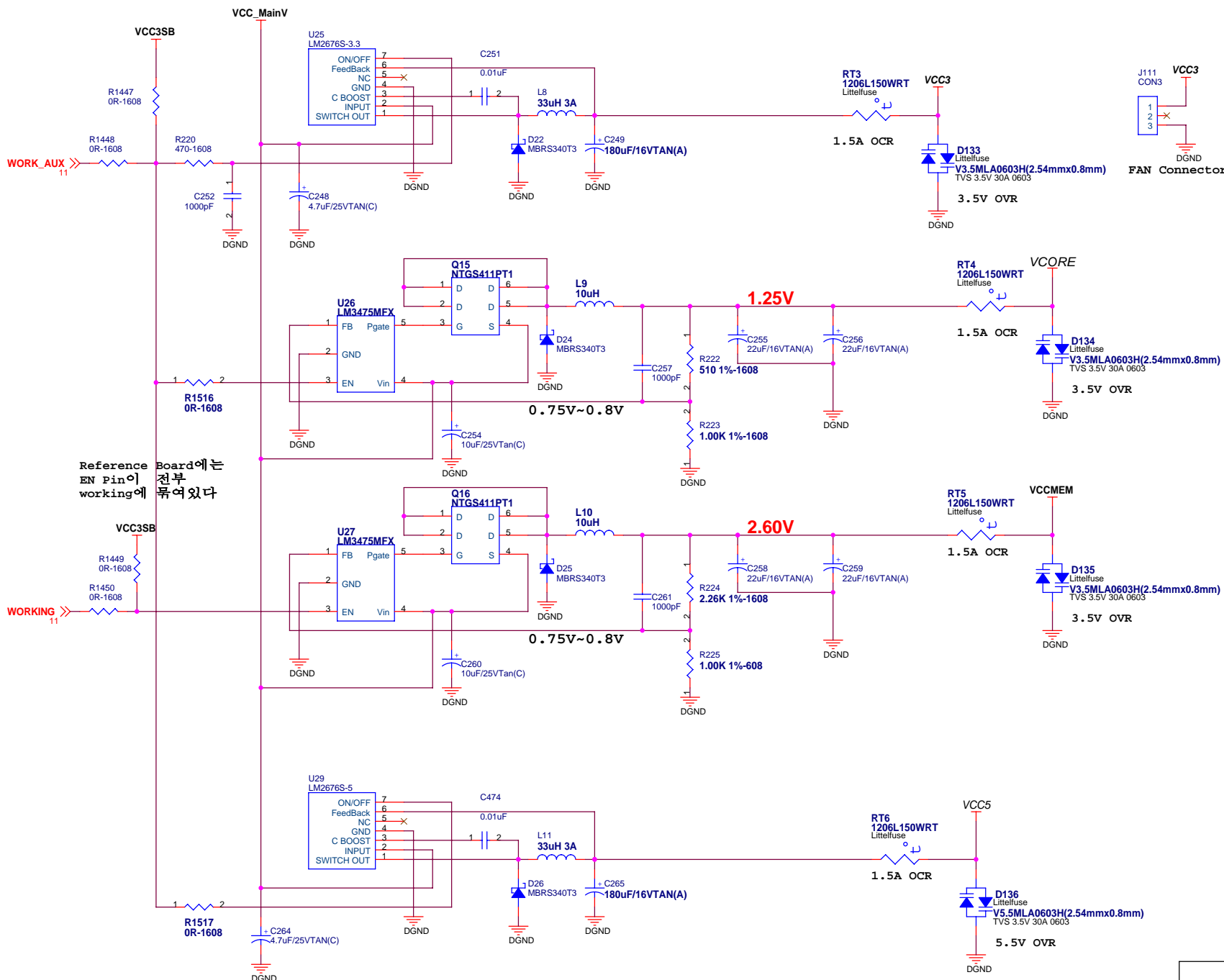


11월10일 수정

J56 삭제

DESIGN NOTE:Formula for Vout  
when using the LM3475:

$$V_{out} = ((R1 + R2) / R2) * 0.8V$$



PWRBUT#(CS5536-A8) WORK\_AUX(CS5536-C9)  
PWRBUT#(CS5536-A8) WORKING(CS5536-C5)

Title		MAIN BOARD	
Size	A3	Document Number	Power
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		Rev	<RevCode>

100

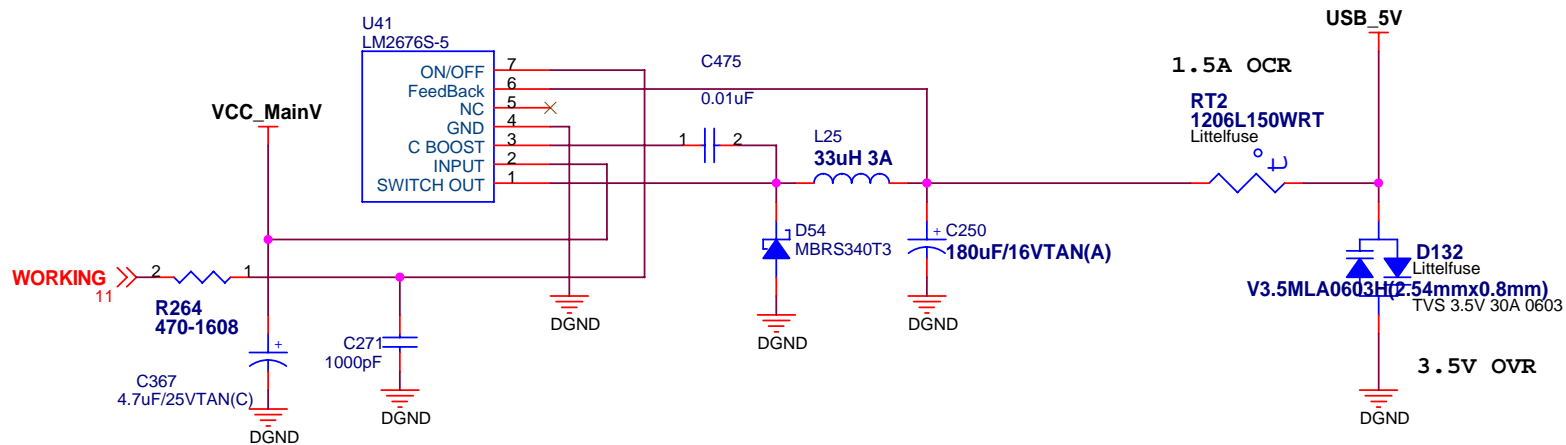


22uF/20VTan(A) -> 22uF/20VTan(A)\_NL

## A



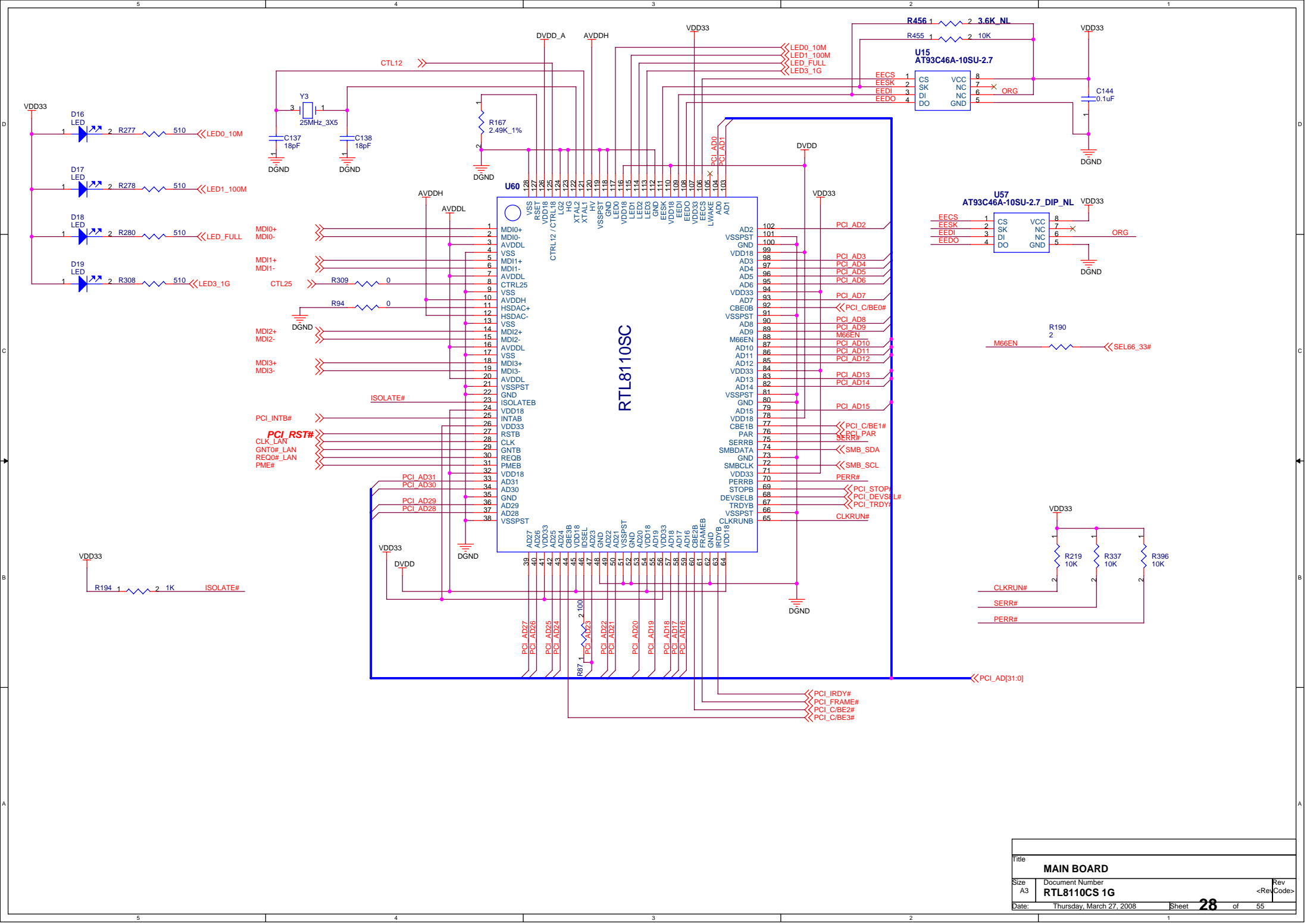
Title <b>MAIN BOARD</b>			
Size A4	Document Number <b>Always Power</b>		Rev <b>26</b>
Date:	Thursday, March 27, 2008	Sheet <b>26</b>	of 55 <Rev Code>



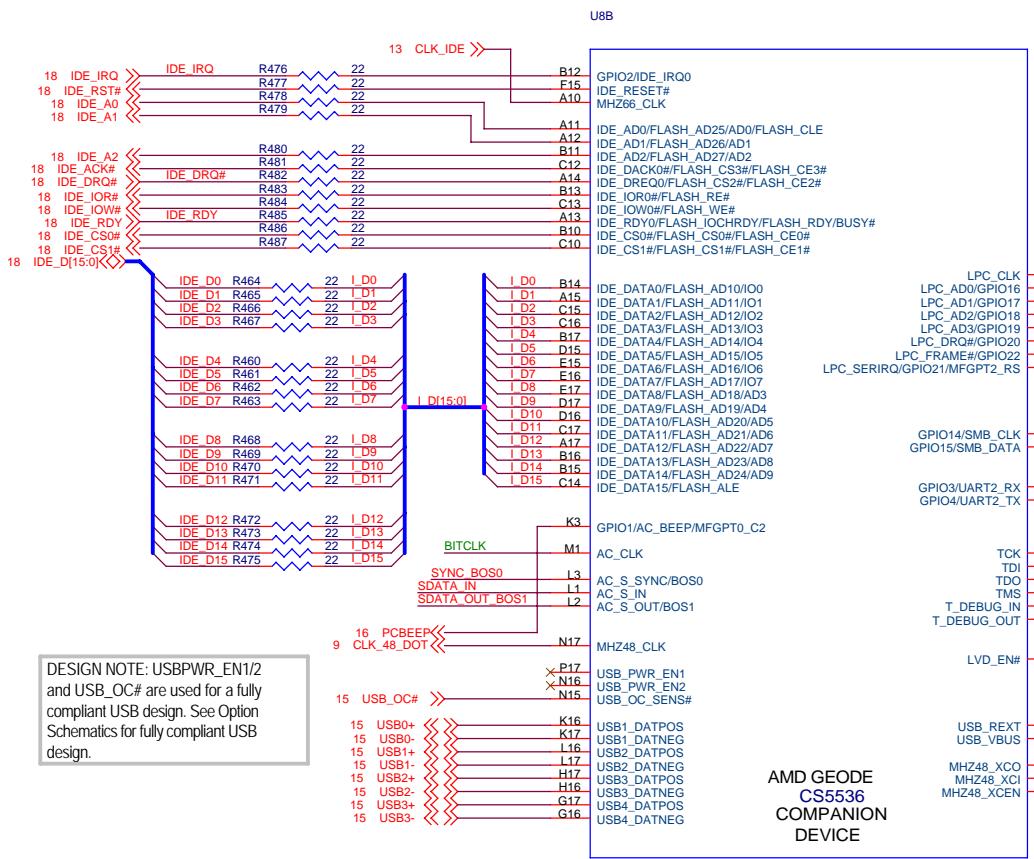
DESIGN NOTE:Formula for Vout  
when using the LM3475:

$$V_{out} = ( ( R1 + R2 ) / R2 ) * 0.8V$$

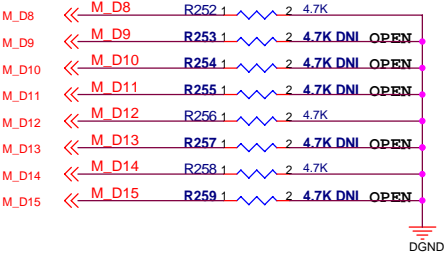
Title		
MAIN BOARD		
Size A4	Document Number Expansion Power	Rev <RevCode>
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2007.06.15  
R253, R254, R255, R257, R259  
4.7K -> 4.7K\_NL



Panel Display Settings:

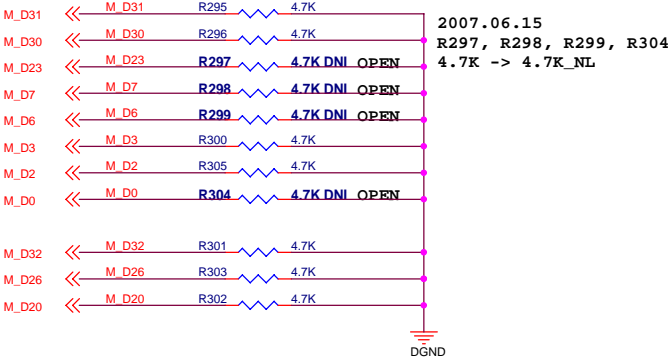
SW	MD Bits	STATE	SETTING
1	MD8	0	TFT
		1	DSTN
2	MD9	0	FPCLK Normal
		1	FPCLK Inverted
4:3	MD[11:10]	0 0	640 x 480
		0 1	800 x 600
		1 0	1024 x 768
		1 1	1280 x 1024
7:6:5	MD[14:12]	0 0 0	9-BIT TFT
		0 0 1	12-BIT TFT
		0 1 0	18-BIT TFT
		0 1 1	24-BIT TFT
		1 0 0	12x2 - BIT TFT
		1 0 1	ANALOGTFT
		1 1 0	18x2 - BIT TFT
8	MD15	0	16-BIT DSTN
		1	24-BIT DSTN

Notes:

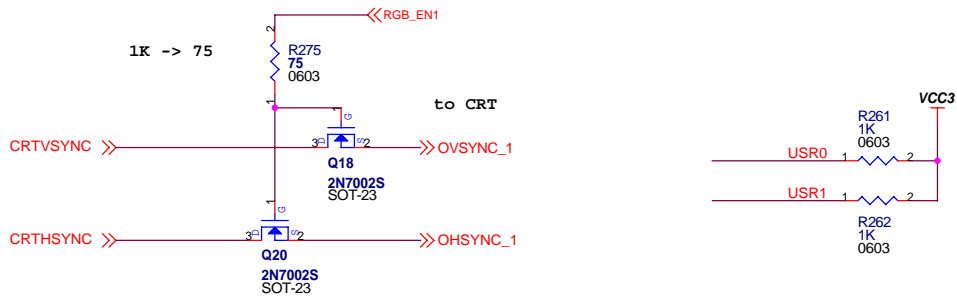
- (1) MD pins have internal pull-up resistors  
(2) 0 = Switch is SHORTED; 1 = Switch is OPEN

Configuration Settings: (MD3, MD2 and MD0 for Lynx3DM+ only)

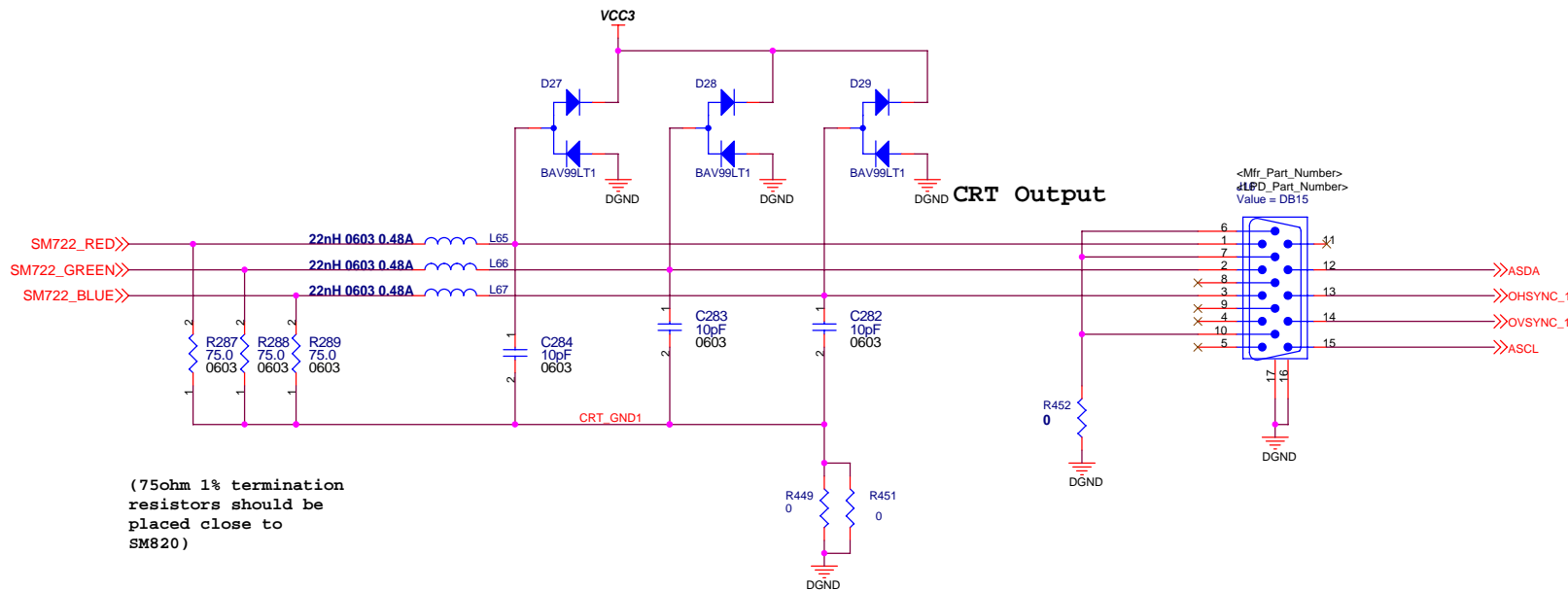
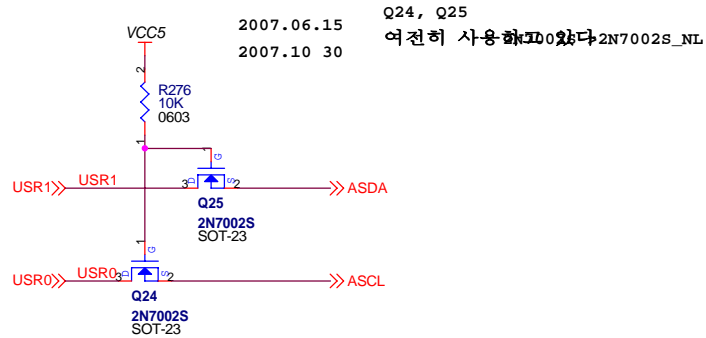
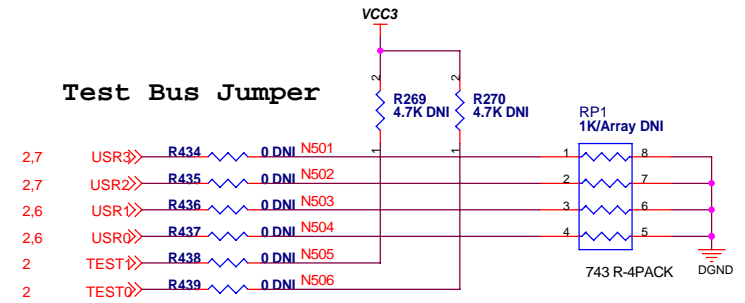
SW	MD Bits	STATE	SETTING
1	MD32	0	Disable AGP Sideband Signals
		1	Enable AGP Sideband Signals
2,3	MD2,MD0		MCB MEM. INTERFASE SIZE SELECT
			SIZEINT.INTERFASEMCLK MODE
			1164 BITS64 BITS1X
			0064 BITS128 BITS2X
			1032 BITS64 BITS2X
		01	RESERVED
4:5	MD[31:30]	00	8 MB
		01	16 MB MCB Memory Size Setting
		11	4 MB
		10	RESERVED
6:7	MD[7:6]		RESERVED
8	MD3	0	4 SDRAM Mem. bank
		1	2 SDRAM Mem. bank



2007.06.15  
R297, R298, R299, R304  
4.7K -> 4.7K\_NL

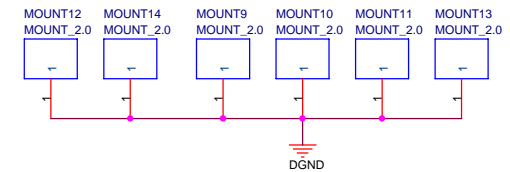
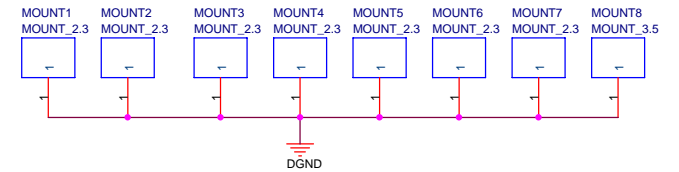
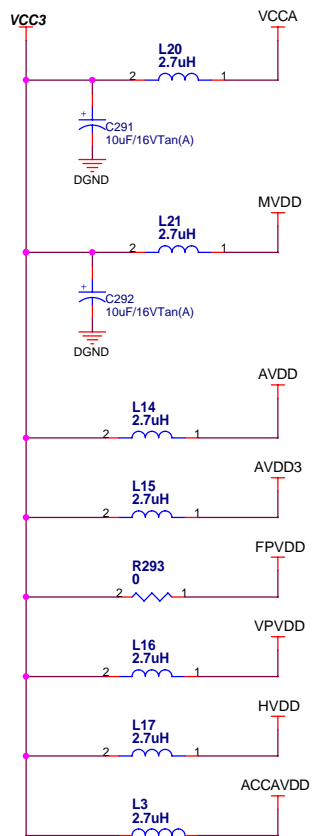


## Test Bus Jumper

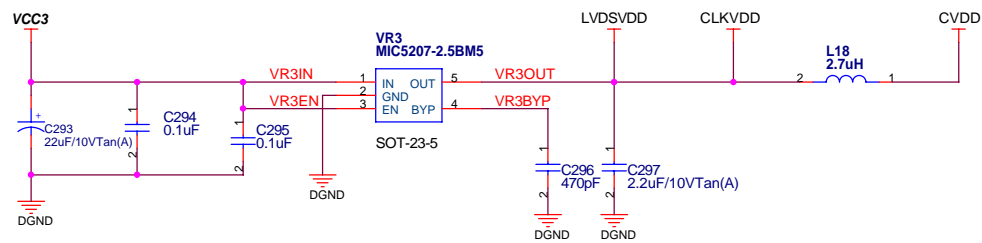


(75ohm 1% termination resistors should be placed close to SM820)

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Size	A3	Document Number	SM722 CRT	
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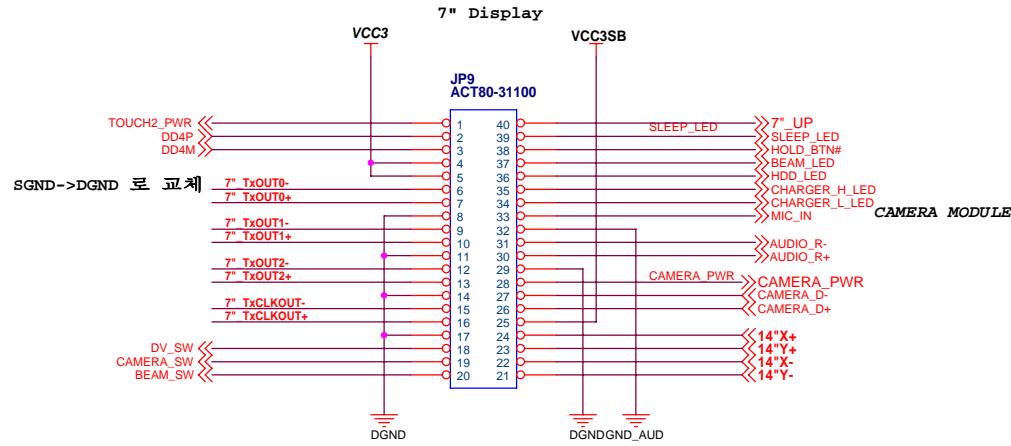


## +2.5V Voltage Regulators



Title			
MAIN BOARD			
Size	Document Number	Rev	
A3	SM722 POWER	<RevCode>	
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2008년 3월 4일 JP9 Pin번호 수정함  
40Pin으로 수정요망  
VDD\_3.3V없음->VCC3으로교체

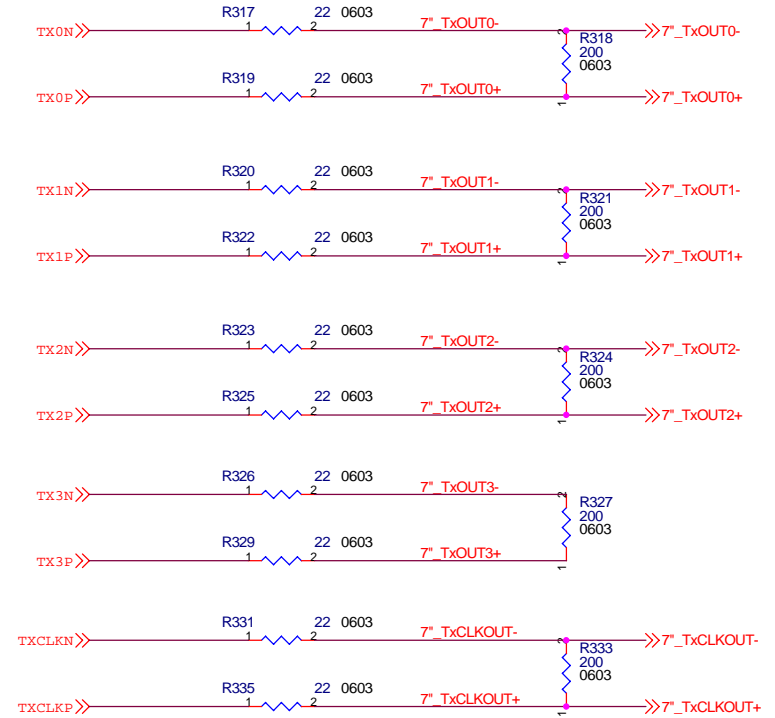


## LVDS Transmitter

Transmitter Strobe Setting:

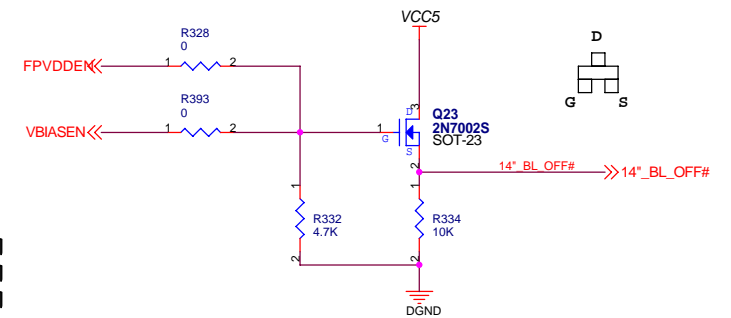
R121=10k, Rising Edge Strobe

R121=OPEN, Falling Edge Strobe



Load it If no U7 Loaded  
for Lynx3DM+

Only install if no  
termination on the  
LVDS penal side



2007-11-18, R165 삭제

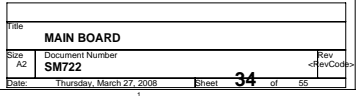
2007-11-18, R161 삭제

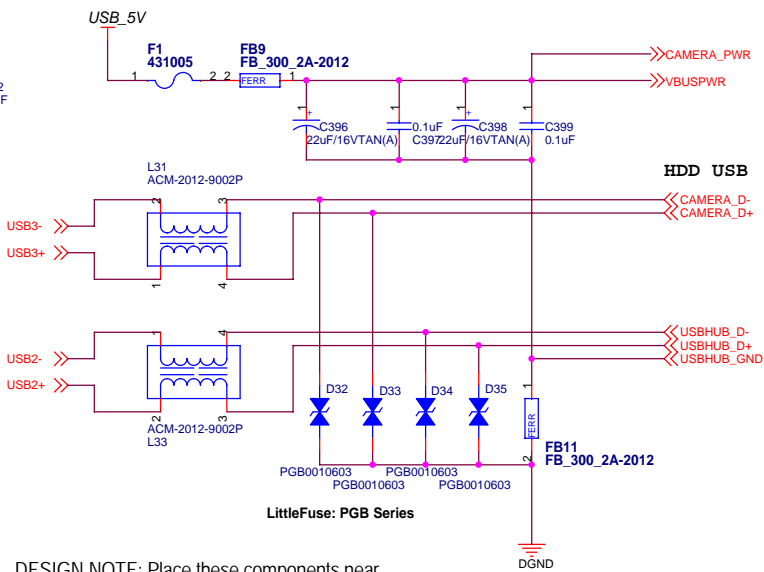
2007-11-18, R164 삭제

2007-11-18, L37 삭제

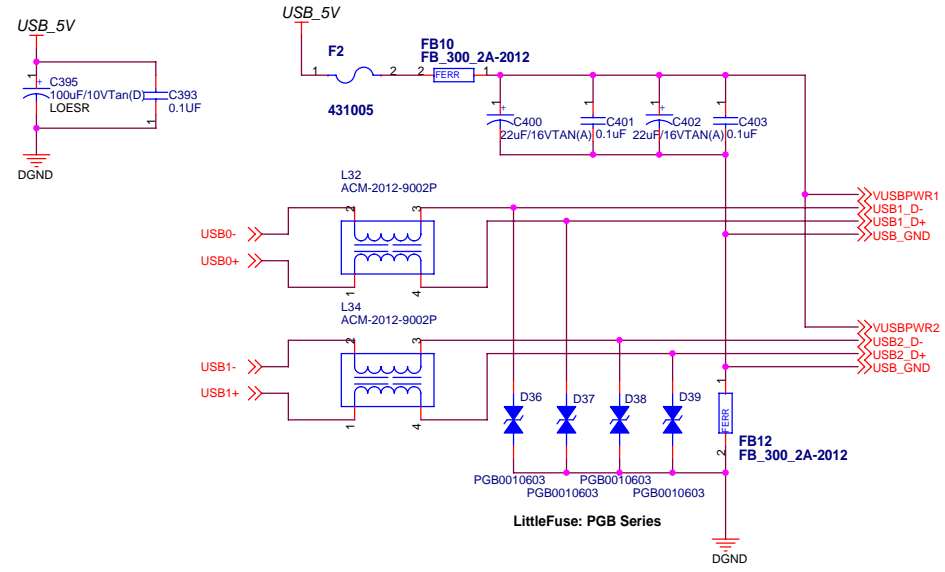
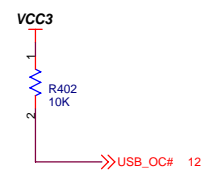
2007-11-18, L36 삭제

Title		
MAIN BOARD		
Size	Document Number	Rev
A3	SM722 LVDS	<RevCode>
Date:	Thursday, March 27, 2008	Sheet 33 of 55



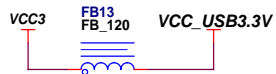


DESIGN NOTE: Place these components near the connectors.



DESIGN NOTE: Place these components near the connectors.

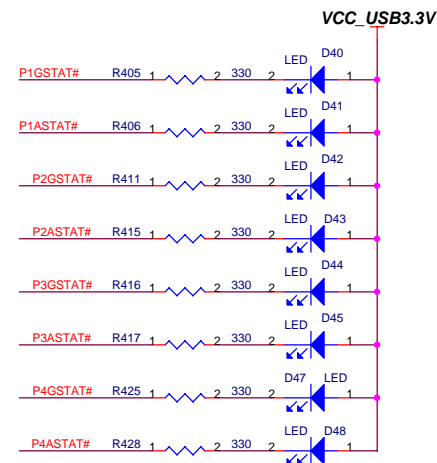
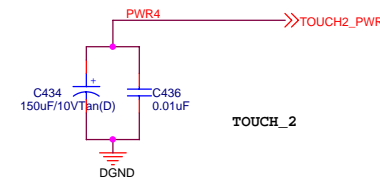
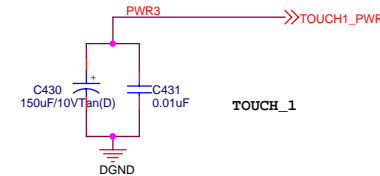
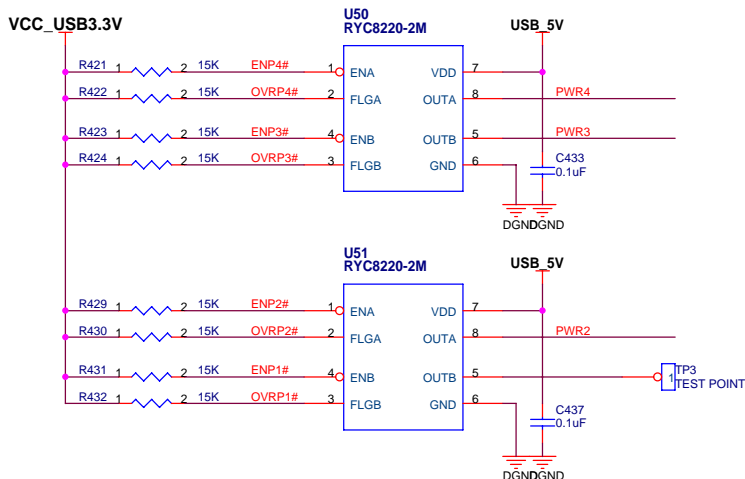
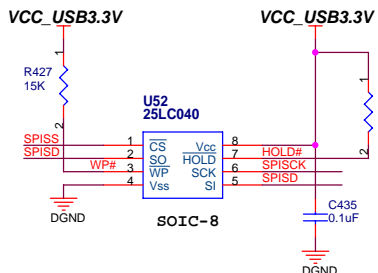
Title		
MAIN BOARD		
Size	Document Number	Rev
A3	USB Connector	<RevCode>
Date:	Thursday, March 27, 2008	Sheet 35 of 55



SUB Touch

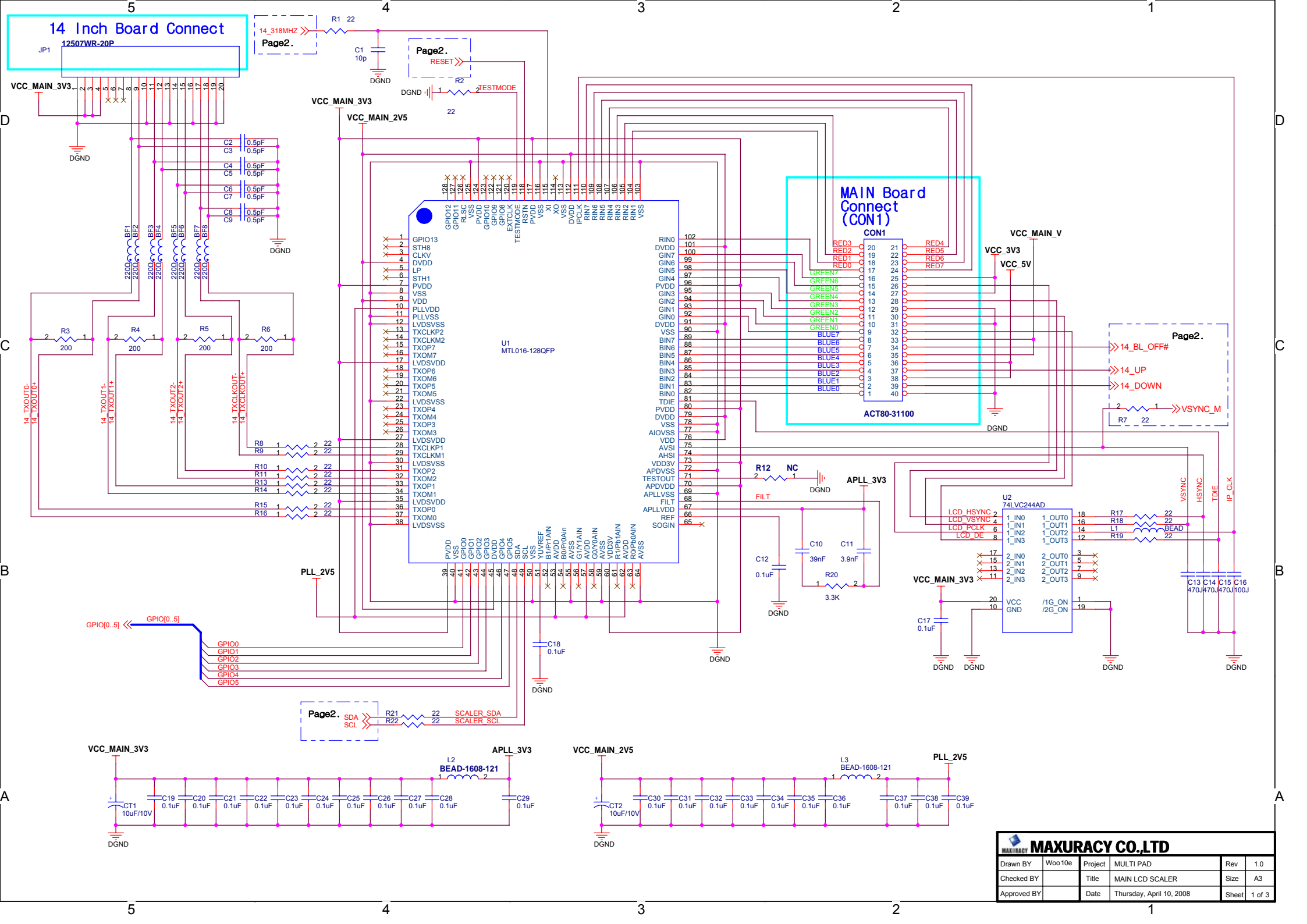
The schematic shows the electrical connections for the USB-to-serial interface. A USB 5V supply is connected to the IN pin of the U49 LT1763CS8-3.3-DNI voltage converter. The output of the converter is connected to the VCC\_USB3.3V rail through a 0.01µF capacitor (C428). The SHDN# pin of the converter is pulled up to the 5V supply by a 0.1µF capacitor (C432). The GND pins of the converter are connected to DGND. The VCC\_USB3.3V rail is also connected to the LED(R)-DNI (D46) through a 1.5K resistor (R419) and to ground through a 330Ω resistor (R420). A 10µF/16V tantalum capacitor (C429) is used for decoupling on the VCC\_USB3.3V rail.

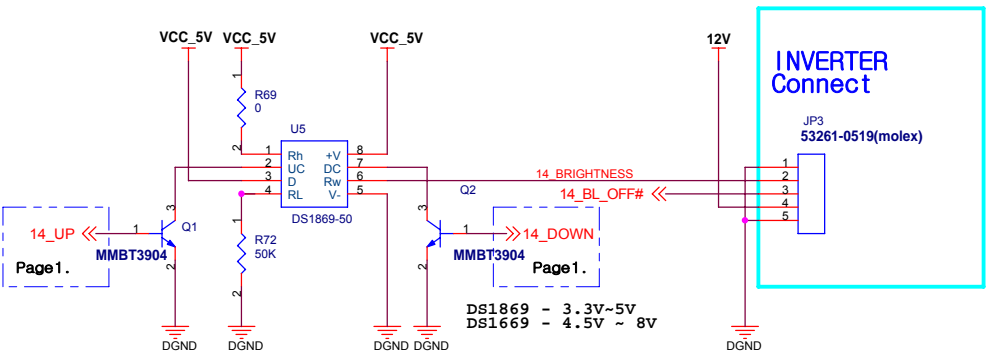
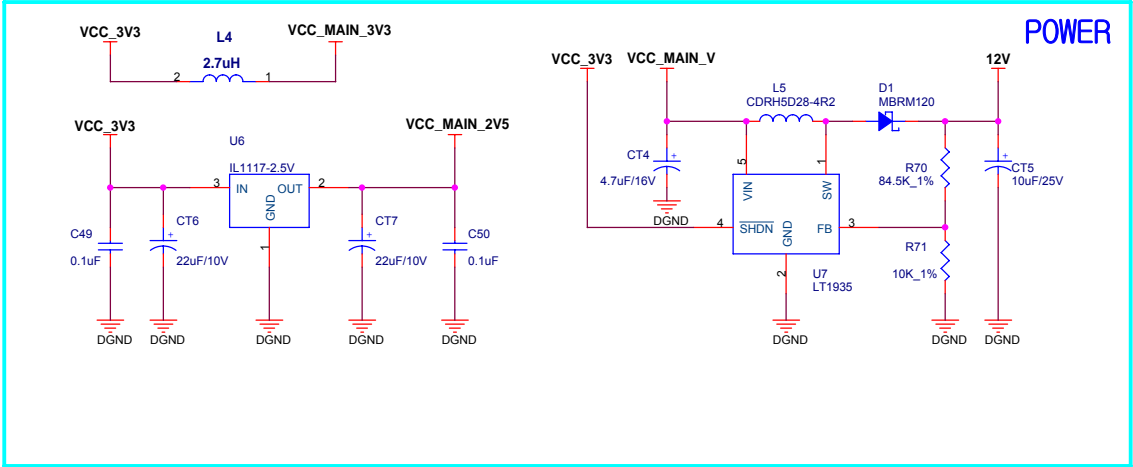
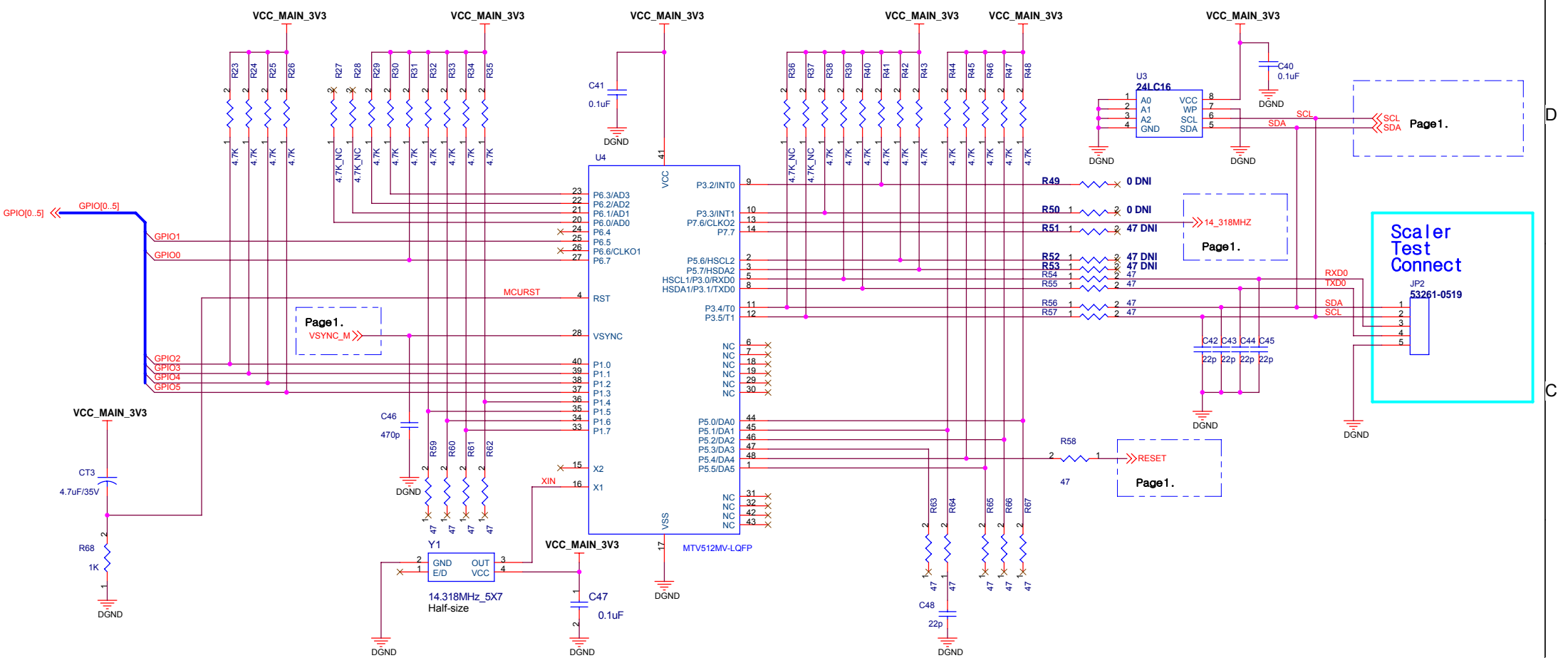
U49삭제 이전에 VCC3에  
연결하고 전류 Drive  
상태를 먼저확인 한다

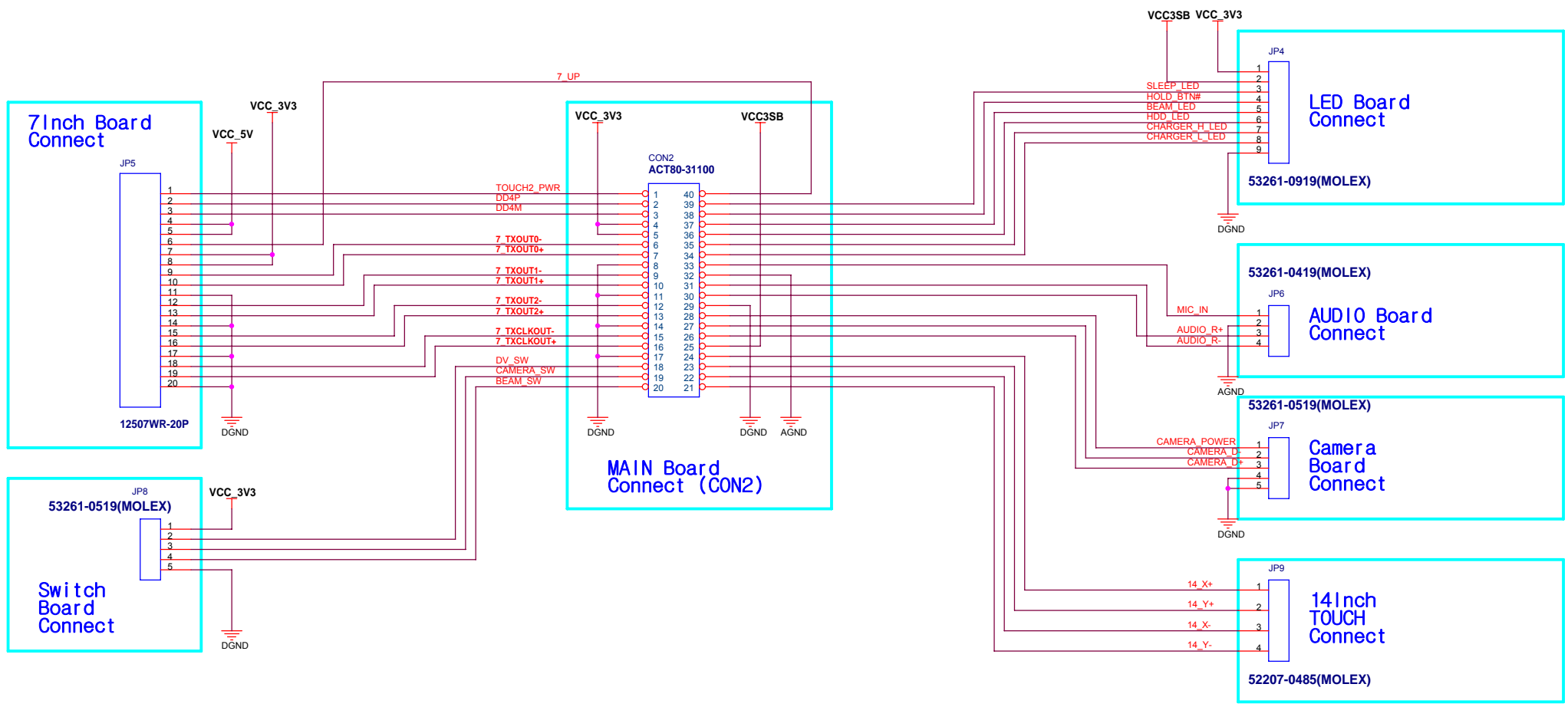


Title			
MAIN BOARD			
Size A3	Document Number USB HUB	Rev <Rev Code>	
Date:	Thursday, March 27, 2008	Sheet	36 of 55









FPCB Interface Connect

Hard Interface Connect

CON1	IDE-40P
1	factory use
2	factory use
3	RESET
4	GND
5	DD7
6	DD8
7	DD6
8	DD9
9	DD5
10	DD10
11	DD4
12	DD11
13	DD3
14	DD12
15	DD2
16	DD13
17	DD1
18	DD14
19	DD0
20	DD15
21	GND
22	DMARQ
23	GND
24	D1QW
25	D1OR
26	GND
27	IORDY
28	GND
29	DMACK
30	INTRQ
31	DA1
32	PD1AG
33	DA0
34	DA2
35	CS0
36	CS1
37	DASP
38	+3.3V
39	+3.3V
40	DEVADR

USB1	USB-A
5	SHELL
4	GND
3	D+
2	D-
1	VCC

USB2	USB-A
5	SHELL
4	GND
3	D+
2	D-
1	VCC

USB3	USB-A
5	SHELL
4	GND
3	D+
2	D-
1	VCC

CON2	05002HR-60J02-L
1	HDRESET
2	D7
3	D8
4	D6
5	D9
6	D5
7	D10
8	D4
9	D11
10	D3
11	D12
12	D2
13	D13
14	D1
15	D14
16	D0
17	D15
18	DMA
19	IOW
20	IOR
21	IORDY
22	DMACK
23	DINTRQ
24	DA1
25	DA0
26	DA2
27	DCS0
28	DCS1
29	SGND
30	USB3 DP
31	USB3 DM
32	USB3 POWER
33	SGND
34	USB2 DP
35	USB2 DM
36	USB2 POWER
37	SGND
38	USB1 DP
39	USB1 DM
40	USB1 POWER
41	SGND
42	AUDIO L-
43	AUDIO L+
44	WAKE UP
45	HOLD
46	SLEEP
47	POWER
48	SGND
49	DGND
50	DGND
51	DGND
52	DGND
53	DGND
54	DGND
55	DGND
56	DGND
57	DGND
58	DGND
59	DGND
60	DGND

CON1	IDE-40P
1	factory use
2	factory use
3	RESET
4	GND
5	DD7
6	DD8
7	DD6
8	DD9
9	DD5
10	DD10
11	DD4
12	DD11
13	DD3
14	DD12
15	DD2
16	DD13
17	DD1
18	DD14
19	DD0
20	DD15
21	GND
22	DMARQ
23	GND
24	D1QW
25	D1OR
26	GND
27	IORDY
28	GND
29	DMACK
30	INTRQ
31	DA1
32	PD1AG
33	DA0
34	DA2
35	CS0
36	CS1
37	DASP
38	+3.3V
39	+3.3V
40	DEVADR

USB1	USB-A
5	SHELL
4	GND
3	D+
2	D-
1	VCC

USB2	USB-A
5	SHELL
4	GND
3	D+
2	D-
1	VCC

USB3	USB-A
5	SHELL
4	GND
3	D+
2	D-
1	VCC

CON2	05002HR-60J02-L
1	HDRESET
2	D7
3	D8
4	D6
5	D9
6	D5
7	D10
8	D4
9	D11
10	D3
11	D12
12	D2
13	D13
14	D1
15	D14
16	D0
17	D15
18	DMA
19	IOW
20	IOR
21	IORDY
22	DMACK
23	DINTRQ
24	DA1
25	DA0
26	DA2
27	DCS0
28	DCS1
29	SGND
30	USB3 DP
31	USB3 DM
32	USB3 POWER
33	SGND
34	USB2 DP
35	USB2 DM
36	USB2 POWER
37	SGND
38	USB1 DP
39	USB1 DM
40	USB1 POWER
41	SGND
42	AUDIO L-
43	AUDIO L+
44	WAKE UP
45	HOLD
46	SLEEP
47	POWER
48	SGND
49	DGND
50	DGND
51	DGND
52	DGND
53	DGND
54	DGND
55	DGND
56	DGND
57	DGND
58	DGND
59	DGND
60	DGND

CON1	IDE-40P
1	factory use
2	factory use
3	RESET
4	GND
5	DD7
6	DD8
7	DD6
8	DD9
9	DD5
10	DD10
11	DD4
12	DD11
13	DD3
14	DD12
15	DD2
16	DD13
17	DD1
18	DD14
19	DD0
20	DD15
21	GND
22	DMARQ
23	GND
24	D1QW
25	D1OR
26	GND
27	IORDY
28	GND
29	DMACK
30	INTRQ
31	DA1
32	PD1AG
33	DA0
34	DA2
35	CS0
36	CS1
37	DASP
38	+3.3V
39	+3.3V
40	DEVADR

USB1	USB-A
5	SHELL
4	GND
3	D+
2	D-
1	VCC

USB2	USB-A
5	SHELL
4	GND
3	D+
2	D-
1	VCC

USB3	USB-A
5	SHELL
4	GND
3	D+
2	D-
1	VCC

CON2	05002HR-60J02-L
1	HDRESET
2	D7
3	D8
4	D6
5	D9
6	D5
7	D10
8	D4
9	D11
10	D3
11	D12
12	D2
13	D13
14	D1
15	D14
16	D0
17	D15
18	DMA
19	IOW
20	IOR
21	IORDY
22	DMACK
23	DINTRQ
24	DA1
25	DA0
26	DA2
27	DCS0
28	DCS1
29	SGND
30	USB3 DP
31	USB3 DM
32	USB3 POWER
33	SGND
34	USB2 DP
35	USB2 DM
36	USB2 POWER
37	SGND
38	USB1 DP
39	USB1 DM
40	USB1 POWER
41	SGND
42	AUDIO L-
43	AUDIO L+
44	WAKE UP
45	HOLD
46	SLEEP
47	POWER
48	SGND
49	DGND
50	DGND
51	DGND
52	DGND
53	DGND
54	DGND
55	DGND
56	DGND
57	DGND
58	DGND
59	DGND
60	DGND

CON1	IDE-40P
1	factory use
2	factory use
3	RESET
4	GND
5	DD7
6	DD8
7	DD6
8	DD9
9	DD5
10	DD10
11	DD4
12	DD11
13	DD3
14	DD12
15	DD2
16	DD13
17	DD1
18	DD14
19	DD0
20	DD15
21	GND
22	DMARQ
23	GND
24	D1QW
25	D1OR
26	GND
27	IORDY
28	GND
29	DMACK
30	INTRQ
31	DA1
32	PD1AG
33	DA0
34	DA2
35	CS0
36	CS1
37	DASP
38	+3.3V
39	+3.3V
40	DEVADR

USB1	USB-A
5	SHELL
4	GND
3	D+
2	D-
1	VCC

USB2	USB-A
5	SHELL
4	GND
3	D+
2	D-
1	VCC

USB3	USB-A
5	SHELL
4	GND
3	D+
2	D-
1	VCC

CON2	05002HR-60J02-L
1	HDRESET
2	D7
3	D8
4	D6
5	D9
6	D5
7	D10
8	D4
9	D11
10	D3
11	D12
12	D2
13	D13
14	D1
15	D14
16	D0
17	D15
18	DMA
19	IOW
20	IOR
21	IORDY
22	DMACK
23	DINTRQ
24	DA1
25	DA0
26	DA2
27	DCS0
28	DCS1
29	SGND
30	USB3 DP
31	USB3 DM
32	USB3 POWER
33	SGND
34	USB2 DP
35	USB2 DM
36	USB2 POWER
37	SGND
38	USB1 DP
39	USB1 DM
40	USB1 POWER
41	SGND
42	AUDIO L-
43	AUDIO L+
44	WAKE UP
45	HOLD
46	SLEEP
47	POWER
48	SGND
49	DGND
50	DGND
51	DGND
52	DGND
53	DGND
54	DGND
55	DGND
56	DGND
57	DGND
58	DGND
59	DGND
60	DGND

CON1	IDE-40P
1	factory use
2	factory use
3	RESET
4	GND
5	DD7
6	DD8
7	DD6
8	DD9
9	DD5
10	DD10
11	DD4
12	DD11
13	DD3
14	DD12
15	DD2
16	DD13
17	DD1
18	DD14
19	DD0
20	DD15
21	GND
22	DMARQ
23	GND
24	D1QW
25	D1OR
26	GND
27	IORDY
28	GND
29	DMACK
30	INTRQ
31	DA1
32	PD1AG
33	DA0
34	DA2
35	CS0
36	CS1
37	DASP
38	+3.3V
39	+3.3V
40	DEVADR

USB1	USB-A
5	SHELL
4	GND
3	D+
2	D-
1	VCC

USB2	USB-A
5	SHELL
4	GND
3	D+
2	D-
1	VCC

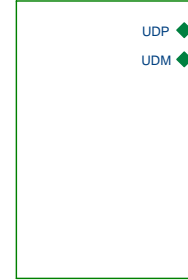
USB3	USB-A
5	SHELL
4	GND
3	D+
2	D-
1	VCC

CON2	05002HR-60J02-L
1	HDRESET
2	D7
3	D8
4	D6
5	D9
6	D5
7	D10
8	D4
9	D11
10	D3
11	D12
12	D2
13	D13
14	D1
15	D14
16	D0
17	D15
18	DMA
19	IOW
20	IOR
21	IORDY
22	DMACK
23	DINTRQ
24	DA1
25	DA0
26	DA2
27	DCS0
28	DCS1
29	SGND
30	USB3 DP
31	USB3 DM
32	USB3 POWER
33	SGND
34	USB2 DP
35	USB2 DM
36	USB2 POWER
37	SGND
38	USB1 DP
39	USB1 DM
40	USB1 POWER
41	SGND
42	AUDIO L-
43	AUDIO L+
44	WAKE UP
45	HOLD
46	SLEEP
47	POWER
48	SGND
49	DGND
50	DGND
51	DGND
52	DGND
53	DGND
54	DGND
55	DGND
56	DGND
57	DGND
58	DGND
59	DGND
60	DGND

CON1	IDE-40P
1	factory use
2	factory use
3	RESET
4	GND
5	DD7
6	DD8
7	DD6
8	DD9
9	DD5
10	DD10
11	DD4
12	DD11
13	DD3
14	DD12
15	DD2
16	DD13
17	DD1
18	DD14
19	DD0
20	DD15
21	GND
22	DMARQ
23	GND
24	D1QW
25	D1OR
26	GND
27	IORDY
28	GND
29	DMACK
30	INTRQ
31	DA1
32	PD1AG
33	DA0
34	DA2
35	CS0
36	CS1
37	DASP
38	+3.3V
39	+3.3V
40	DEVADR

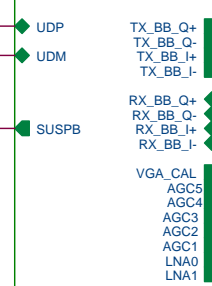
USB1	USB-A
5	SHELL
4	GND
3	D+
2	

# USB I/F and Power



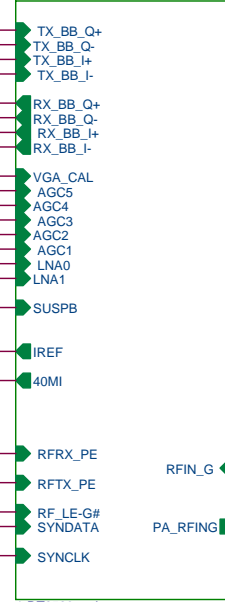
4-USB-A

## MAC RT2571W WLAN



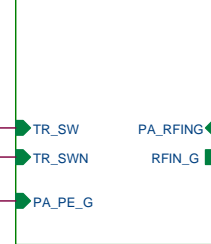
3-RT2571W

## Transceiver RT2528



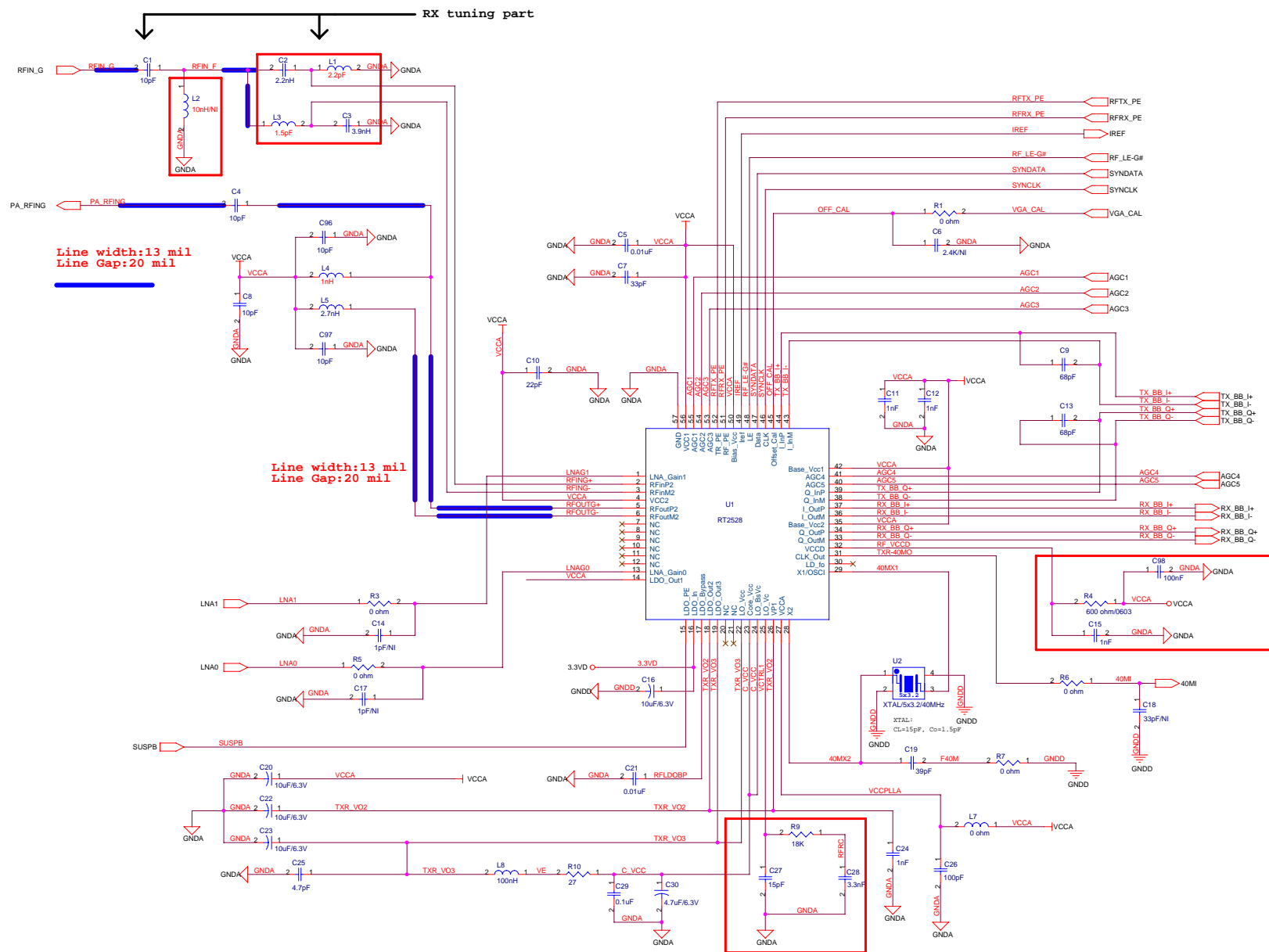
1-RT2528-xtal

## PA and TX/RX

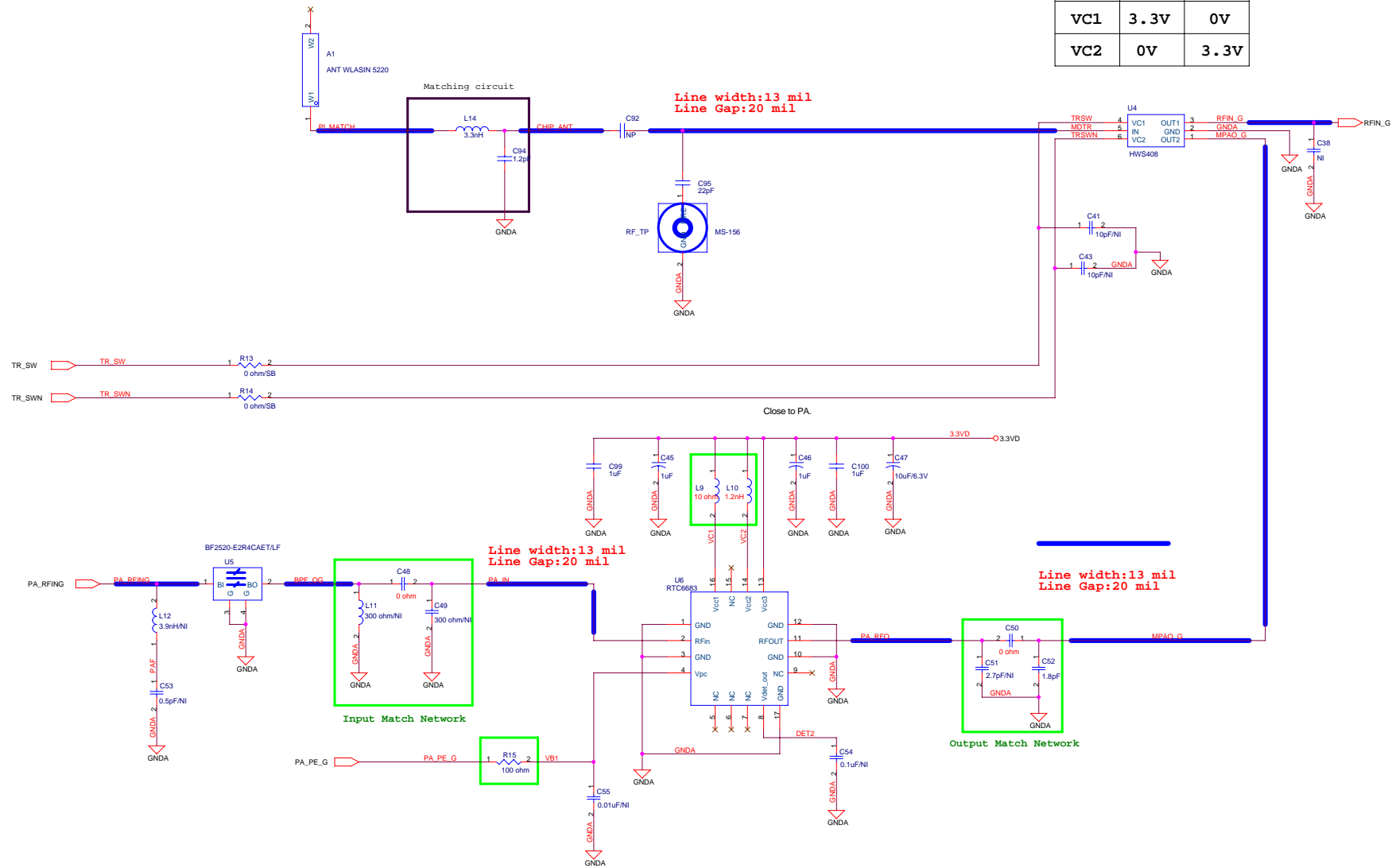


2-PA-RTC6683

Title		
802.11b/g Mini Wireless LAN USB 2.0 Adapter		
Size	Document Number	Rev
B	WUG2700_E2.SCH	1.0
Date:	Tuesday, November 21, 2006	Sheet 1 of 5



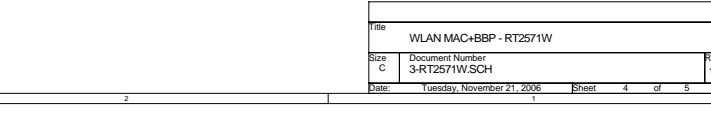
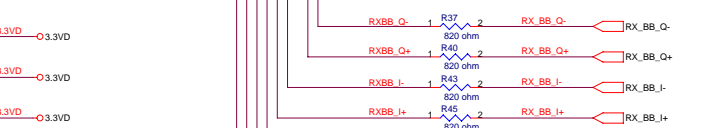
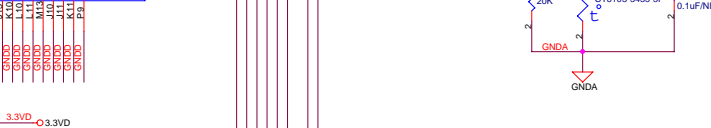
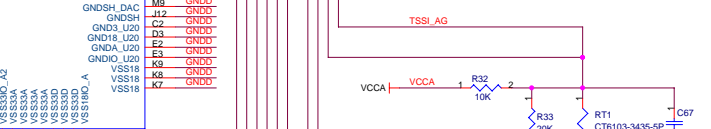
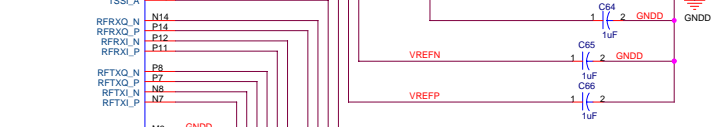
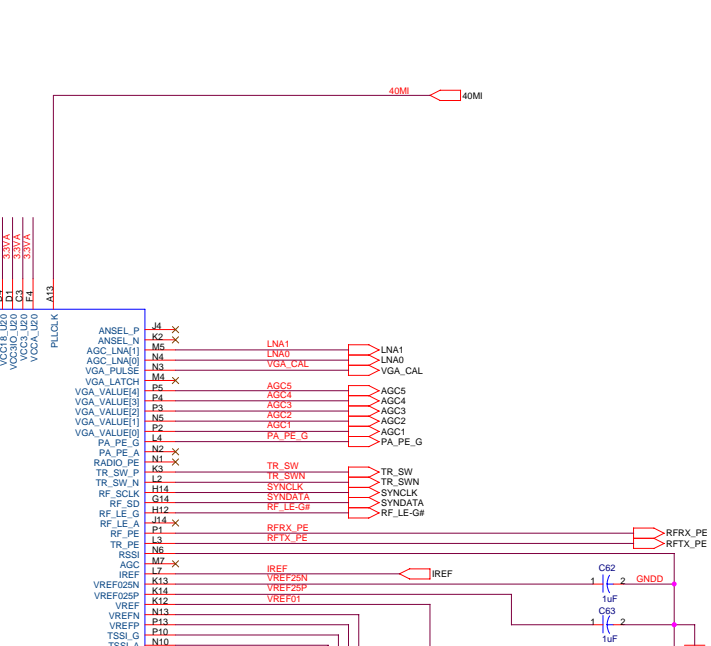
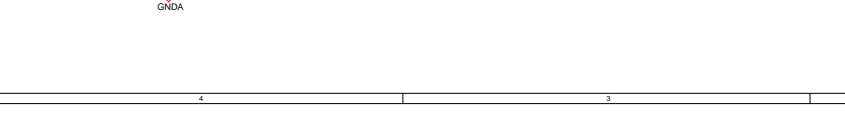
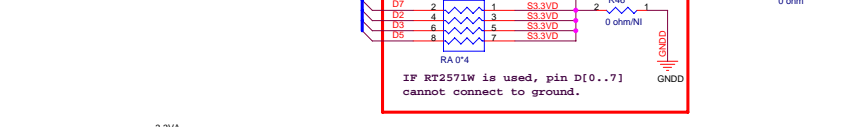
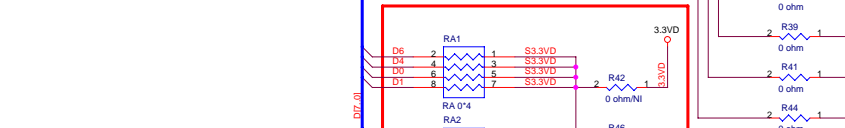
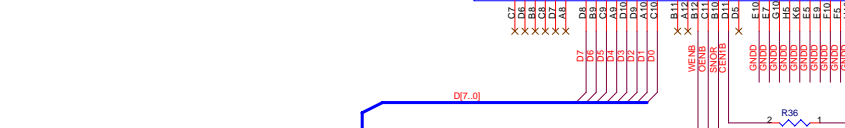
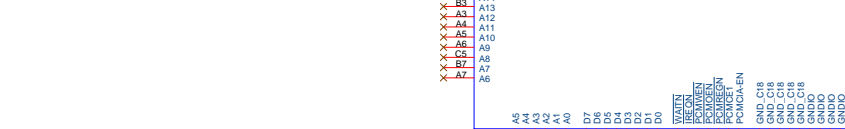
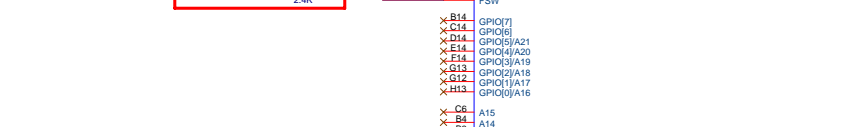
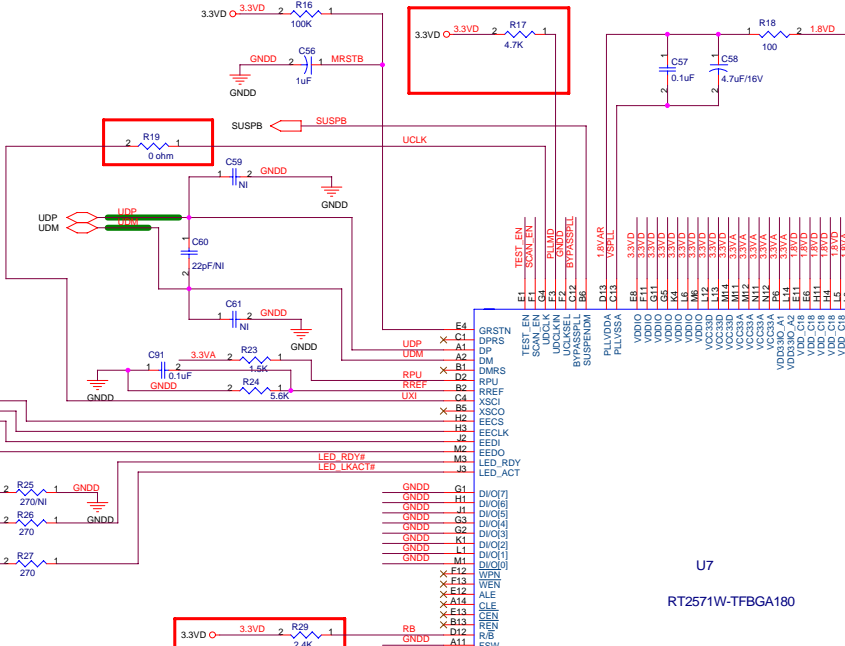
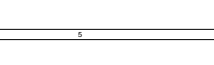
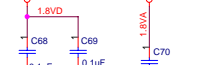
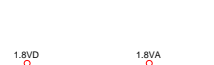
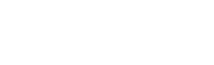
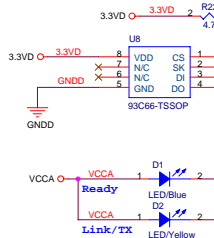
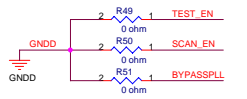
	TX	RX
VC1	3.3V	0V
VC2	0V	3.3V



File	PA and TX/RX
Size	Document Number
C	2-PA-RTC6883.SCH
Date	Tuesday, November 21, 2006
Sheet	3 of 5
Rev	1.0

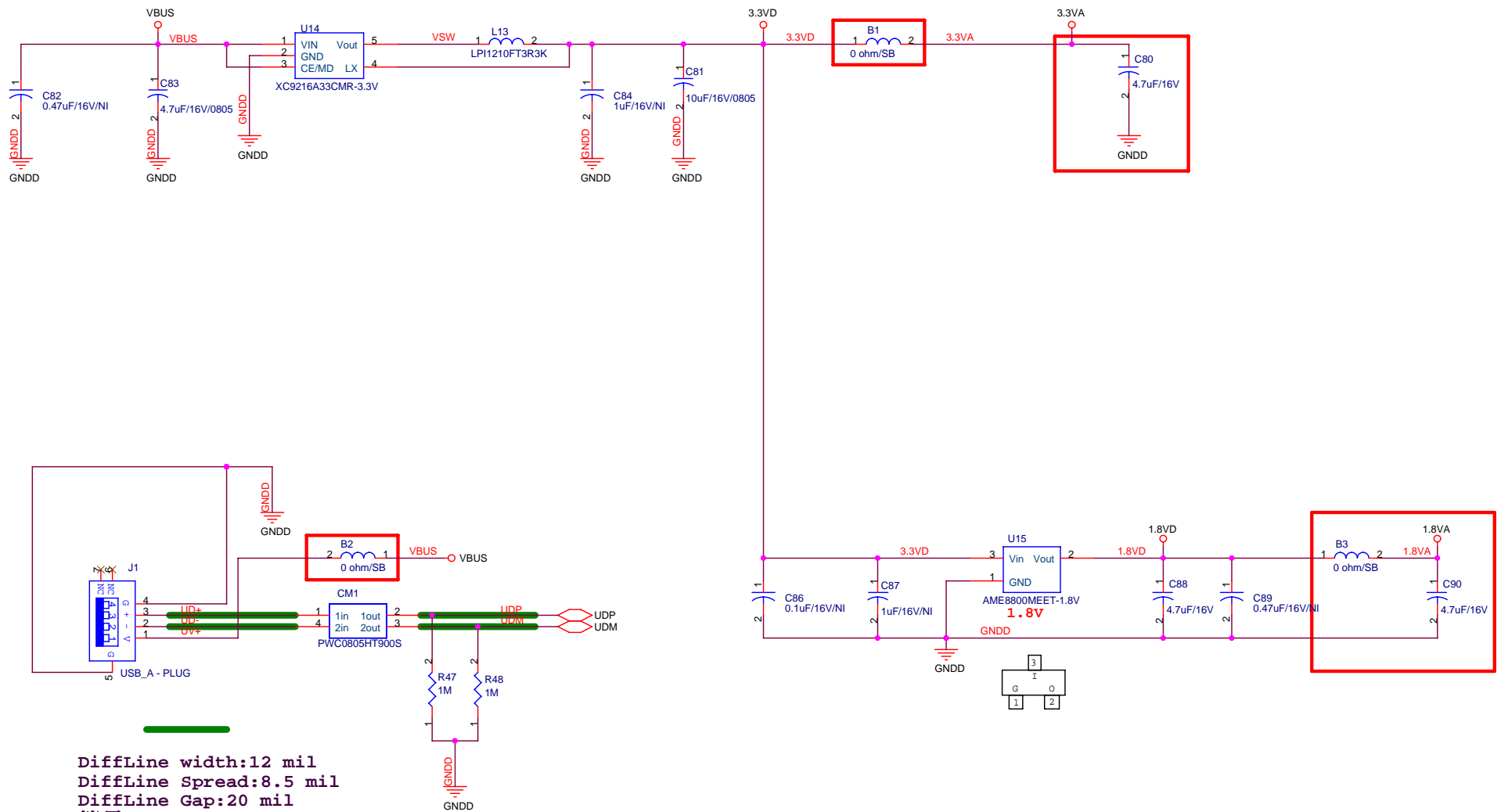
DiffLine width:12 mil  
DiffLine Spread:8.5 mil  
DiffLine Gap:20 mil  
等長

3 Test pin connect to ground for  
USB-IF test.

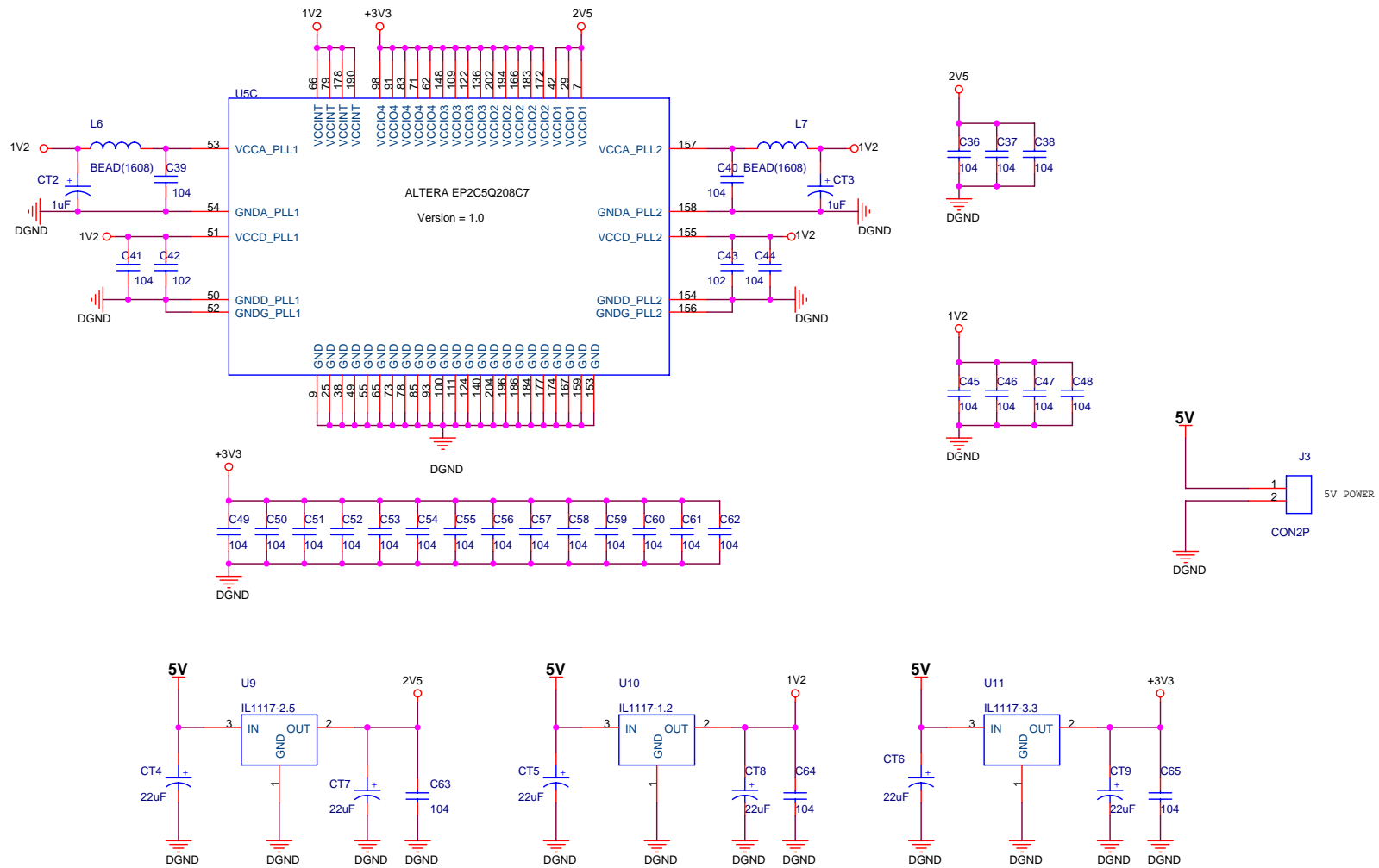


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