

5

4

3

2

1

PentaOne TE Board Reference

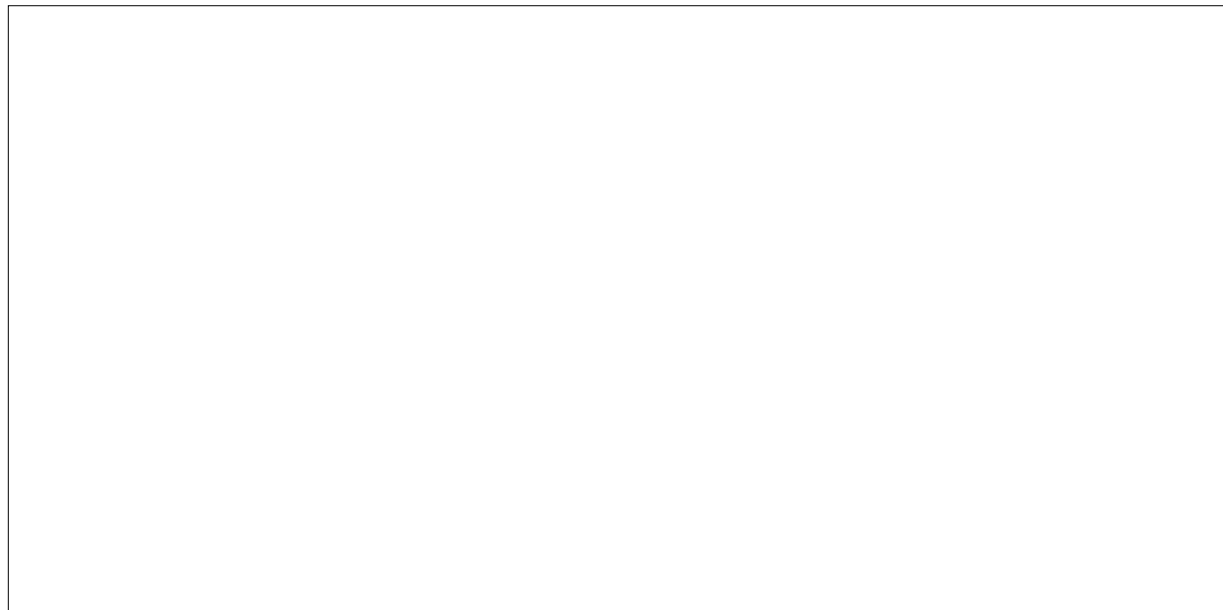
** Version History

Date	Ver.	Author	Description
2007-04-06	0.9	by hslee	- Initial designed
2008-02-29	1.0	by hslee	70 Set 양산

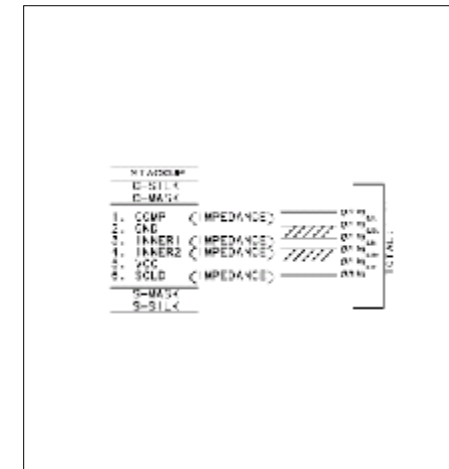
** Remark Recommend

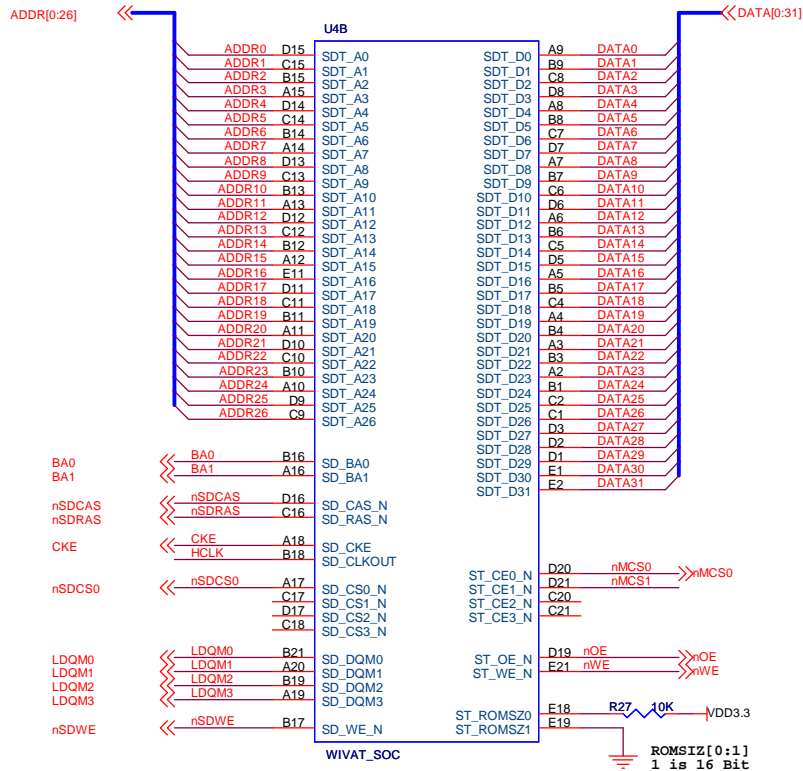
외부 보드와 인터페이스 하는 CONNECTER
번호와 방향 유의 (JTAG, MODEM)

** Board Placement



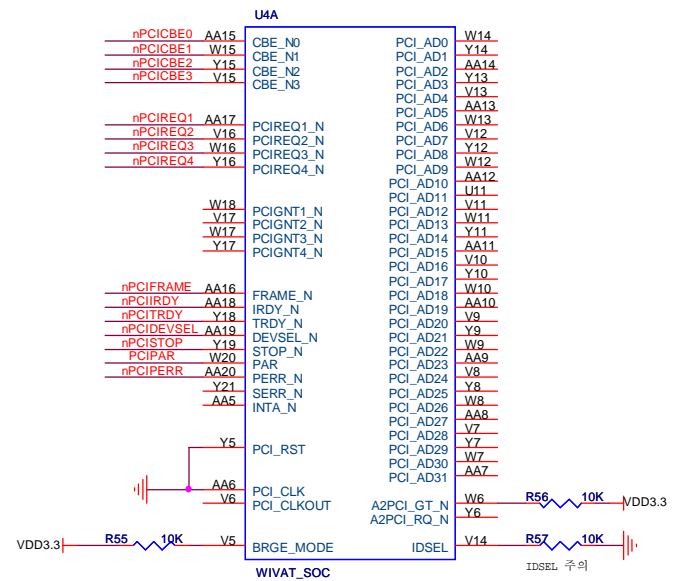
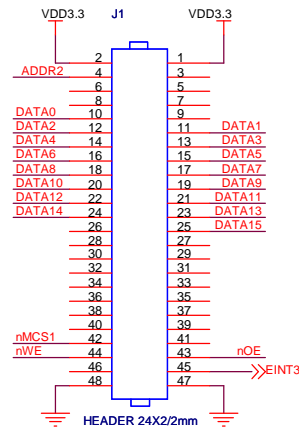
** PCB Layer



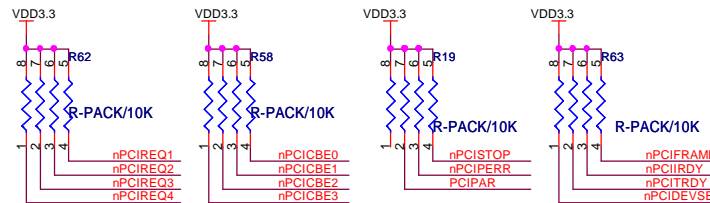


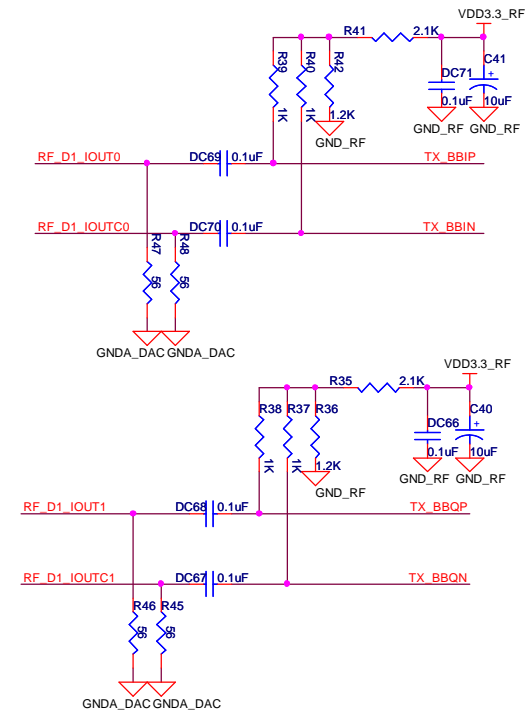
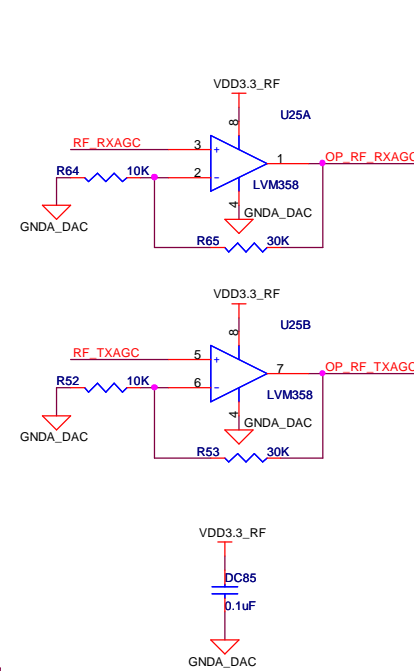
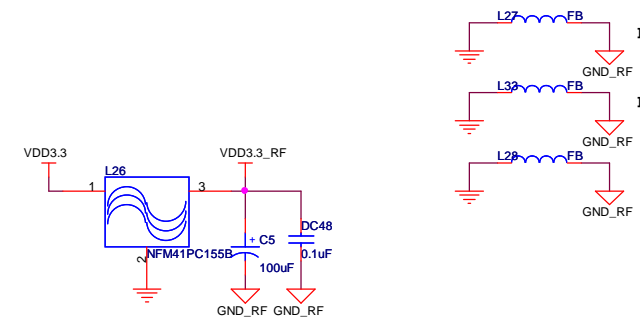
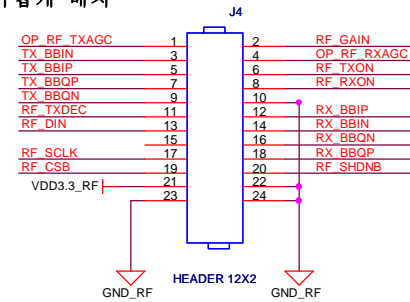
BA0
BA1
nSDCAS
nSDRAS
CKE
nSDCS0
LDQM0
LDQM1
LDQM2
LDQM3
nSDWE

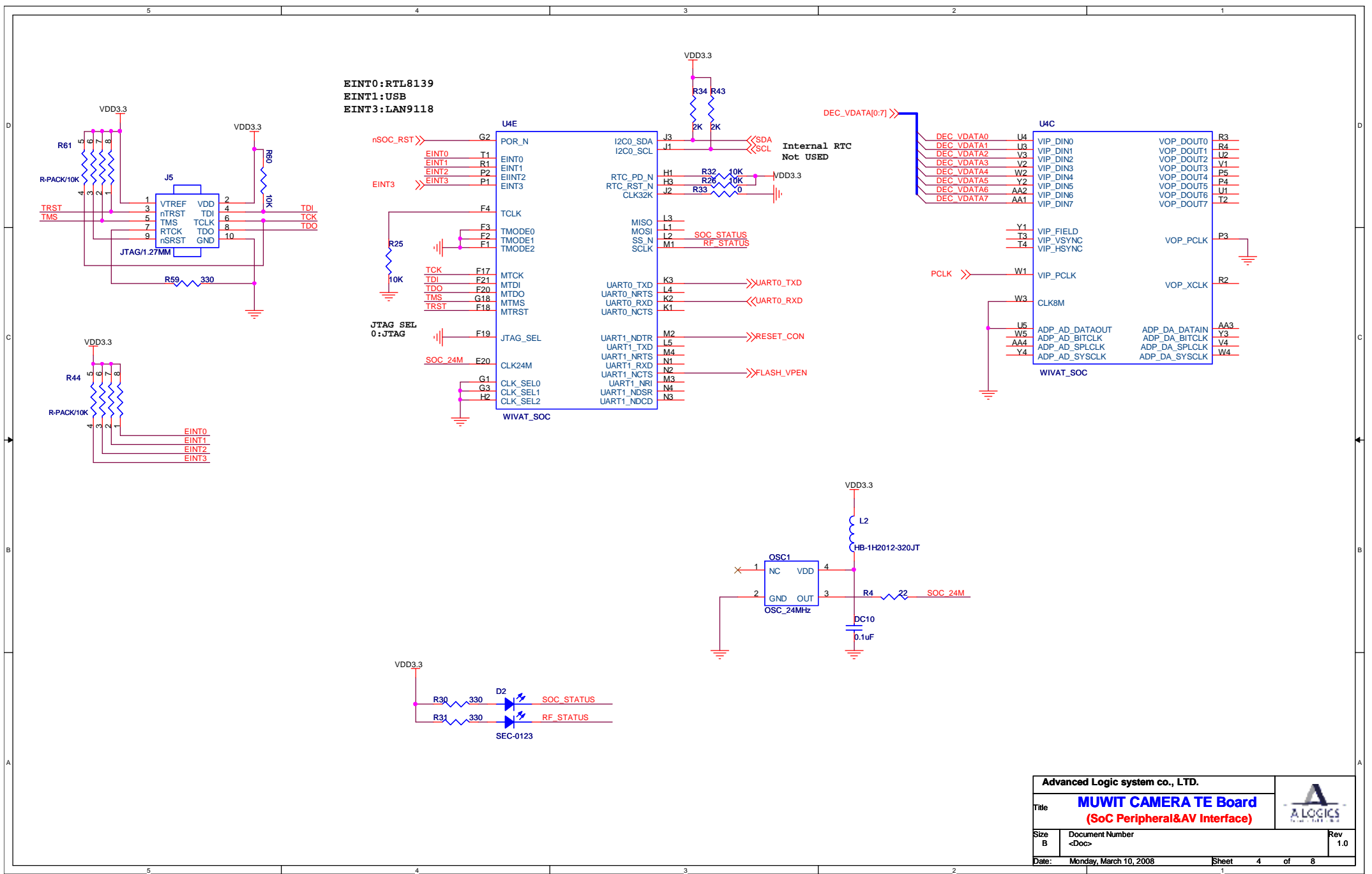
HCLKO <<< HCLKO R23 22 HCLK
 22 OHM soc에 가깝게

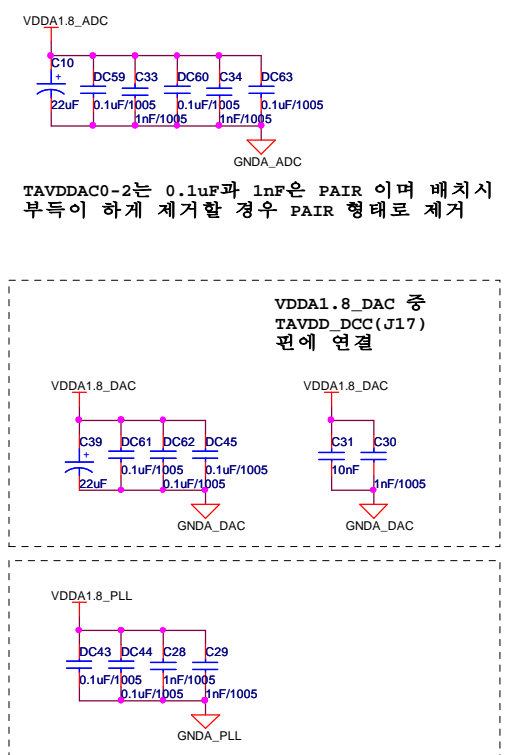
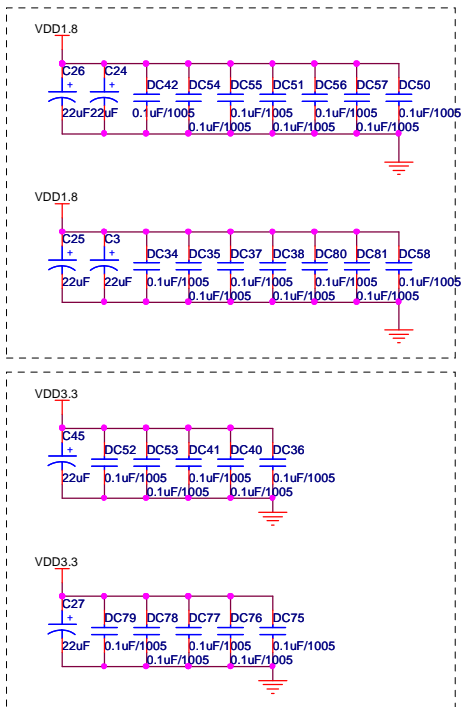
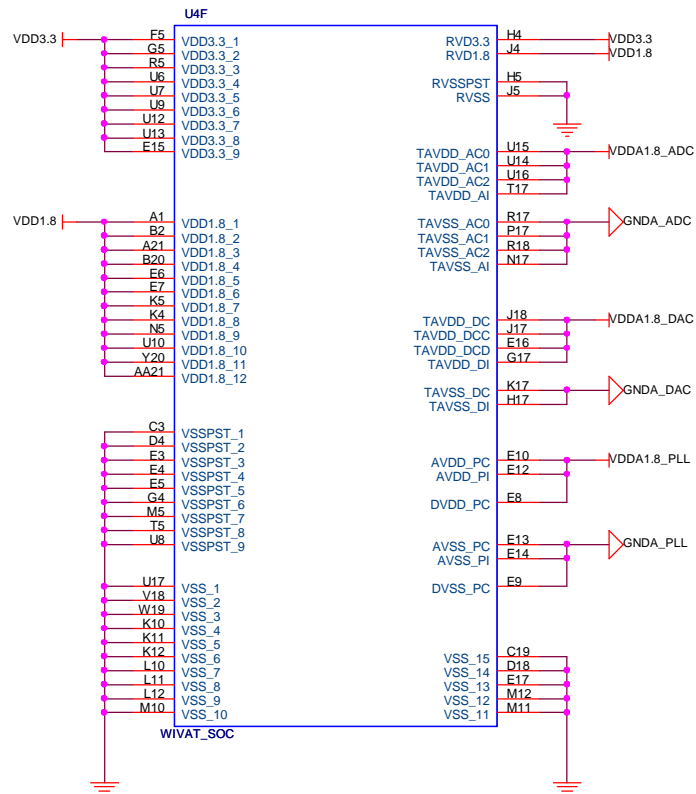


- PCI HOST BRIDGE(Master)





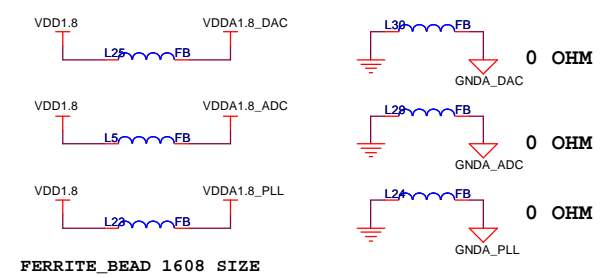





TAVDDAC0-2는 0.1uF과 1nF은 PAIR 이며 배치시 부득이 하게 제거할 경우 PAIR 형태로 제거

VDDA1.8_DAC 중 TAVDD_DCC(J17) 핀에 연결

SOC 의 22uF(3216 SIZE) CAP은 배치 상황에 따라 조절 가능
 SOC CAP(0.1uF, 1nF) 은 1005 SIZE 이며 배치 상황에 따라 조절가능
 SOC 를 제외한 나머지 저항및 CAP는 1608 size



Advanced Logic system co., LTD.			
Title			
MUWIT CAMERA TE Board (SoC Power Interface)			
Size B	Document Number <Doc>		Rev 1.0
Date:	Monday, March 10, 2008	Sheet 5 of 8	

