

Title		
PIN MUX		
Size	Document Number	Rev
B	OTIPCAM8126	1VA
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XDDR2_DQ[0..15]
XDDR2_DQS[0..1]
XDDR2_DQSB[0..1]
XDDR2_DQM[0..1]
XDDR2_ADDR[0..13]
XDDR2_BA[0..2]
XDDR2_ODT
XDDR2_CK
XDDR2_CKB
XDDR2_CKE
XDDR2_CSB
XDDR2_WEB
XDDR2_RASB
XDDR2_CASB

XDDR2_DQ5 R94 22 DDR_DQ5
XDDR2_DQ2 R95 22 DDR_DQ2
XDDR2_DQ0 R96 22 DDR_DQ0
XDDR2_DQ7 R97 22 DDR_DQ7

XDDR2_DQS0 R60 22 DDR_DQS0
XDDR2_DQSB0 R61 22 DDR_DQSB0

XDDR2_DQ13 R98 22 DDR_DQ13
XDDR2_DQ10 R99 22 DDR_DQ10
XDDR2_DQ8 R100 22 DDR_DQ8
XDDR2_DQ15 R101 22 DDR_DQ15

XDDR2_DQS1 R62 22 DDR_DQS1
XDDR2_DQSB1 R63 22 DDR_DQSB1

XDDR2_DQ3 R102 22 DDR_DQ3
XDDR2_DQ4 R103 22 DDR_DQ4
XDDR2_DQ1 R104 22 DDR_DQ1
XDDR2_DQM0 R105 22 DDR_DQM0

XDDR2_DQ6 R106 22 DDR_DQ6
XDDR2_DQ12 R107 22 DDR_DQ12
XDDR2_DQ11 R108 22 DDR_DQ11
XDDR2_DQ9 R109 22 DDR_DQ9

XDDR2_DQM1 R66 22 DDR_DQM1
XDDR2_DQ14 R67 22 DDR_DQ14

XDDR2_ADDR7 R110 22 DDR_A7
XDDR2_ADDR12 R111 22 DDR_A12
XDDR2_ADDR9 R112 22 DDR_A9
XDDR2_ADDR3 R113 22 DDR_A3

XDDR2_ADDR5 R114 22 DDR_A5
XDDR2_ADDR10 R115 22 DDR_A10
XDDR2_ADDR1 R116 22 DDR_A1
XDDR2_BA2 R117 22 DDR_BA2

XDDR2_BA1 R118 22 DDR_BA1
XDDR2_BA0 R119 22 DDR_BA0
XDDR2_WEB R120 22 DDR_WEn
XDDR2_CKE R121 22 DDR_CKE

XDDR2_ADDR11 R122 22 DDR_A11
XDDR2_ADDR13 R123 22 DDR_A13
XDDR2_ADDR8 R124 22 DDR_A8
XDDR2_ADDR6 R125 22 DDR_A6

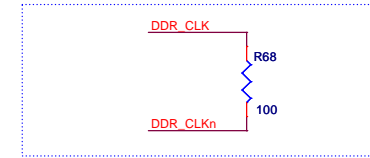
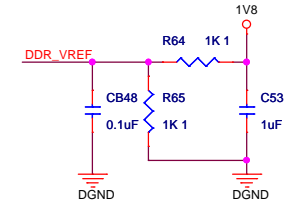
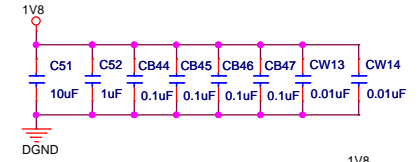
XDDR2_ADDR4 R126 22 DDR_A4
XDDR2_ADDR2 R127 22 DDR_A2
XDDR2_ADDR0 R128 22 DDR_A0
XDDR2_CASB R129 22 DDR_CASn

XDDR2_CSB R69 22 DDR_CS
XDDR2_RASB R70 22 DDR_RASn
XDDR2_ODT R71 22 DDR_ODT
XDDR2_CKB R72 22 DDR_CLKn
XDDR2_CK R73 22 DDR_CLK

DDR2-DRAM

DDR_DQ0 G8 LDQ0
DDR_DQ1 G2 LDQ1
DDR_DQ2 H7 LDQ2
DDR_DQ3 H3 LDQ3
DDR_DQ4 H1 LDQ4
DDR_DQ5 H9 LDQ5
DDR_DQ6 F1 LDQ6
DDR_DQ7 F9 LDQ7
DDR_DQS0 F7 LDQS
DDR_DQSn0 E8 LDQS
DDR_DQM0 F3 LDM
DDR_DQ8 C8 UDQ0
DDR_DQ9 C2 UDQ1
DDR_DQ10 D7 UDQ2
DDR_DQ11 D3 UDQ3
DDR_DQ12 D1 UDQ4
DDR_DQ13 D9 UDQ5
DDR_DQ14 B1 UDQ6
DDR_DQ15 B9 UDQ7
DDR_DQS1 B7 UDQS
DDR_DQSn1 A8 UDQS
DDR_DQM1 B3 UDM
DDR_VREF J2 VREF
DDR_CLK J8 CK
DDR_CLKn K8 CK
DDR_ODT K9 ODT
DDR_CKE K2 CKE
DDR_WEn K3 WE
DDR_RASn K7 RAS
DDR_CASn L7 CAS
DDR_CS L8 CS
DDR_A0 M8 A0
DDR_A1 M3 A1
DDR_A2 M7 A2
DDR_A3 N2 A3
DDR_A4 N8 A4
DDR_A5 N3 A5
DDR_A6 N7 A6
DDR_A7 P2 A7
DDR_A8 P8 A8
DDR_A9 F3 A9
DDR_A10 W2 A10
DDR_A11 P7 A11
DDR_A12 R2 A12

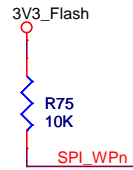
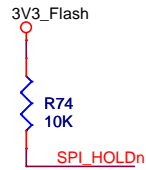
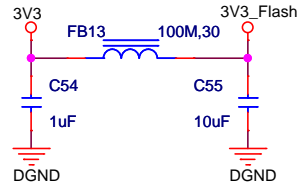
K4T13164QE-HCE7.FBGA84



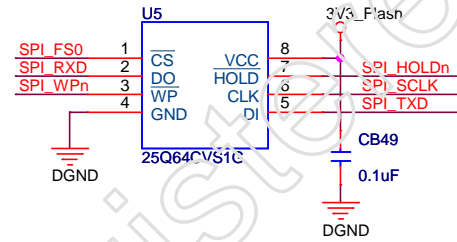
Title		
DDR2		
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SPI_SCLK
SPI_FS0
SPI_FS1
SPI_TXD
SPI_RXD

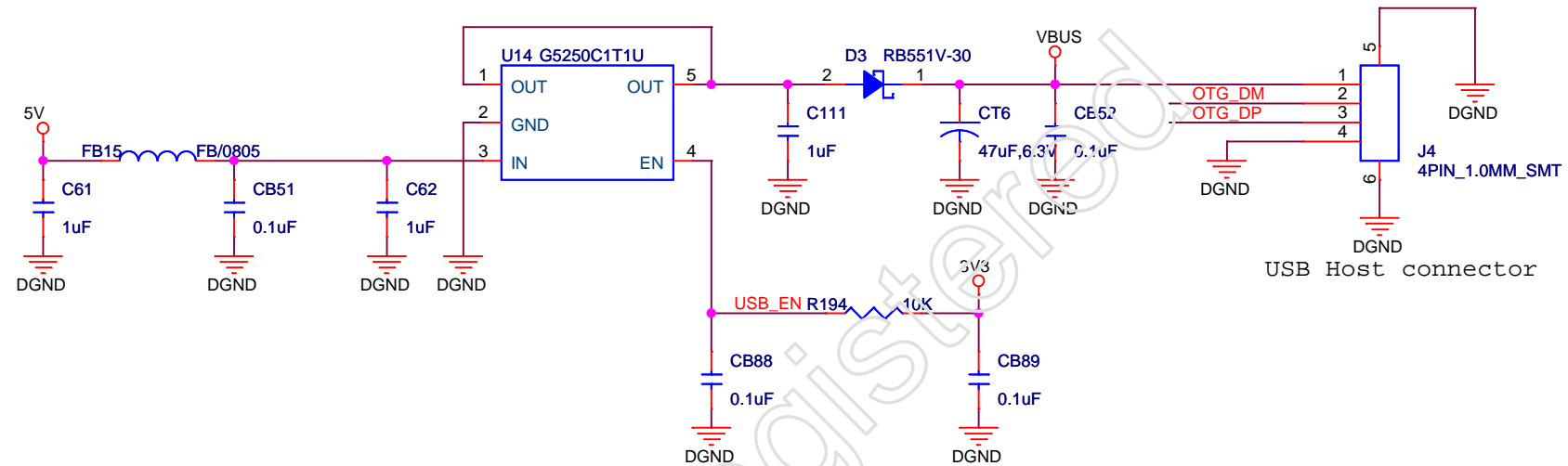
SPI_SCLK
SPI_FS0
SPI_FS1
SPI_TXD
SPI_RXD



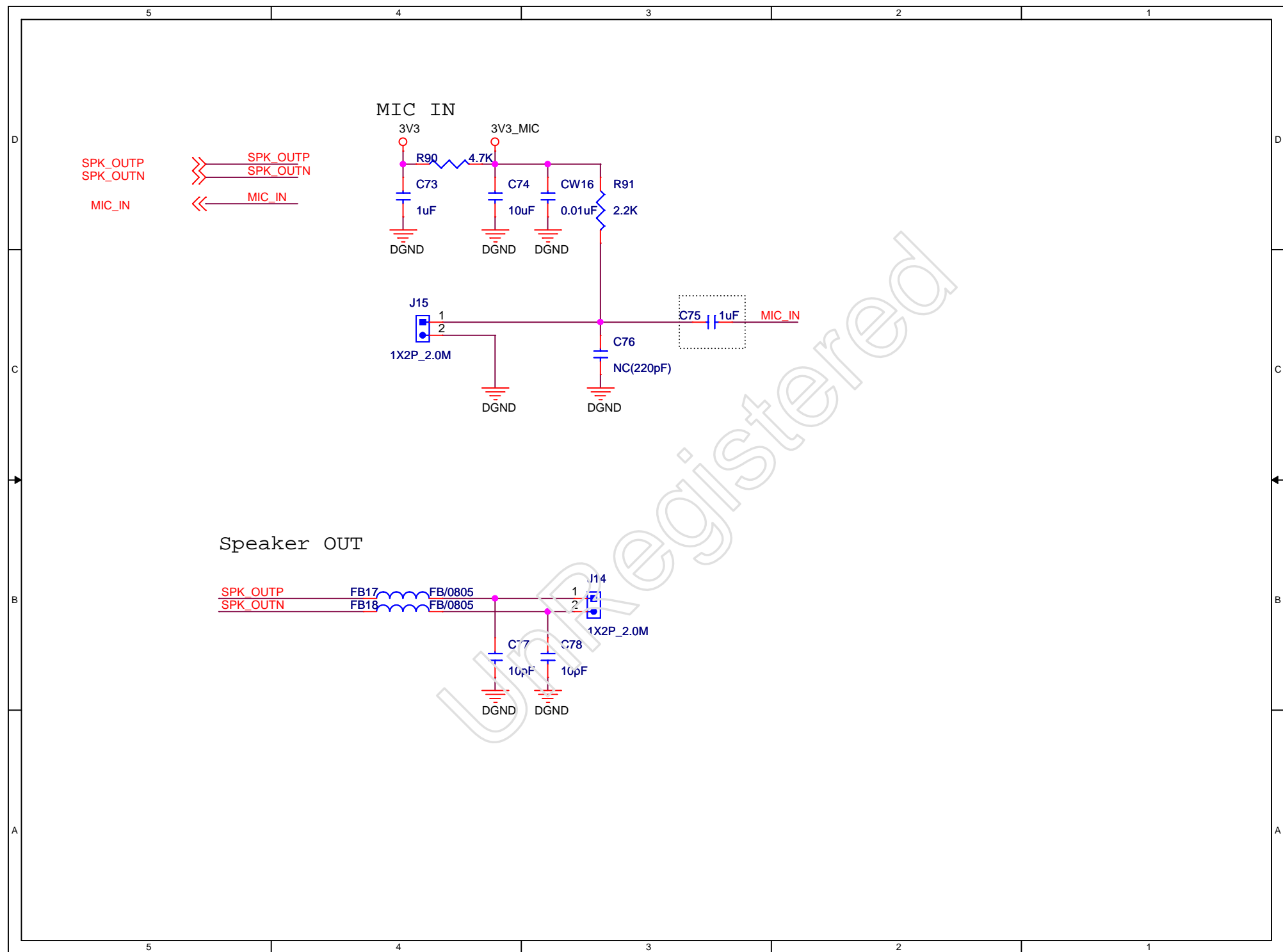
Overlap

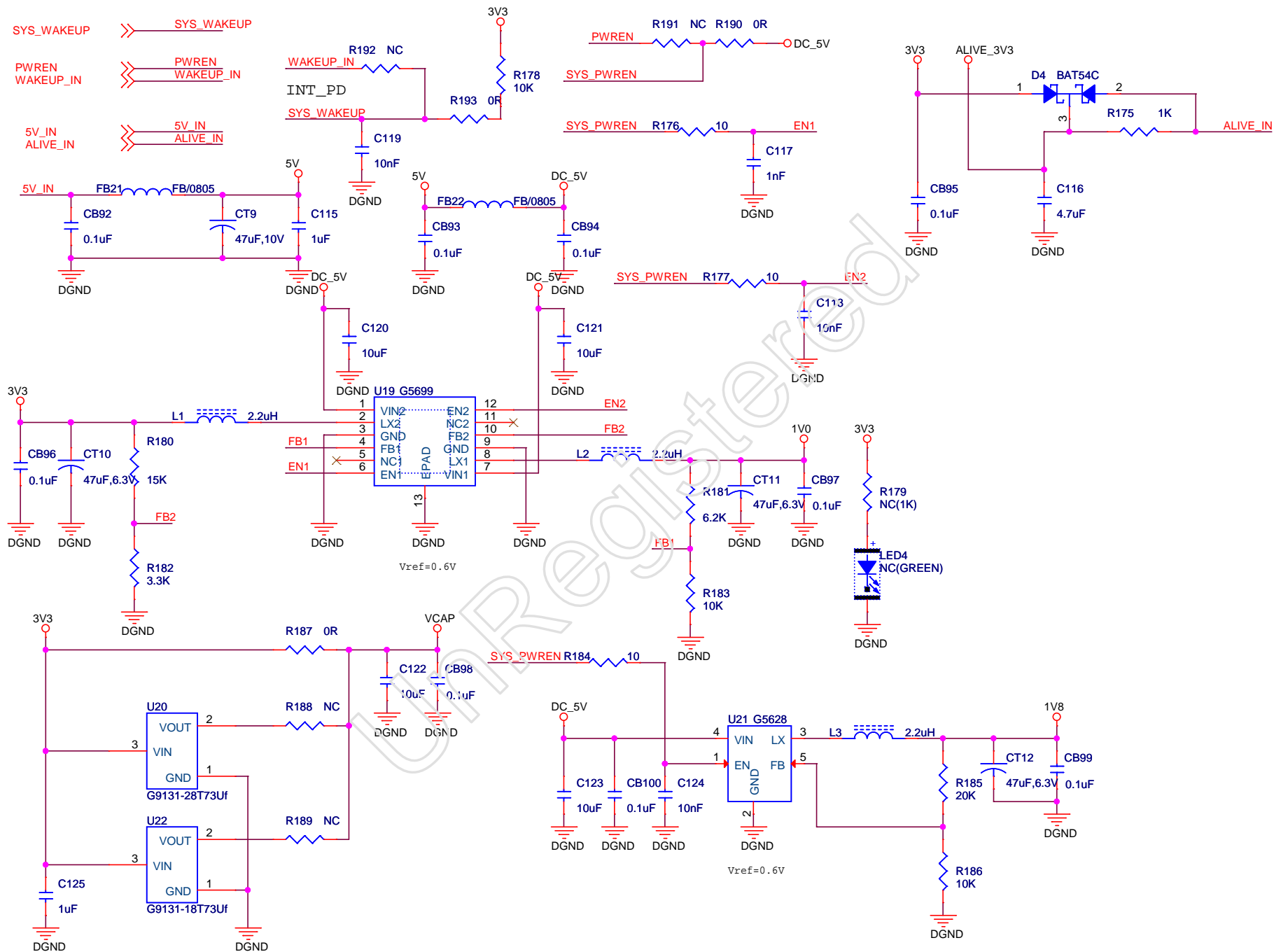


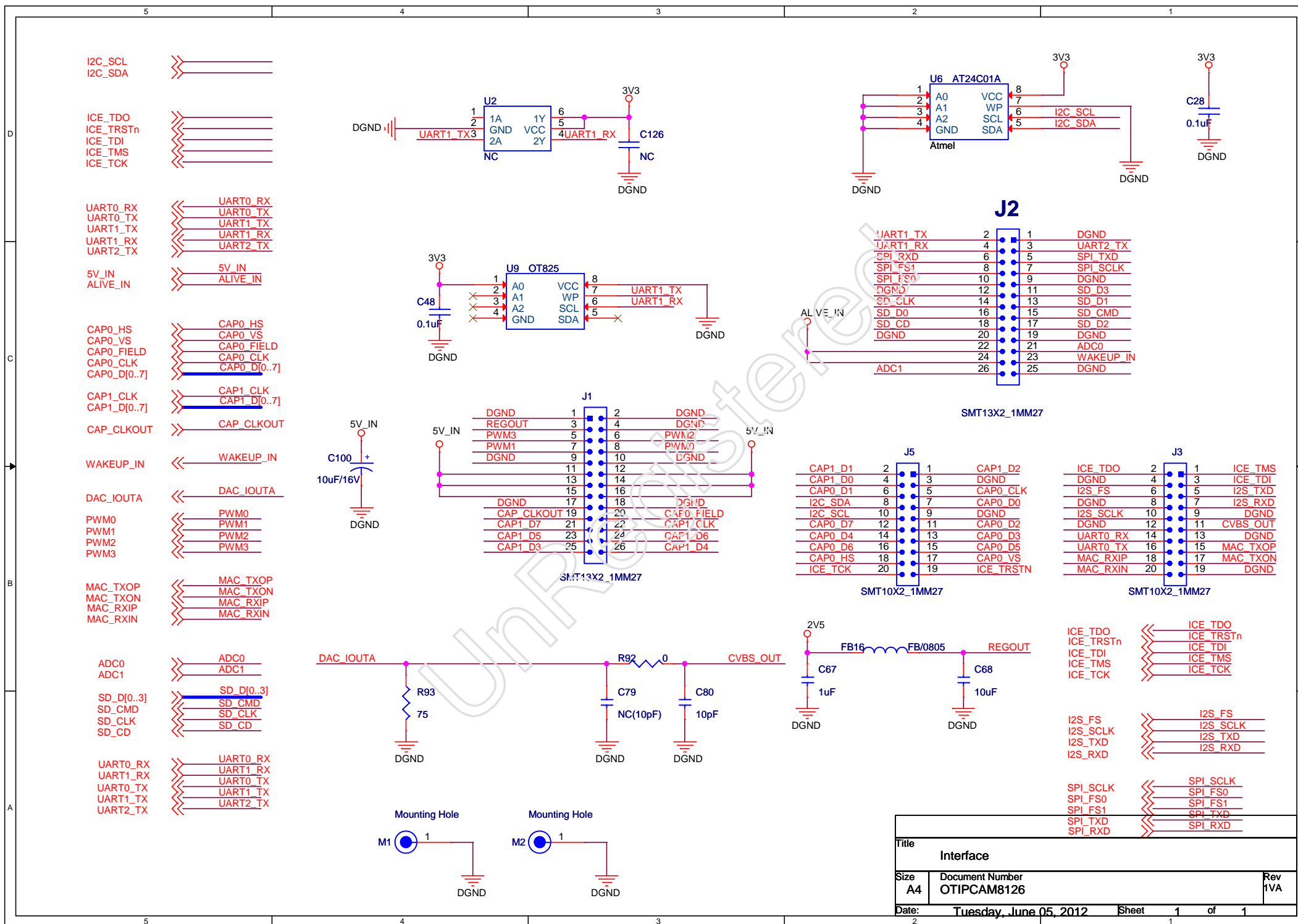
OTG_DP
OTG_DM



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USB		
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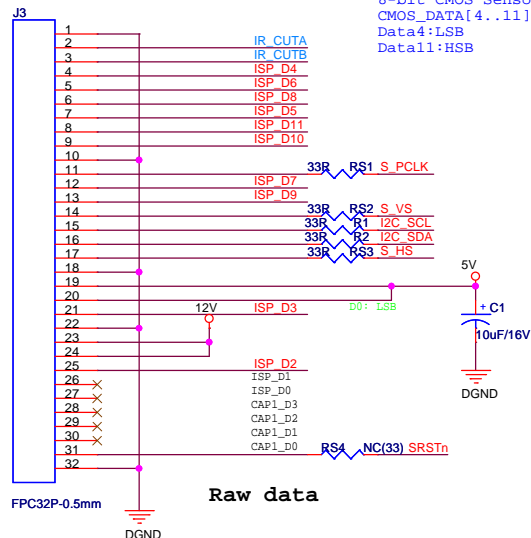




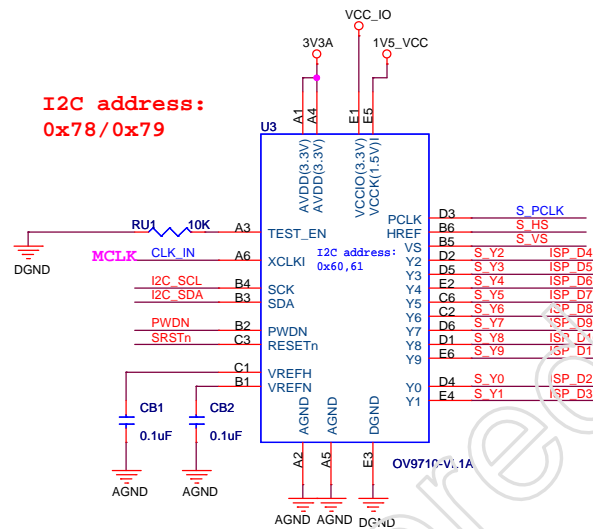


CMOS Sensor to ISP

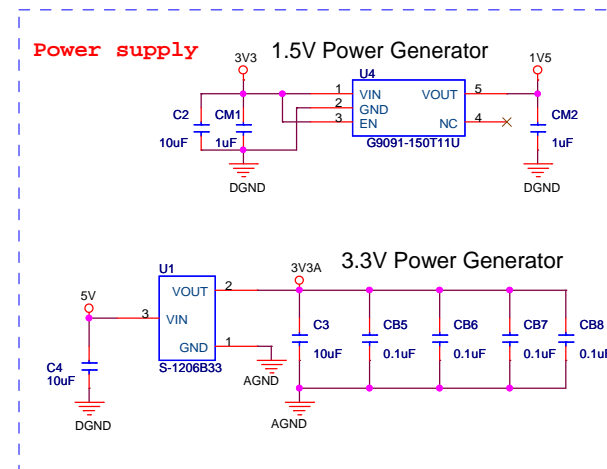
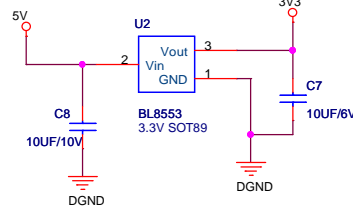
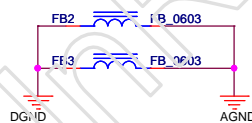
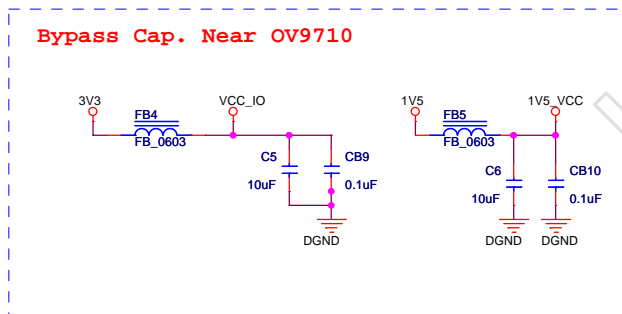
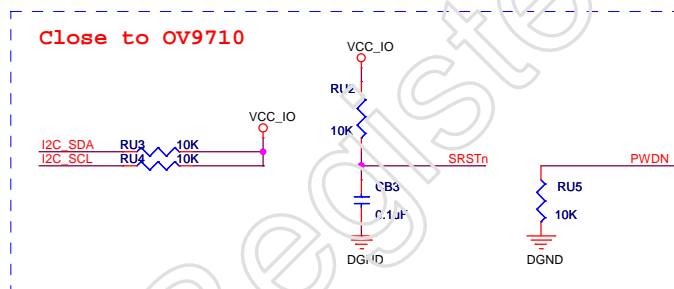
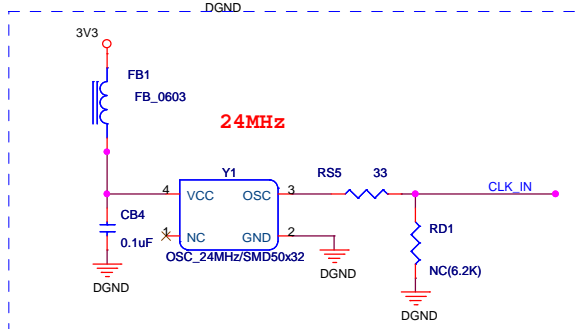
8-bit CMOS Sensor
CMOS_DATA[4..11]
Data4:LSB
Data11:HSB



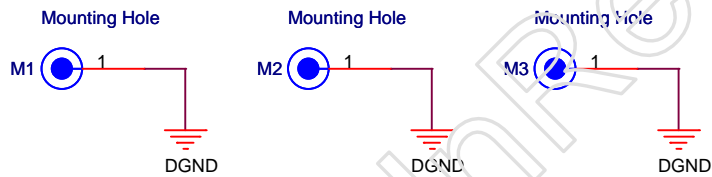
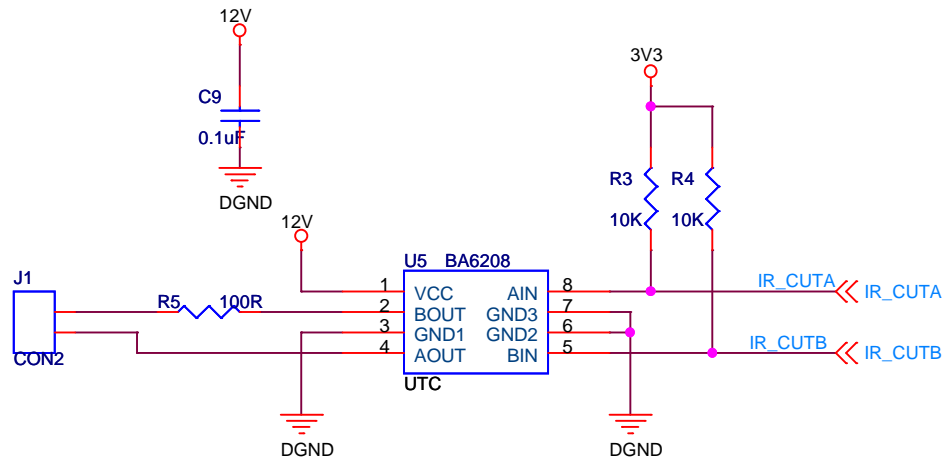
I2C address:
0x78/0x79



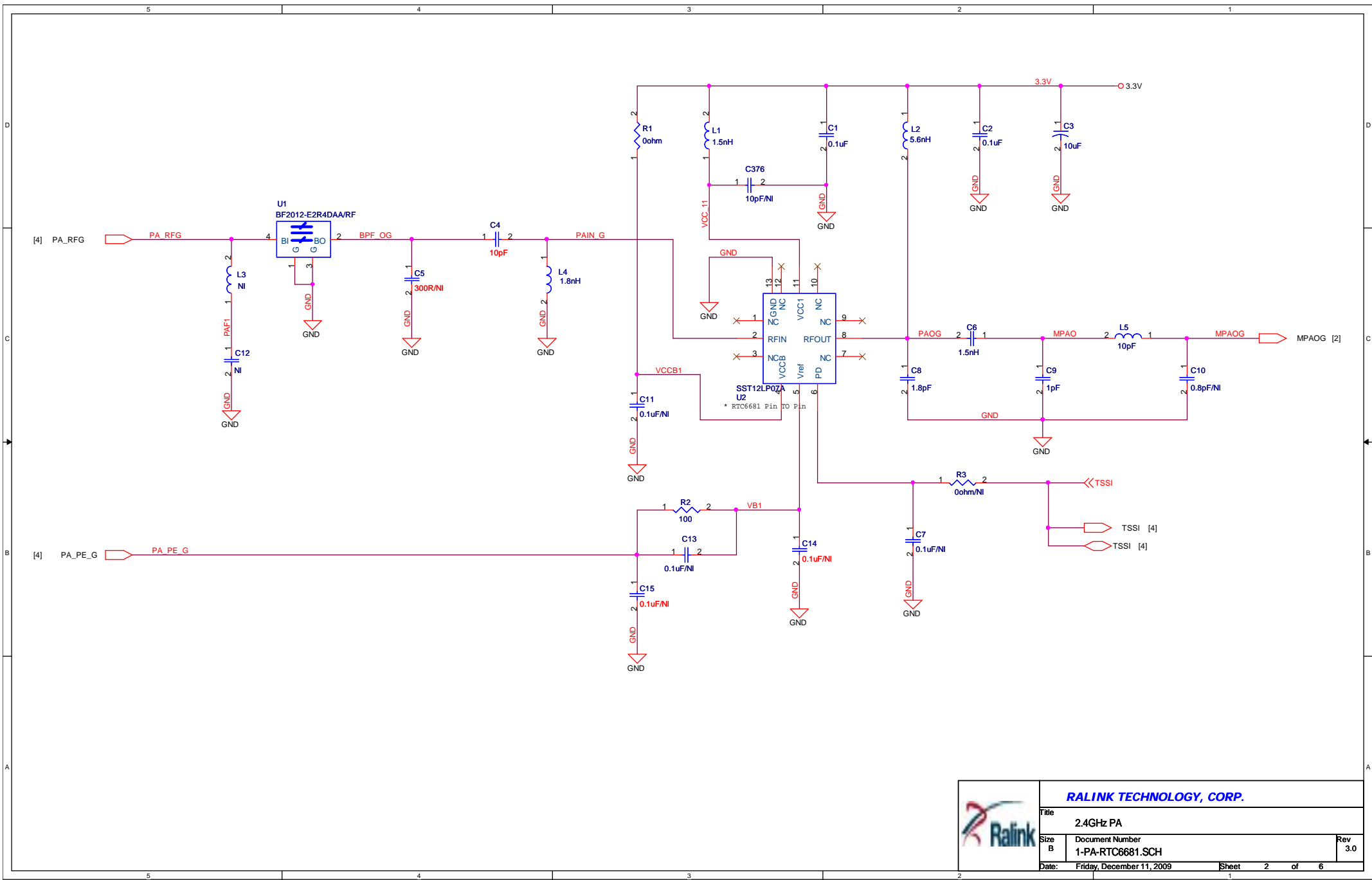
IR_CUTA << IR_CUTA
IR_CUTB << IR_CUTB




Title		
OV9712		
Size	Document Number	Rev
	OTIP316BOV97121VA	
Date:	Friday, January 13, 2012	Sheet 2 of 3

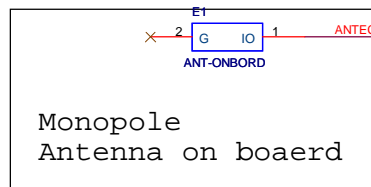


Title		
IR CUT		
Size	Document Number	Rev
	OTIP316BOV97121VA	
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RALINK TECHNOLOGY, CORP.			
Title	2.4GHz PA		
Size B	Document Number		Rev
	1-PA-RTC6681.SCH		3.0
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(ANTENNA0)
(TX0/RX0) **WIFI 2412-2462MHz**

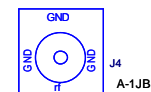


[4] TR_SW0 TR_SW0


[4] TR_SWN0 TR_SWN0

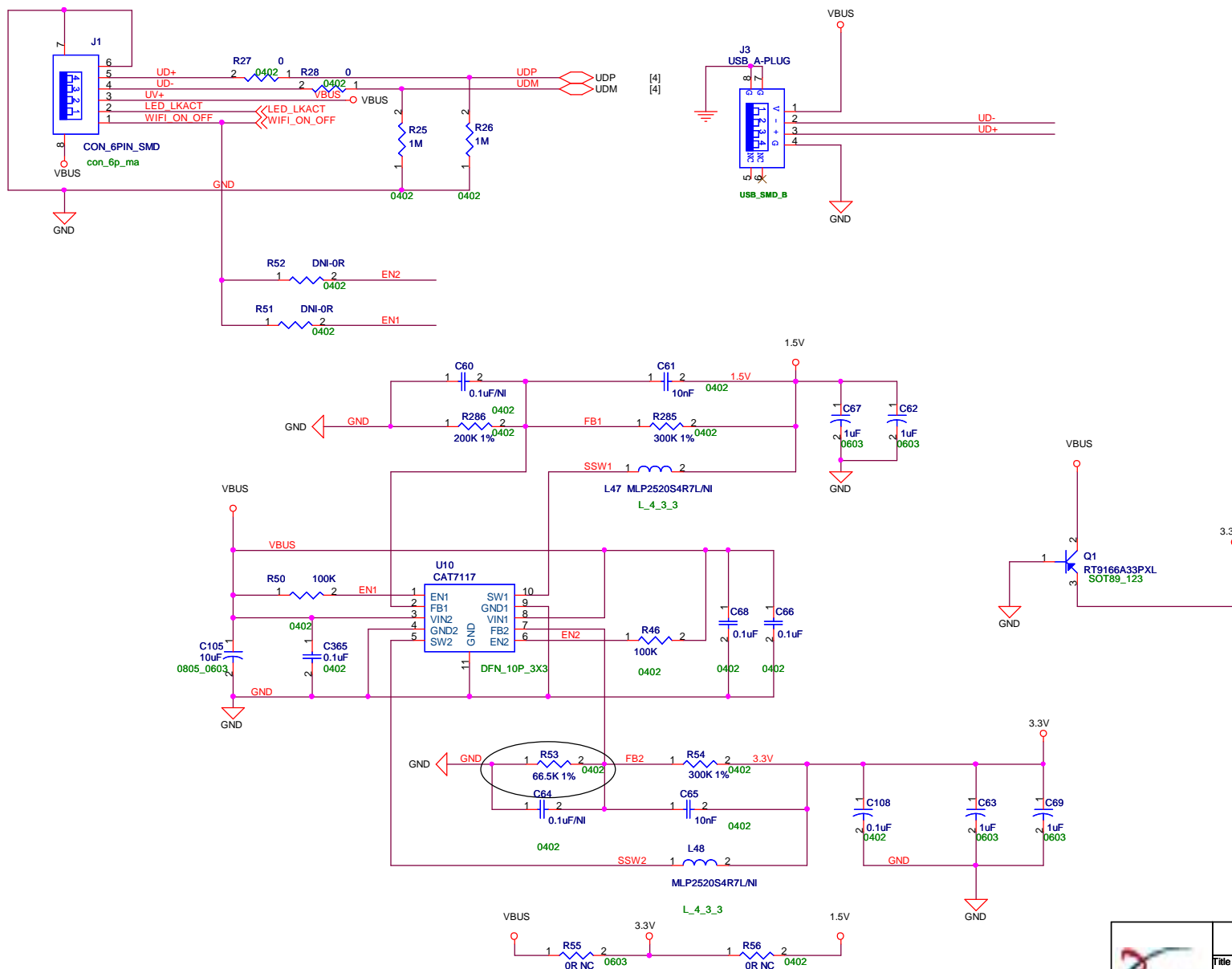
[3] MPAOG MPAOG


ADD J4



ADD cap for DC coupling
20091216

		RALINK TECHNOLOGY, CORP.					
Title		Front-End					
Size B	Document Number					Rev 3.0	
	2-FrontEnd.SCH						
Date:		Wednesday, December 16, 2009		Sheet	3	of	6



		RALINK TECHNOLOGY, CORP.	
		Title: PEN USB Interface and Power	
Size B	Document Number: 4-USB-A.SCH		Rev 3.0
Date: Thursday, September 22, 2011	Sheet 5	of 6	