Circuit Description

- 1. The IC-JZ4730 is master micro controller, it include U3A, U3B, U3C, U3D four part, the oscillate component Y700 is generated a 3.6864MHz clock frequency, and input to micro controller as base clock signal.
- 2. The IC-K4S51632C is SDRAM memory, and the U6-K9F4G08U0A-P is nana flash, it can storage a lot of data file, for example the music file, word file etc, its capability is 4G byte.
- 3. The touch panel, keyboard, mouse and RJ45 net port all external signal and data input to CPU as operating, and display on LCD screen.
- 4. The work voltage is DC 12V, supply by power adapter.