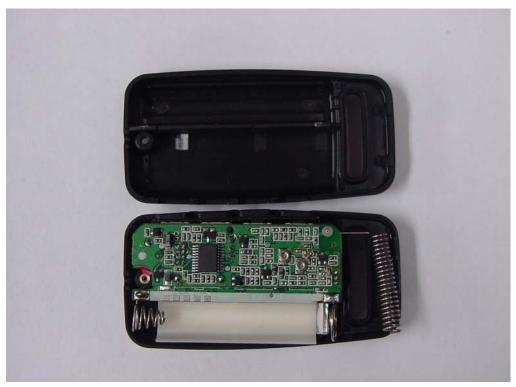
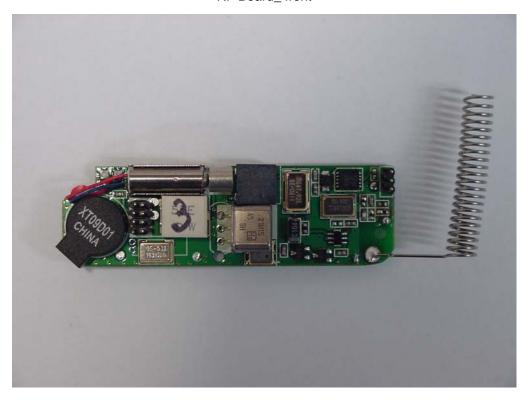


# Appendix 3. Photographs of EUT [Internal View]

#### Inside



RF Board\_ front

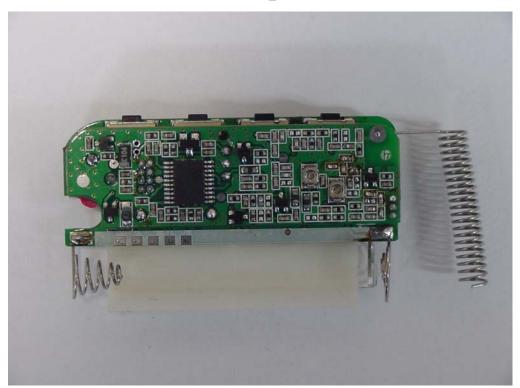


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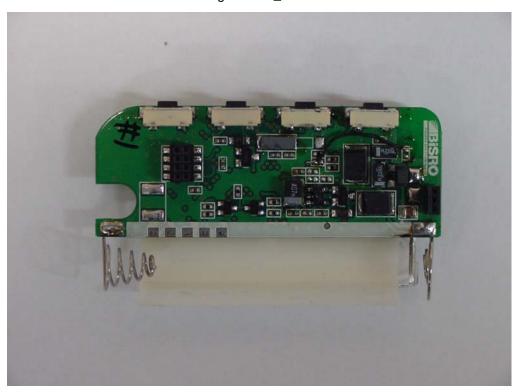


# Appendix 3. Photographs of EUT [Internal View]

### RF Board\_ Rear



Logic Board\_ Front

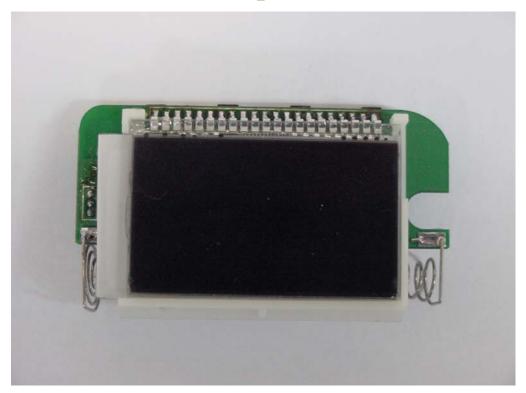


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# Appendix 3. Photographs of EUT [Internal View]

# LCD\_Front



# **Blank**

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