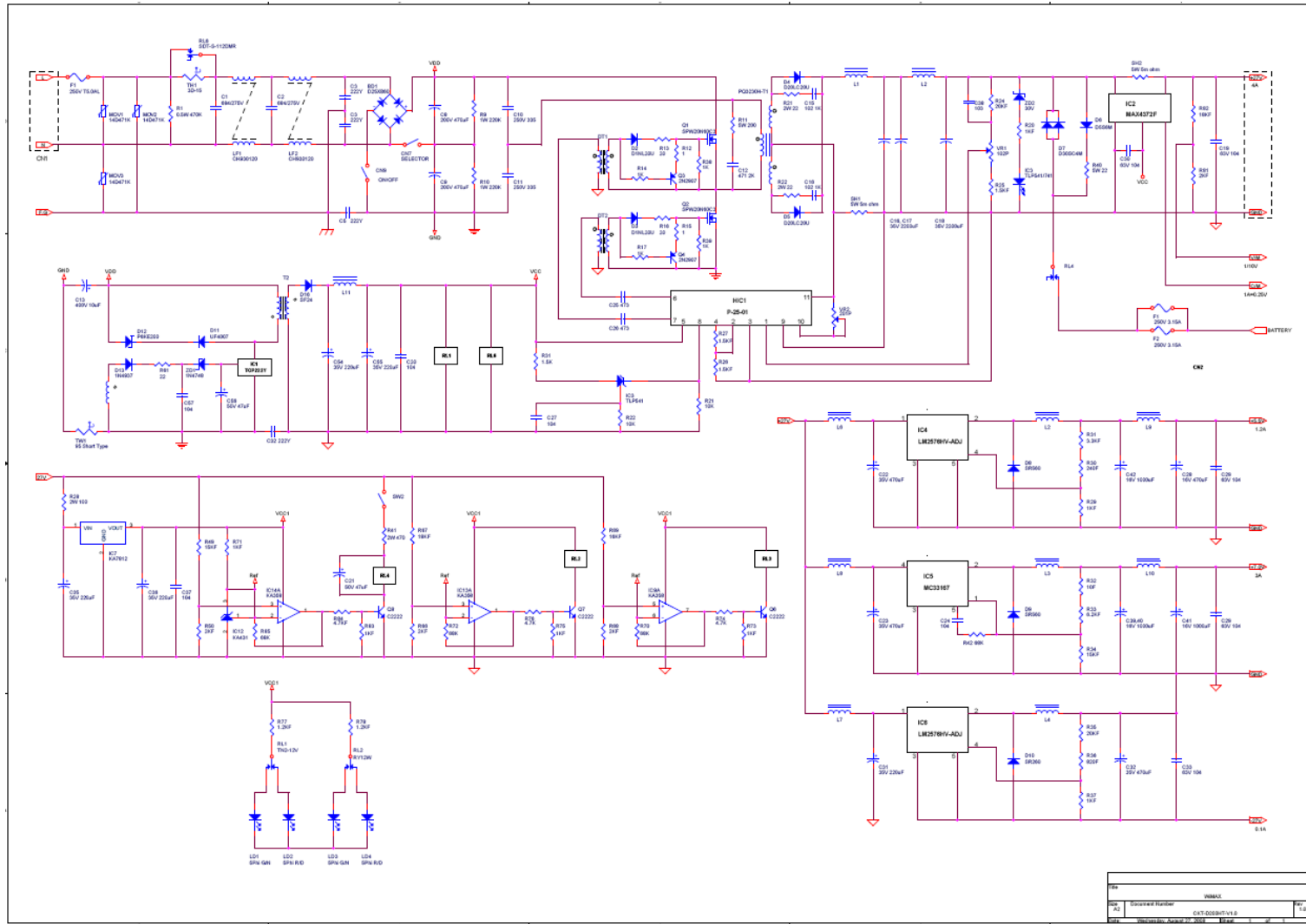
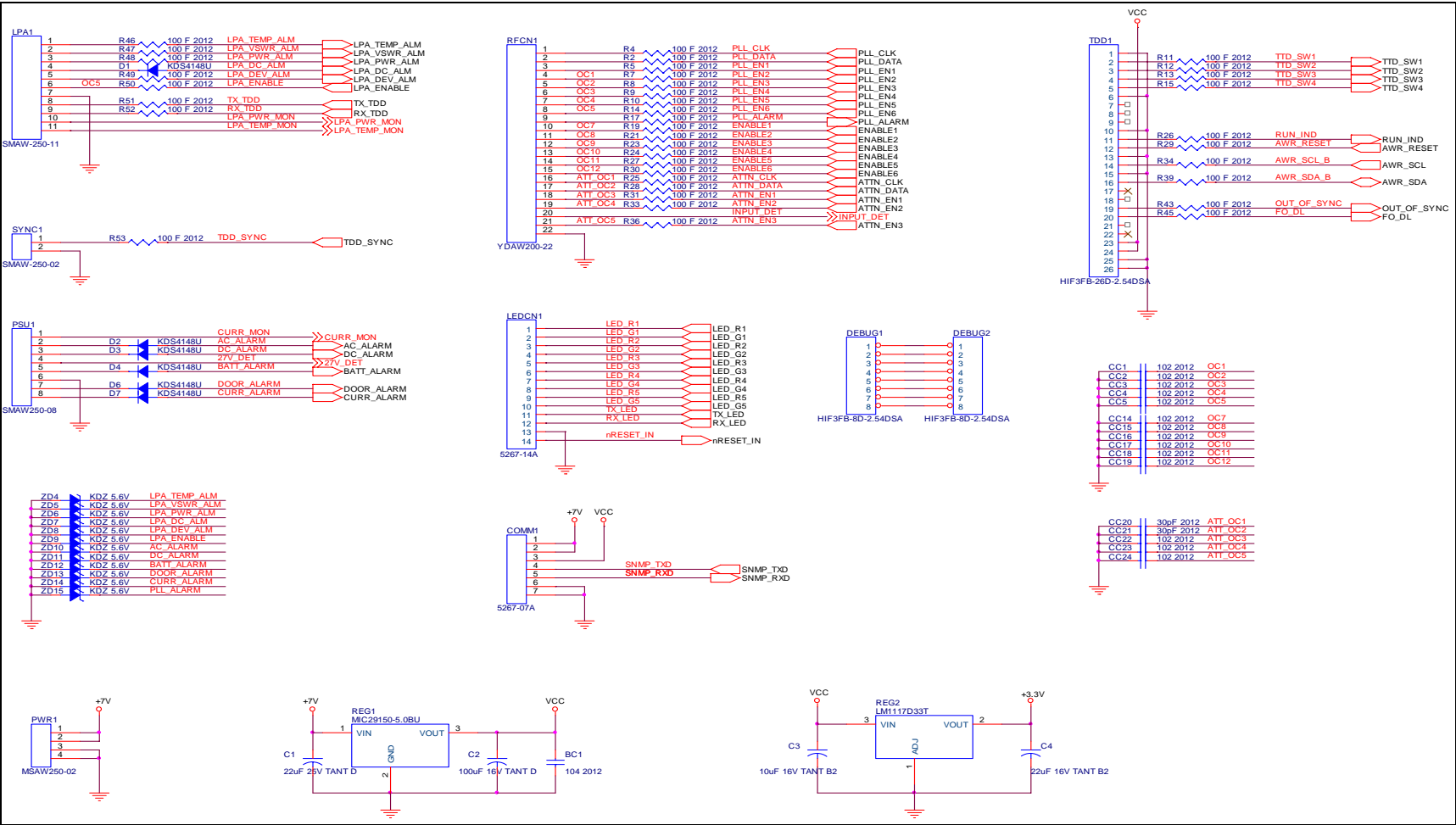


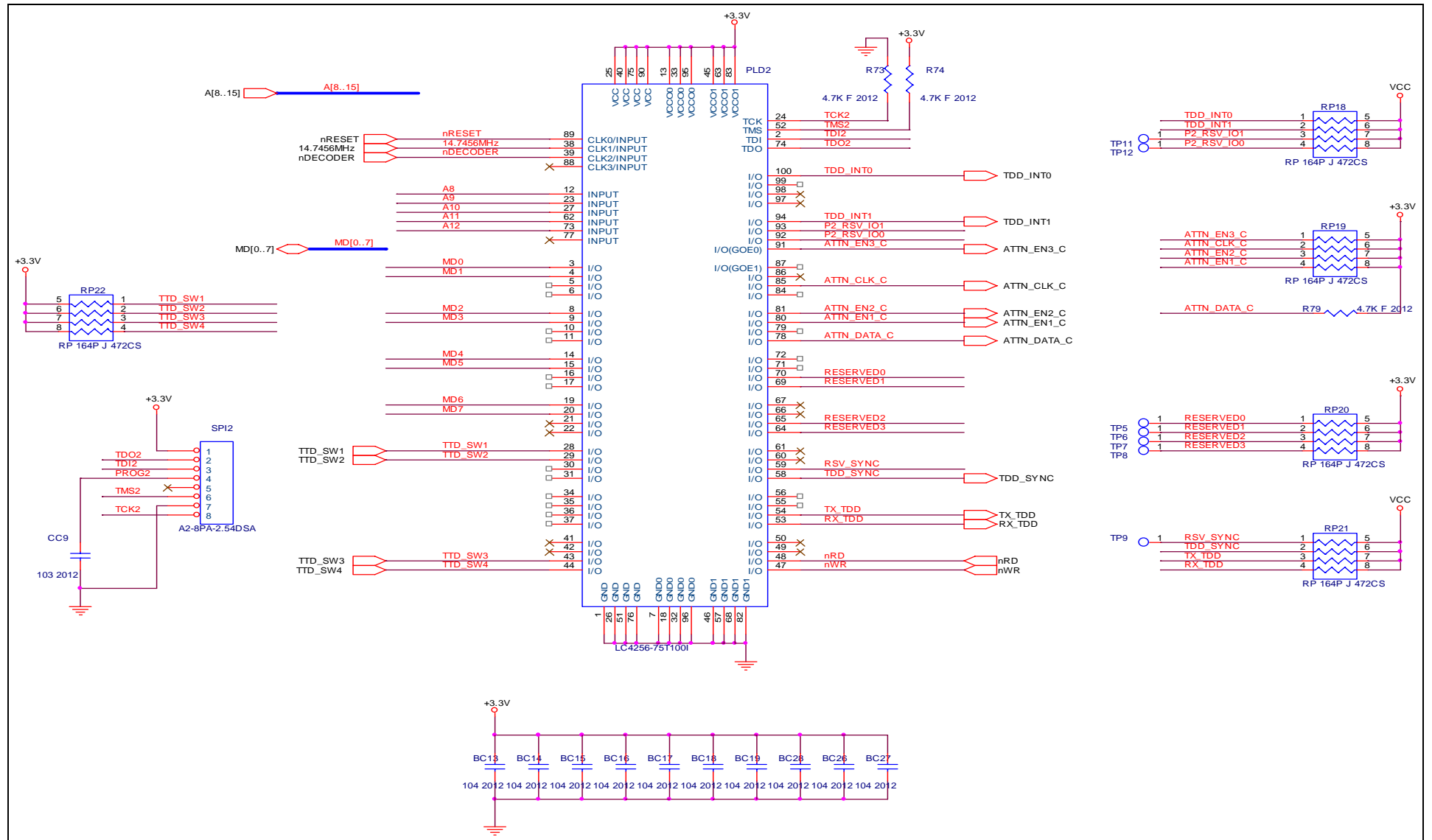
Circuit diagram

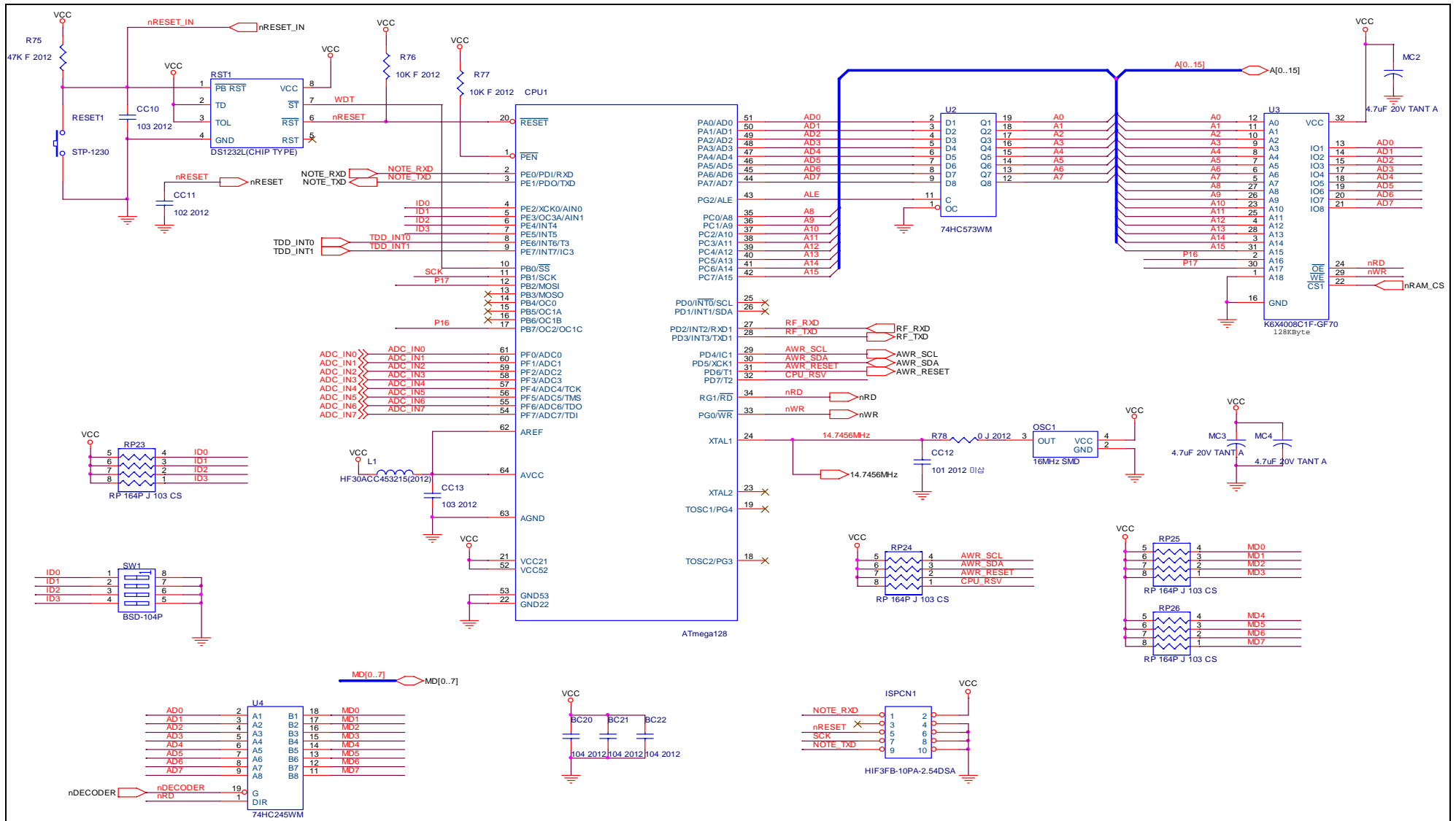
PSU

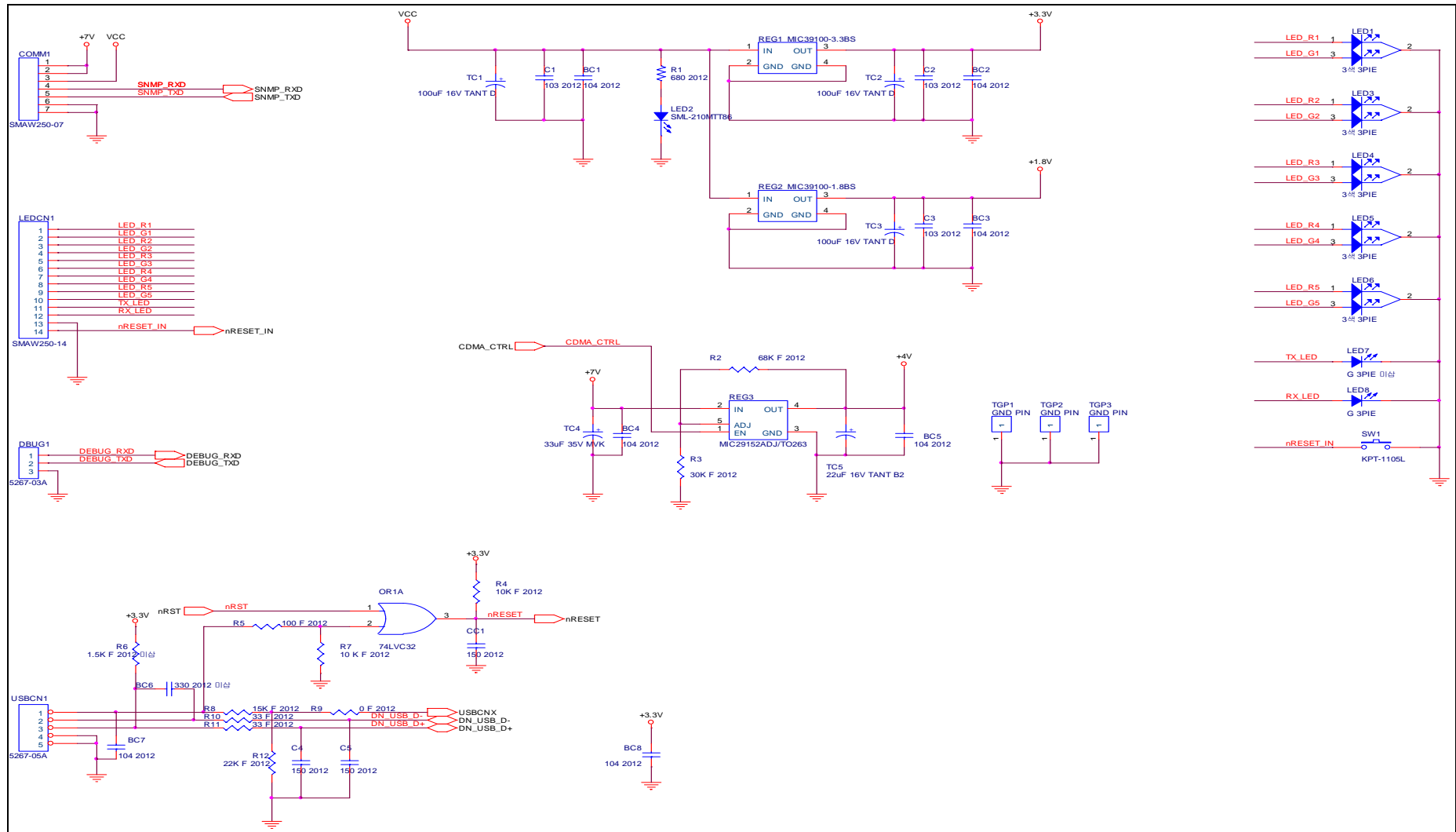


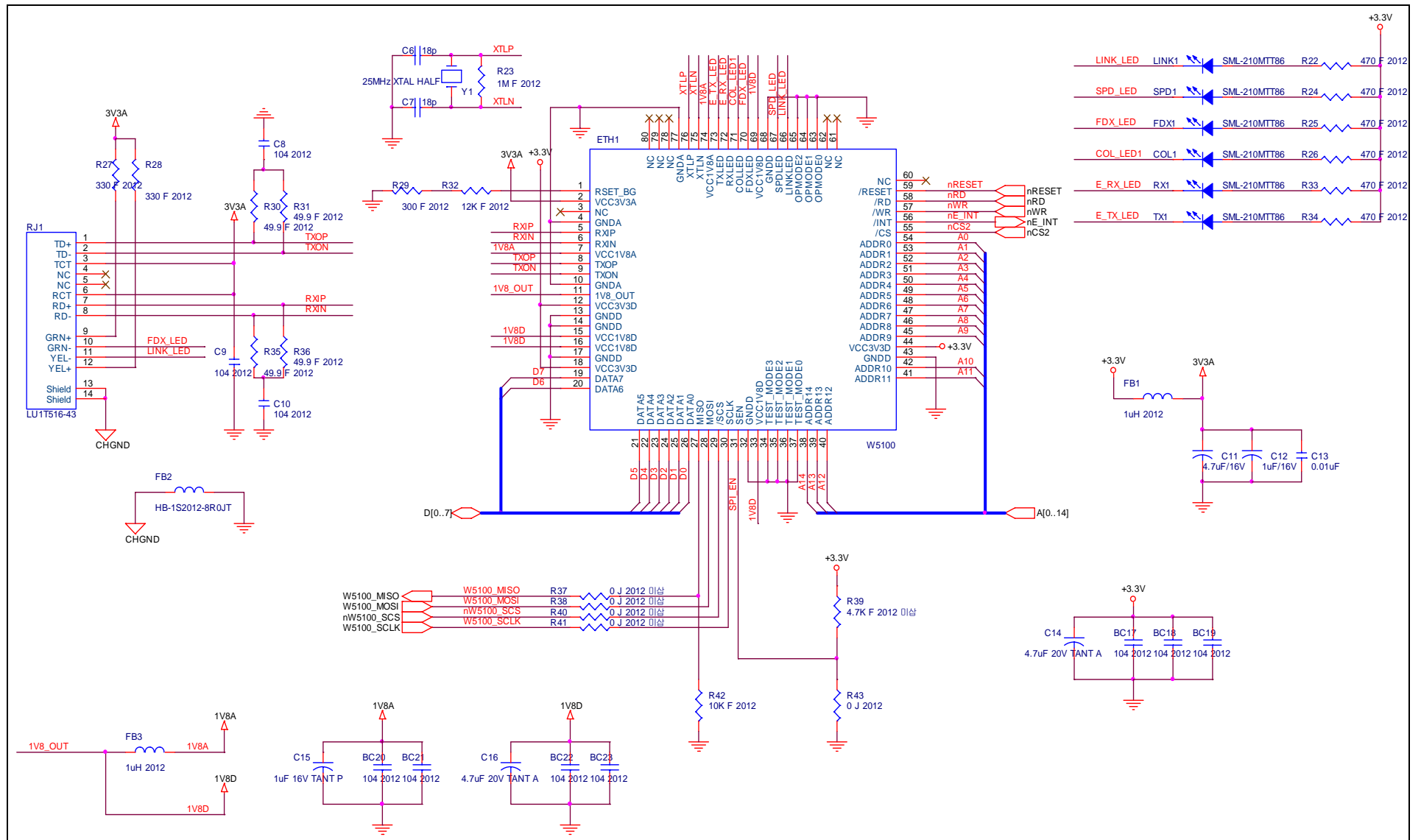
CTRL

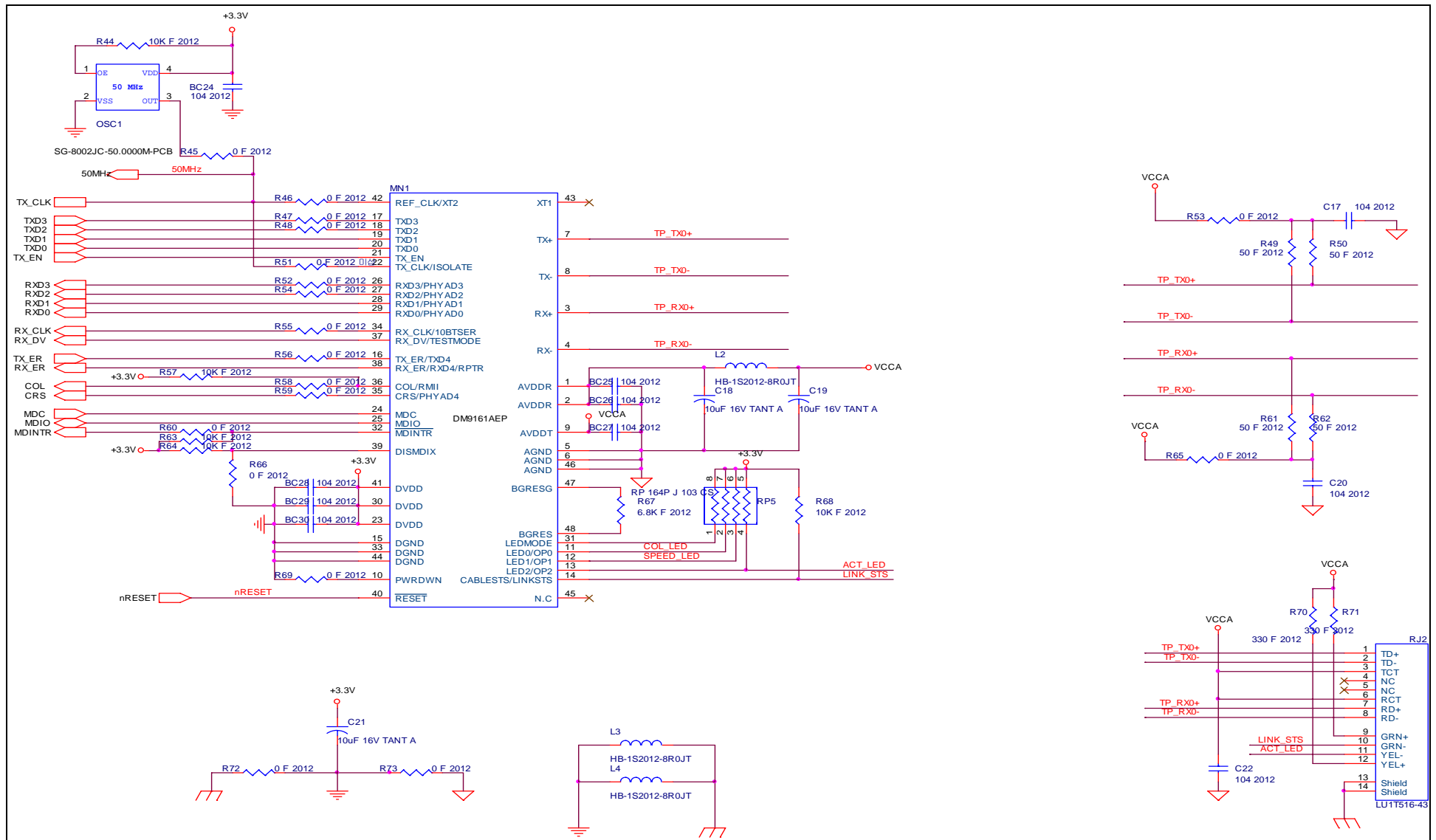


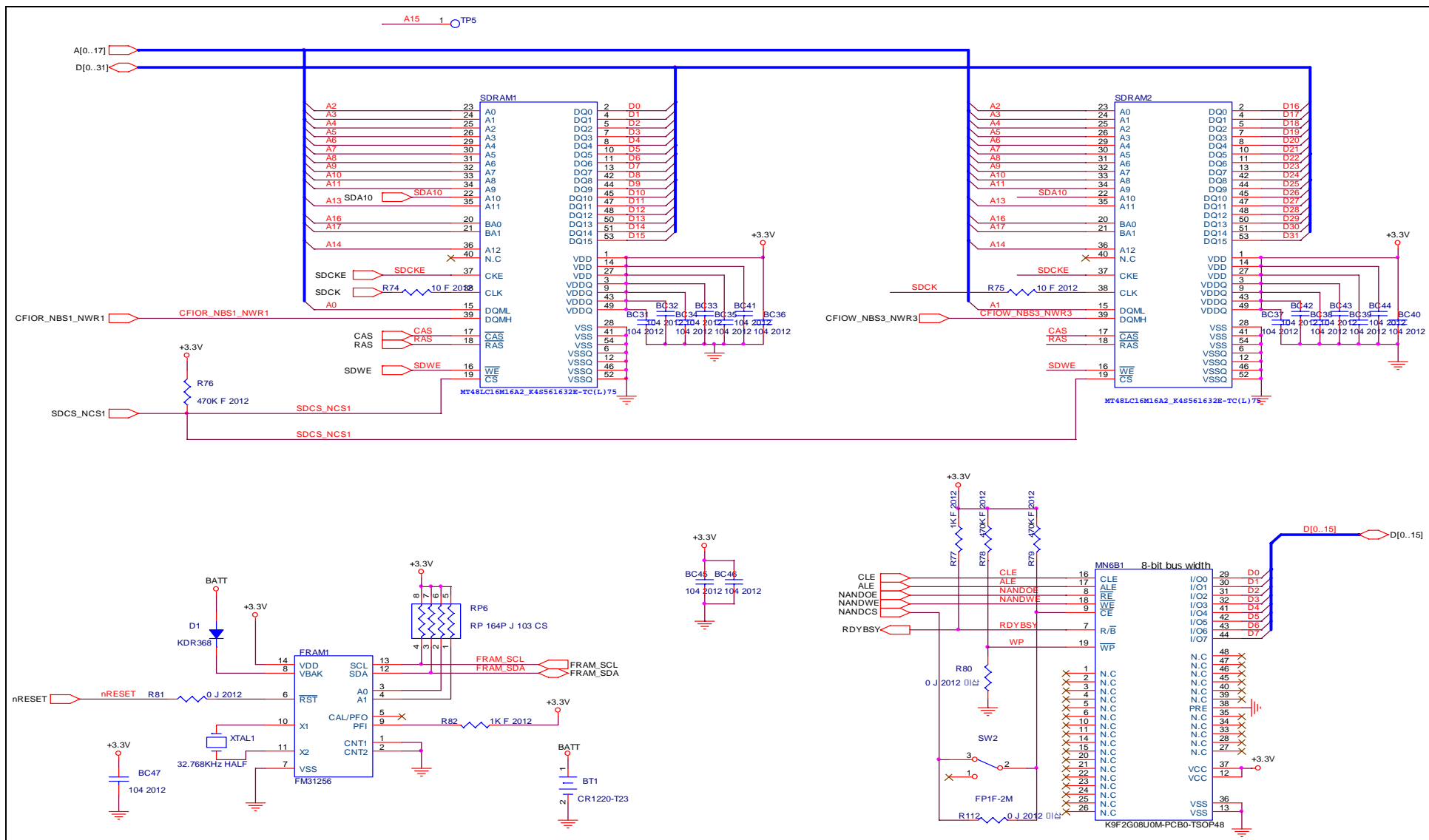


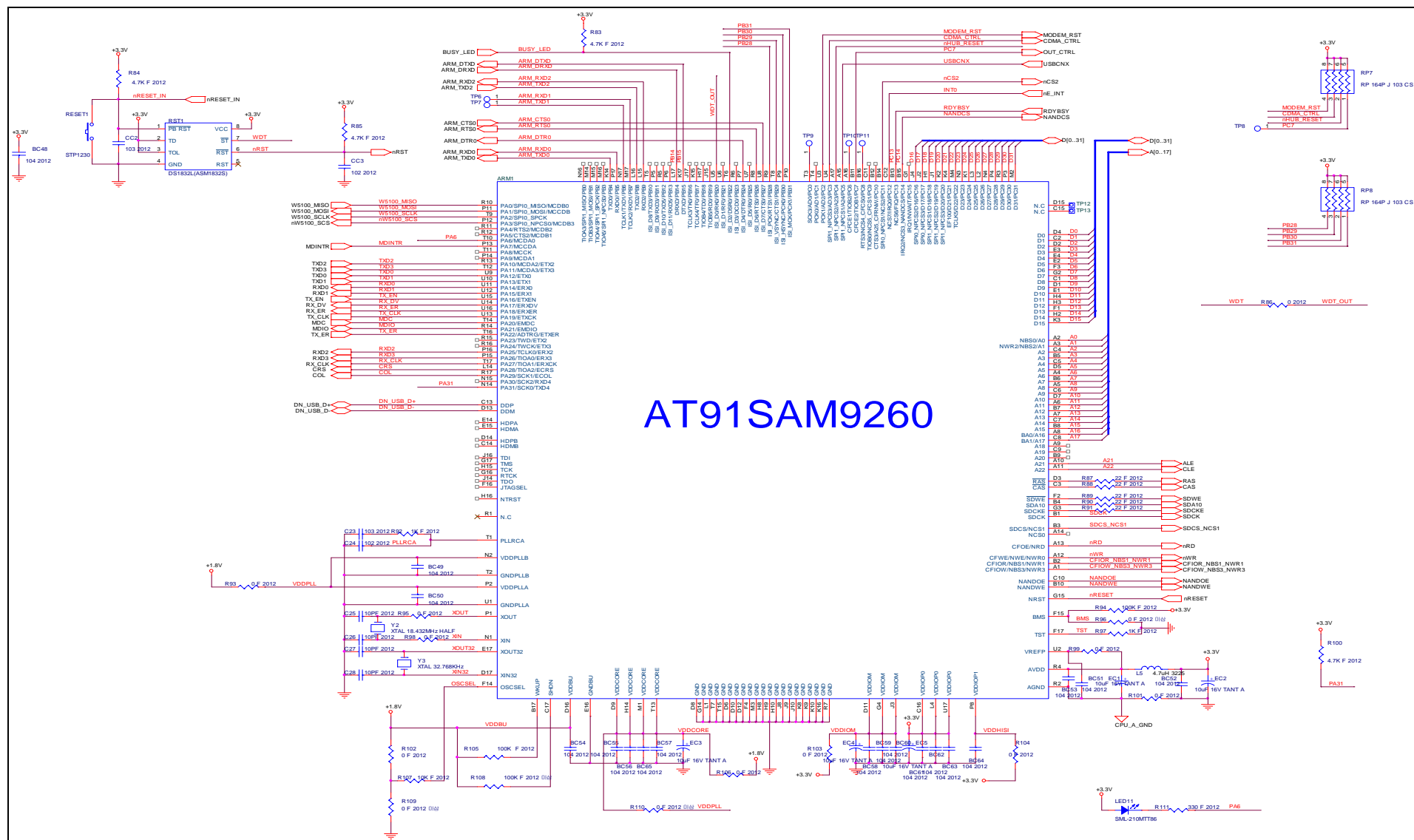


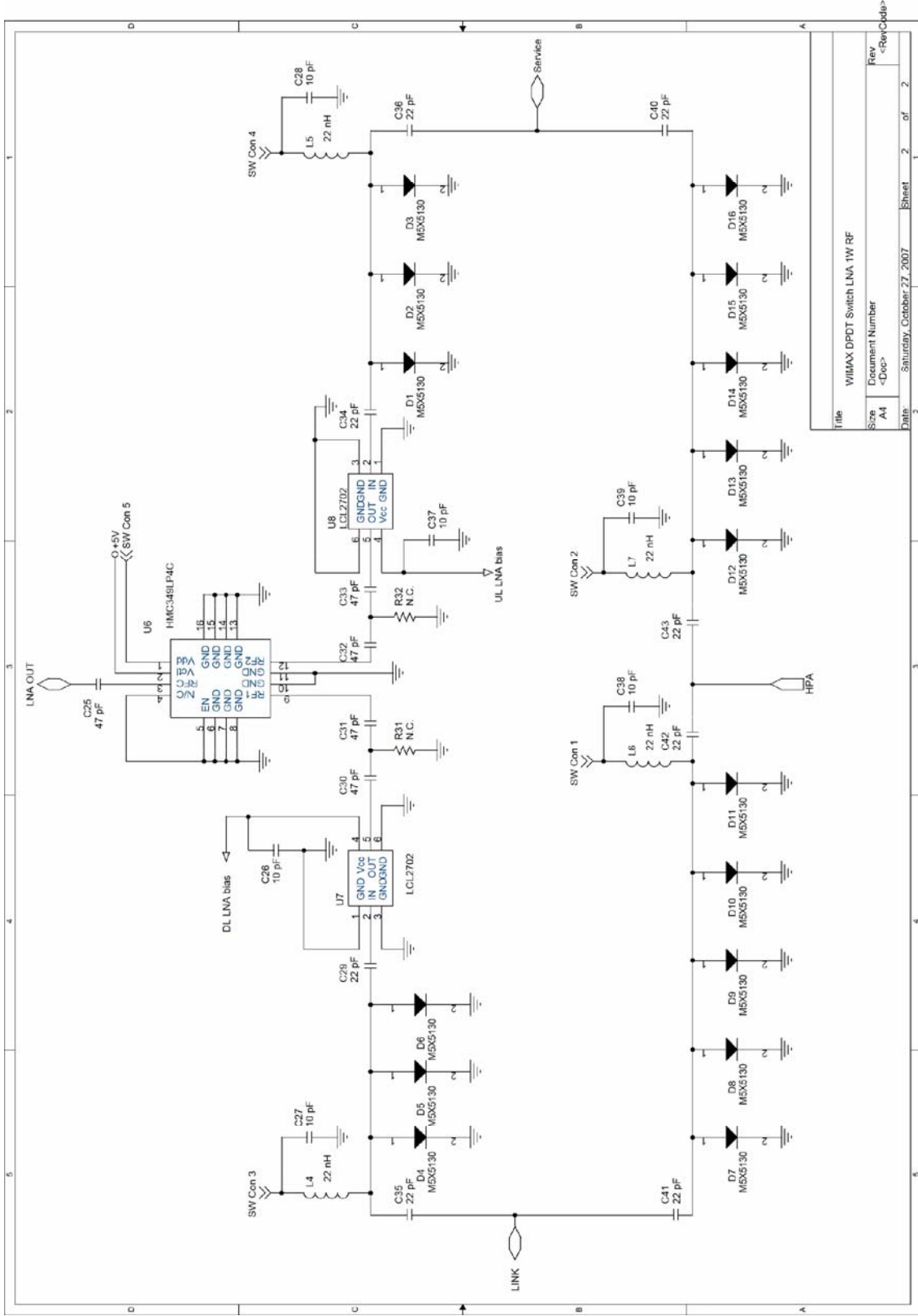




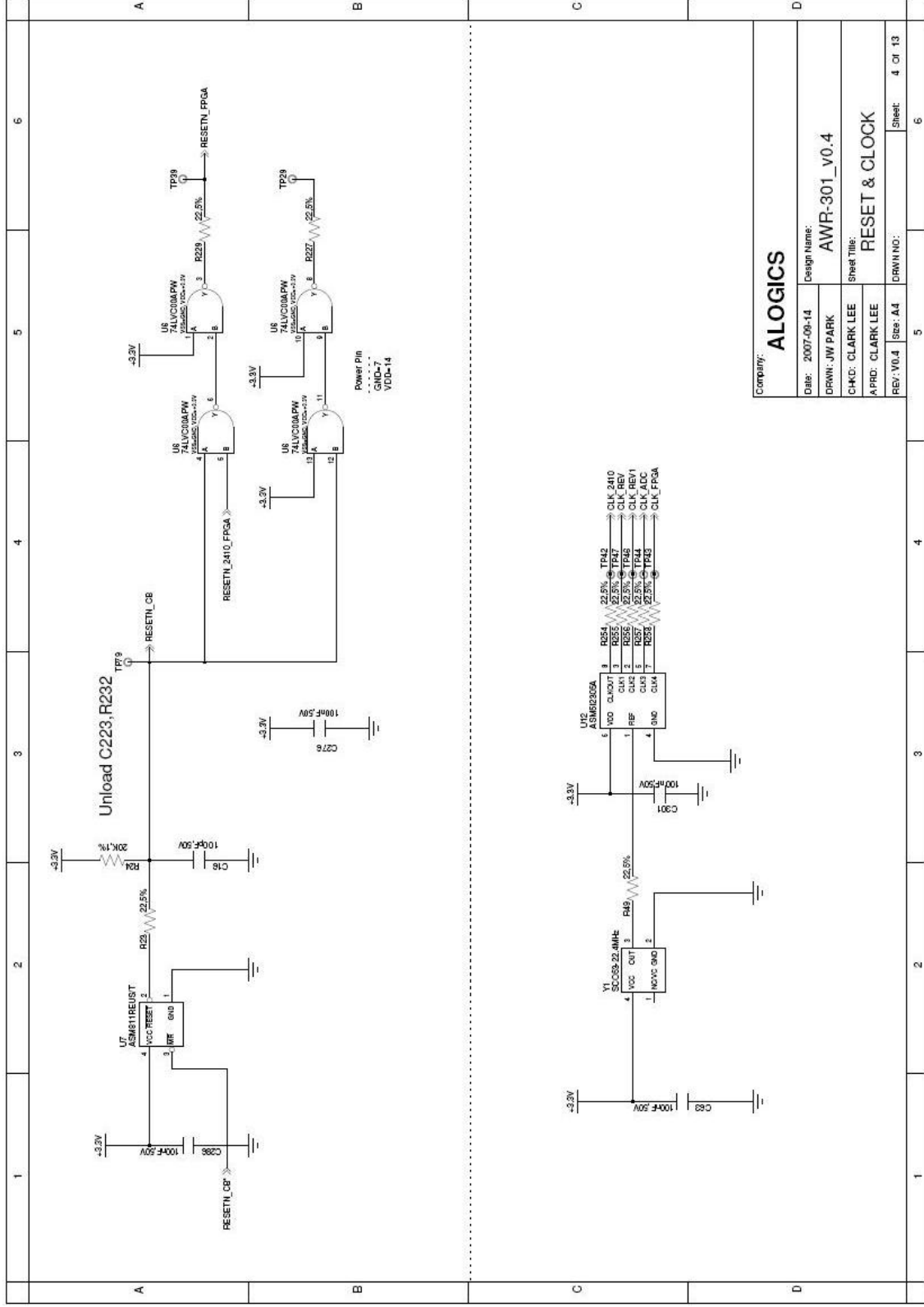


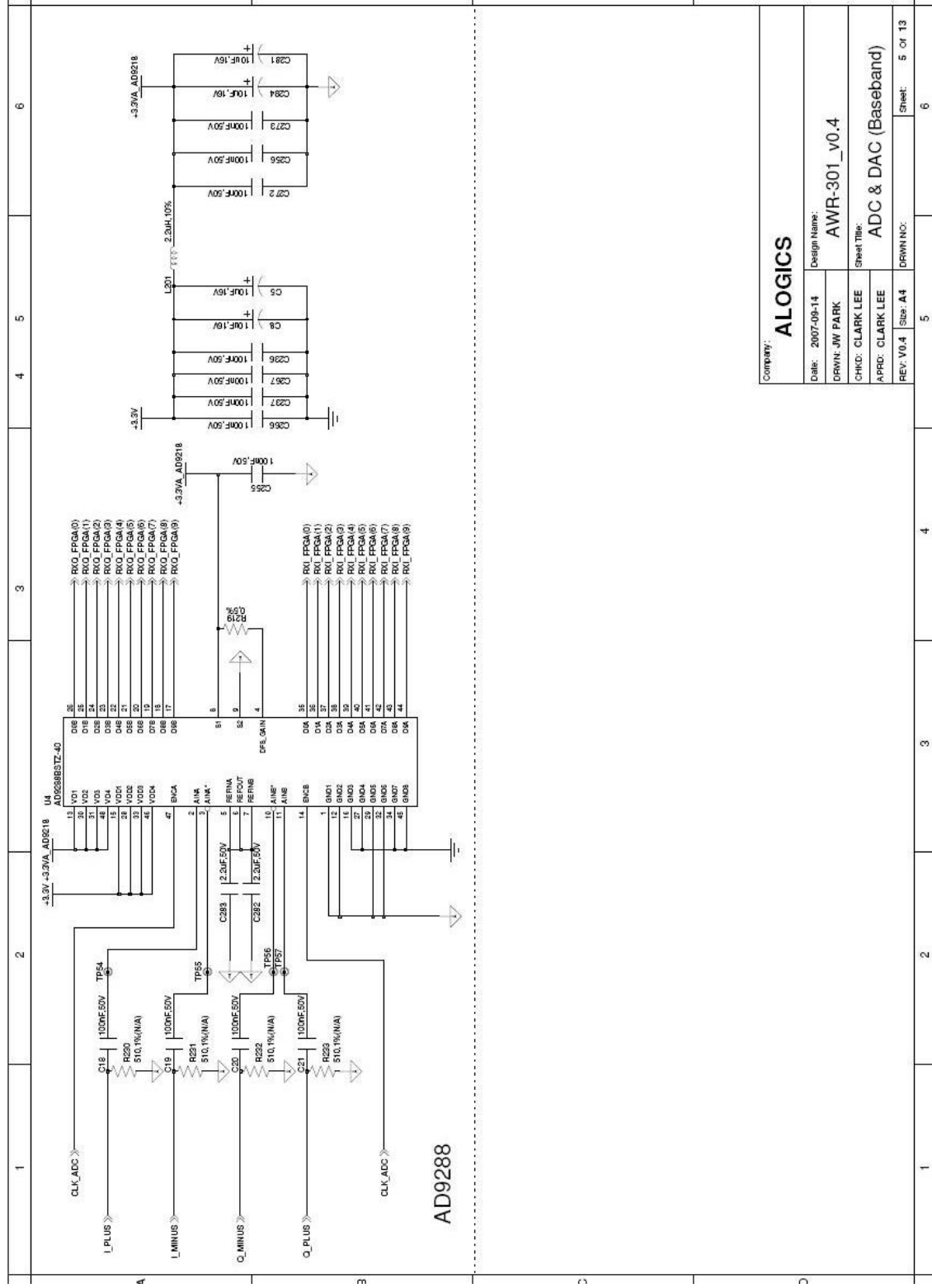






Title		WIMAX DPDT Switch LNA 1W RF	
Size	Document Number	Rev	<RevCpde>
A4	<Doc>		
Date	Saturday, October 27, 2007	Sheet	2 of 2





Company:

ALLOGICS

Date: 2007-09-14	Design Name:
------------------	--------------

Date: 2007-09-14

FORM: 01/10/2010

CHKD: CLARK LEE	Sheet Title:
-----------------	--------------

CHKD: CLARK LE

CHKD: CLARK LEE	Sheet Title:
-----------------	--------------

CHRD. CLARK LLC	ADC & DAC (Baseband)
APDN. CLARK LLC	

NAME: CLARK LEE	
-----------------	--

ATTN: CLARK L.	
----------------	--

NAME: CLARK LEE	
-----------------	--

NAME: CLARK LEE	
-----------------	--

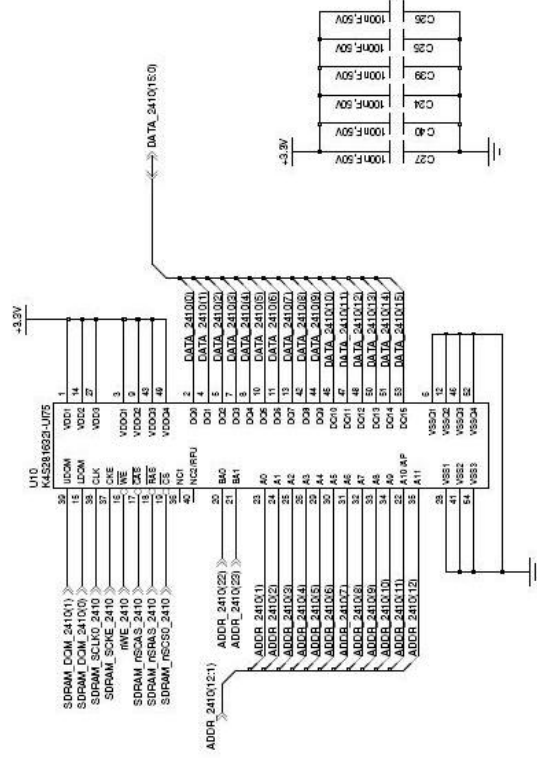
NAME: CLARK LEE	
-----------------	--

NAME: CLARK LEE	
-----------------	--



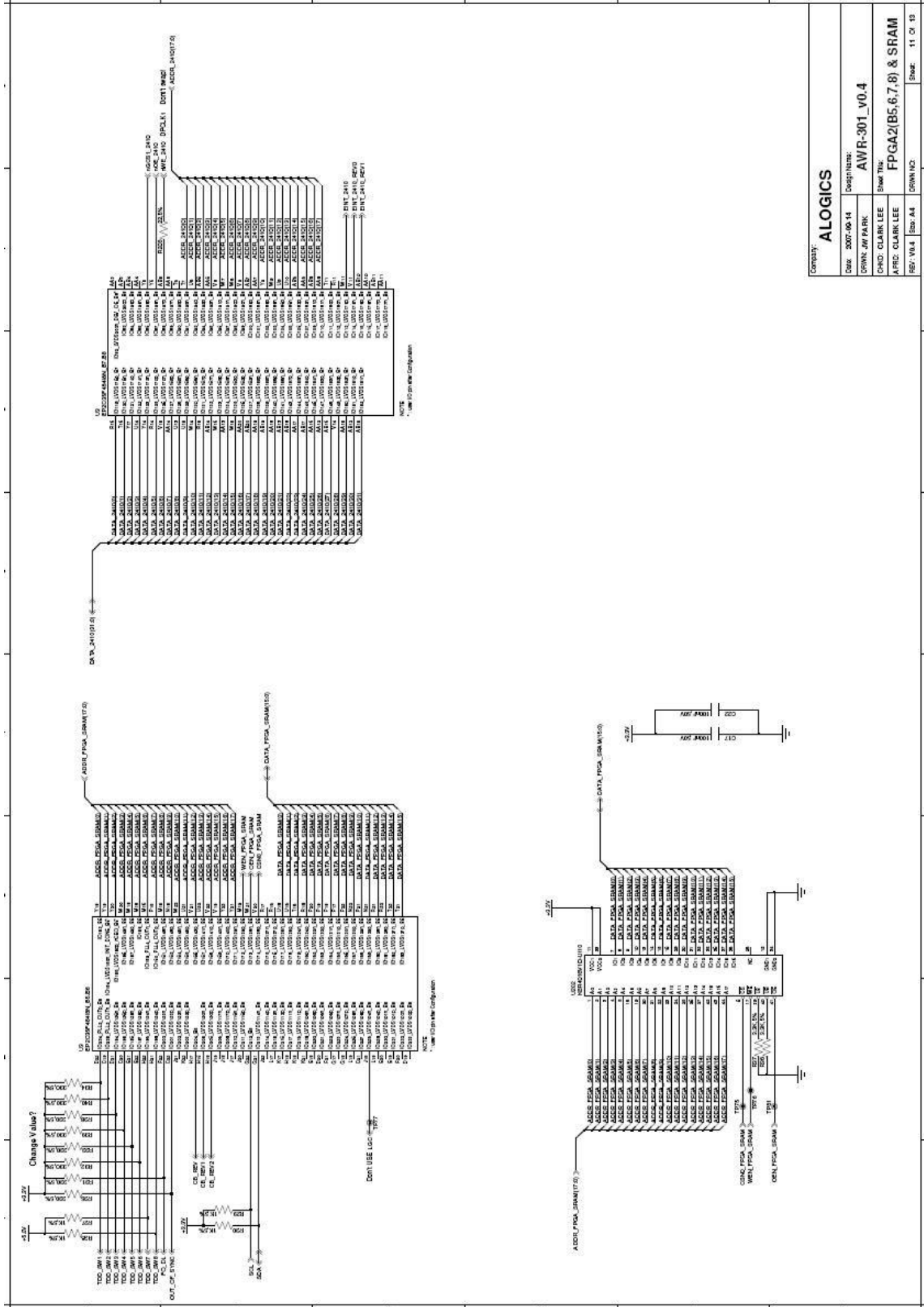
ALLOGICS

Date: 2007-09-14	Design Name: AWR-301_v0.4	
DRWN: JW PARK		
CHKD: CLARK LEE	Sheet Title: S3C2410A 2 & FRAM	
APRD: CLARK LEE		
REV: V0.4	Size: A4	DRWN NO: Sheet: 7 of 13



ALLOGICS

Design Name:		AWR-301_v0.4	
Date: 2007-09-14			
DRWN: JIM PARK			
CHCD: CLARK LEE	Sheet Title: External Mem of 2410		
APRD: CLARK LEE			
REV: V0.4	Size: A4	DRWN NO:	Sheet: 9 of 13



Company

ALOGICS

Date: 2007-06-14

Design Name: AWR-301_v0.4

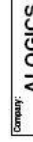
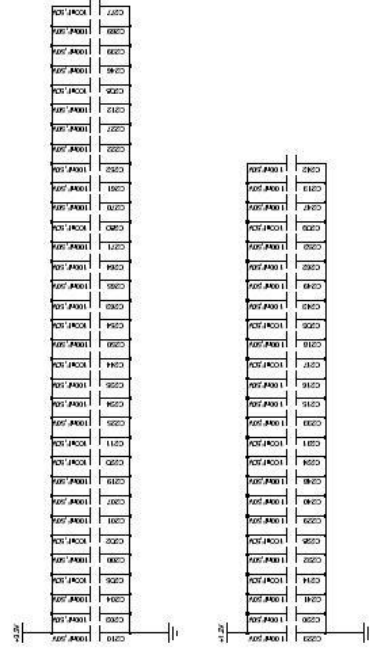
Drawn By: PARK

Checked By: CLARK LEE

Part No: FPG2(B5,6,7,8) & SRAM

Rev: V0.4

Sheet: 11 of 13



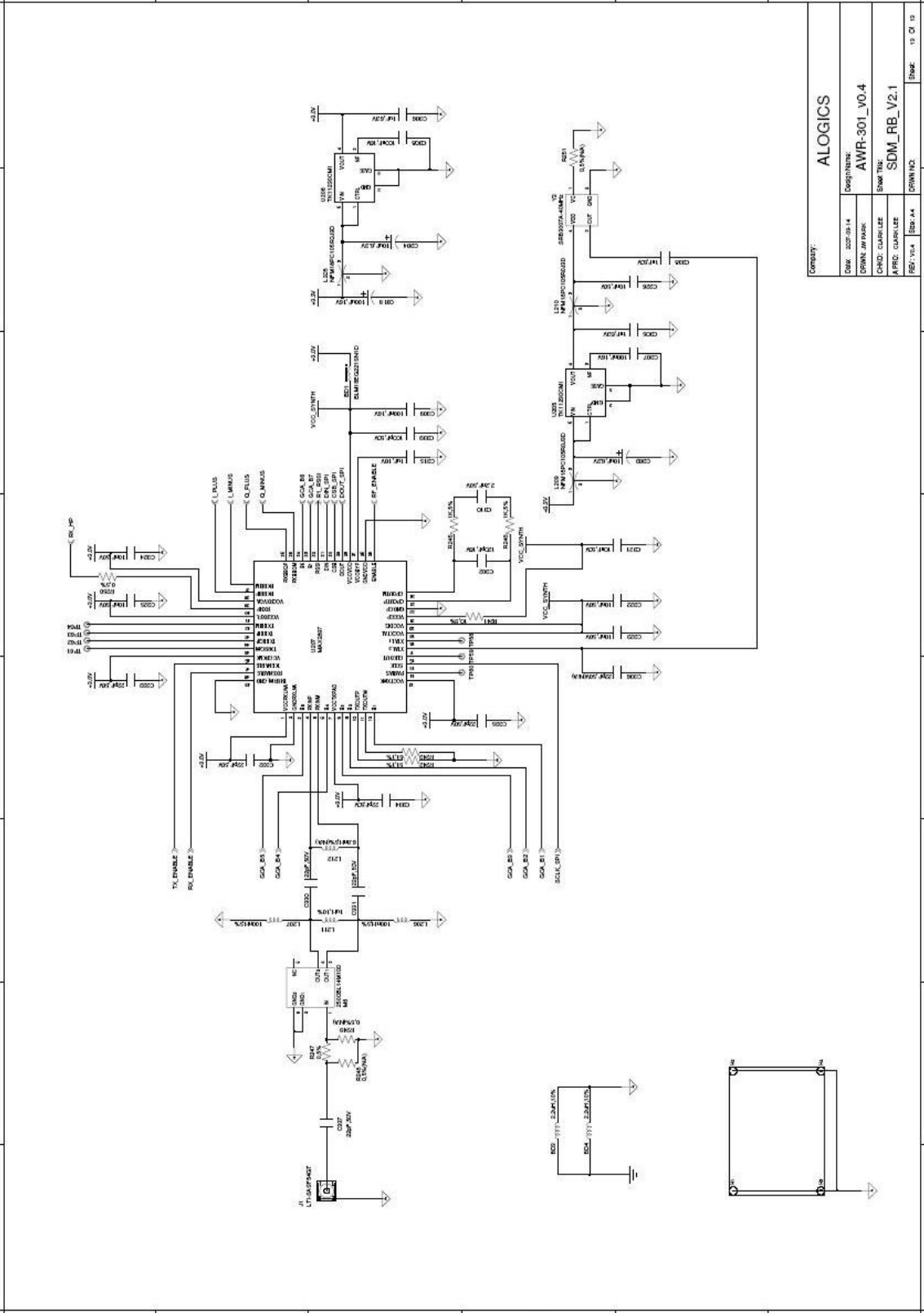
Date: 2007-09-14	Design Name: AWR-301 v0.4
Drawn: JIM PARK	

Design Name:	
Date:	2007-09-14

CHD: CLARK LEE	Sheet Title:
----------------	--------------

APRD: CLARK LEE	FPGA3 (POWER, CONFIG)
-----------------	-----------------------

REV: V0.4	Size: A4	Sheet:	12 of 13
-----------	----------	--------	----------



ALGOS

Doc#	301-014
Doc#Name	AWR-301_V0.4
Doc#Rev	001
Doc#Date	2014-01-14
Doc#Author	SDM
Doc#Checked	SDM
Doc#Approved	SDM
Doc#Rev	001
Doc#Date	2014-01-14
Doc#Author	SDM
Doc#Checked	SDM
Doc#Approved	SDM

Company

Doc# 301-014

Doc#Name AWR-301_V0.4

Doc#Rev 001

Doc#Date 2014-01-14

Doc#Author SDM

Doc#Checked SDM

Doc#Approved SDM

Doc#Rev 001

Doc#Date 2014-01-14

Doc#Author SDM

Doc#Checked SDM

Doc#Approved SDM

Doc#Rev 001

Doc#Date 2014-01-14

Doc#Author SDM

Doc#Checked SDM

Doc#Approved SDM

Doc#Rev 001

Doc#Date 2014-01-14

Doc#Author SDM

Doc#Checked SDM

Doc#Approved SDM

Doc#Rev 001

Doc#Date 2014-01-14

Doc#Author SDM

Doc#Checked SDM

Doc#Approved SDM

Doc#Rev 001

Doc#Date 2014-01-14

Doc#Author SDM

Doc#Checked SDM

Doc#Approved SDM

Doc#Rev 001

Doc#Date 2014-01-14

Doc#Author SDM

Doc#Checked SDM

Doc#Approved SDM

Doc#Rev 001

Doc#Date 2014-01-14

Doc#Author SDM

Doc#Checked SDM

Doc#Approved SDM

Doc#Rev 001

Doc#Date 2014-01-14

Doc#Author SDM

Doc#Checked SDM

Doc#Approved SDM

Doc#Rev 001

Doc#Date 2014-01-14

Doc#Author SDM

Doc#Checked SDM

Doc#Approved SDM

Doc#Rev 001

Doc#Date 2014-01-14

Doc#Author SDM

Doc#Checked SDM

Doc#Approved SDM

Doc#Rev 001

Doc#Date 2014-01-14

Doc#Author SDM

Doc#Checked SDM

Doc#Approved SDM

Doc#Rev 001

Doc#Date 2014-01-14

Doc#Author SDM

Doc#Checked SDM

Doc#Approved SDM

Doc#Rev 001

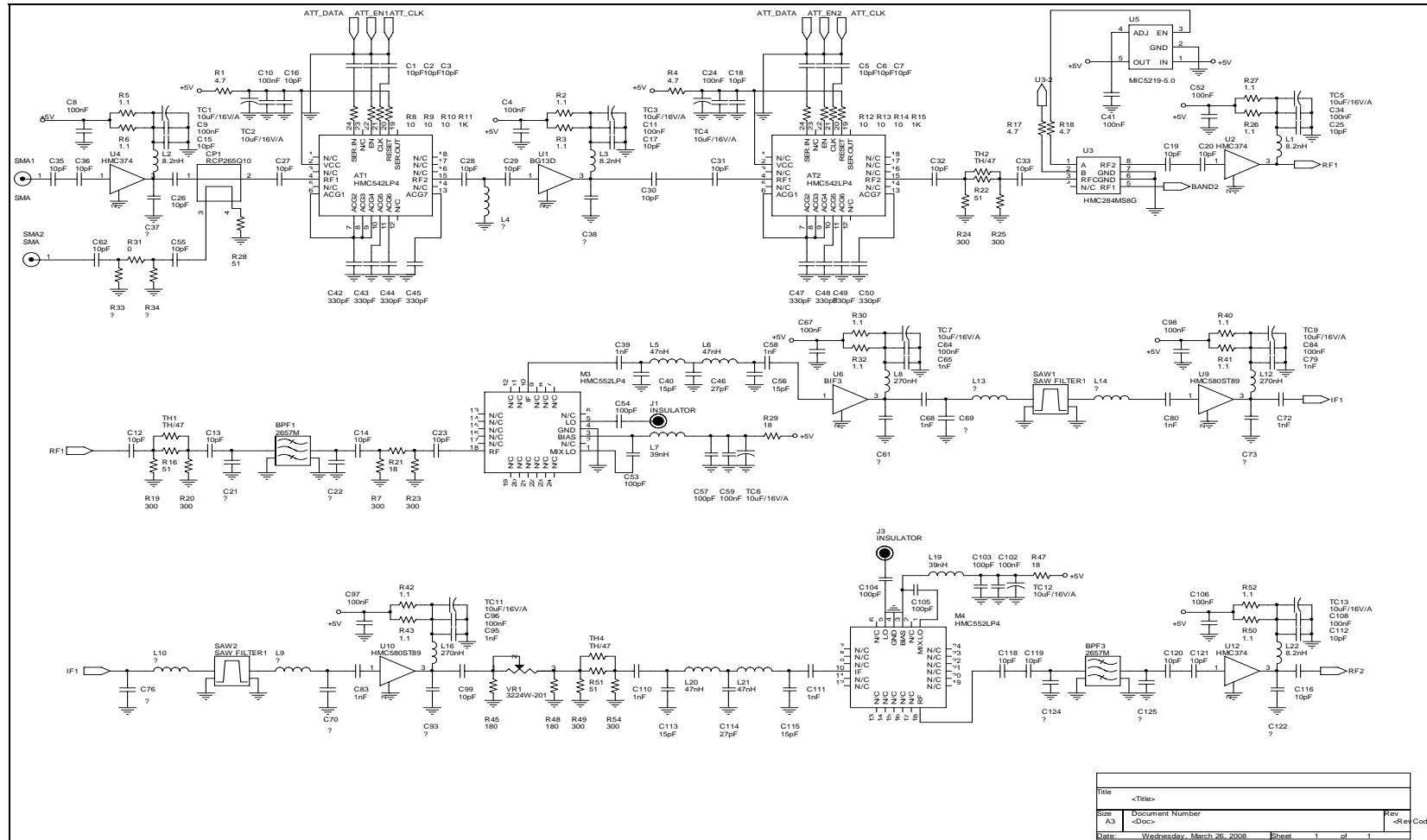
Doc#Date 2014-01-14

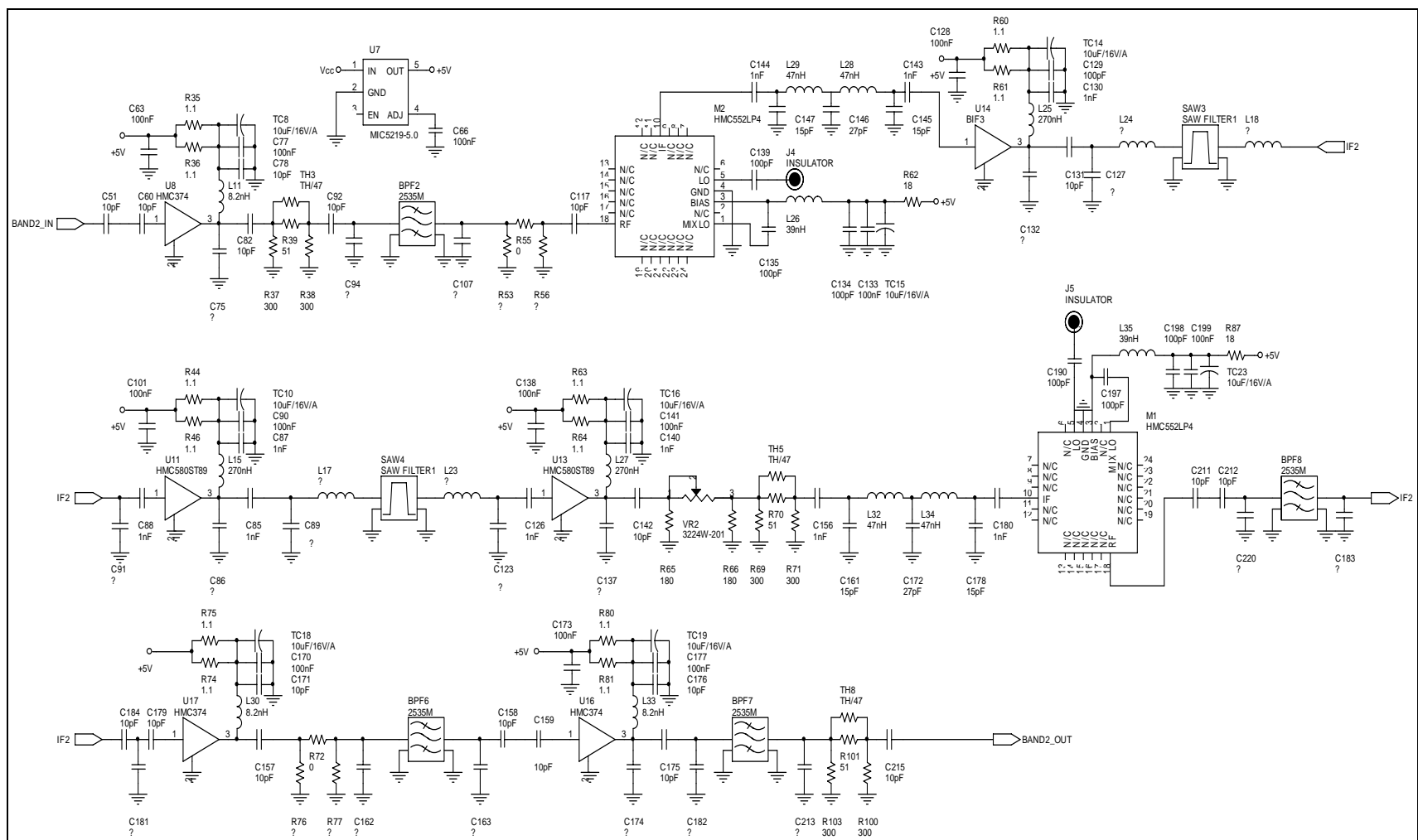
Doc#Author SDM

Doc#Checked SDM

Doc#Approved SDM

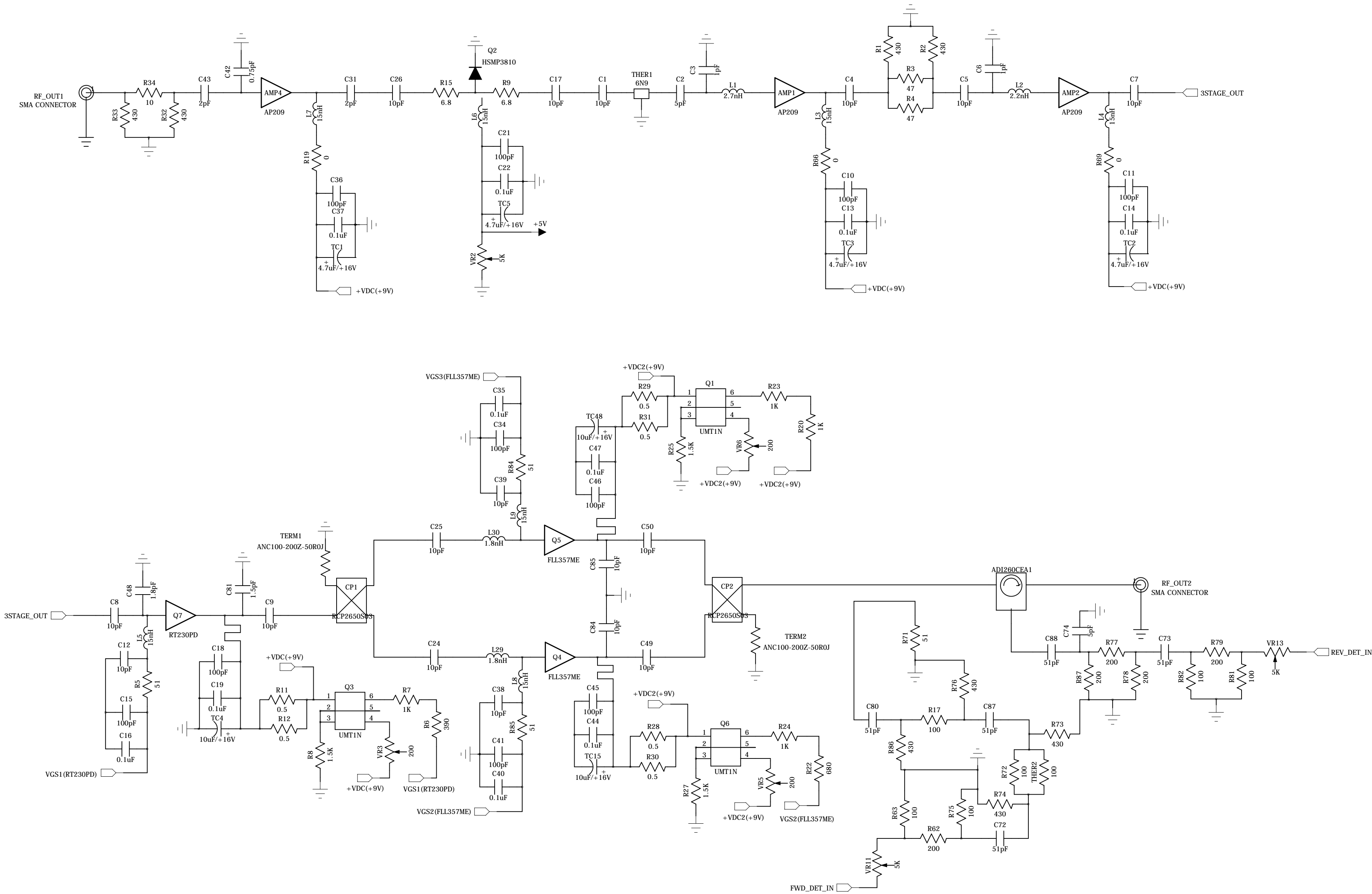
RF module





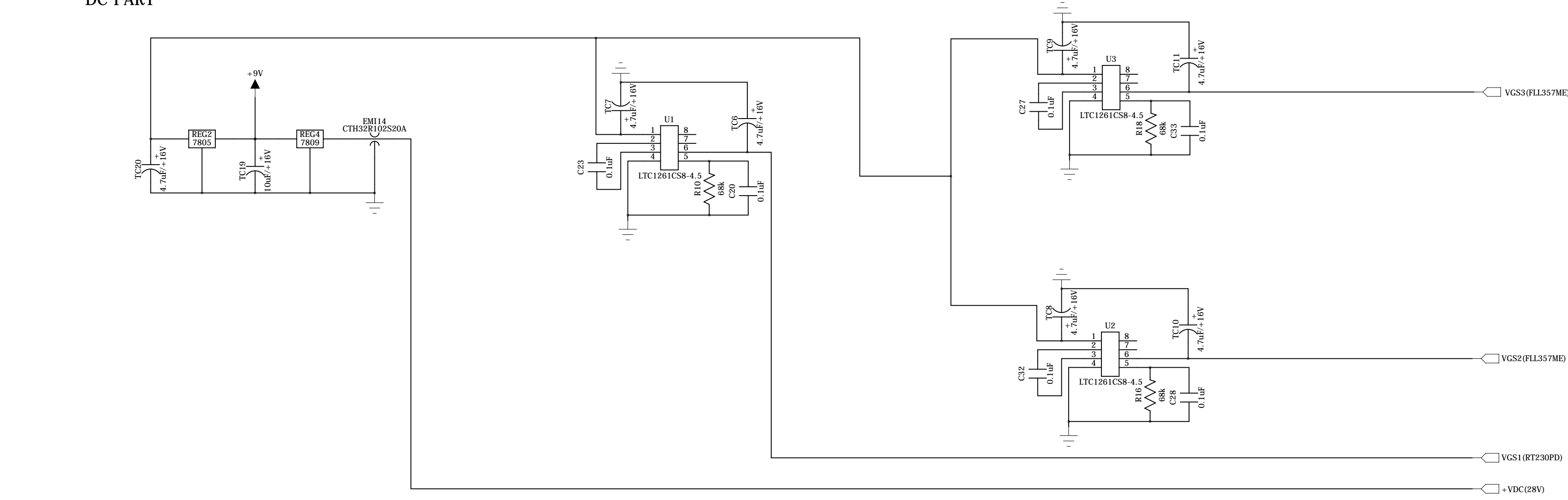
RFP-2590-27-27

RF PART

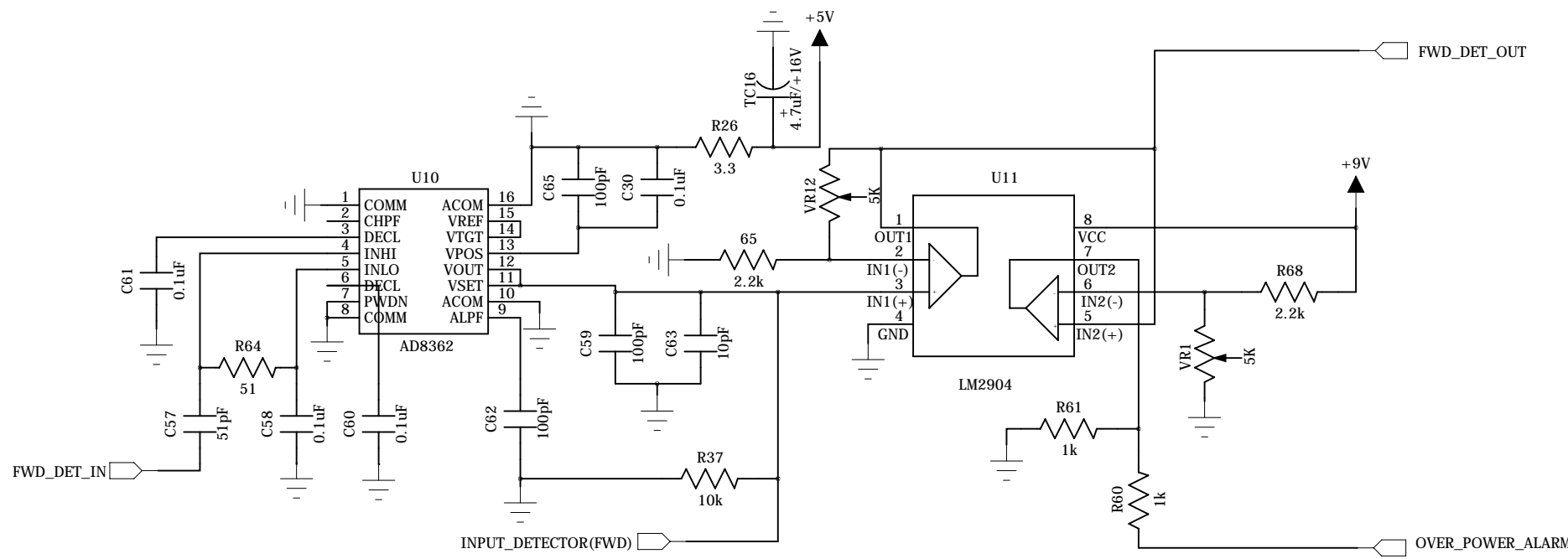


PREPARED	CHECKED	CONFIRMED	APPROVED	TITLE:		
				RFP-2590-27-27 RFHIC		
Designed by: N.I.LEE		Drawn By: N.I.LEE		RF PART		
DATED: 2008.02.29		SIZE: A1		REVISION: 1.0		SHEET: 1 OF 2

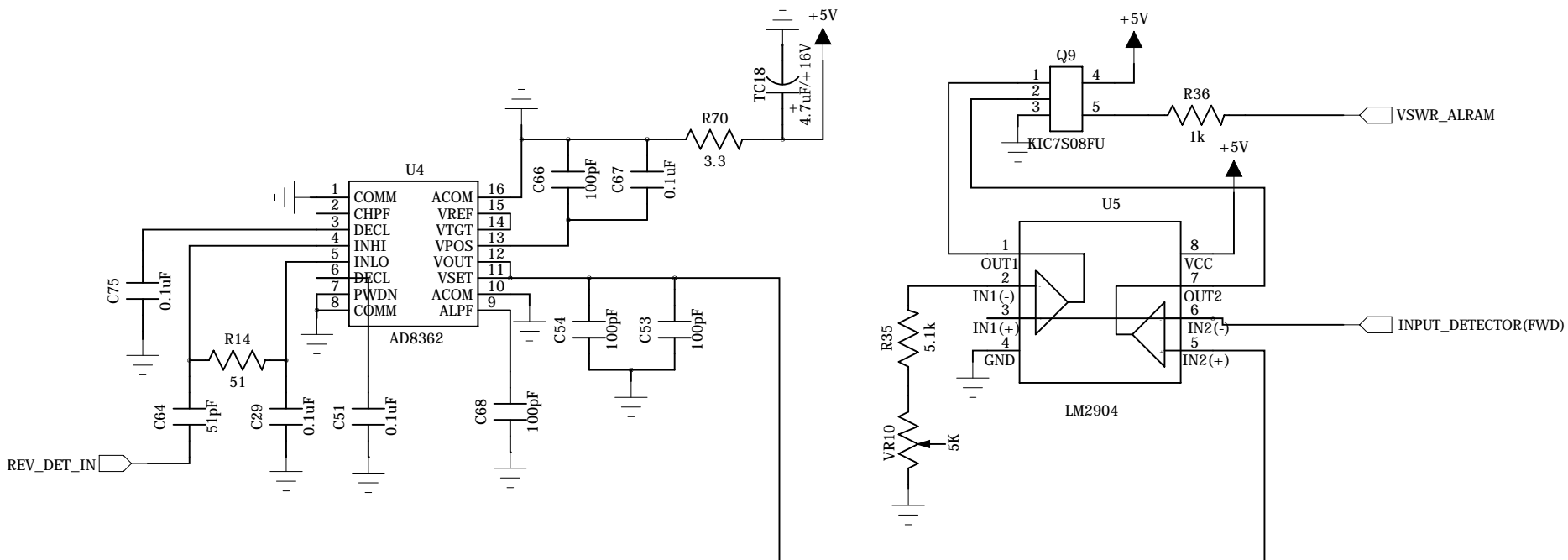
DC PART



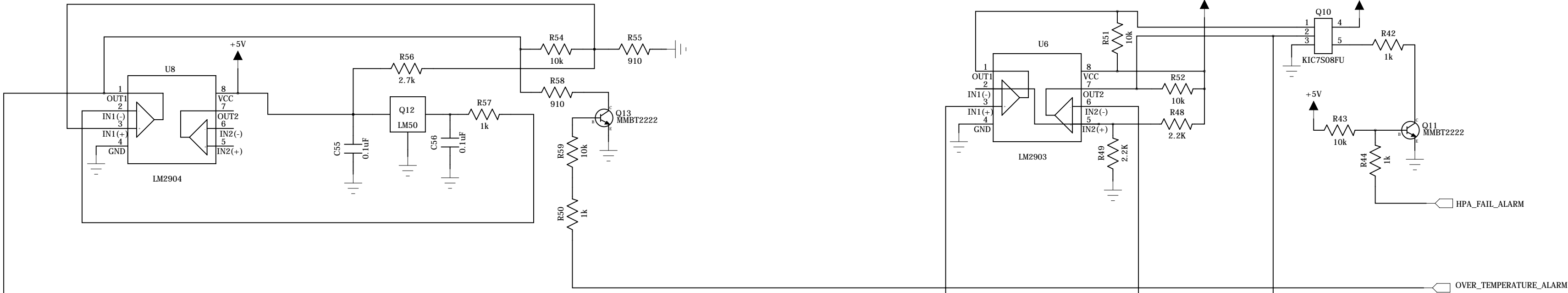
OVER POWER ALARM / FWD DET PART



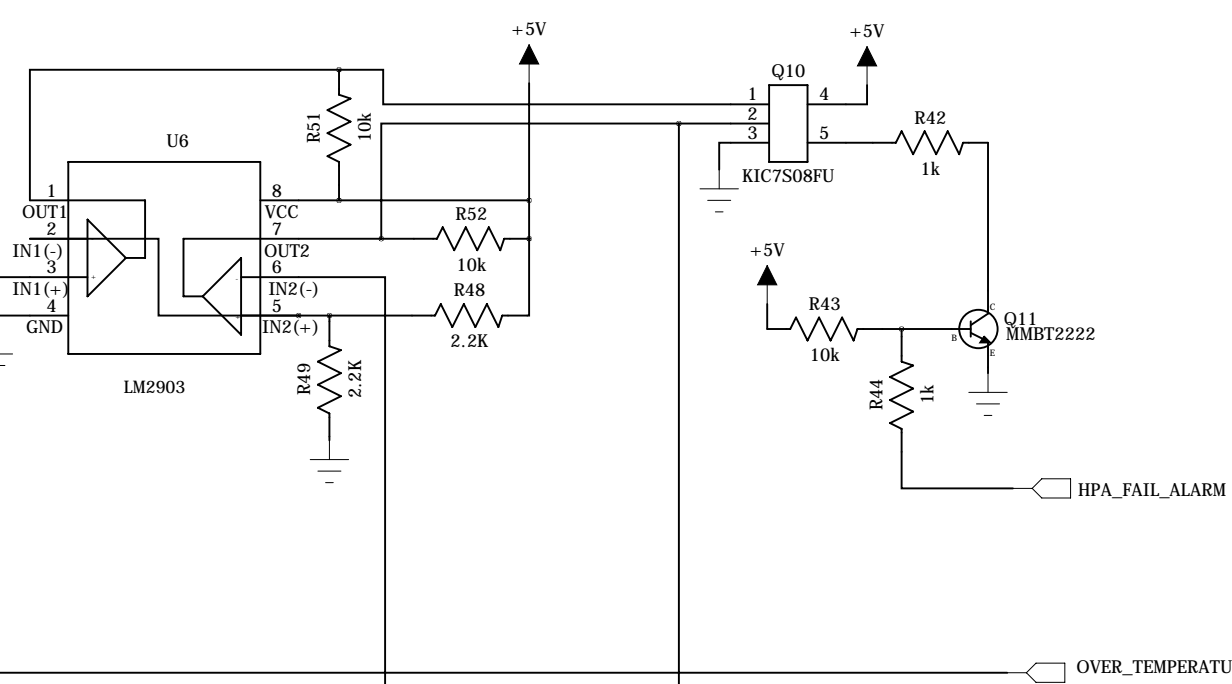
OVER VSWR ALARM PART



OVER TEMPERATURE ALARM PART



HPA FAIL ALARM PART



ENABLE PART

