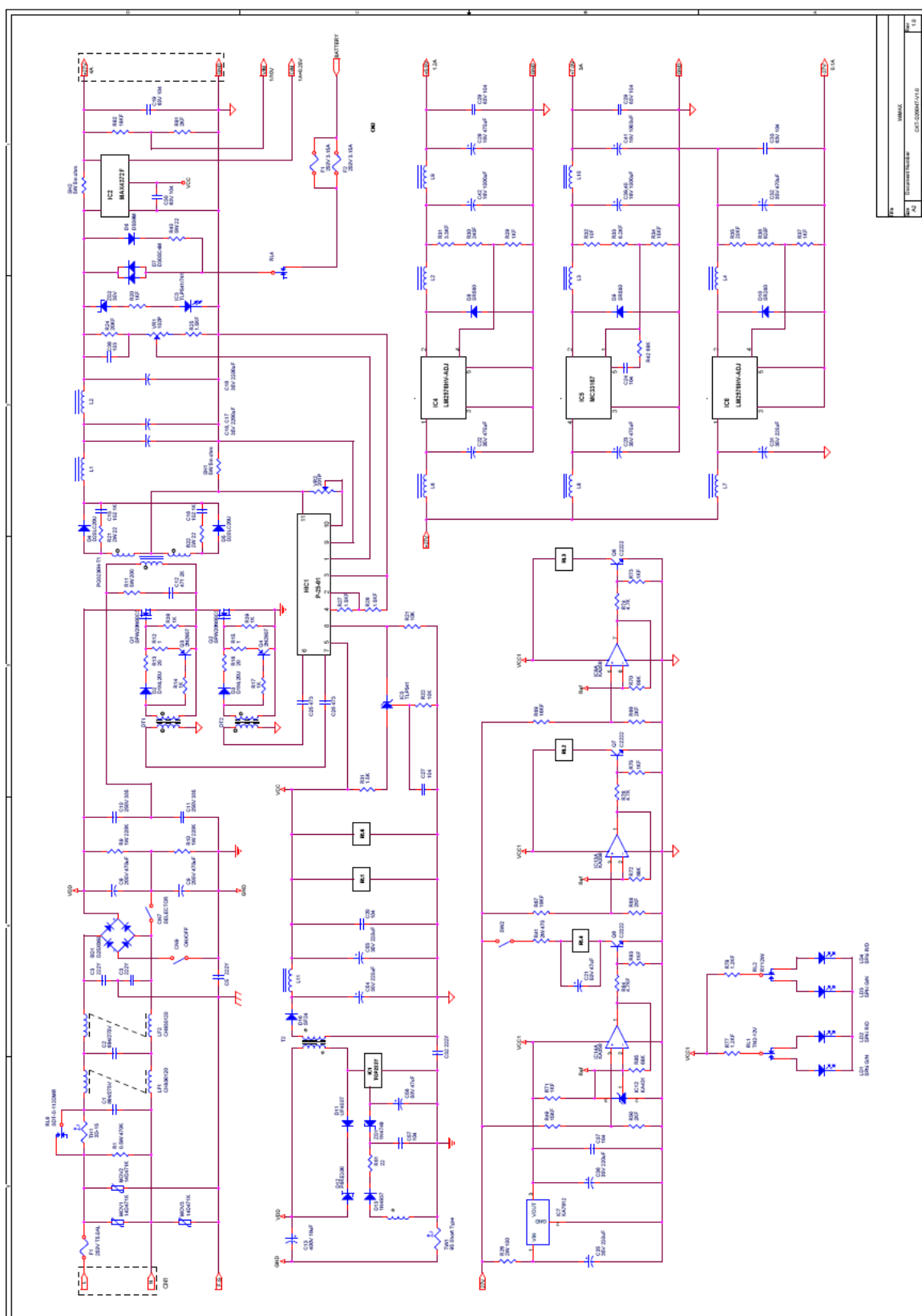
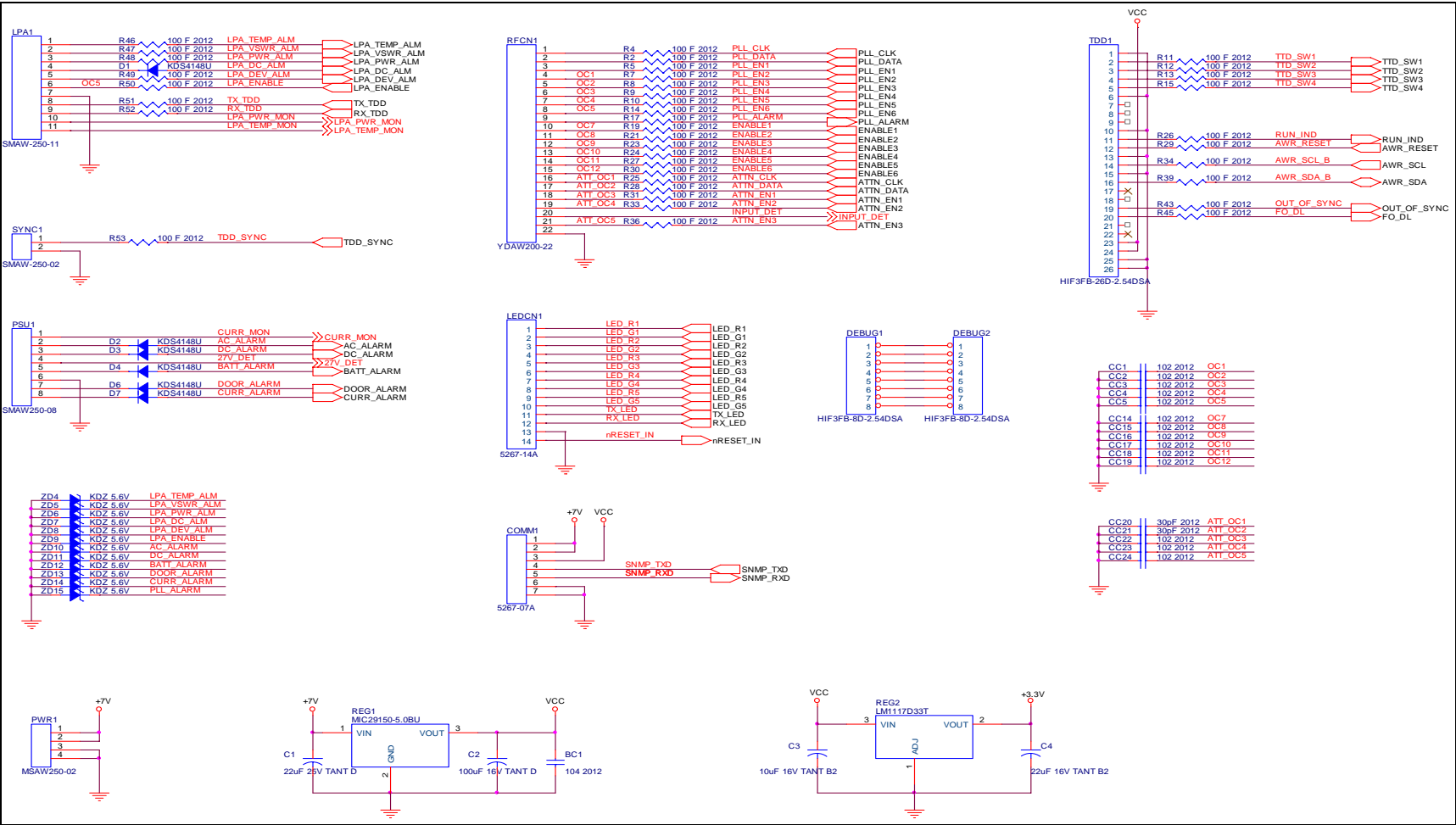
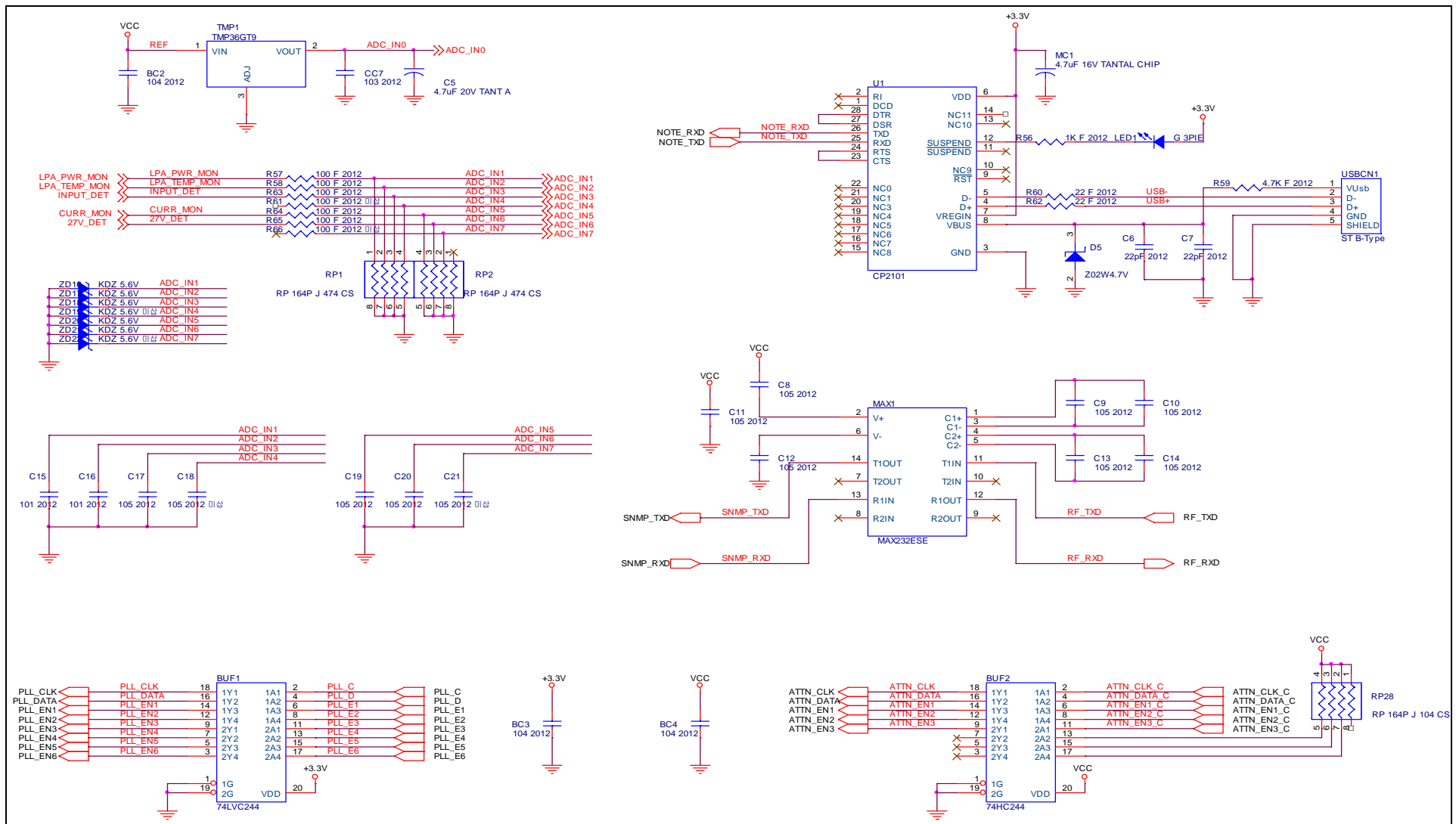


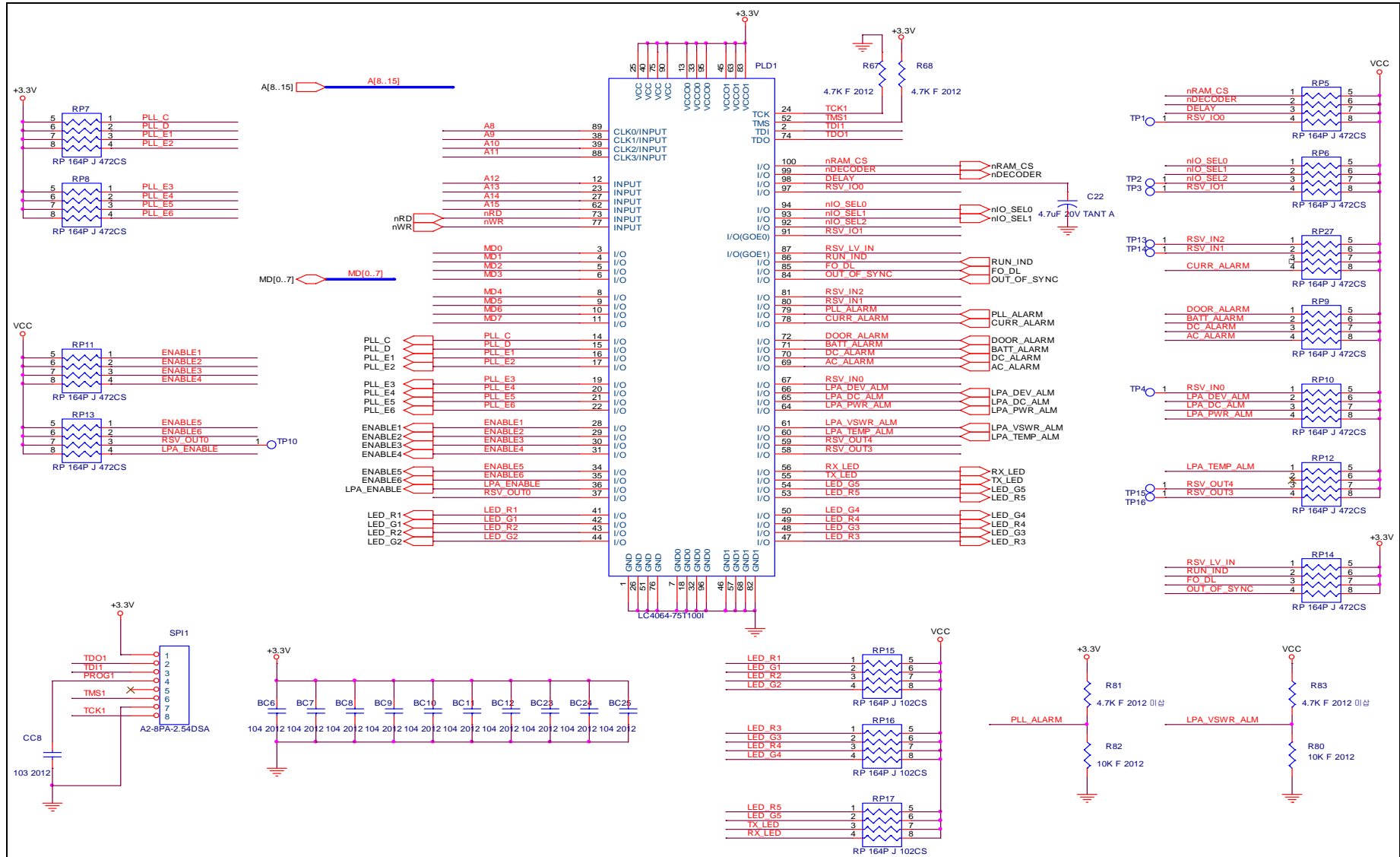
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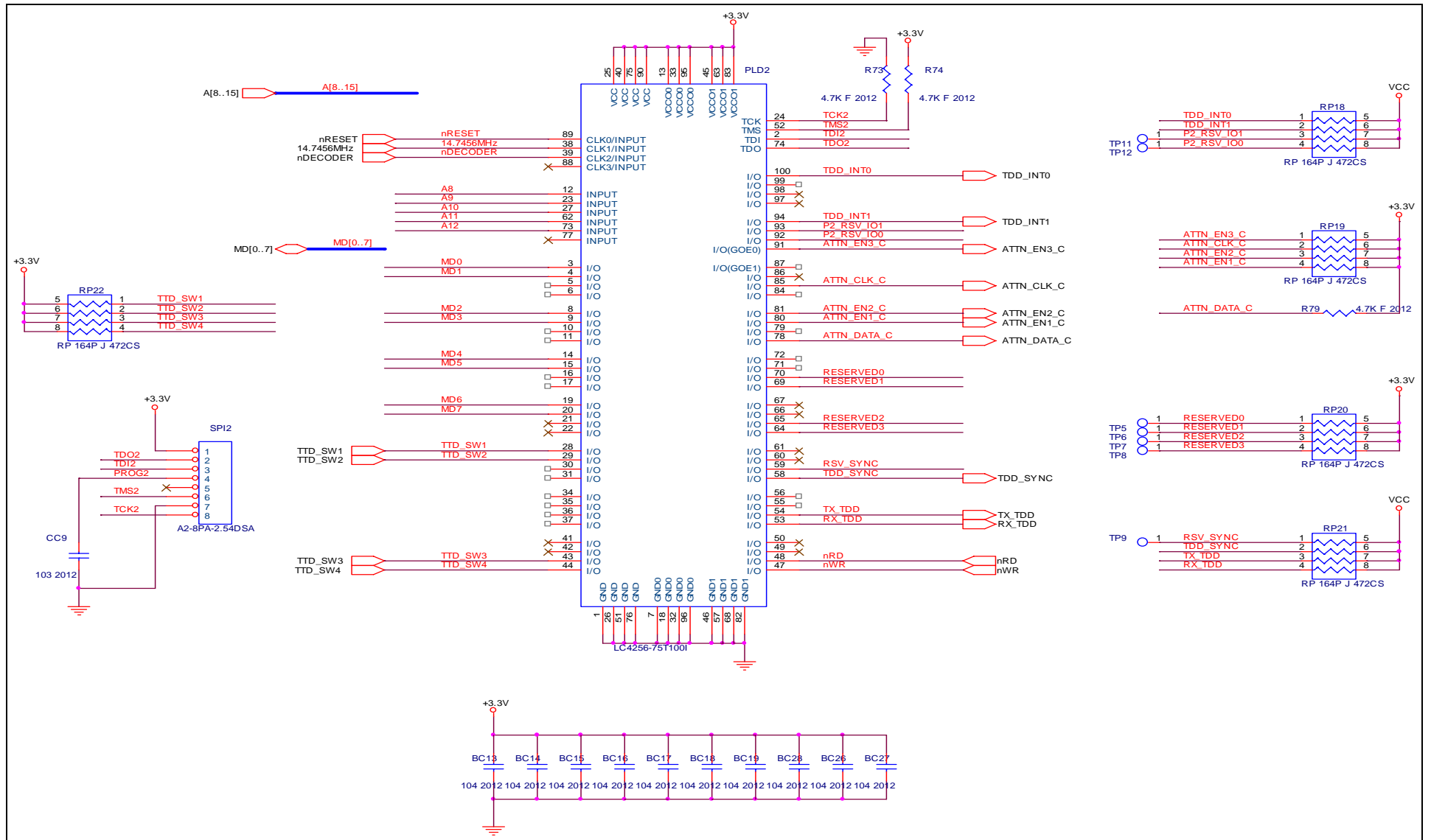


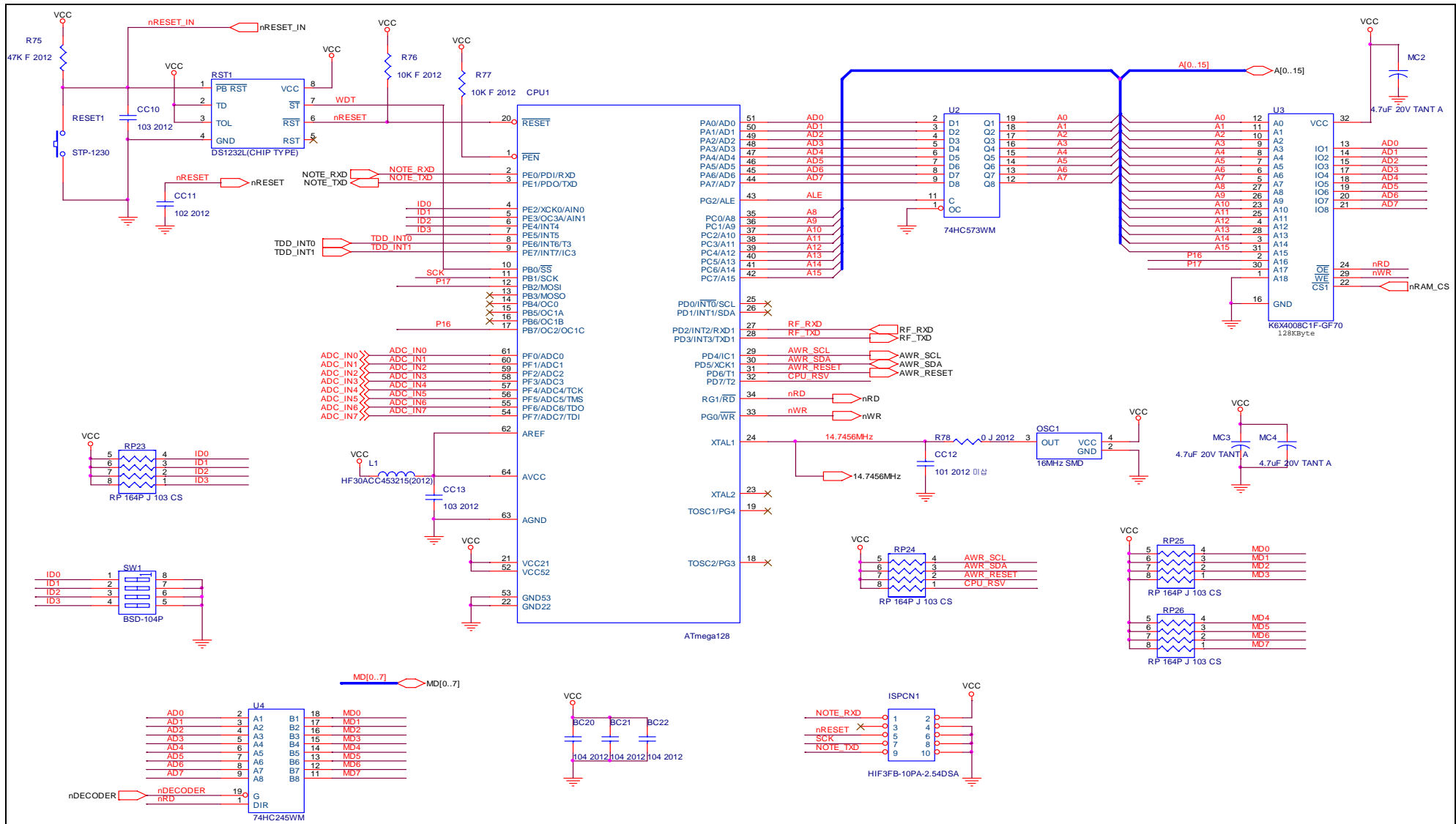
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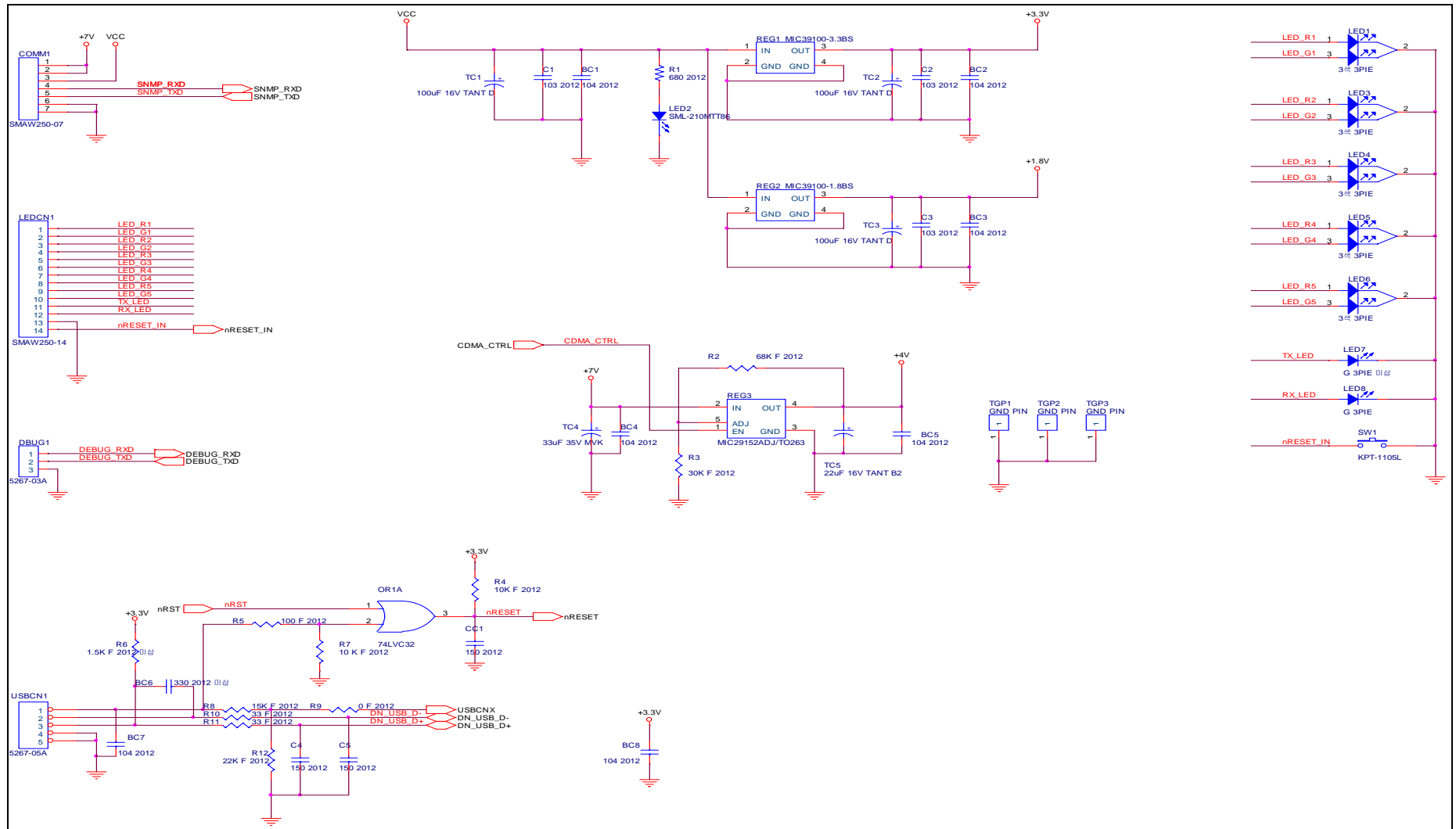


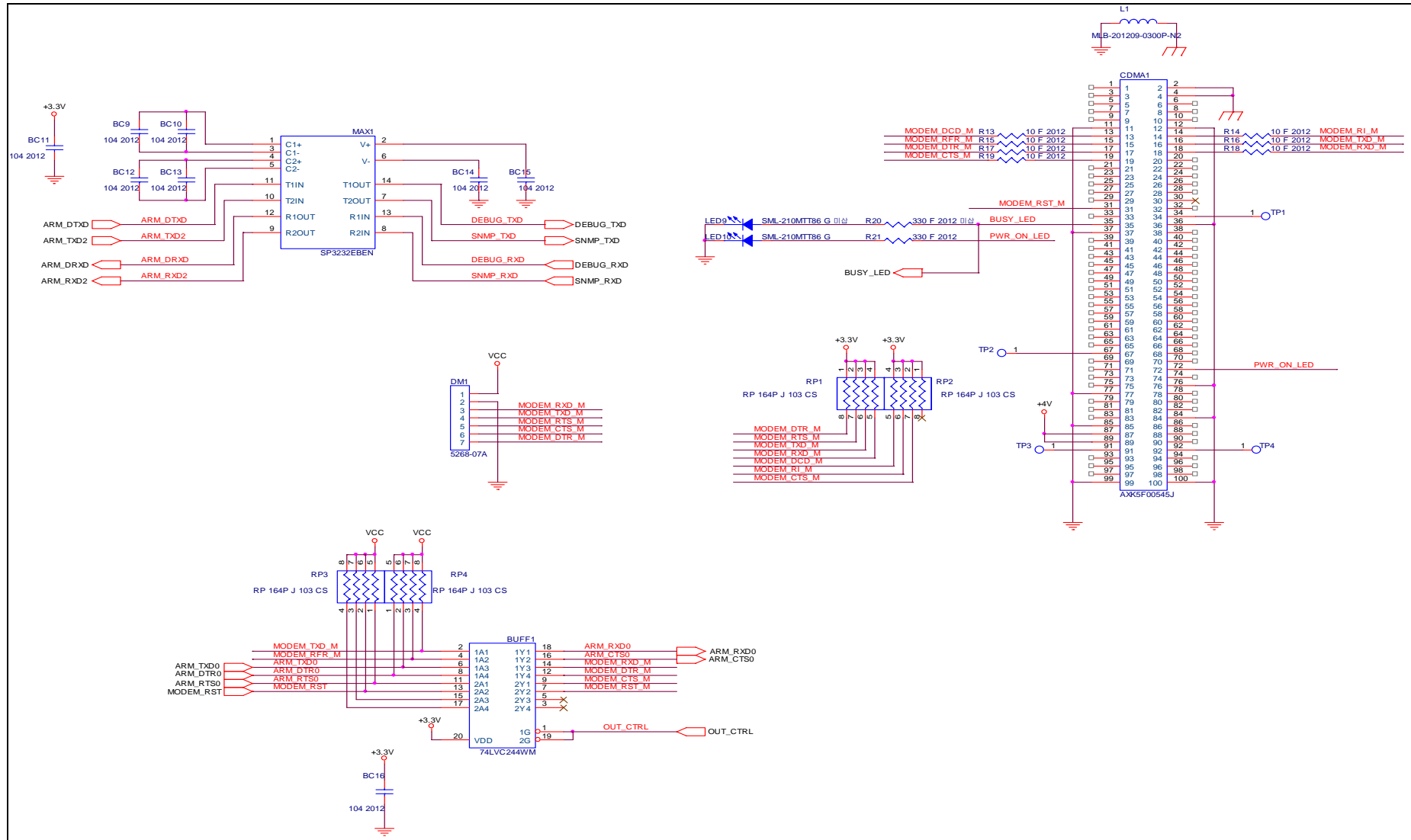




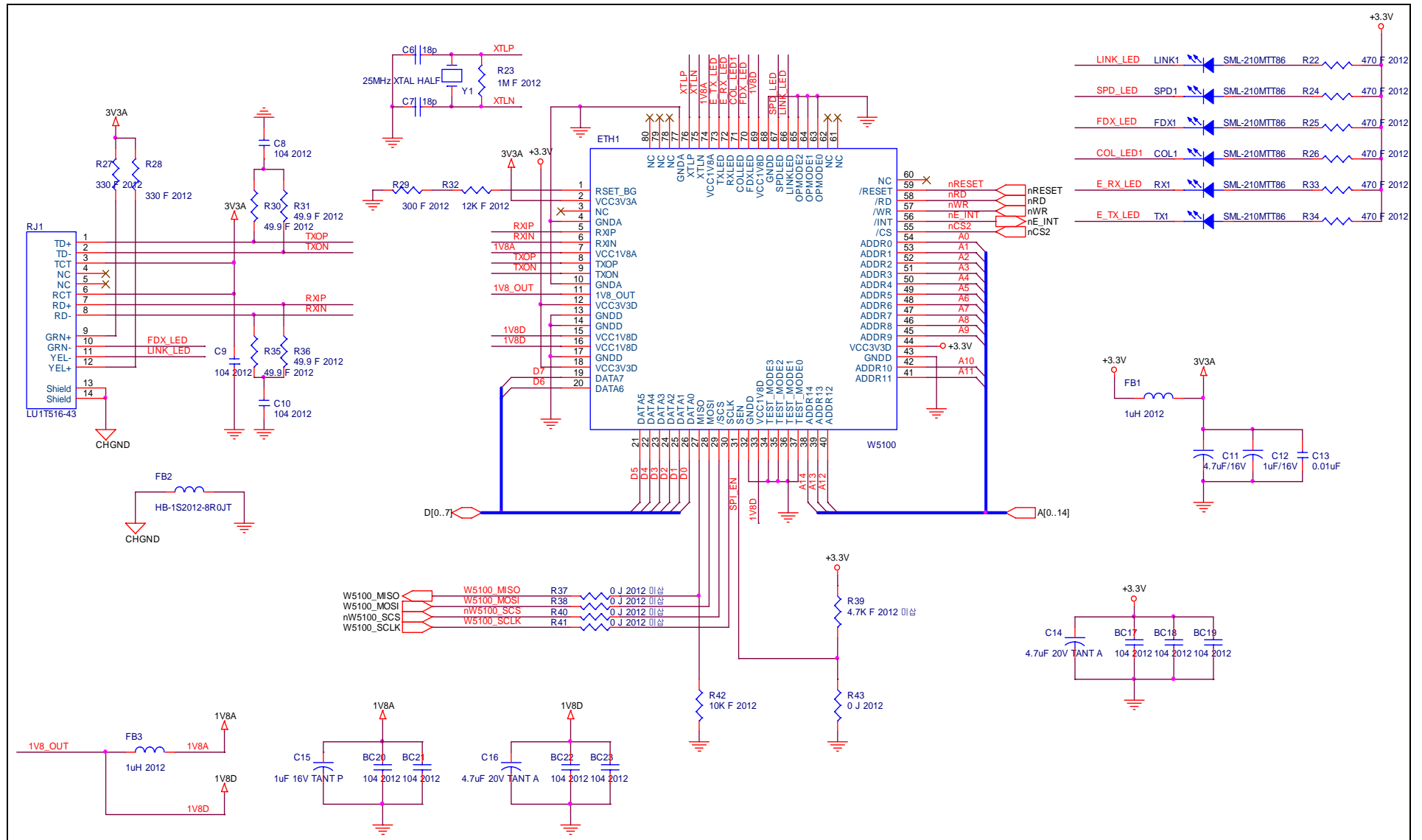


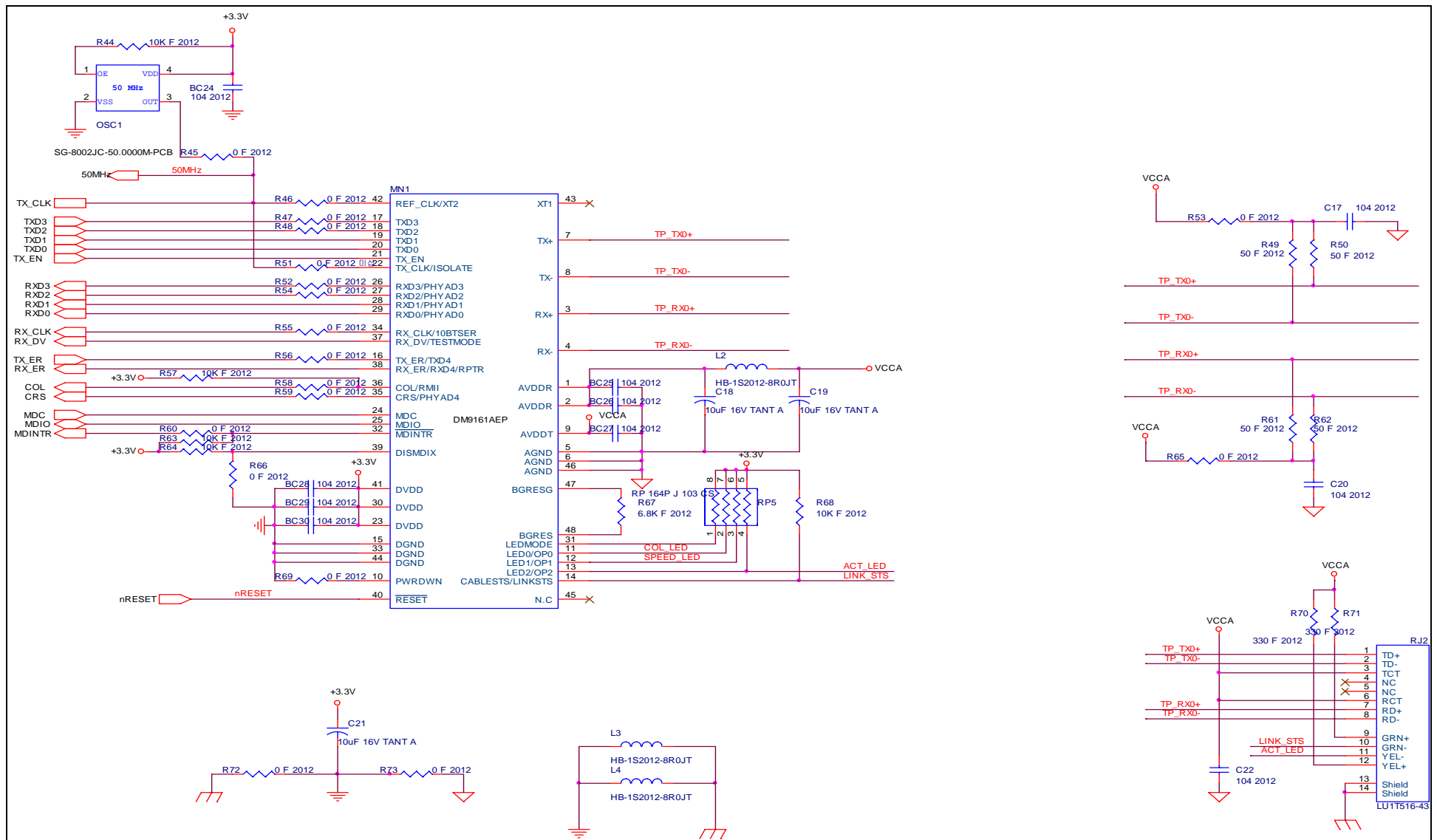


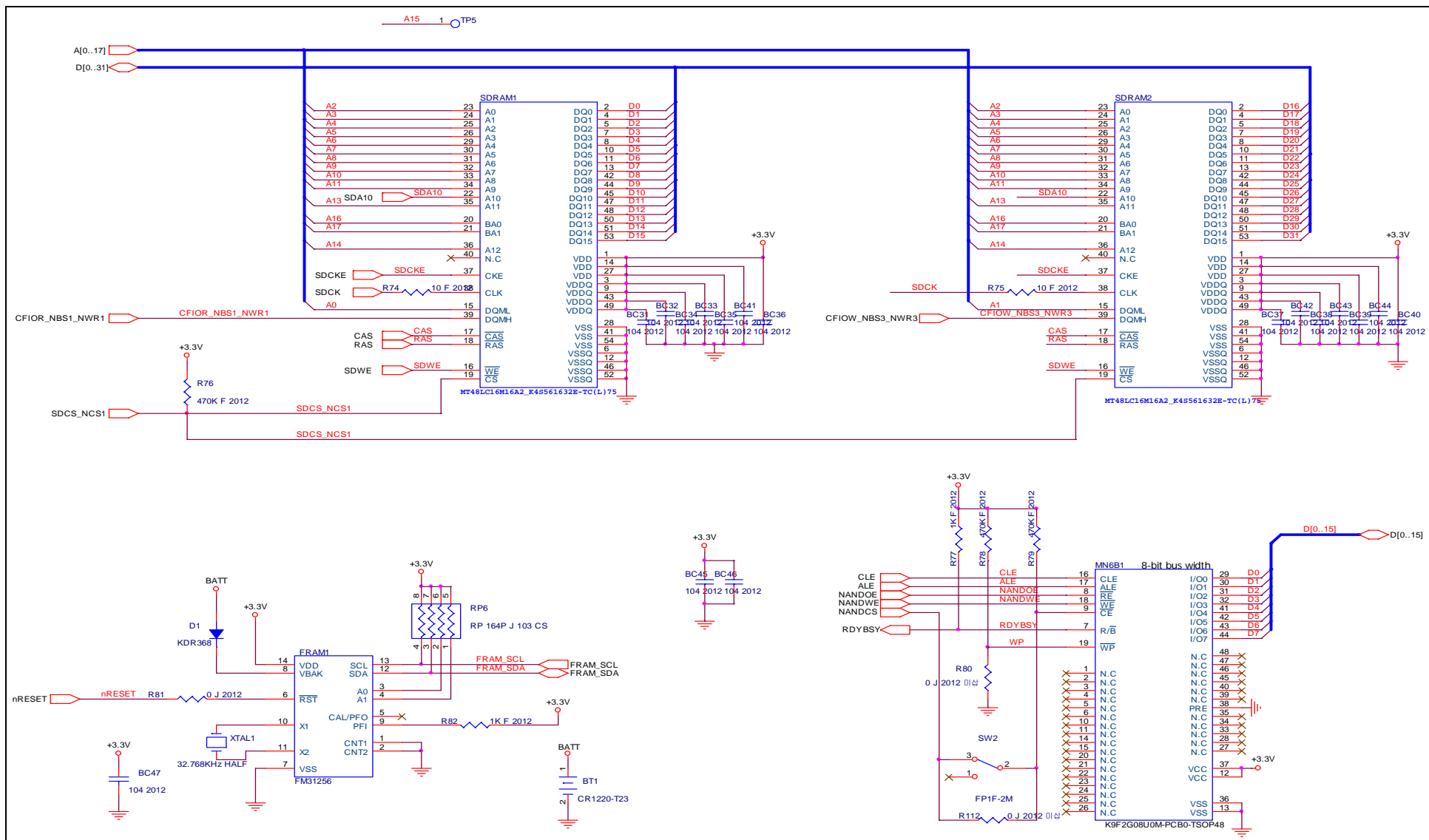






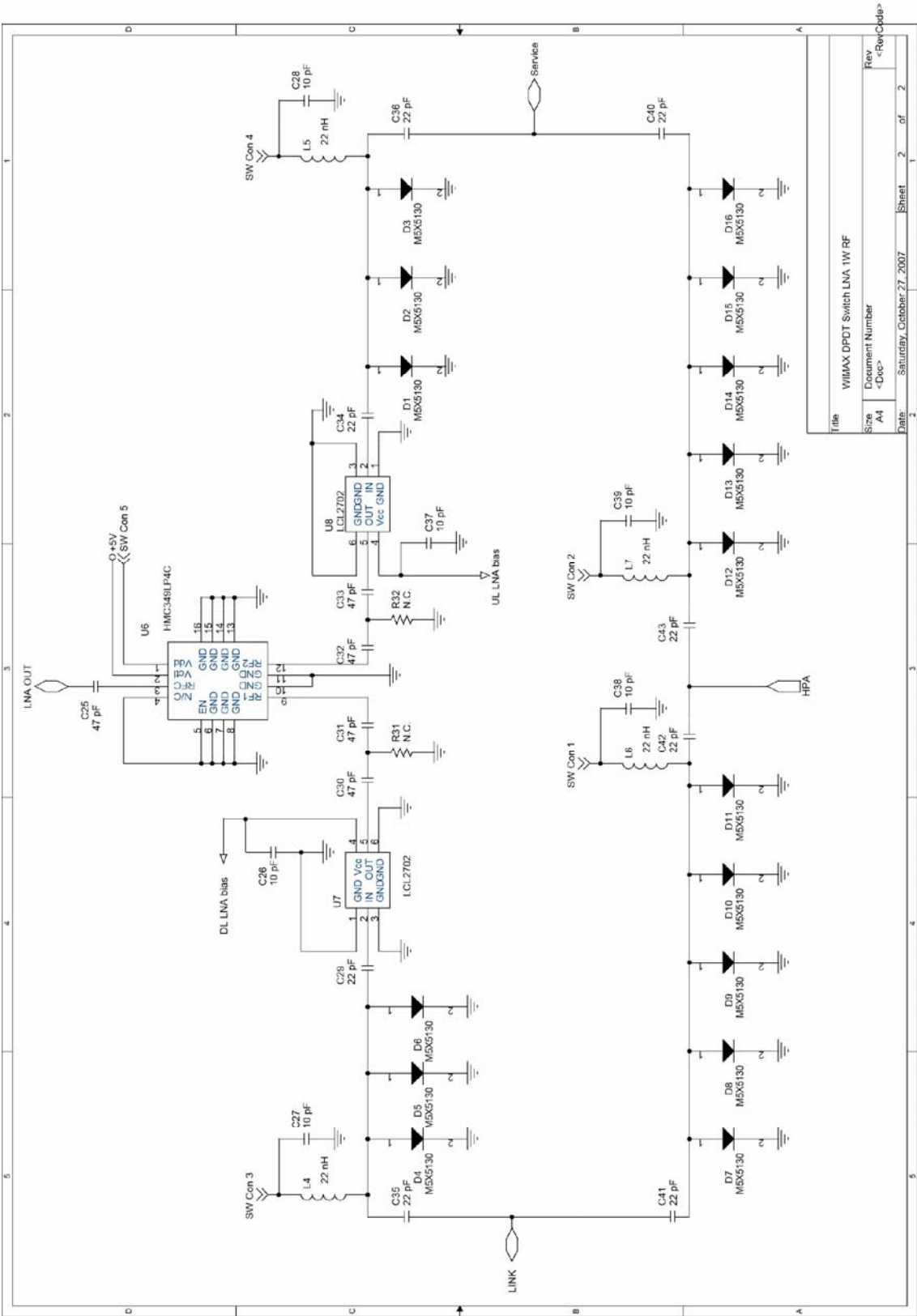




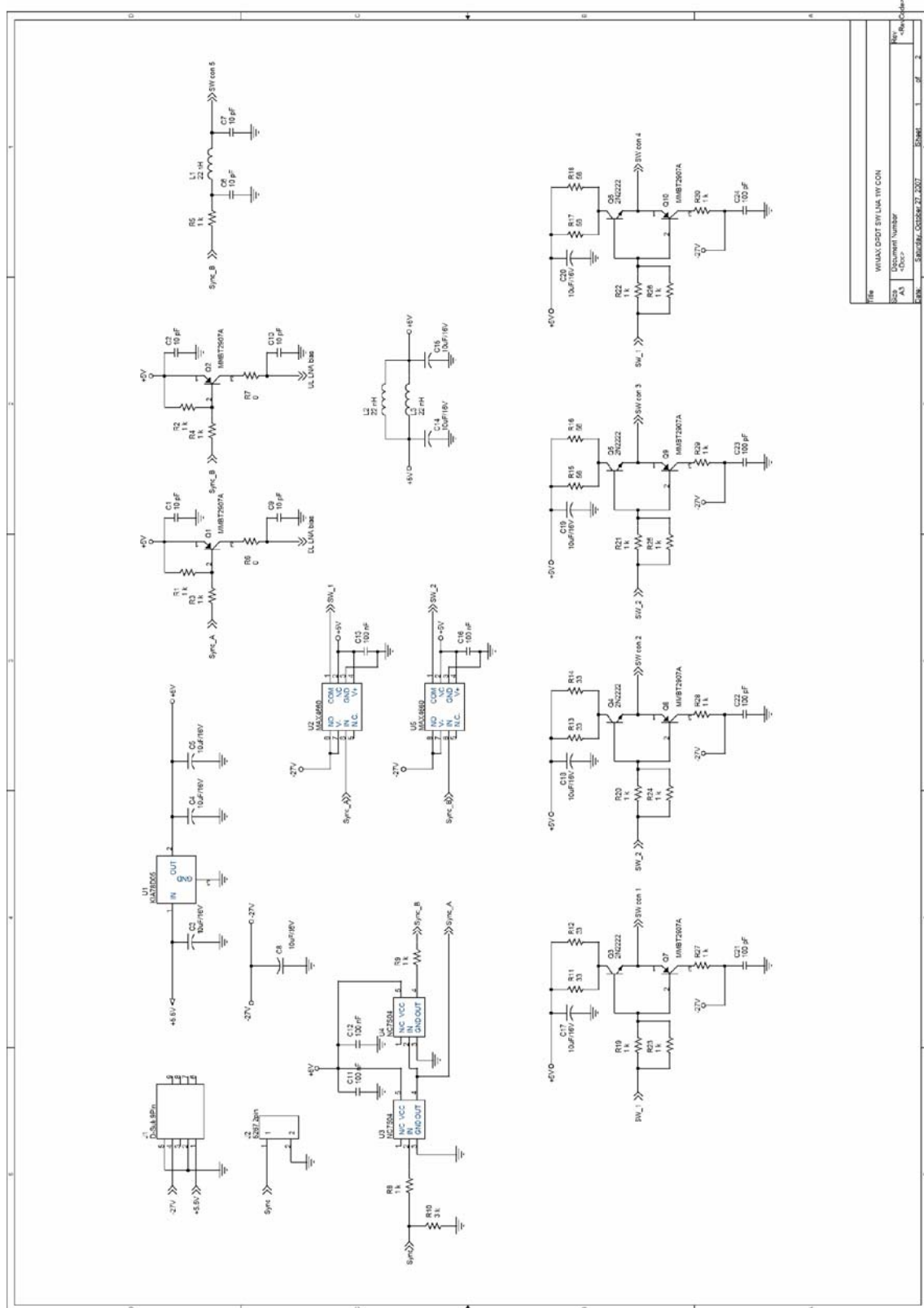




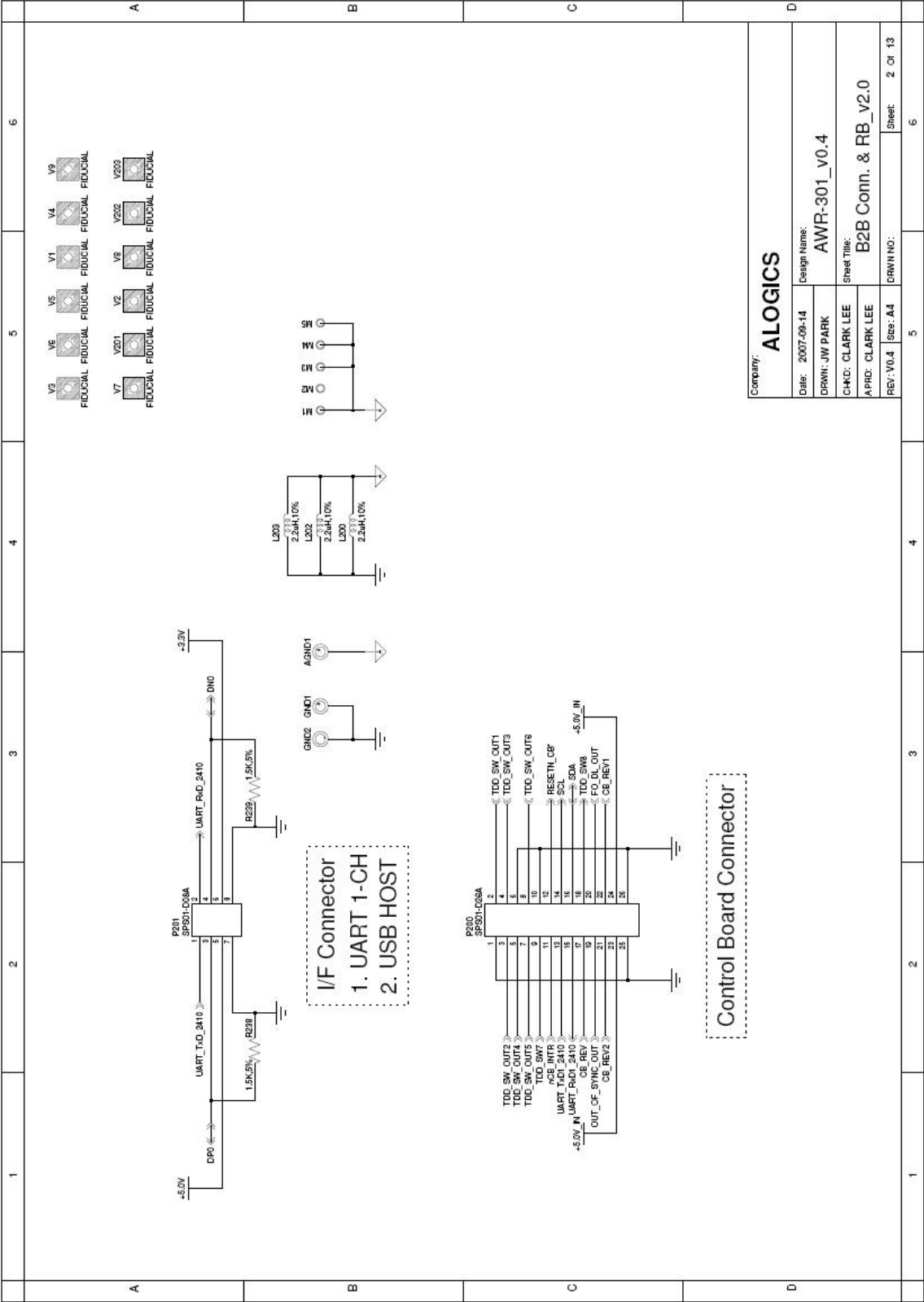
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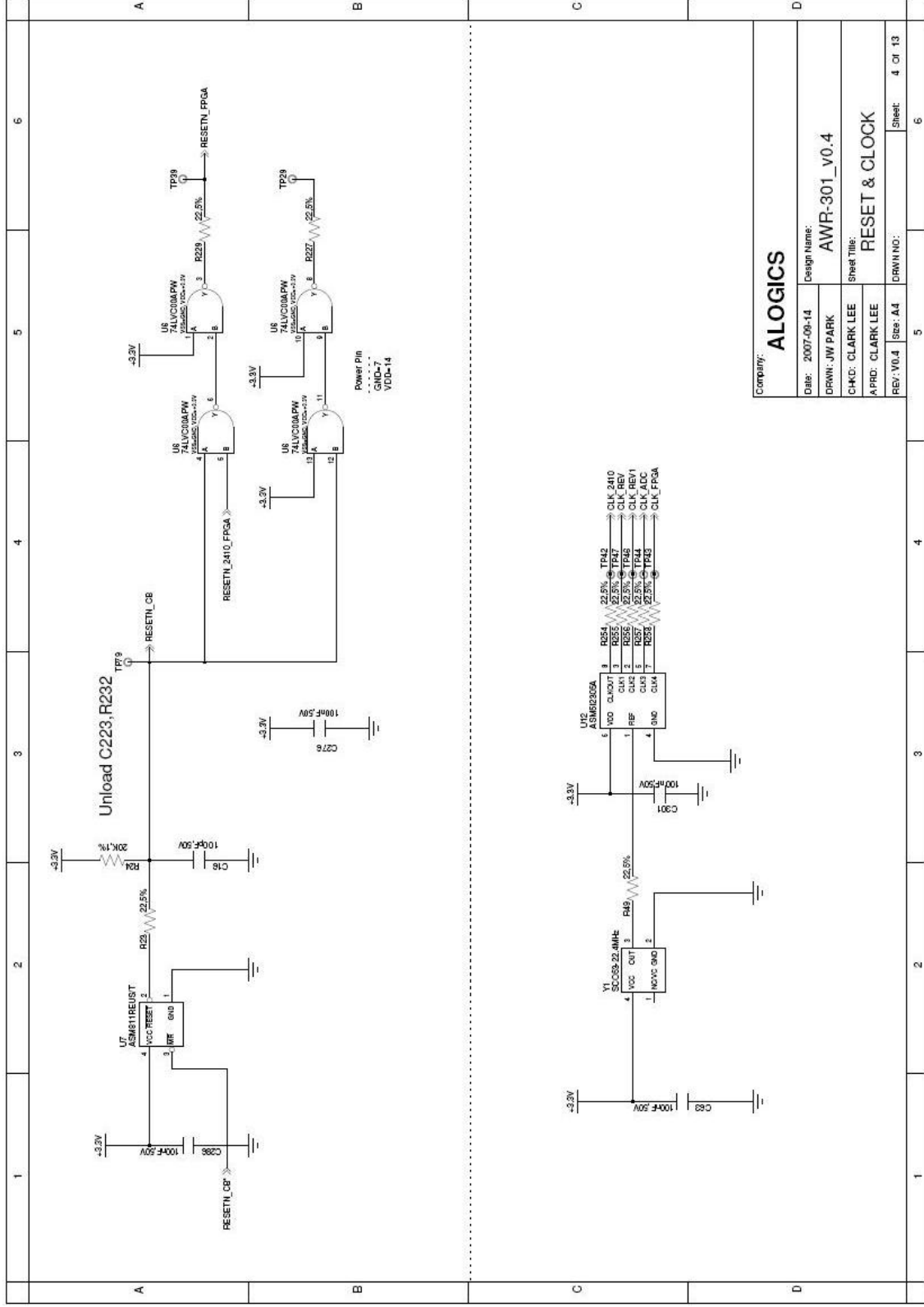


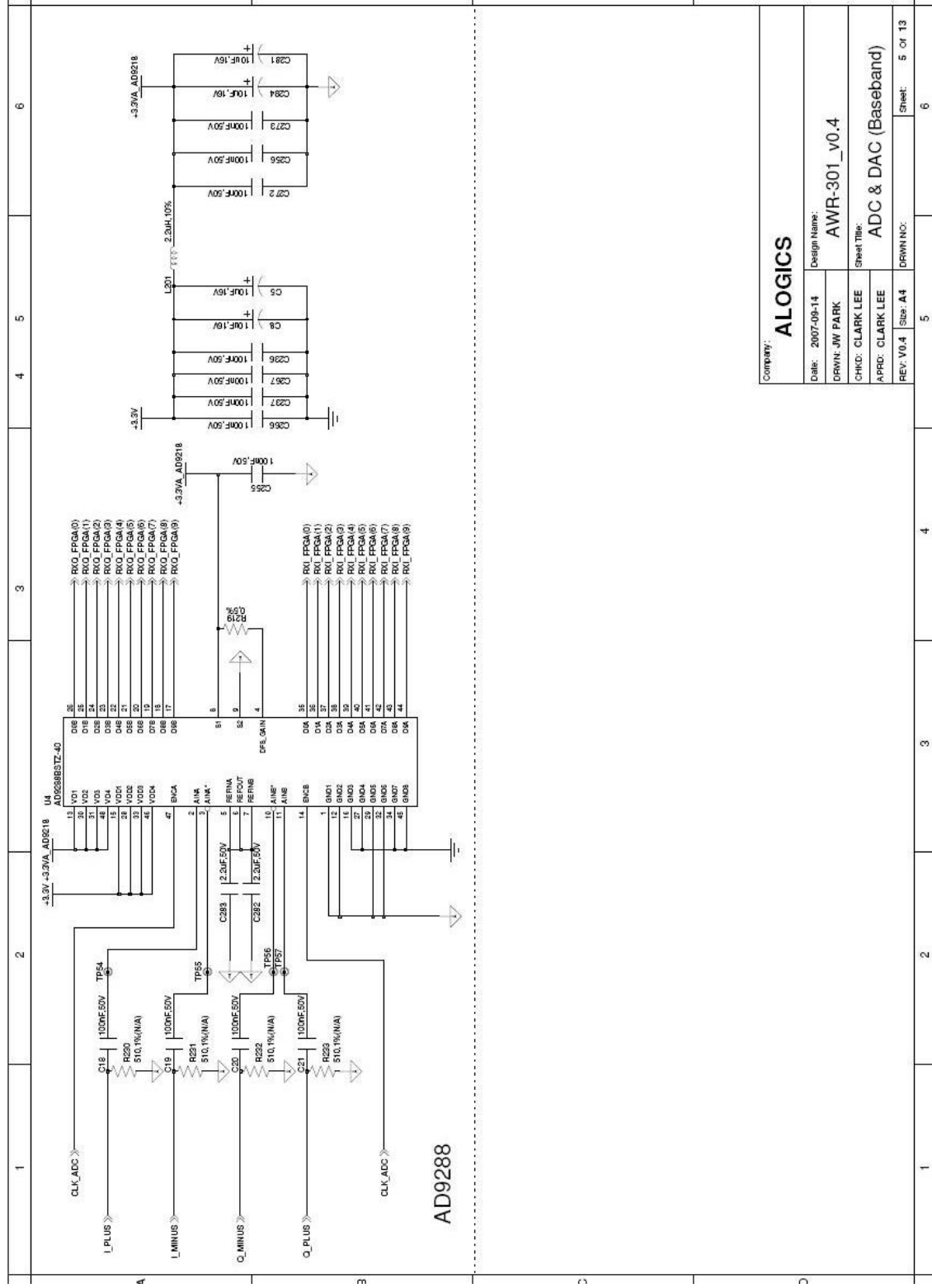
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Date: 2007-09-14

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CHKD: CLARK LEE	Sheet Title:
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Sheet Title:

ADC &amp; DAC (Baseband)

APPRO: CI ABK I R

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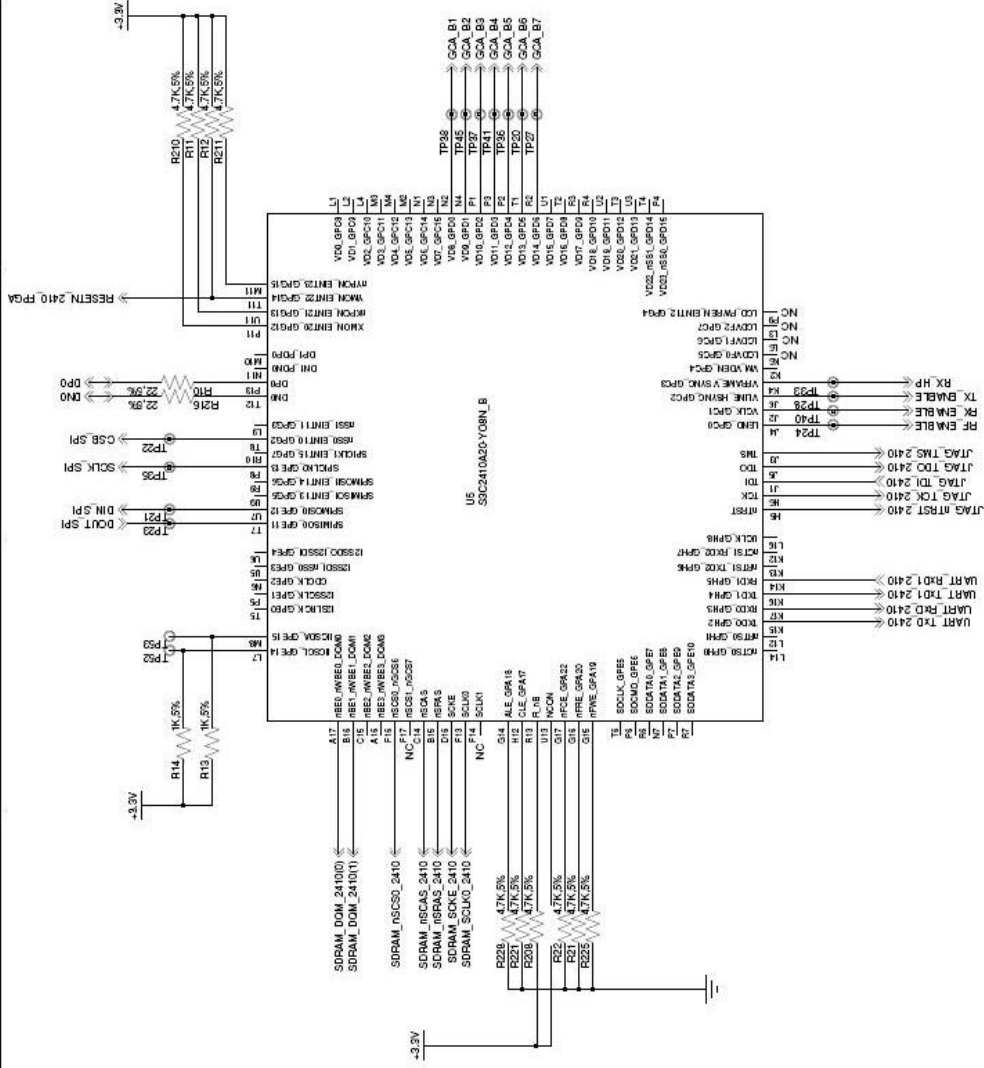
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5



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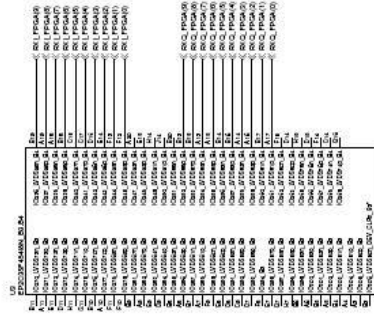


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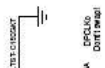
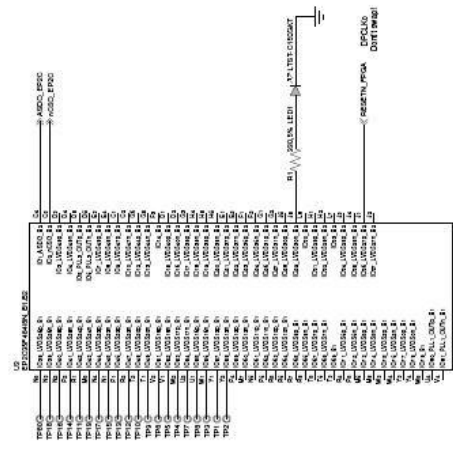
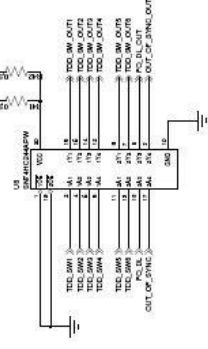
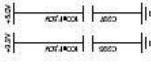
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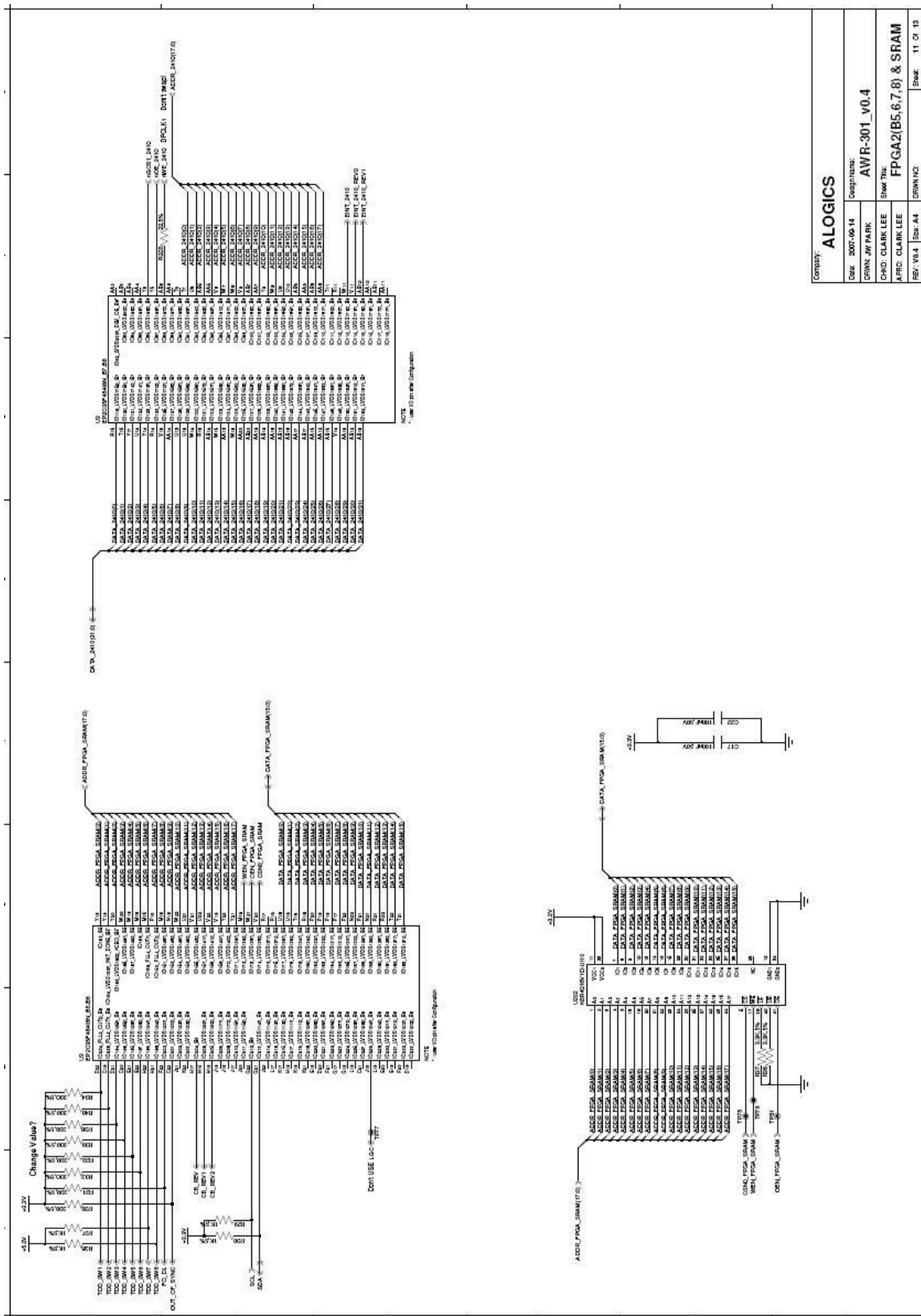


NOTE:  
\*user I/O pin configuration

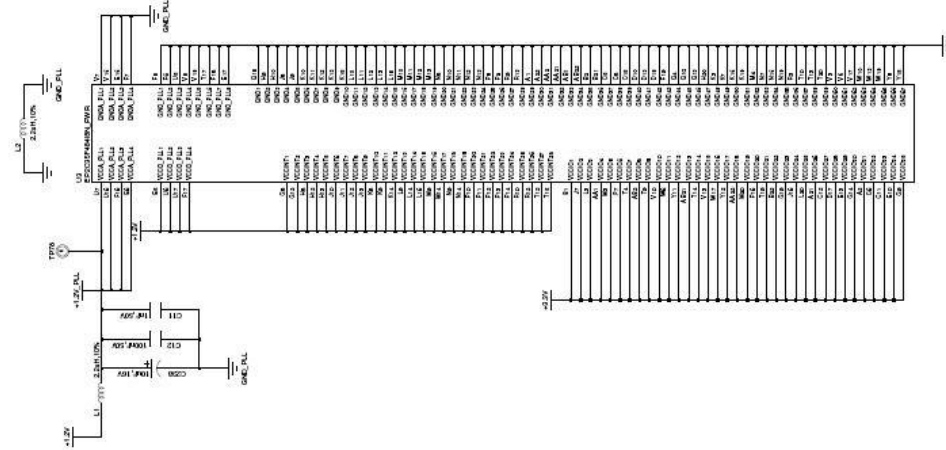
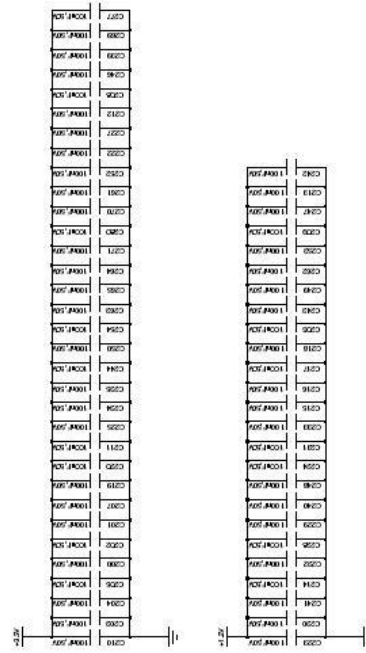


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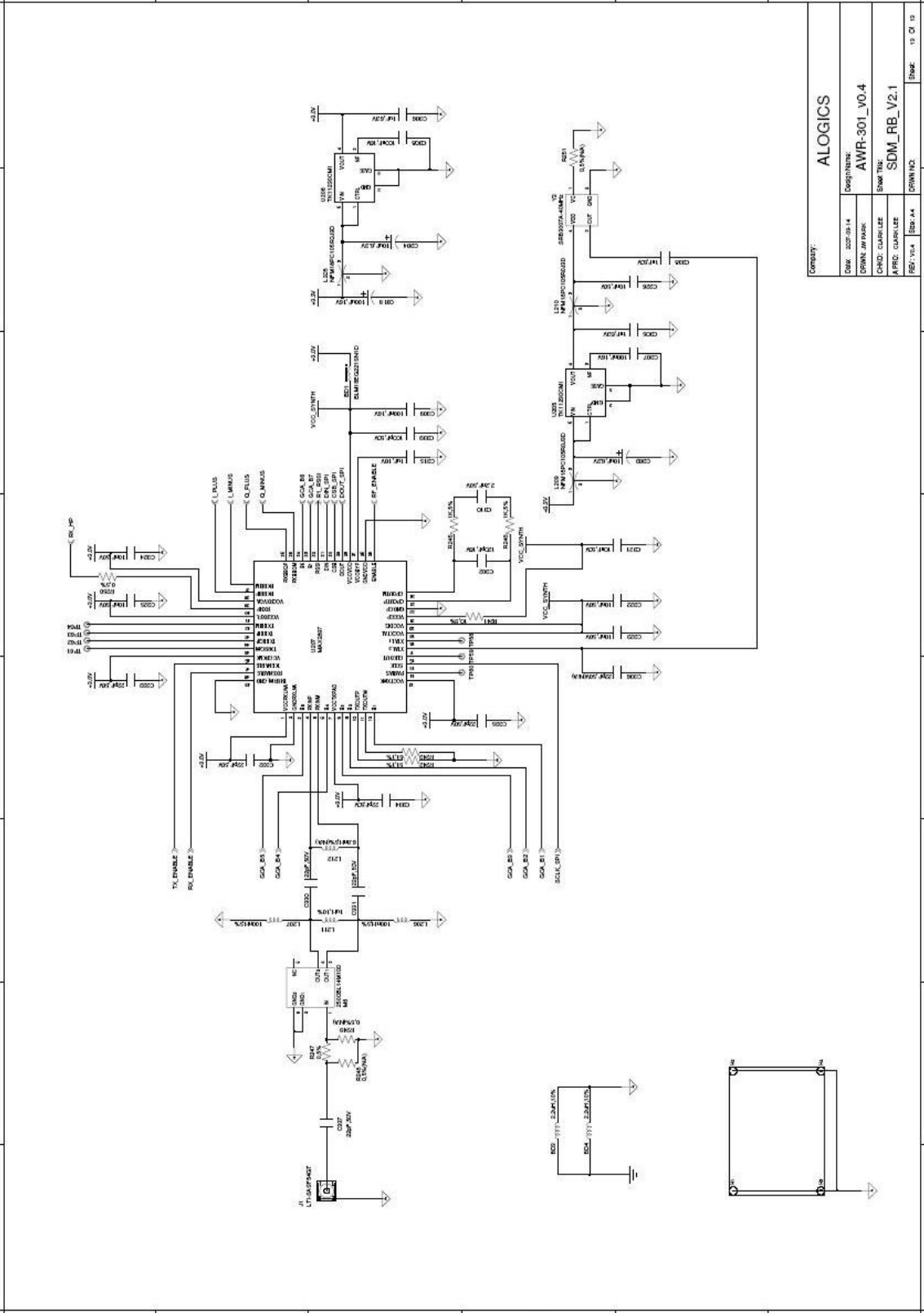
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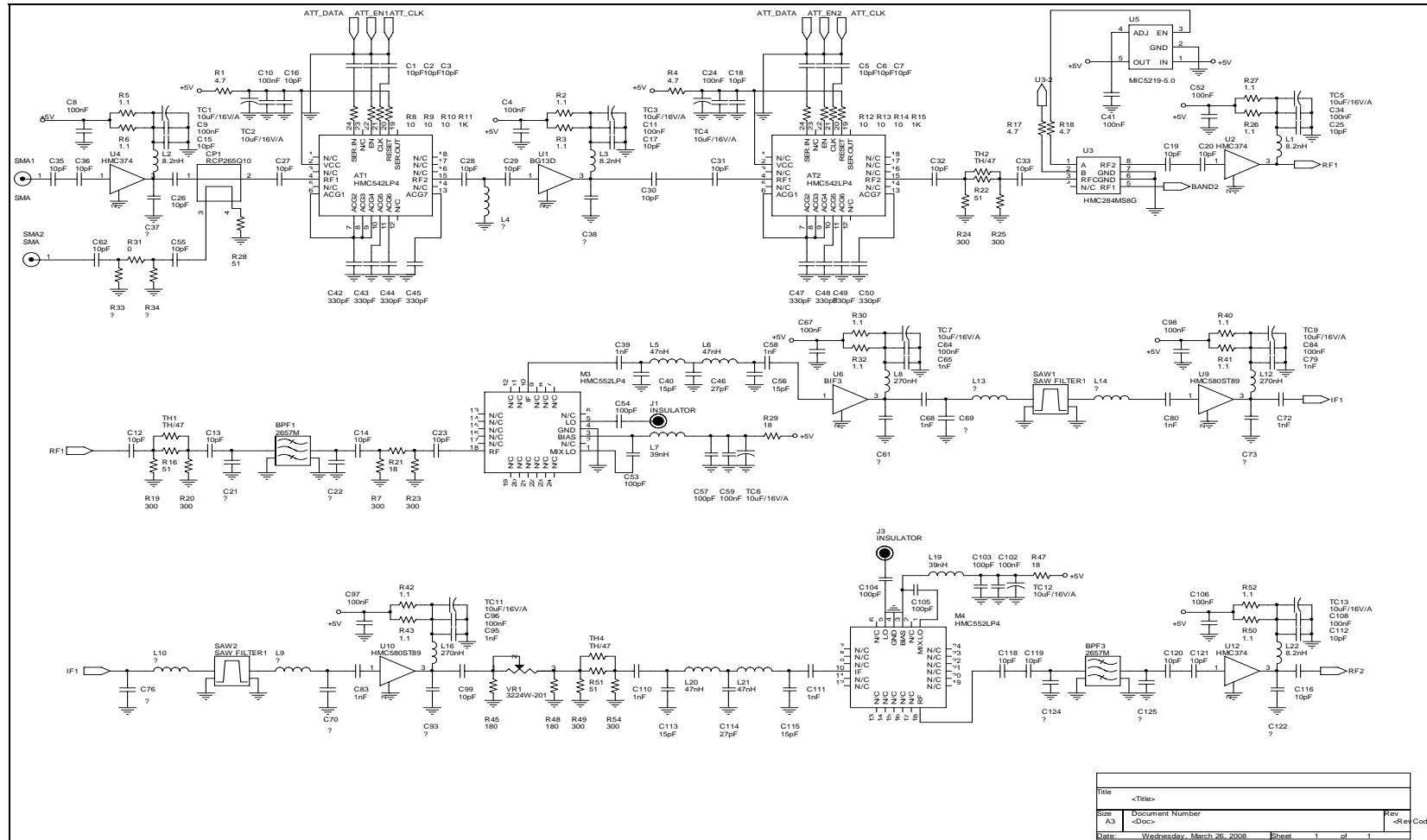
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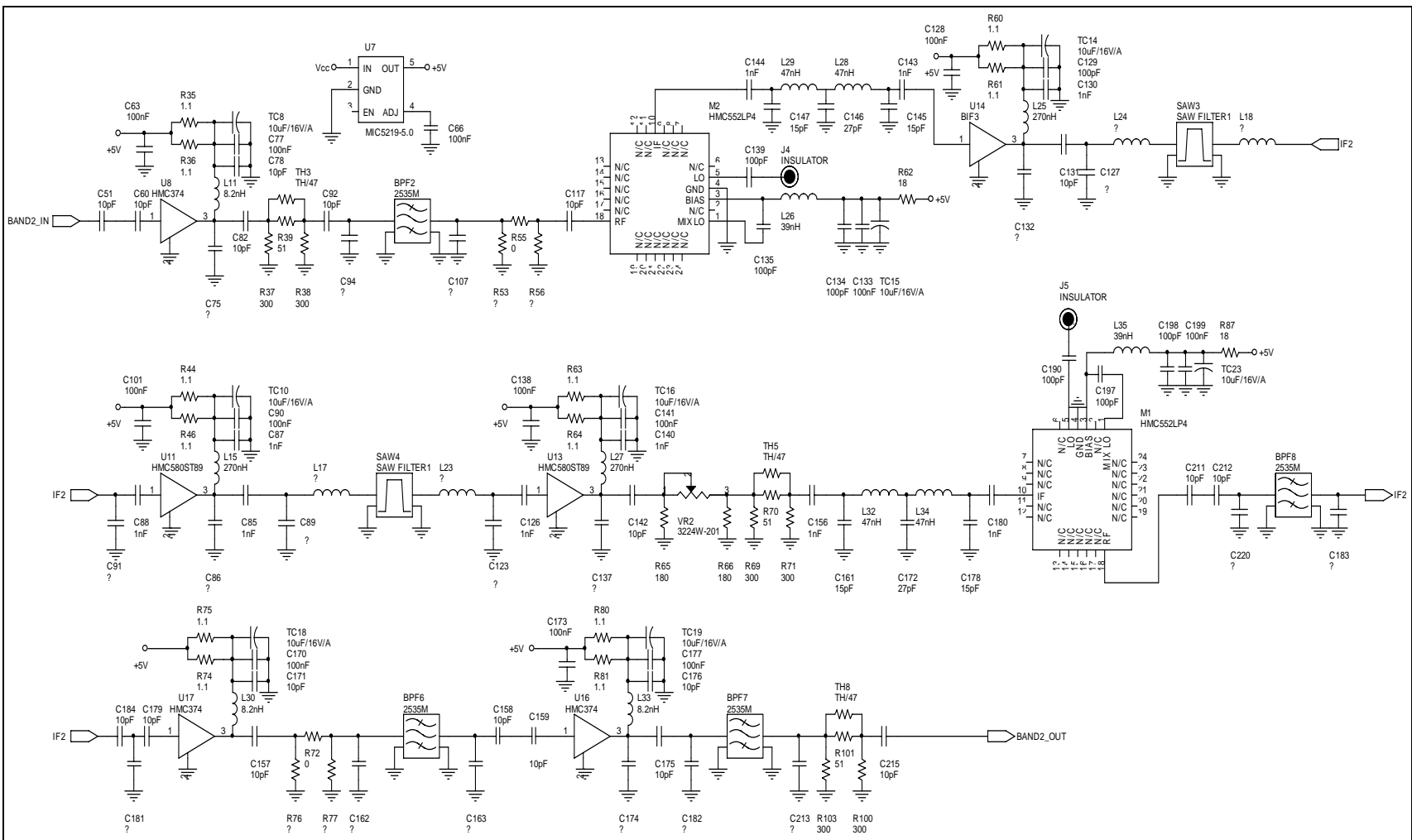
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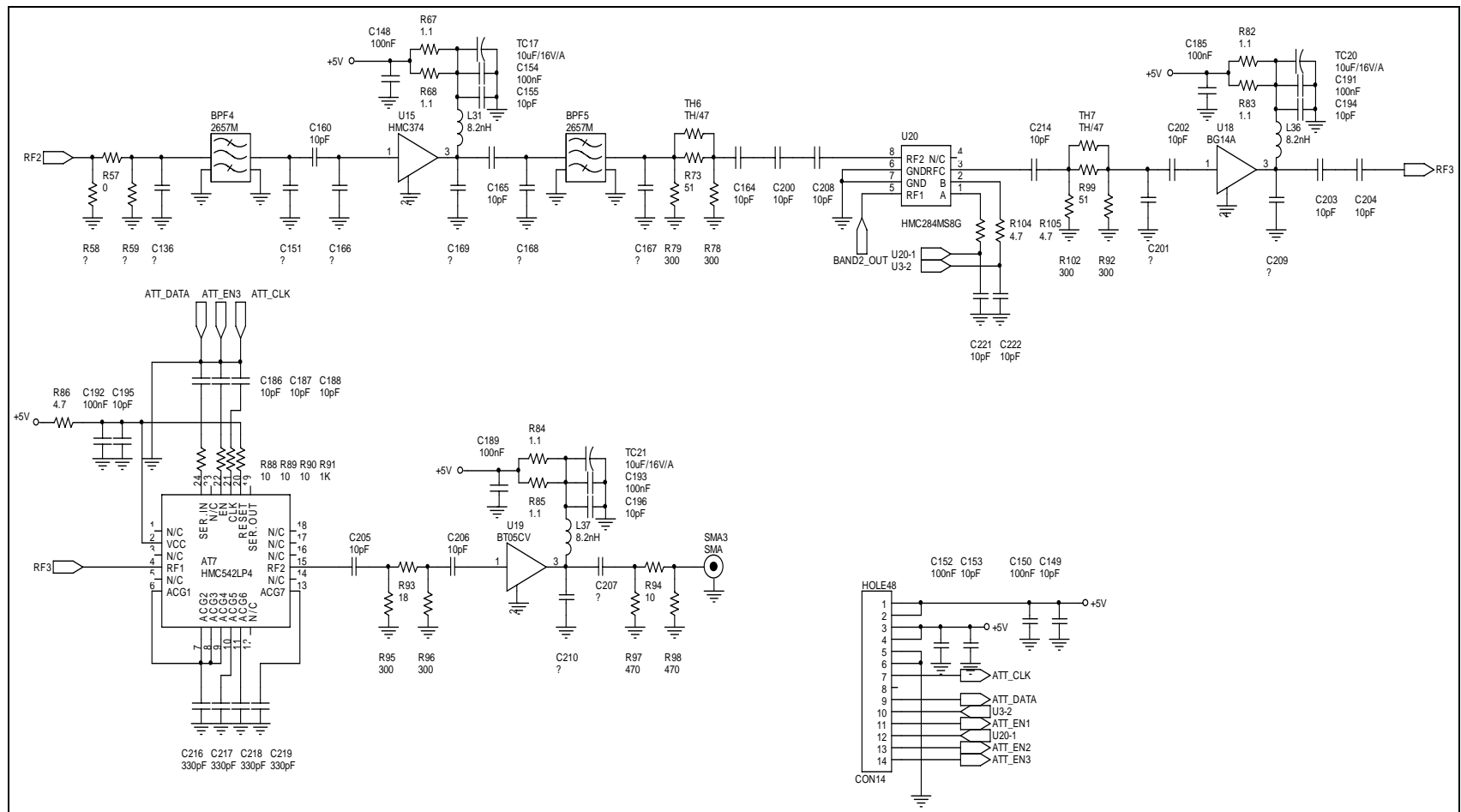


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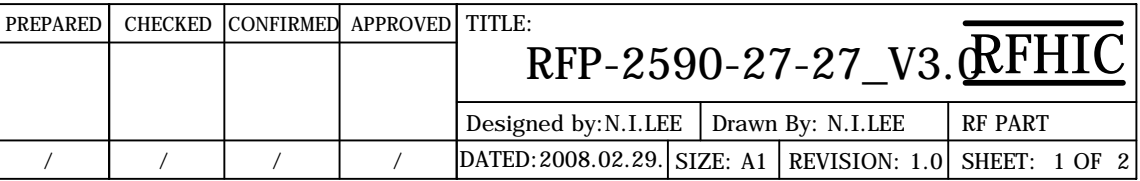




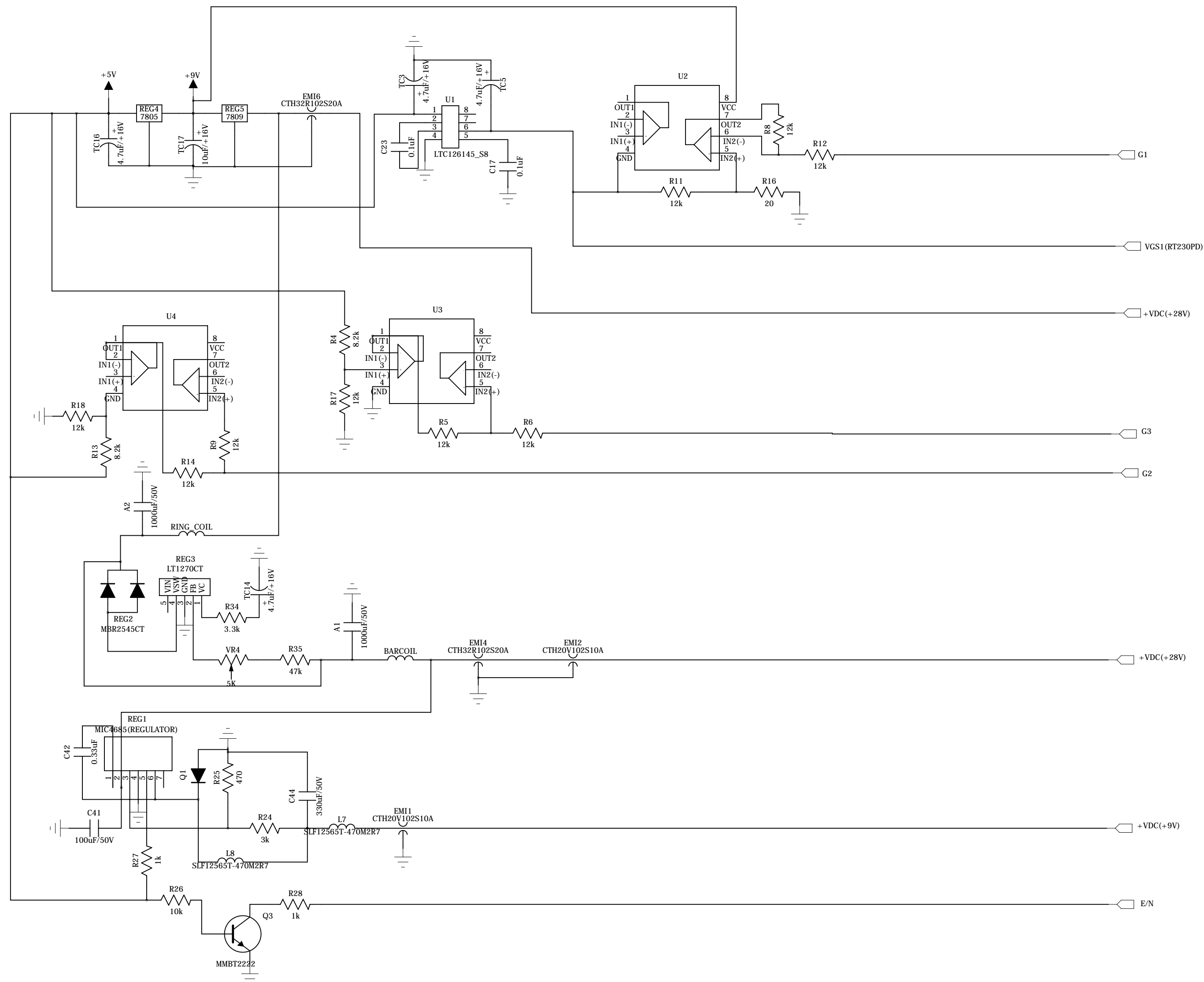




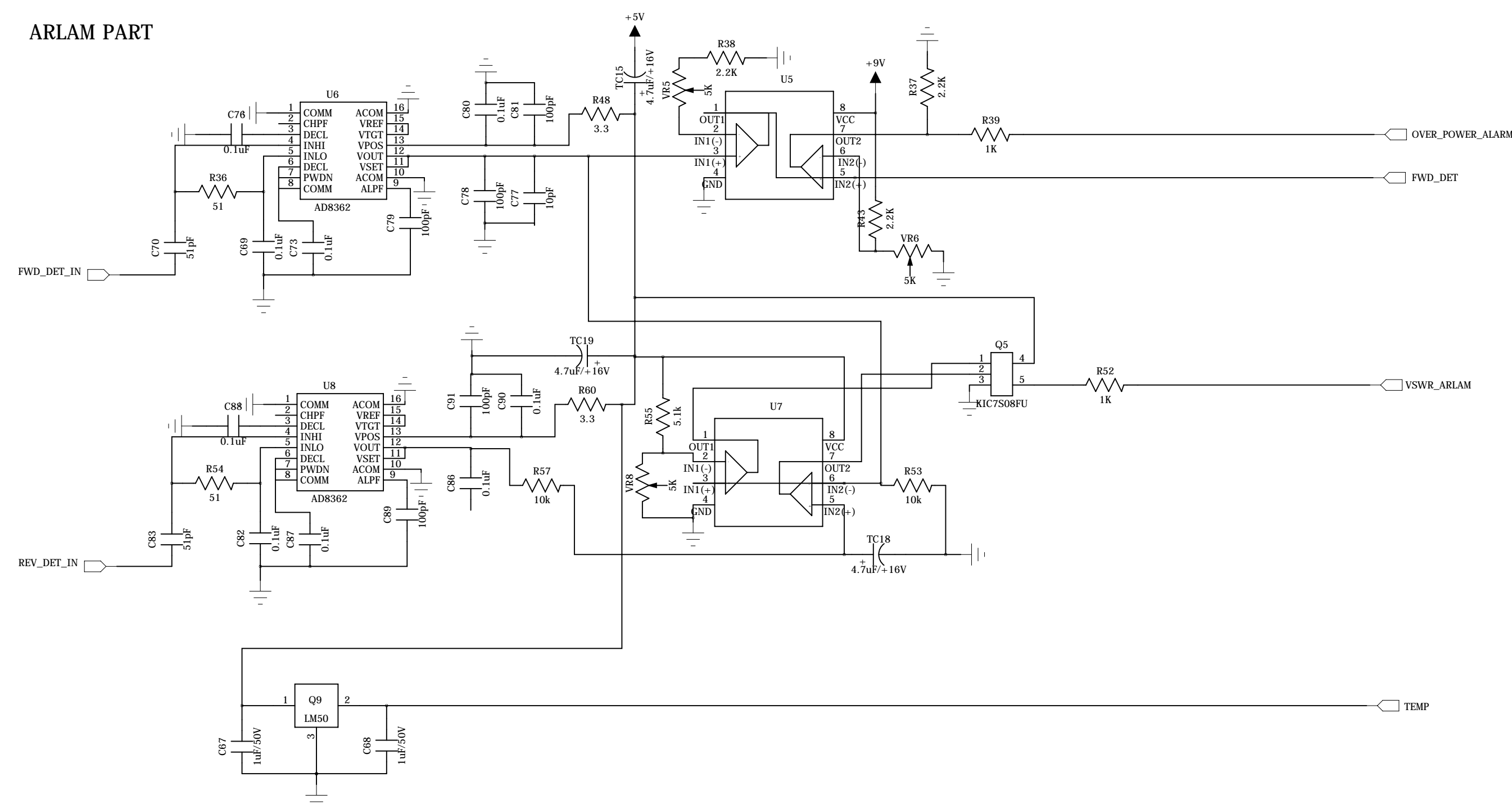
RF PART



## DC PART



## ARLAM PART



PREPARED	CHECKED	CONFIRMED	APPROVED	TITLE:  <b>RFP-2590-27-27</b> <b><u>RFHIC</u></b>			
				Designed by: N.I.LEE		Drawn By: N. I. LEE	CON PART
/	/	/	/	DATED: 2007.08.20	SIZE: A1	REVISION: 1.0	SHEET: 2 OF 2