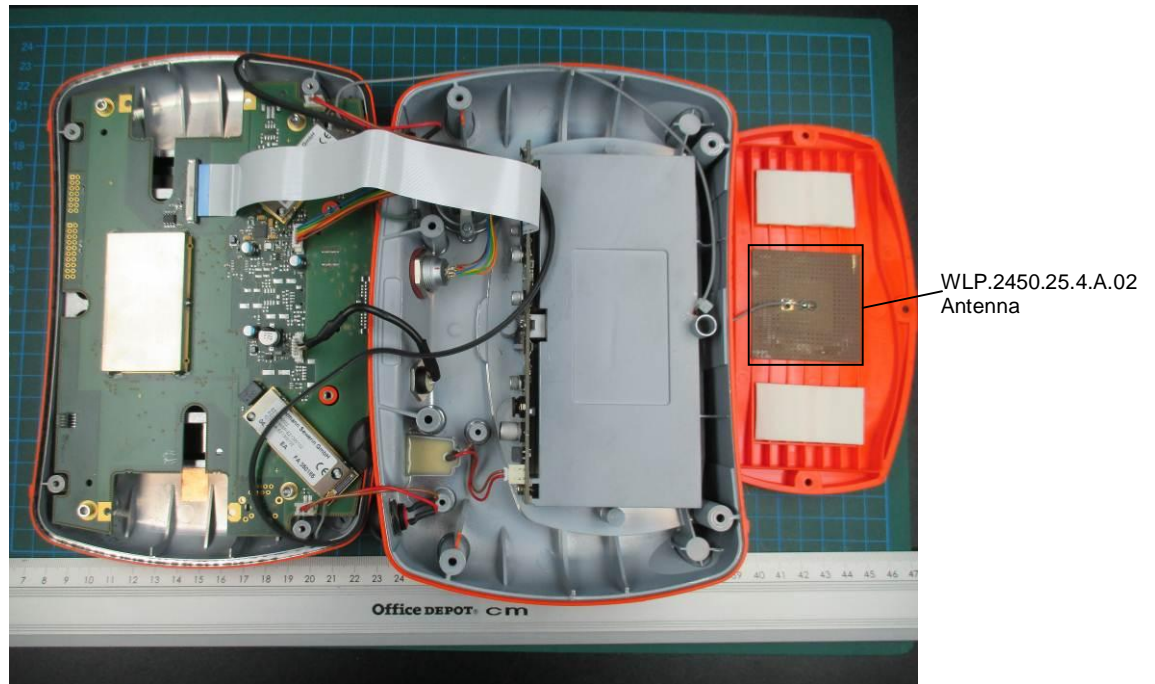
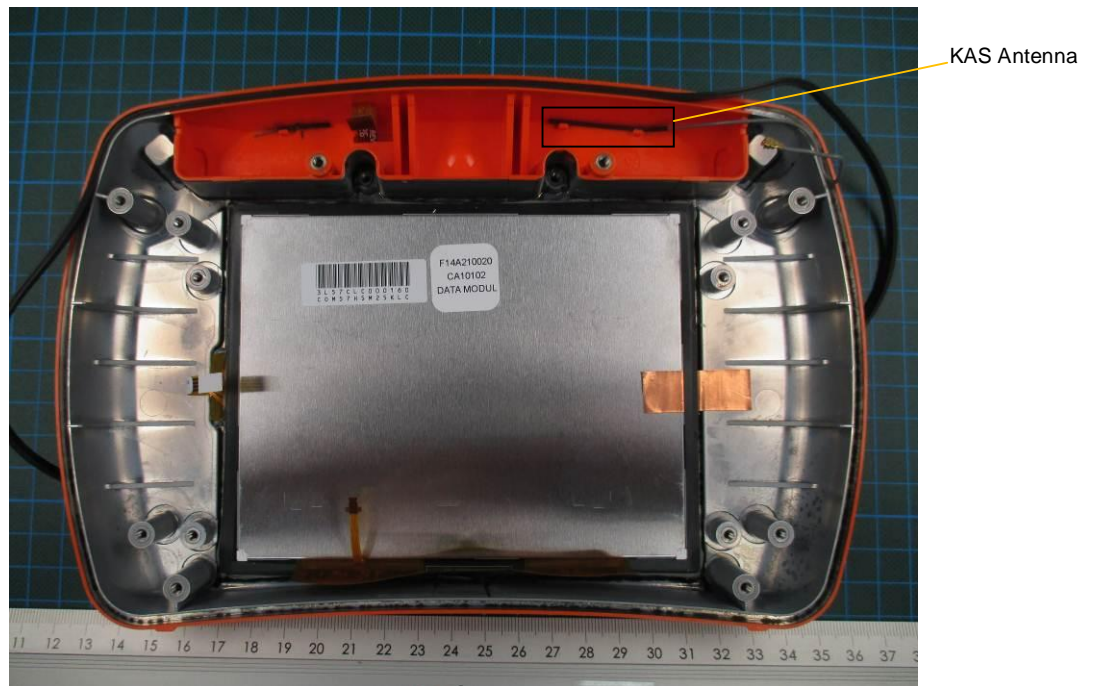


## ANNEX C: INTERNAL PHOTOGRAPHS



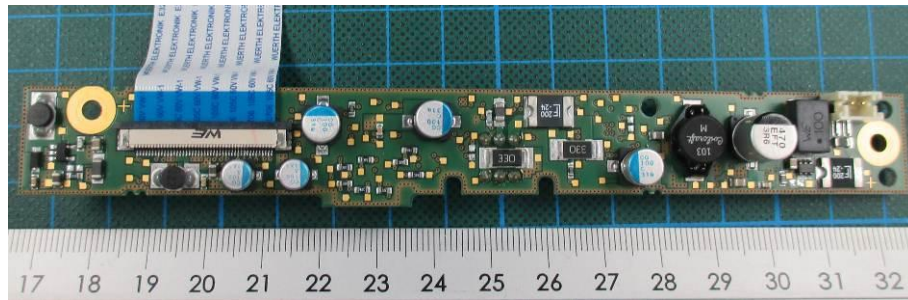
145725\_34.JPG: Host Device – Housing open

## ANNEX C: INTERNAL PHOTOGRAPHS



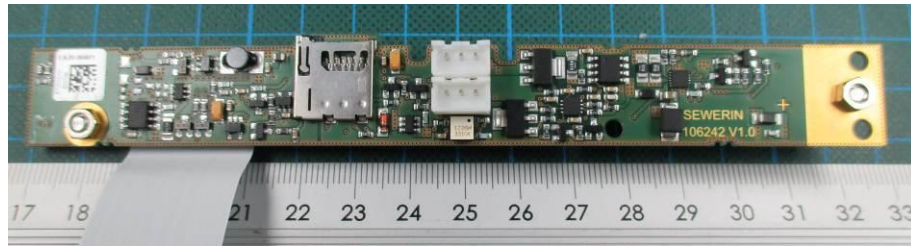
145725\_34.JPG: Host Device – Housing open

## ANNEX C: INTERNAL PHOTOGRAPHS



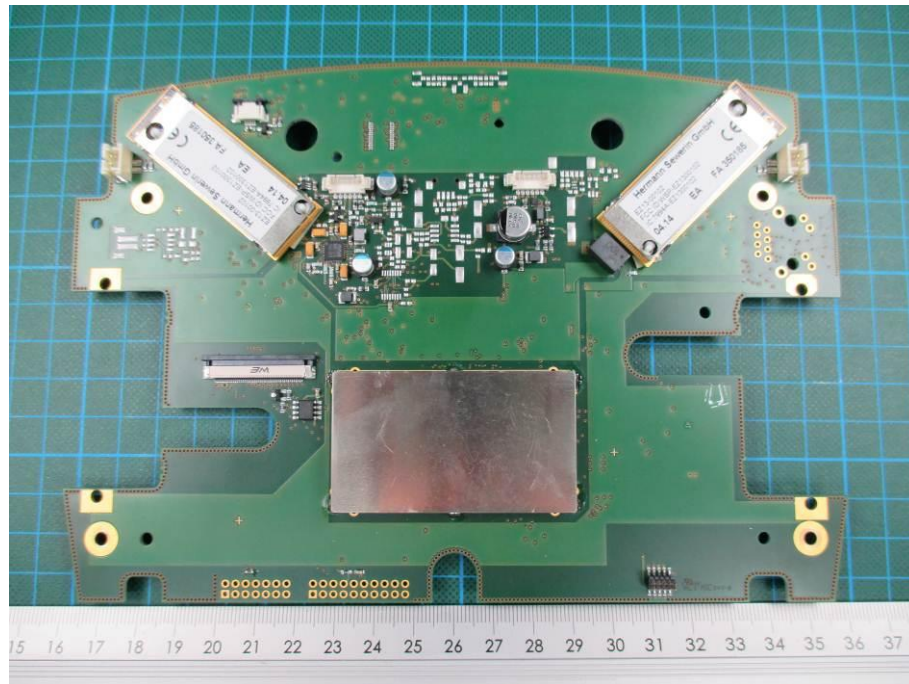
145725\_35.JPG: Ancillary PCB – Top View

## ANNEX C: INTERNAL PHOTOGRAPHS



145725\_36.JPG: Ancillary PCB – Bottom View

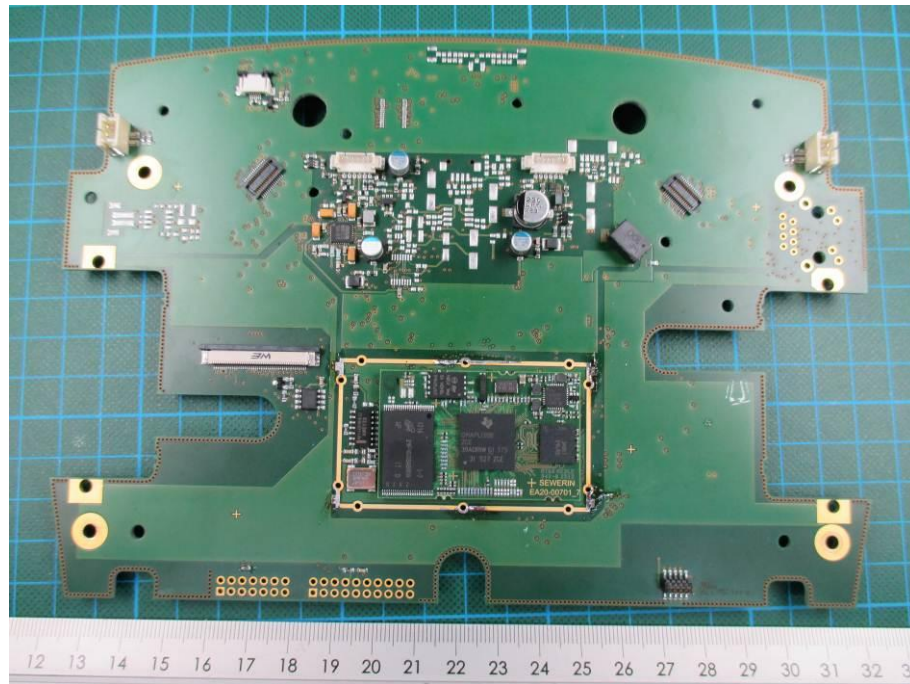
## ANNEX C: INTERNAL PHOTOGRAPHS



145725\_37.JPG: Main PCB with RF modules & Shielding– Top View

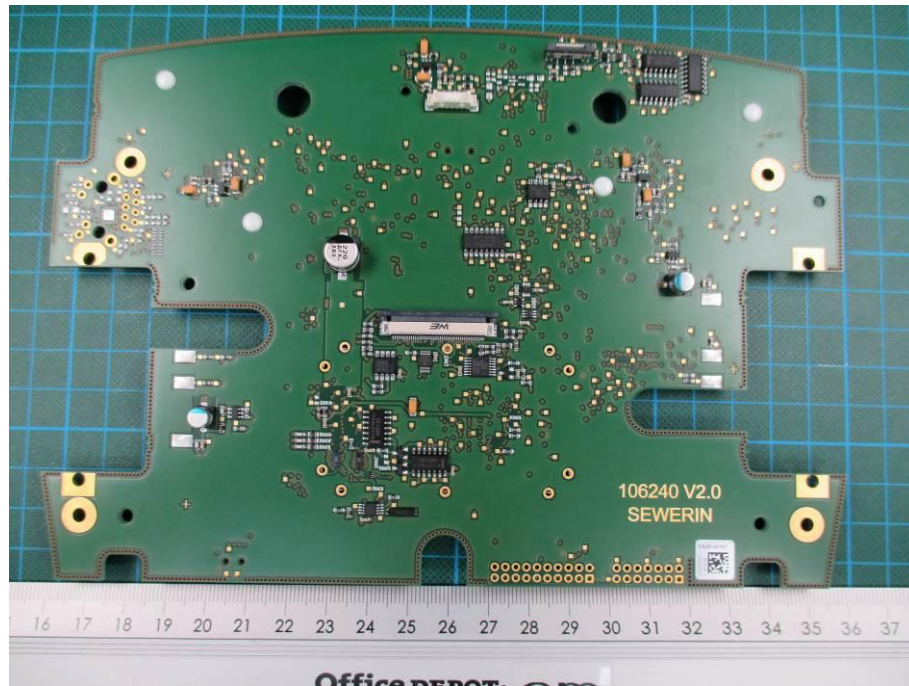


## ANNEX C: INTERNAL PHOTOGRAPHS



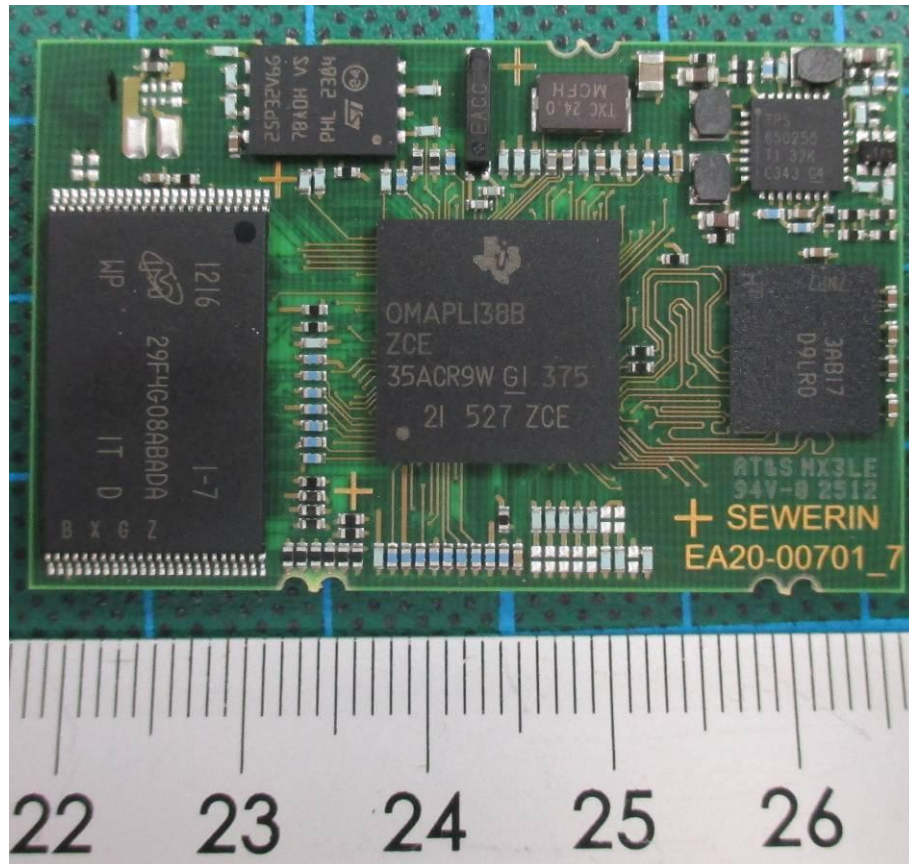
145725\_38.JPG: Main PCB without RF modules & Shielding– Top View

## ANNEX C: INTERNAL PHOTOGRAPHS



145725\_39.JPG: Main PCB – Bottom View

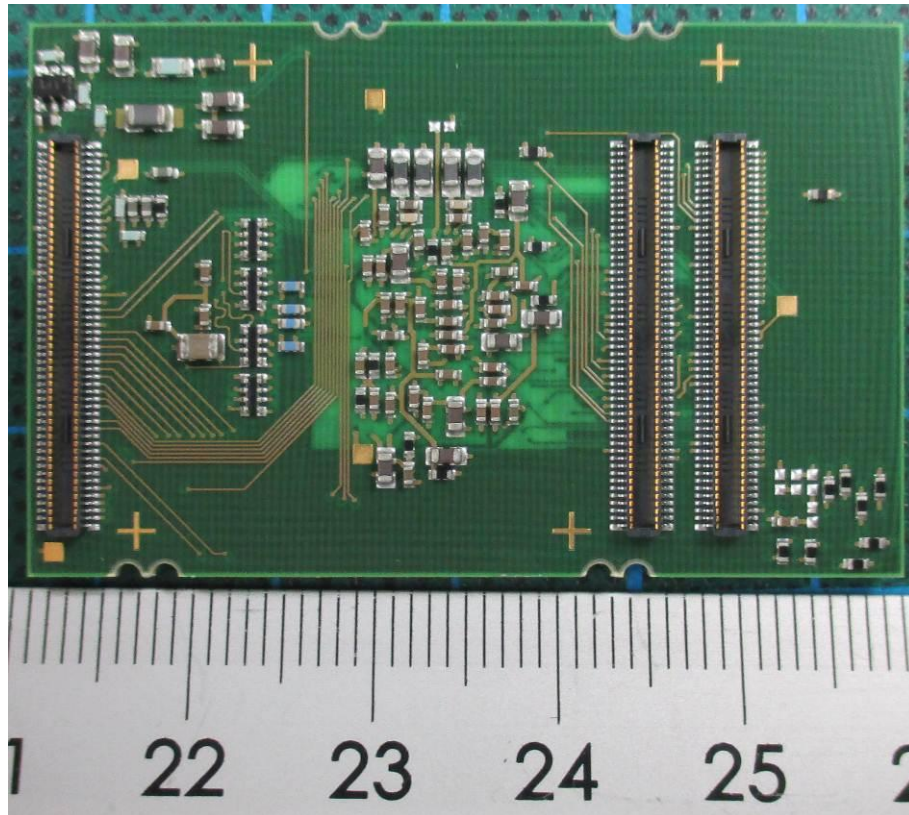
## ANNEX C: INTERNAL PHOTOGRAPHS



145725\_40.JPG: Logic PCB – Top View



## ANNEX C: INTERNAL PHOTOGRAPHS



145725\_41.JPG: Logic PCB – Bottom View

## ANNEX C: INTERNAL PHOTOGRAPHS



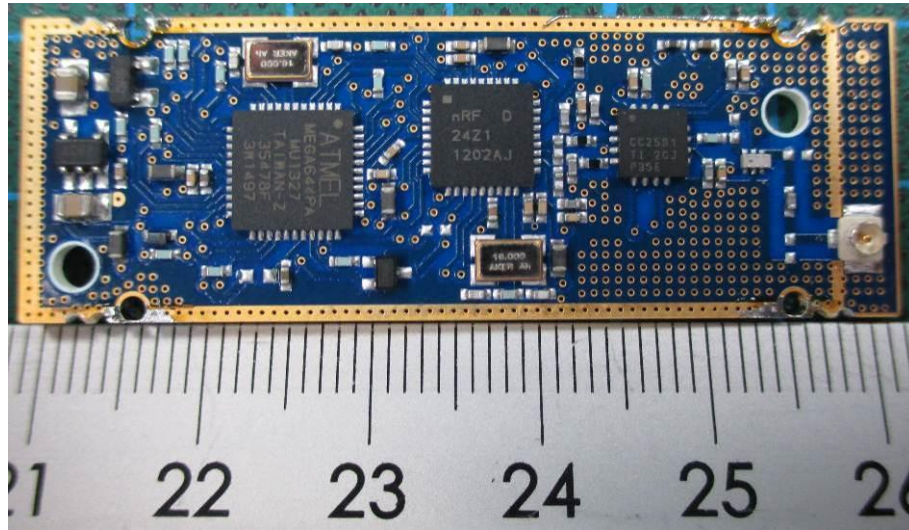
145725\_42.JPG: RF Module 1 – Top View with shielding

## ANNEX C: INTERNAL PHOTOGRAPHS



145725\_44.JPG: RF Module 1 – Bottom View

## ANNEX C: INTERNAL PHOTOGRAPHS



145725\_43.JPG: RF Module 1 – Top View without shielding

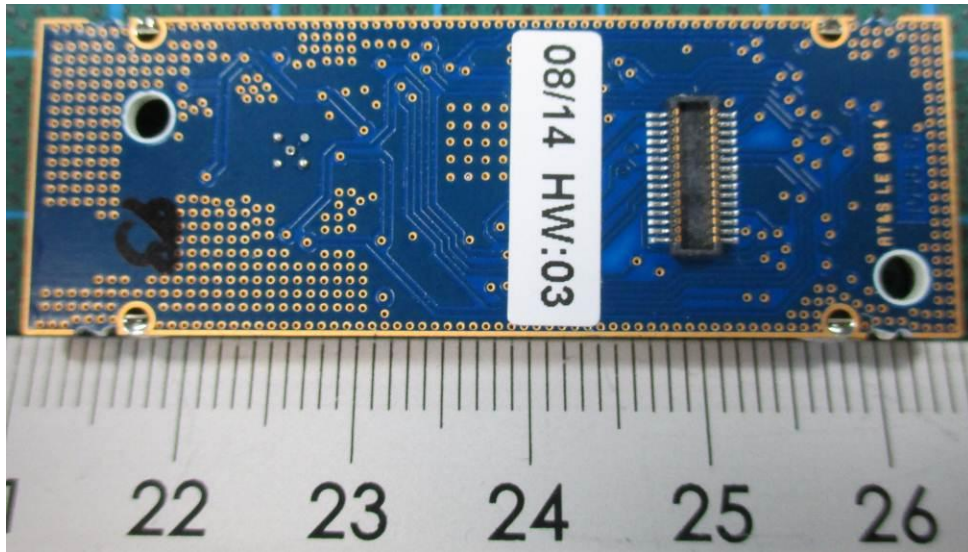
## ANNEX C: INTERNAL PHOTOGRAPHS



145725\_45.JPG: RF Module 2 – Top View with shielding

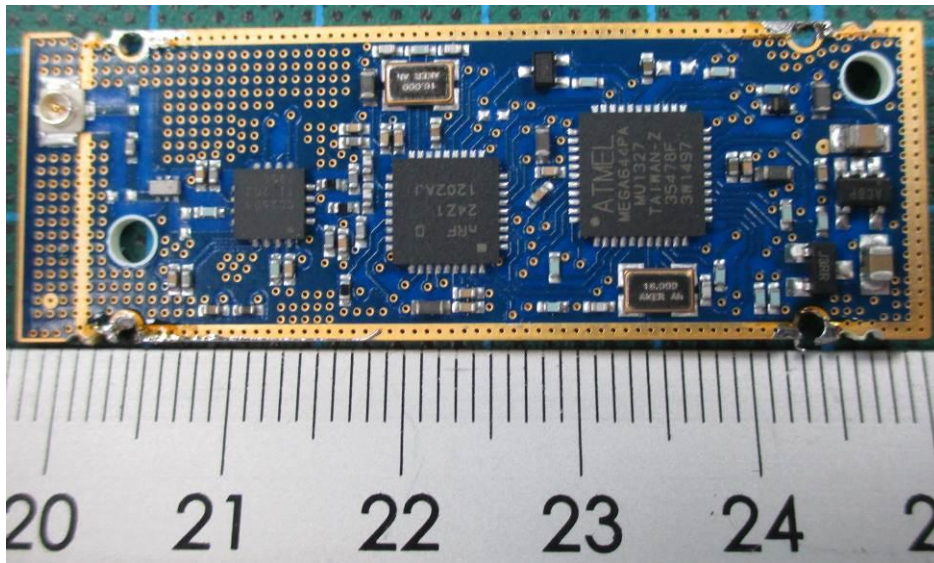


## ANNEX C: INTERNAL PHOTOGRAPHS



145725\_46.JPG: RF Module 2 – Bottom View

## ANNEX C: INTERNAL PHOTOGRAPHS



145725\_47.JPG: RF Module 2 – Top View without shielding