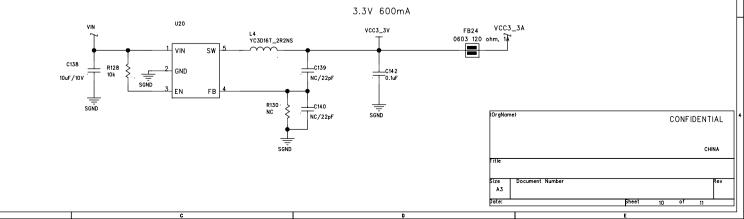
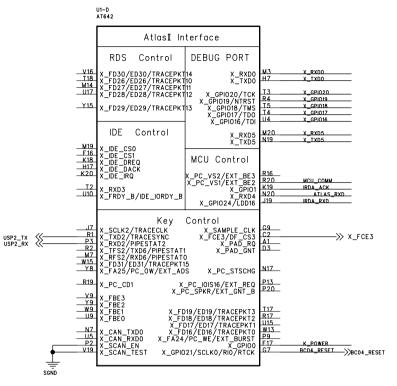


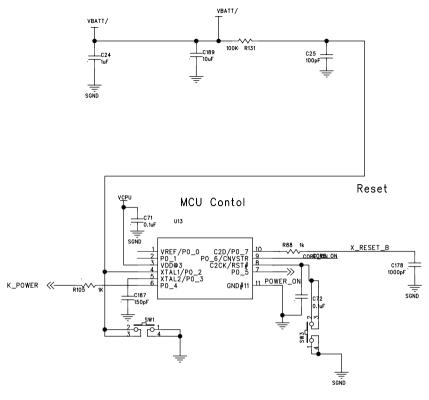
X_GPI026

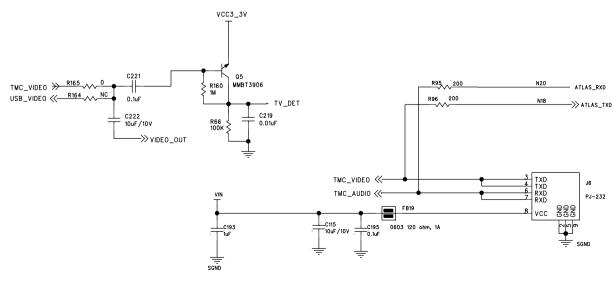
PA_ON

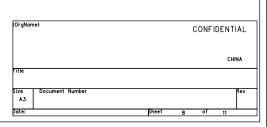


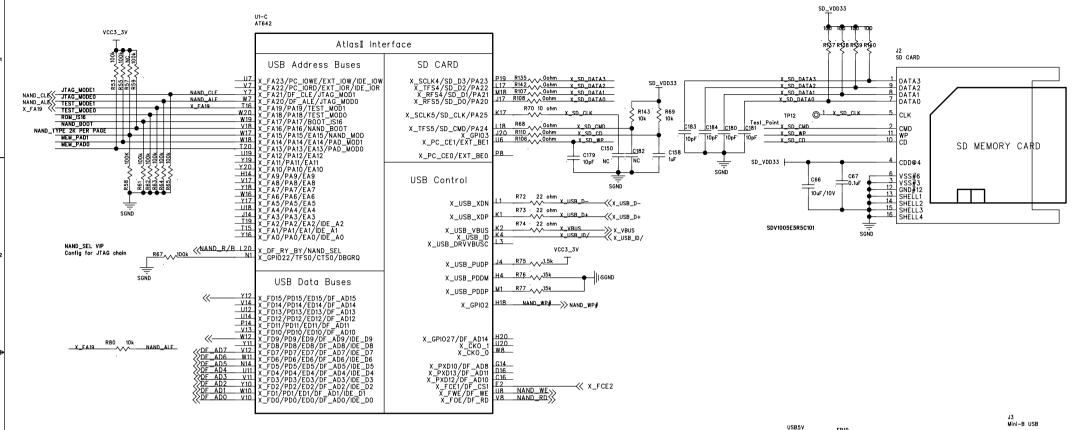
3.5 AND 4.3 GPS SCHEMATI U1-E AT642 AtlasI Interface TPS62200DBV 1.3V 600mA U14 VDDPDN1_3V RESET POWER I/O L3 YC3D16T_2R2NS X_PC_IREQ/EXT_IRQ X_CAN_TXD1 _PC_RESET/EXT_RST X_RESET_B CHG_EN X_RESET_B >> VIN _C113 4.7uF GND _C117 OSC <u>+</u> X_GPI05/PWM1 X_GPI06/PWM2 X_GPI025/LDD17 X_GPI026/DF_AD15 0.1uF - 4.7uF/10V X_XIN SGND ¥ GPIO25 X_GPI025 X_GPI026 EN FB X_XOUT X XINW X PWR EN X_PXD11/DF_AD9 E15_ X_FD23/ED23/TRACEPKT7 V15_ X_CAN_RXD1 V4_ X_RFS3/RXD7 R3_ X_PWR_EN V6_ X_PC_CD0 P17_ **-**~~ R134 · 0 ohm x_xoutw ->> AMP_ON R133 100k ->> x_pwr_en X_BATT_FAULT X_VDD_FAULT SGND __V5_ R117 VDDPRE1_3V U15 VDDPRE1_3V DVSS#Y3 X_USB_VDDL DVSS#W3 DVDD@Y2 DVDD@W2 VPLAD1_2V VIN VOUT AGND AVSS#Y1 LC118 AHVSS#V1 AHVSSG#V2 AVSSIO#V3 POWER _C120 120 121 2.2uF 10.1uF AVDD@W1 GND VPHA3_3V SCND AHVDD@U1 AHVDDG@U2 AVDDIO@U3 CORE_ON >> CORE_ON VSSIO#K8 VSSIO#L8 VSSIO#N10 VSSIO#N13 VSSIO#P11 NC N10 N13 SGND X_USB_VDDA VDDPRE 1.3V 50mA LDO SUPPLY 300mA XVDDIO@Y5 ≟ SGND VCC3_3A I 11 VSS#L11 VSS#L12 VSS#M10 VSS#M11 VDDIO@N12 L12 VDDIO@N8 VDDIO@P10 M11 VDDPDN@K9 VDDPDN@K13 VDDPDN@L9 VSS#M12 U16 U17 X_USB_VSSA VDDPDN1 3V R118 VPHA3_3V VIN VPLAD1_2V VDDPDN@L13 VDDPDN@M9 VDDPDN@M13 X_USB_VSDL 0 ohm T G10 G11 VSSPSSTL#G10 H9 VSSPSSTL#H9 VSSPSSTL#H12 H13 VSSPSSTL#H13 VSSPSSTL#J13 VIN ⊋ VOUT VIN Q VOUT VDDPRE@K7 VDDPRE@K14 _C123 _C124 LC126 _C127 _____0.1uF = luF 1uF 0.1uF -1uF T 1uF VDDPRE1_3V VDDPRE@L7 VDDPRE@L14 VDDPRE@M8 VDDPRE@N9 H10 VSSRSSTL#10 VSSRSSTL#11 XVSSIO#W5 SGND H11 VDHPSSTL@J10 VDHPSSTL@J11 VDHPSSTL@J12 SGND SGND SGND SGND SGND W.5 VDDPDN1_3V VCC3 3A VPHA 3.3V 10mA LDO SUPPLY 100mA VPLAD 1.2V 10mA LDO SUPPLY 100mA VDHPSSTI @K10 H8 VREFSSTL#H8 J9 VREFSSTL#J9 SGND VDHPSSTL@K11 VDHPSSTL@K12 ⊥_c77 _C78 ____C79 __ _C80 _ _0.1uF __0.1uF _____0.1uF _100pE _0.1uF 븣 SGND R111 VDDPRE1_3V R112 VPHA3_3V VPLAD1_2V Test_Point 12 MHz/6035 470 ohm -0 FB12 0603 120 ohm, 1A C73 15pF · C74 15 pF CR3 CRA CRS C87 CRR C107 C108 C109 C110 C111 CRO C112 0.1uF 0.1uF _____0.1uF 100pF 100pF _100pF _0.1uF AGND 0.1uF 0.1uF _____0.1uF \pm AGND DEL C86,C90 를 SGND SGND VCC3_3A R113 (OrgName) ≥ 2M CONFIDENTIAL 32 768KHz/146 _C95 _C96 _C97 _C98 _C100 310 H —ŏ.1uF _____0.1uF _____0.1uF _____0.1uF 0.1uF 0.1uF _____0.1uF _____0.1uF _____0.1uF 0.1uF _____0.1uF C75 CHINA 22pF 22pF SGND A3 SGND Date:











Boot From

NAND MUX with

X_DF_RY_BY

Pull Low

Pull high

X_FA16	Boot From
Pull Low	ROM Interface
Pull high	NAND

Mux With

ROM Interface

8/16 bit boot

X_FA17	Boot in
Pull Low	8bit Mode
Pull high	16bit Mode

Boot NAND's Page Size

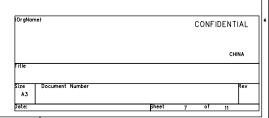
X_F A15	Boot NAND's Page S	ze
Pull Low	512B	
Pull high	2048B	

DRAM Type

X_FA14	X_FA13	DRAM Type
Pull Low	Pull high	2.5V DDR
Pull high	Pull Low	2.5V SDR
Pull high	Pull high	3.3V SDR

Working Mode

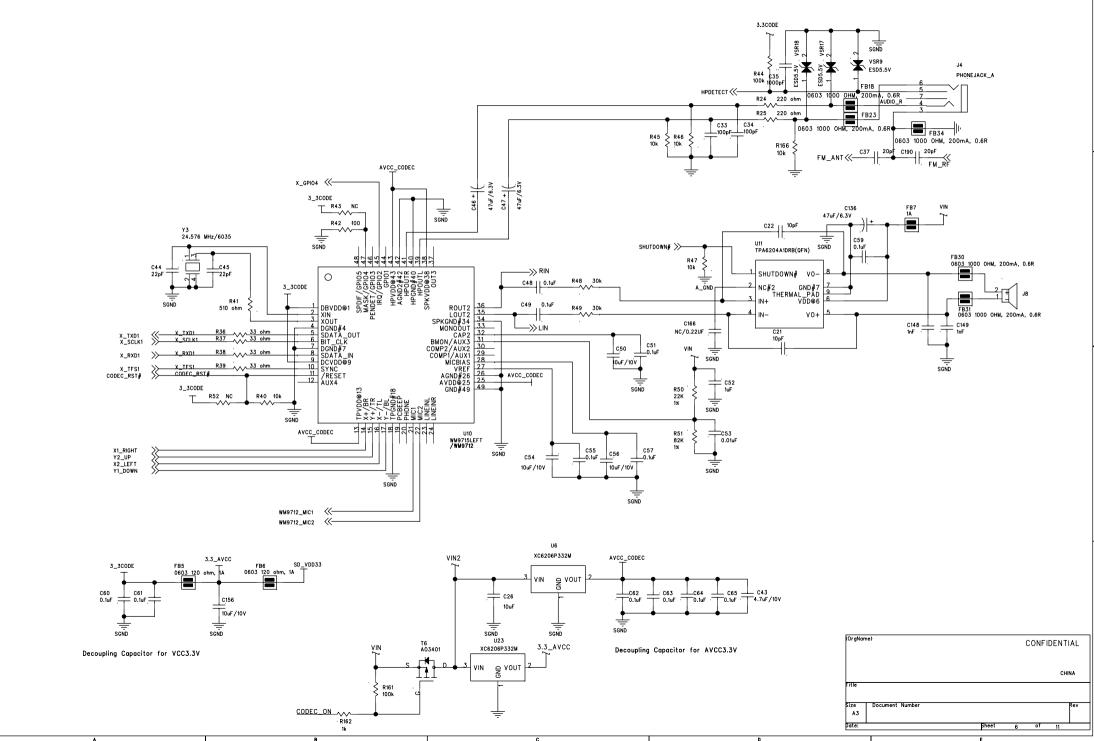
>	(_FA21	X_FA20	X_FA19	X_FA18	GPI022	Mode
Pu	II Low	Pull Low	Pull Low	Pull Low		Normal
Pu	II Low	Pull high	Pull high	Pull Low	Pull Low	RISC JTAG



GNO

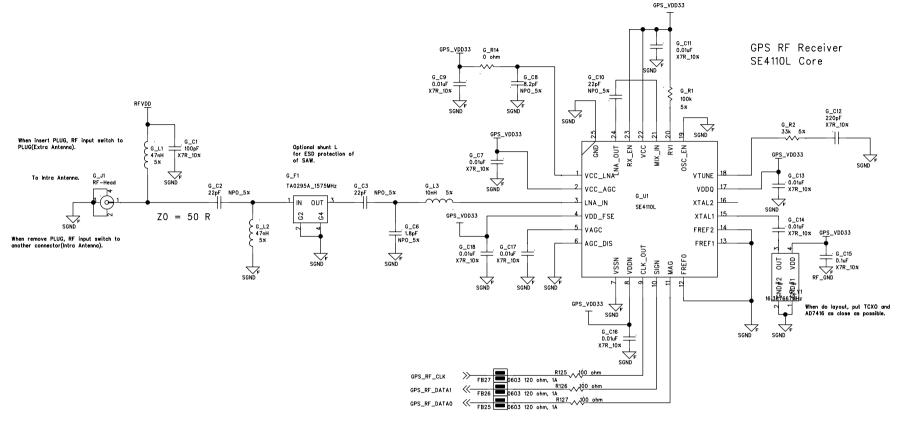
X_USB_D-X_USB_D+

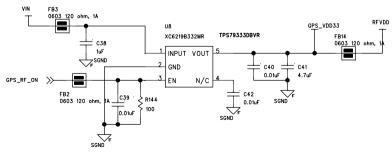
X_USB_ID/

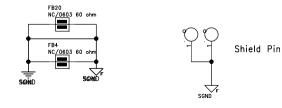


Notes:

- 1. The value of component which marked asterisk "*" may be changed in every prodction. These values are effected by the PCB material parameter, tolerance of component, etc.
- 2. The idiosyncracy and tolerance of passive components such as capacitors, inductances and resistors, has been appointed. These components just can be substituted by which be higher precision.

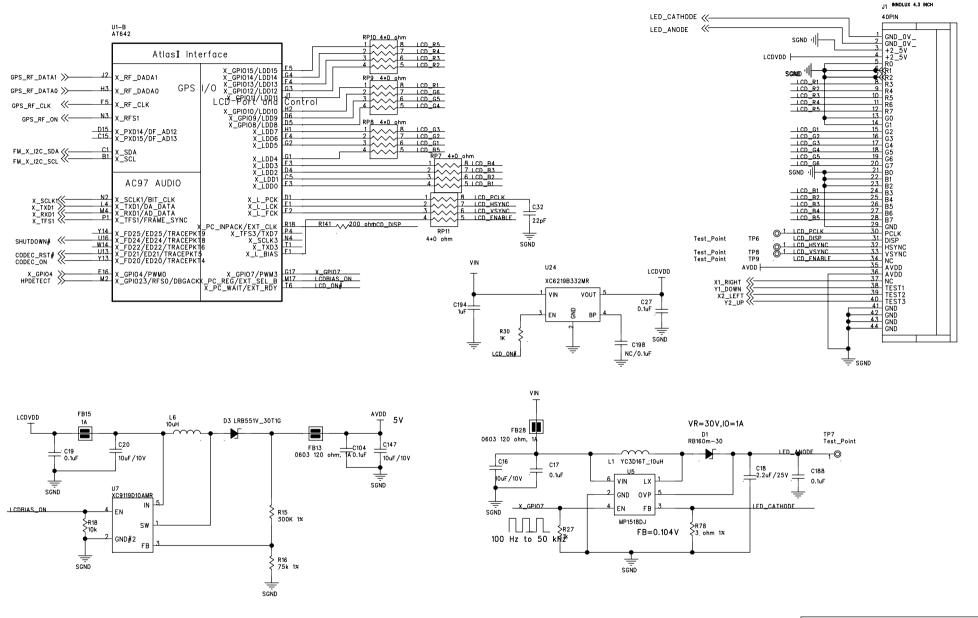






orgname)		CONFIE				DENTIA	
Title					С	HINA	
Size A3	Document Number					Re	
Date:		Sheet	5	of	11		

. . . .



•

