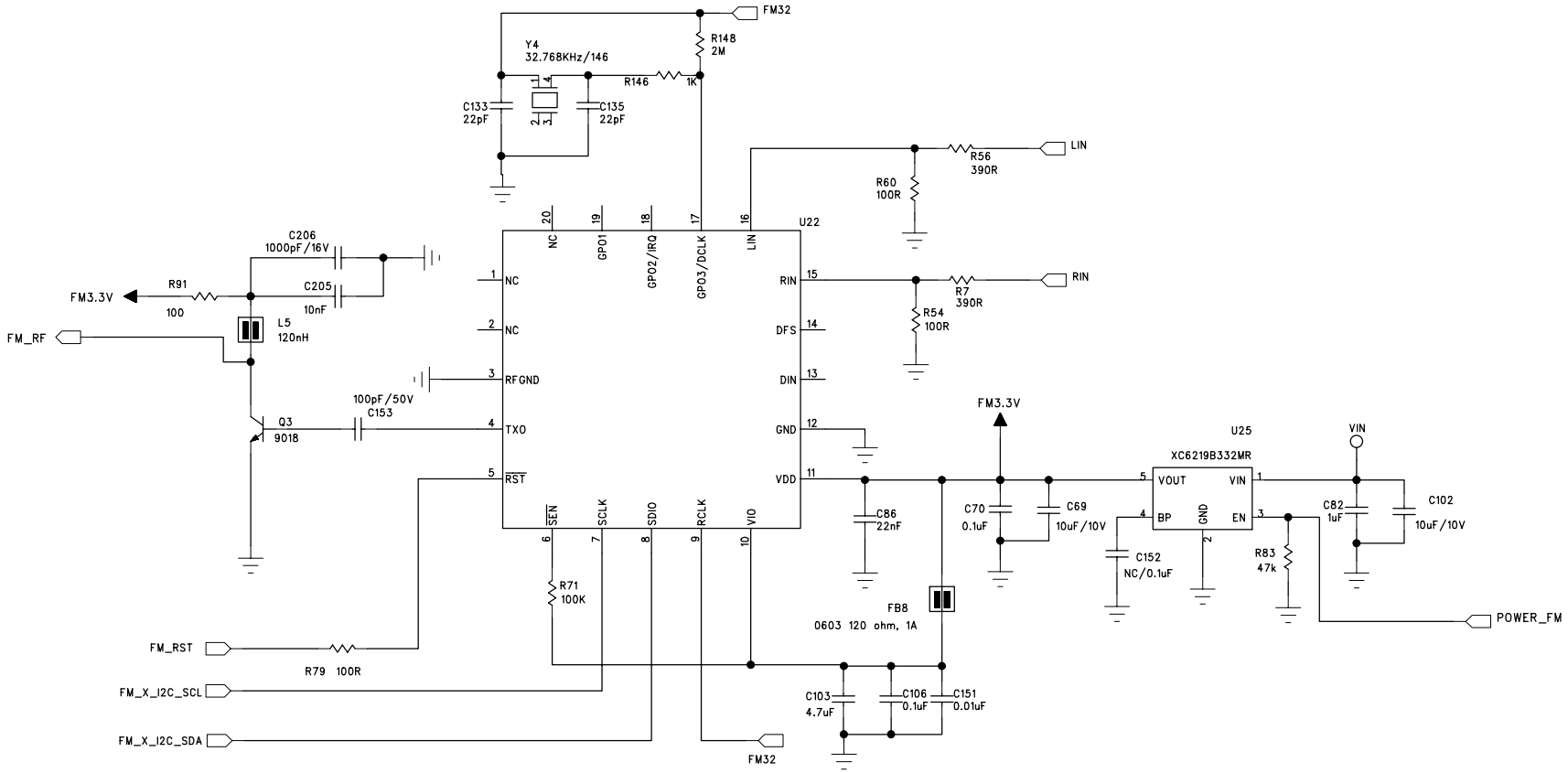
PORTABLEVIEW 3.5 AND 4.3 GPS SCHEMATIC



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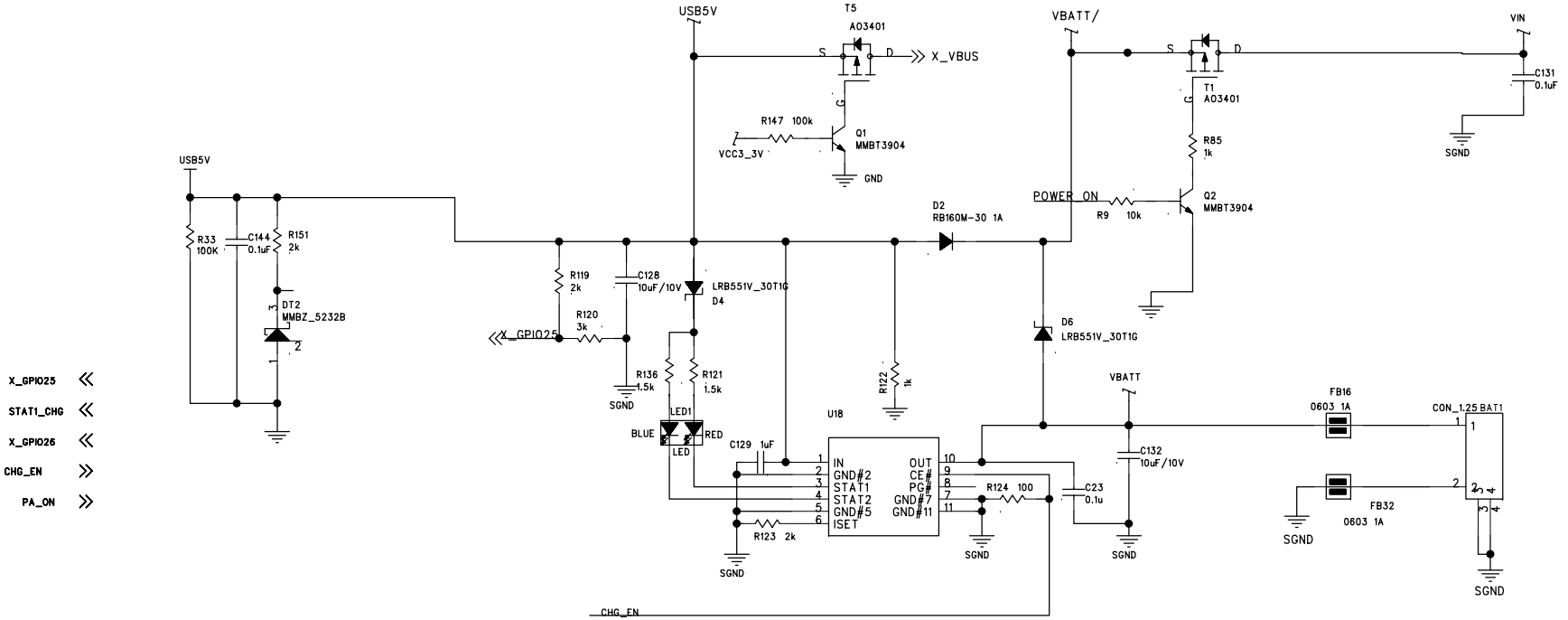
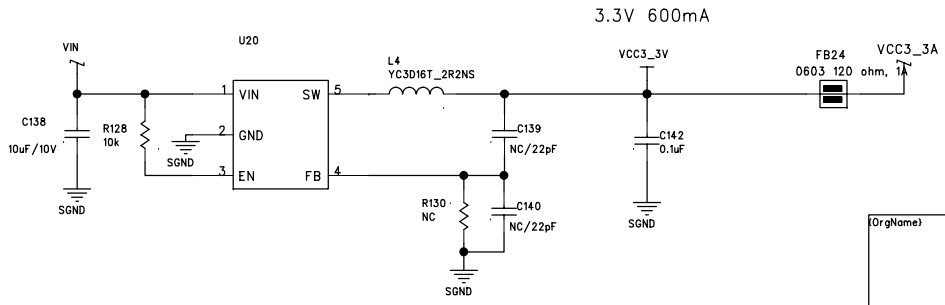
PORTABLEVIEW 3.5 AND 4.3 GPS SCHEMATIC



LED	RED	BLUE
STATE	CHARGE	GOOD



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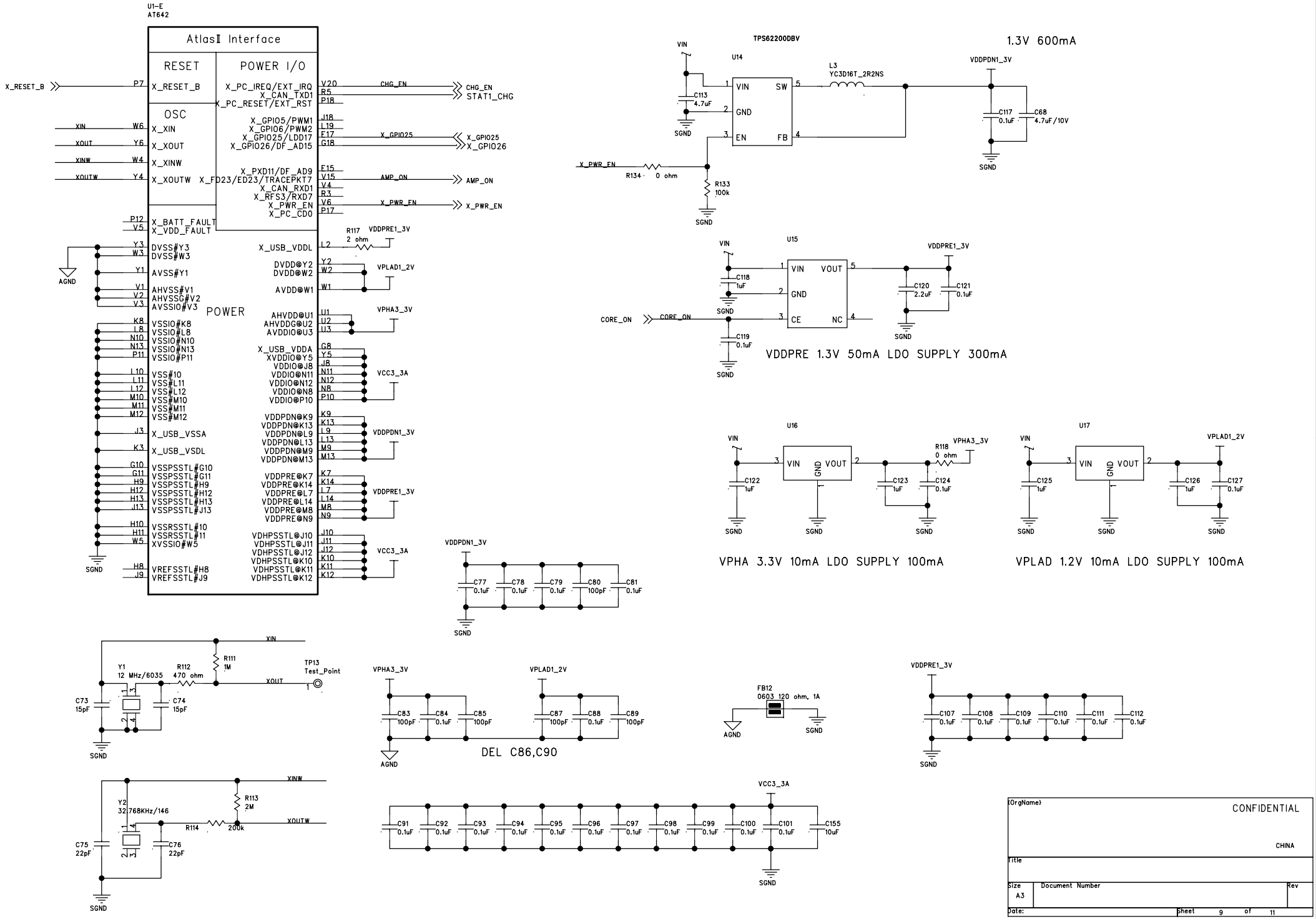
PORTABLEVIEW 3.5 AND 4.3 GPS SCHEMATIC



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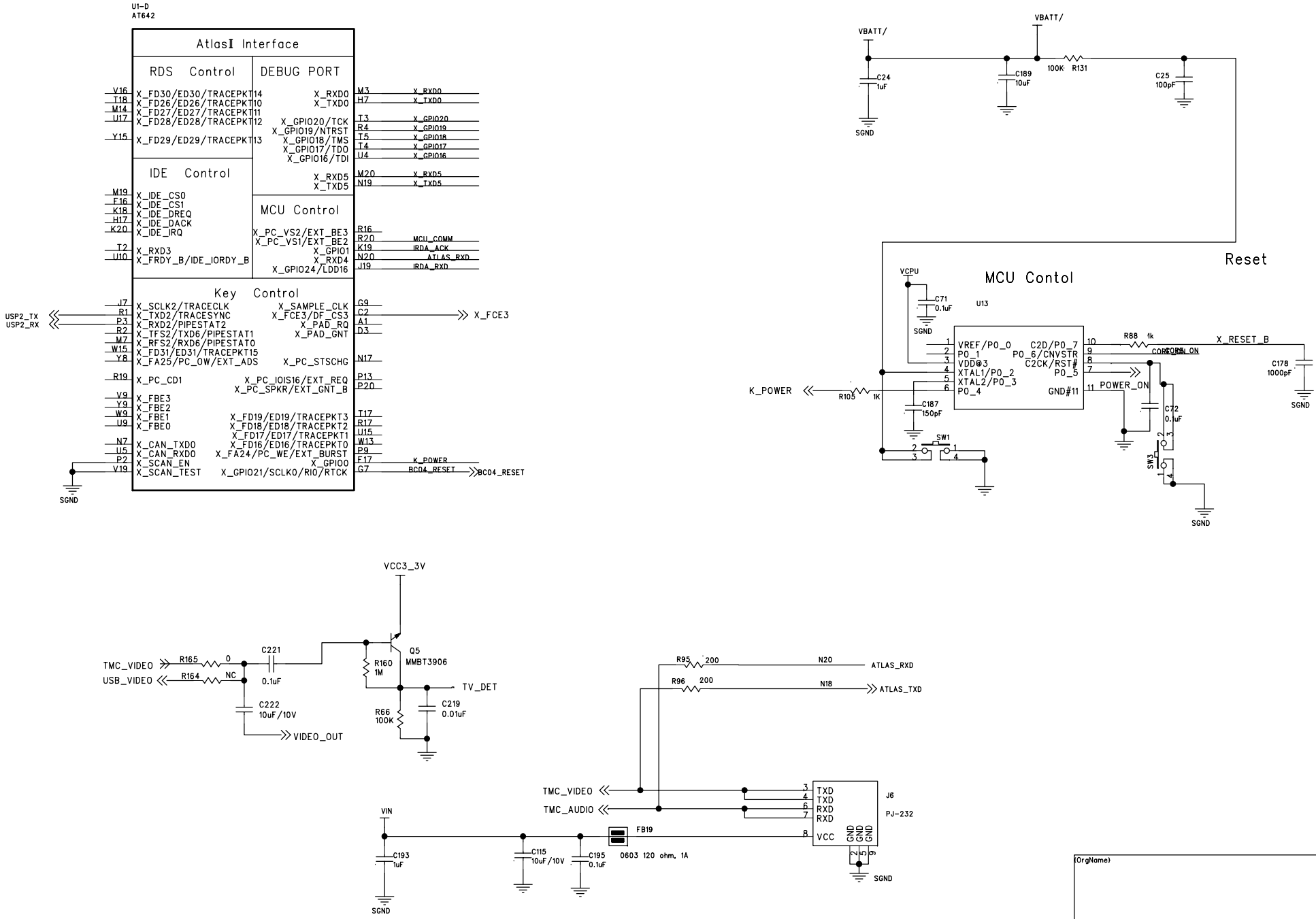

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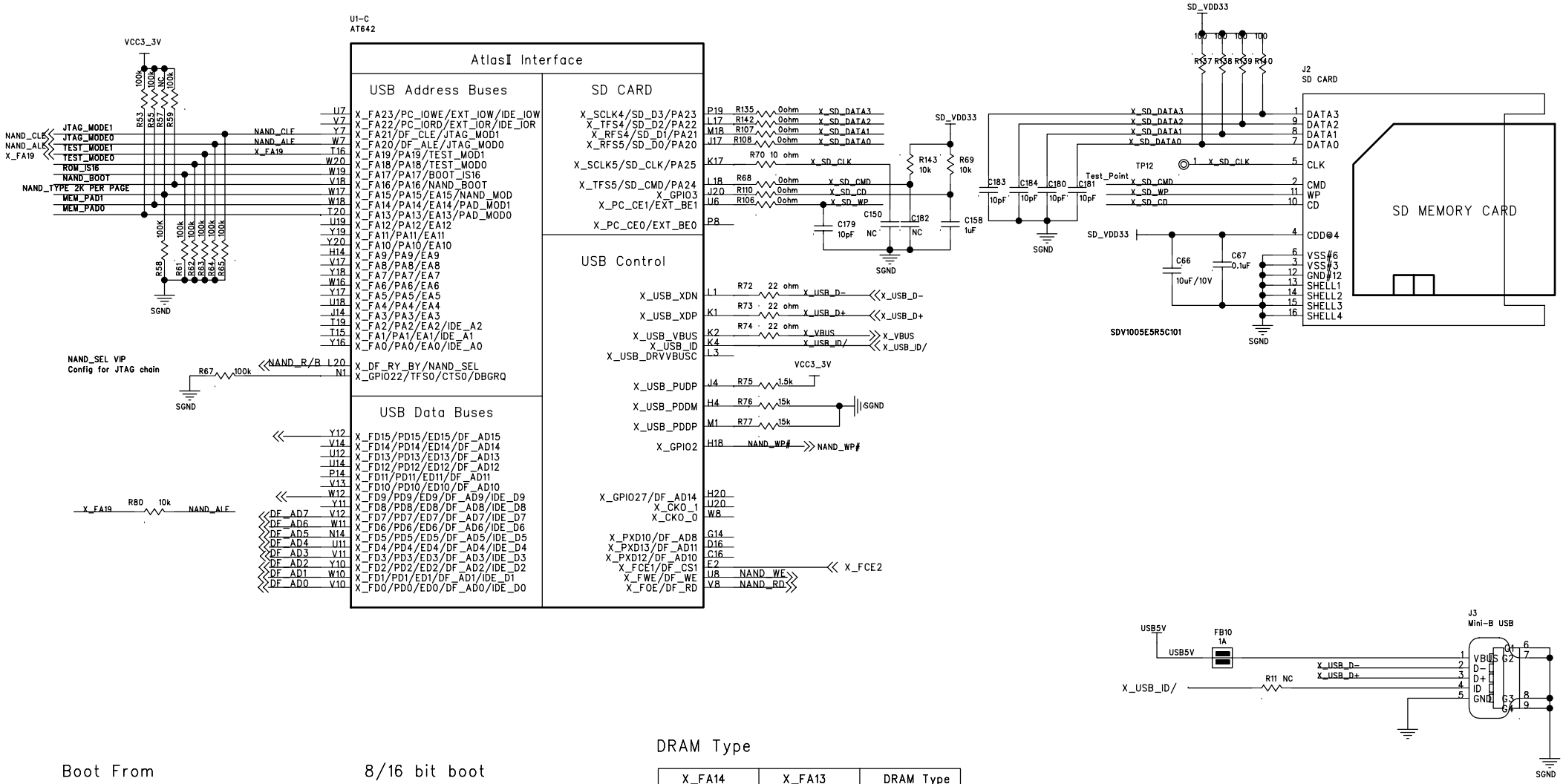
PORTABLEVIEW 3.5 AND 4.3 GPS SCHEMATIC



Boot From

X_FA16	Boot From
Pull Low	ROM Interface
Pull high	NAND

8/16 bit boot

X_FA17	Boot in
Pull Low	8bit Mode
Pull high	16bit Mode

DRAM Type

X_FA14	X_FA13	DRAM Type
Pull Low	Pull high	2.5V DDR
Pull high	Pull Low	2.5V SDR
Pull high	Pull high	3.3V SDR

NAND MUX with

X_DF_RY_BY	Mux With
Pull Low	VIP
Pull high	ROM Interface

Boot NAND's Page Size

X_FA15	Boot NAND's Page Size
Pull Low	512B
Pull high	2048B

Working Mode

X_FA21	X_FA20	X_FA19	X_FA18	GPIO22	Mode
Pull Low	Pull Low	Pull Low	Pull Low		Normal
Pull Low	Pull high	Pull high	Pull Low	Pull Low	RISC JTAG

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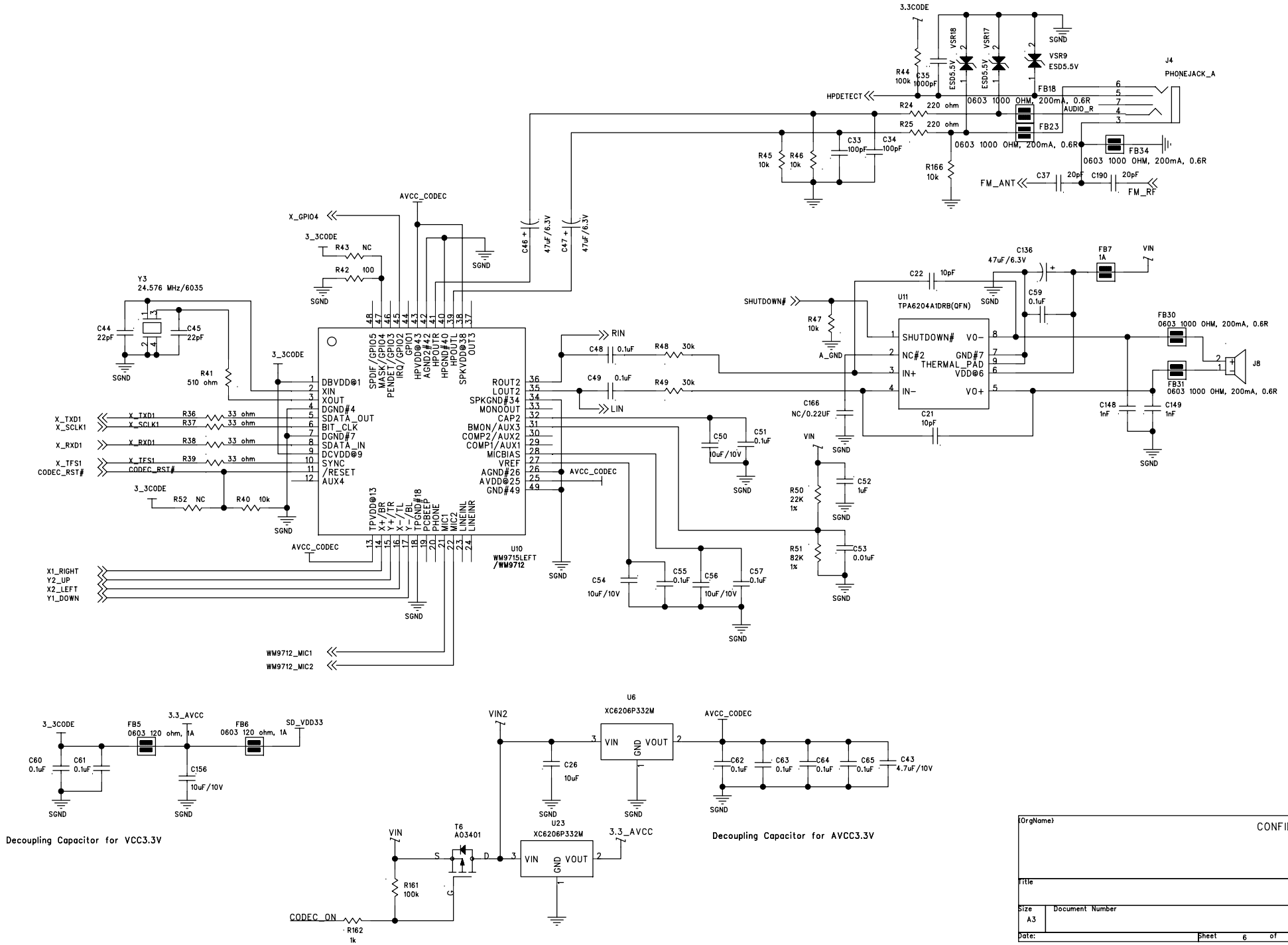

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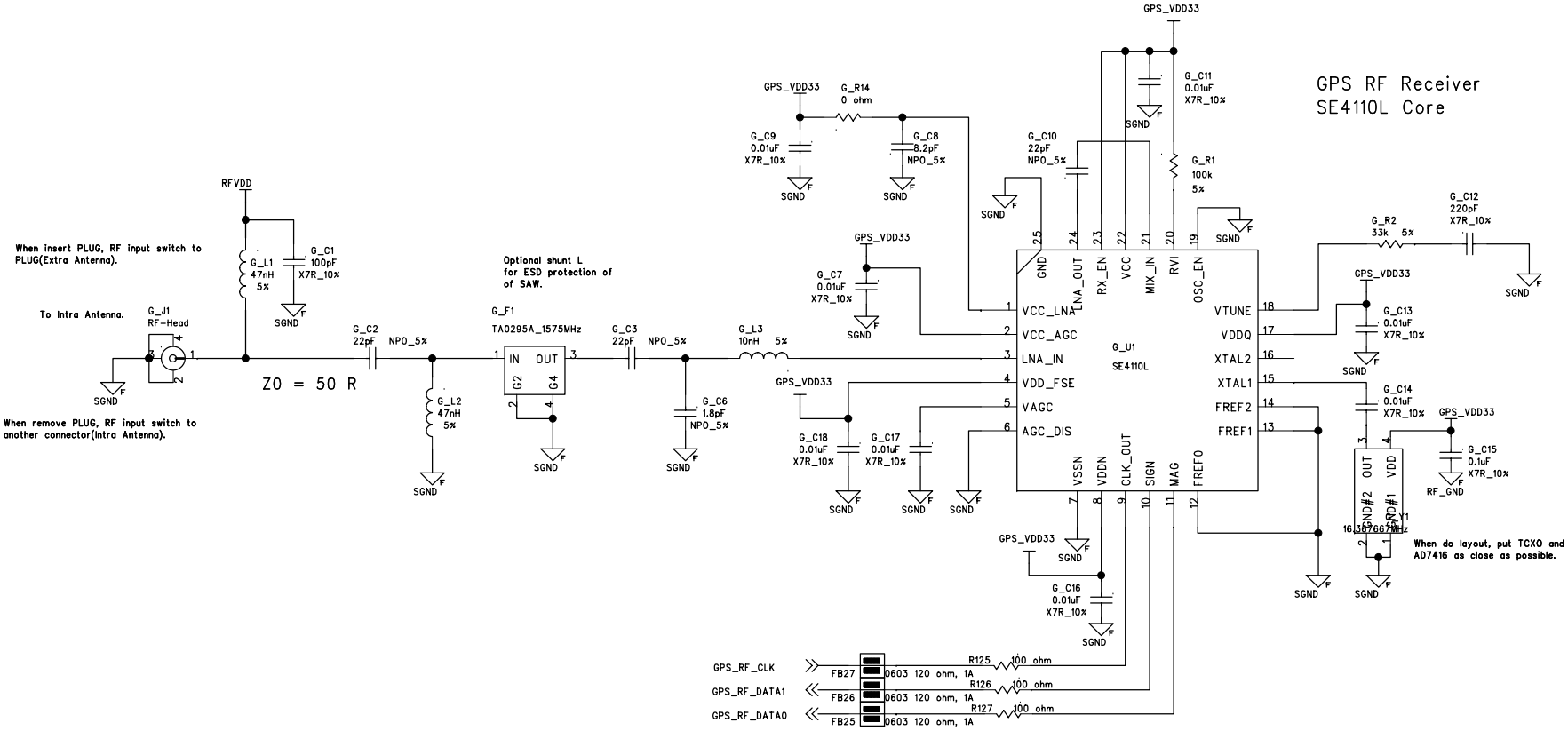
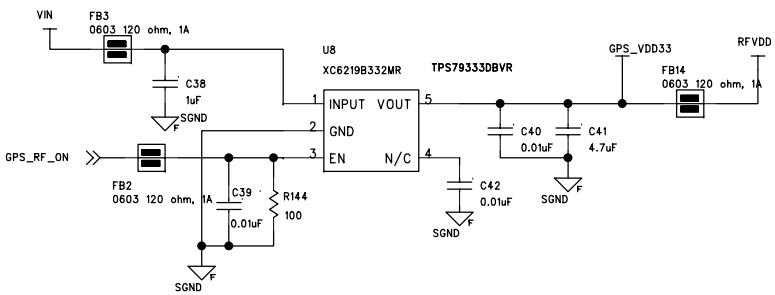
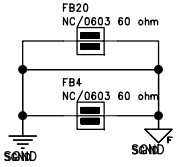
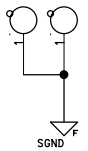
Notes:
1. The value of component which marked asterisk "*" may be changed in every prodction.These values are effected by the PCB material parameter,tolerance of component,etc.
2. The idiosyncracy and tolerance of passive components such as capacitors,inductances and resistors,has been appointed. These components just can be substituted by which be higher precision.









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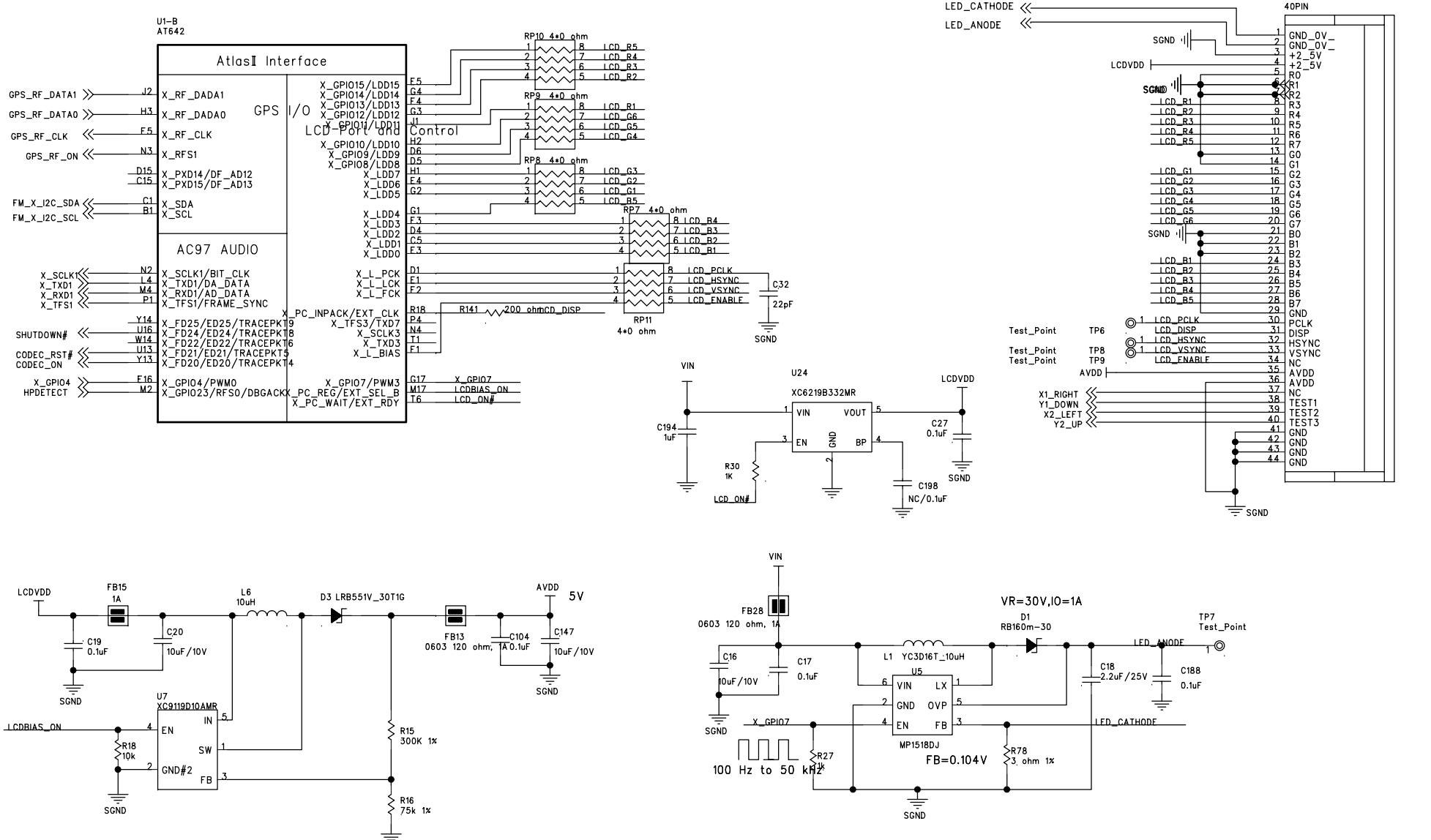

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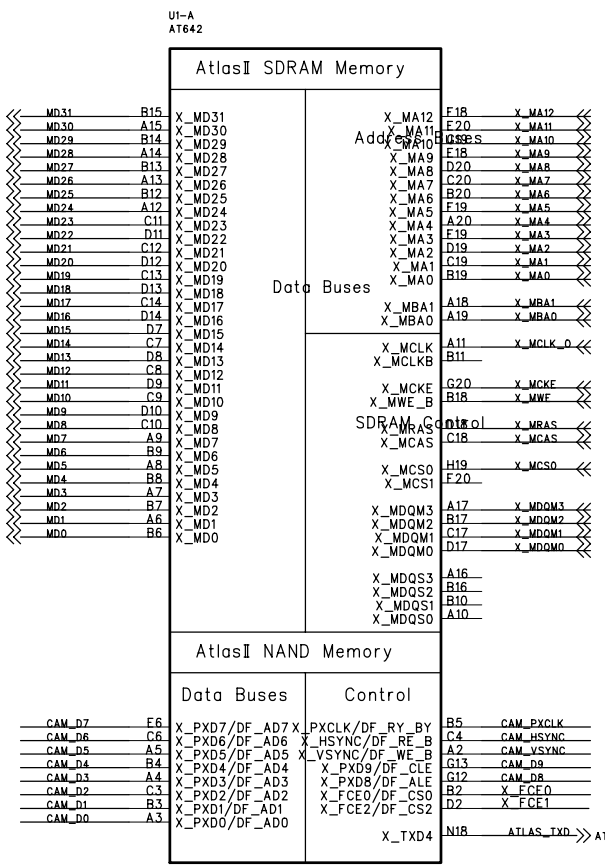
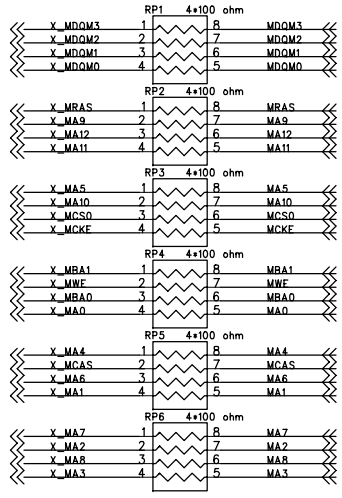
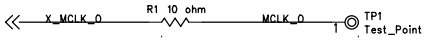
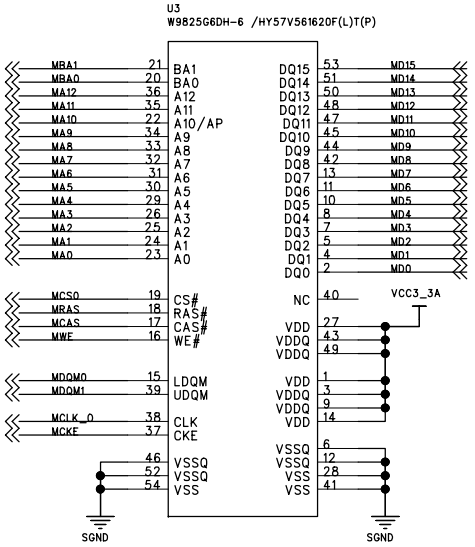
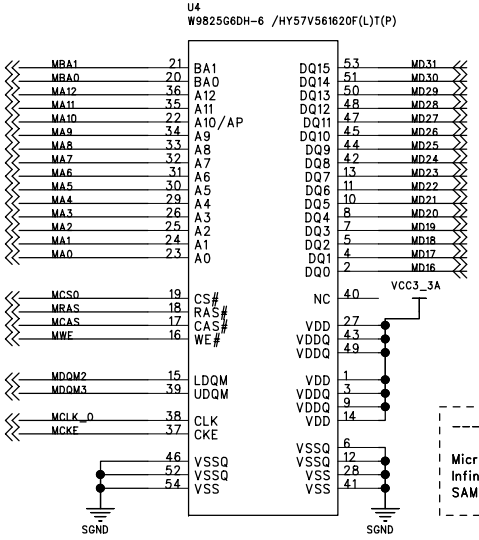
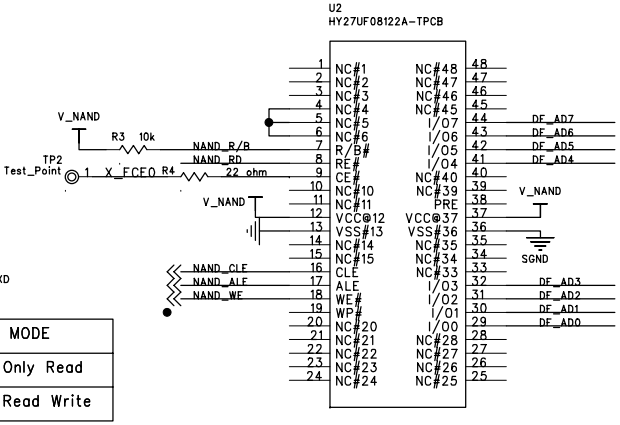










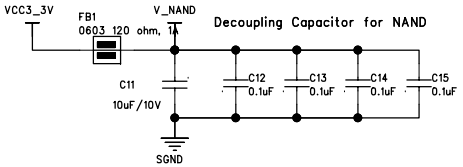


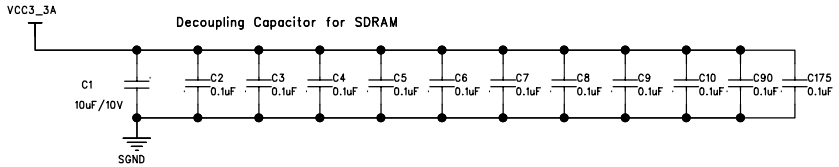
WP#	MODE
Pull Low	Only Read
Pull high	Read Write

wp 拉高后flash可以?也可以写。拉低后只能?。不能写

---32 Mega Bytes SDRAM---

Micron MT48LC16M16A2TG-75 L
Infineon HYB 39S256400DT-7.5
SAMSUNG K4S561633F-75





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