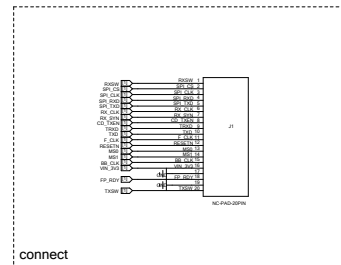
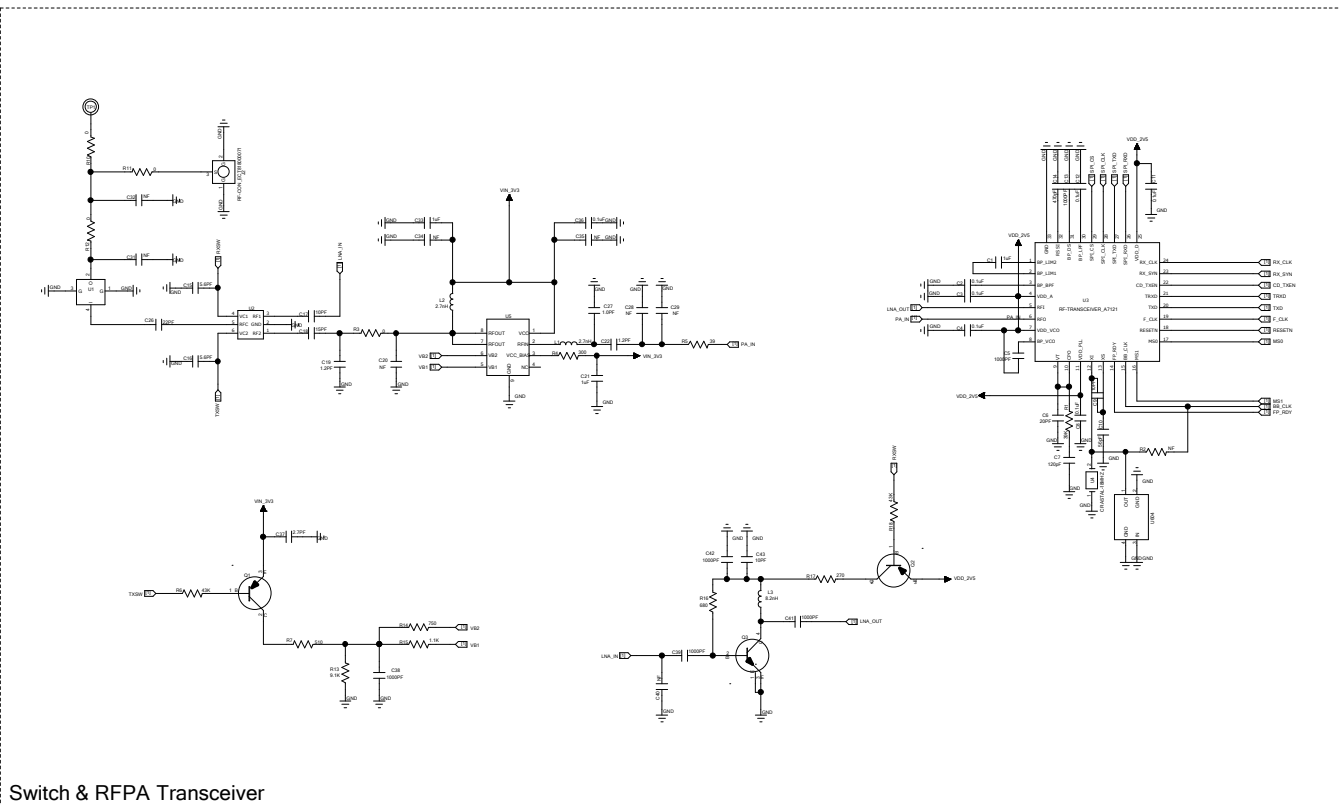


LDO/300ma



connect



Switch & RFPA Transceiver



6

5

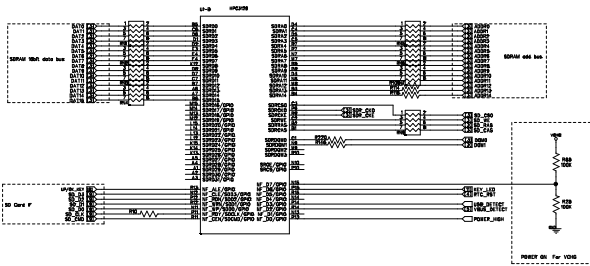
4

3

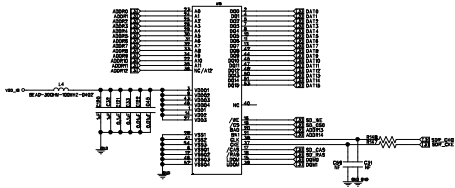
2

1

REVISION HISTORY			
REV	DATE	DESCRIPTION	BY



MCU SDRAM Interface

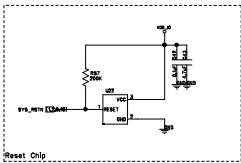
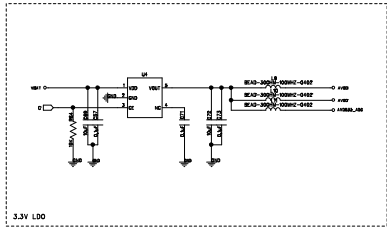
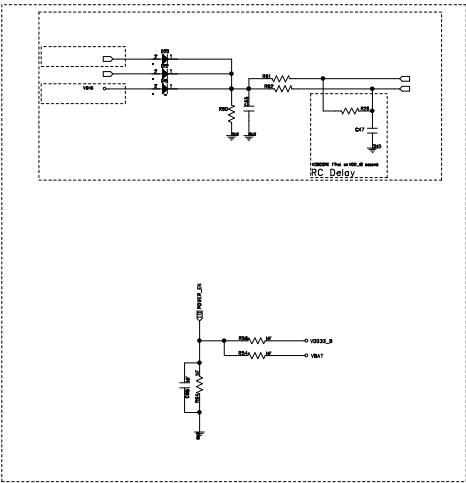
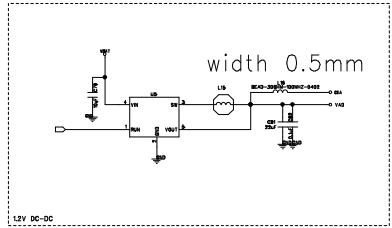
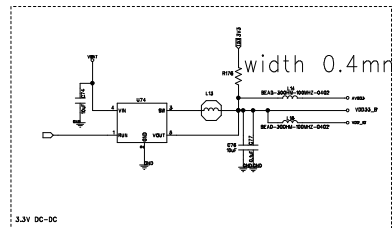
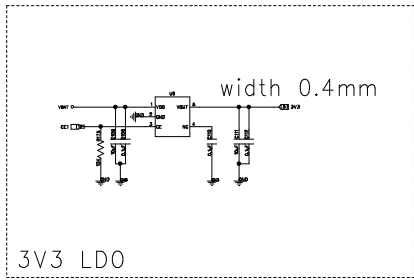
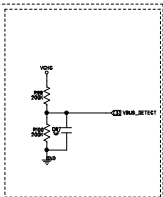
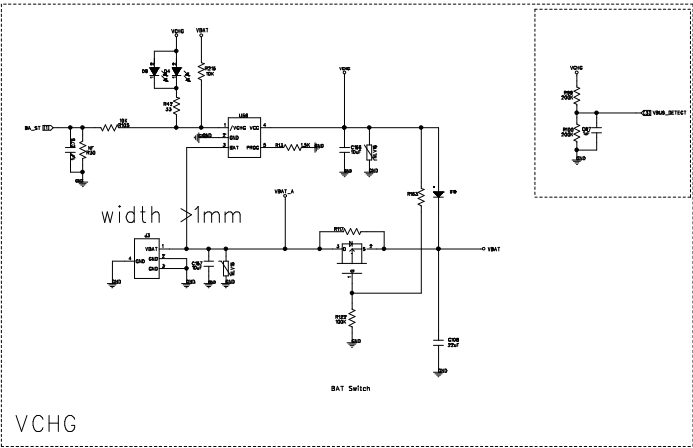


SDRAM

REVISION		DATE		DESCRIPTION	



REVISION HISTORY			
LTN	REV. NO.	APPROVAL	DATE



TITLE & REV			
DOCUMENT NO.			
DESIGNER	DATE	DEVELOPER	
REV	DATE	COMPANY	
APPROVED	DATE	CONFIDENTIAL AND PROPRIETARY	
			SHEET 1 OF 1

6

5

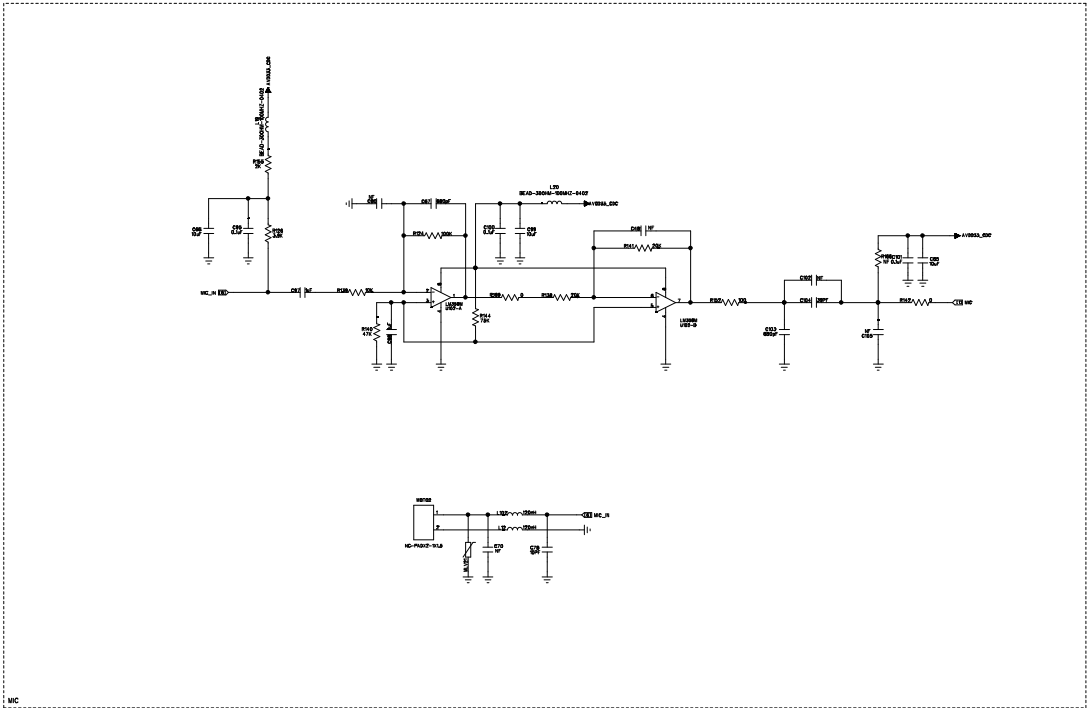
4

3

2

1

REVISION HISTORY			
LTW	REV NO.	APPROVAL	DATE



TITLE & META			
DOCUMENT NO.			
DESIGNER	DATE	DESIGNED BY	
DRA	DATE	CHECKED BY	
APPROVED	DATE	CONFIRMED	
CONFIDENTIAL AND PROPRIETARY			SHEET 1 OF 1

6

5

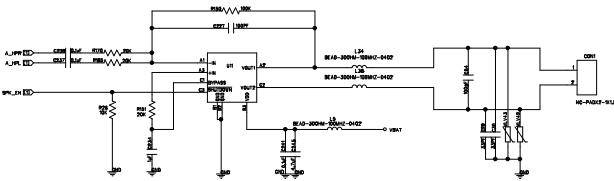
4

3

2

1

REVISION HISTORY			
REV	DATE	DESCRIPTION	BY



Audio PA

TITLE & INFO			
PROJECT NO.			
DESIGNER	DATE	DEVELOPER	
TEST	DATE	COMMENTS	
APPROVED	DATE	CONFIDENTIAL AND PROPRIETARY	



6

5

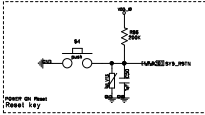
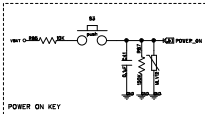
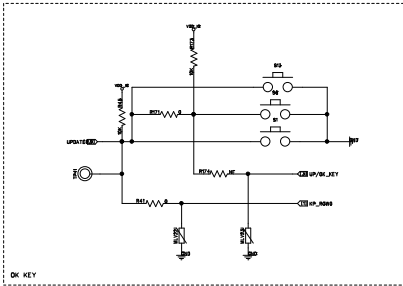
4

3

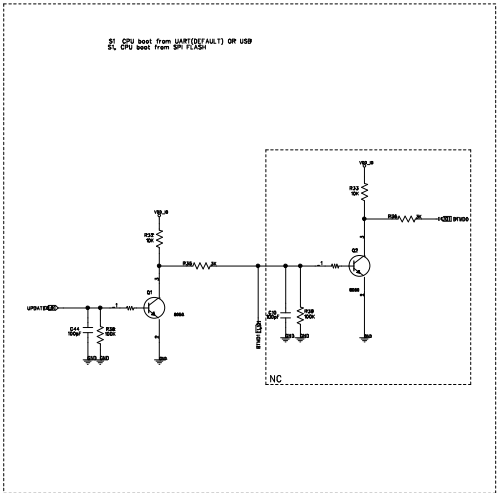
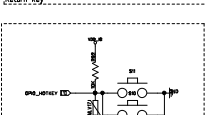
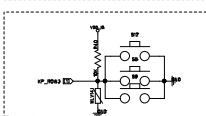
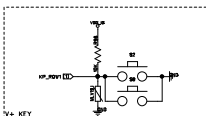
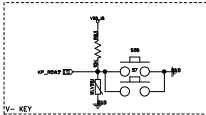
2

1

REVISION RECORD			
REV	APP. NO.	APPROVAL	DATE



KEY



TITLE & REV			
DOCUMENT NO.			
DESIGNED	DATE	DEVELOPED	DATE
CHKD	DATE	CHECKED	DATE
APPROVED	DATE	CONFIRMED	DATE
CONFIDENTIAL AND PROPRIETARY			SHEET 1 OF 1

6

5

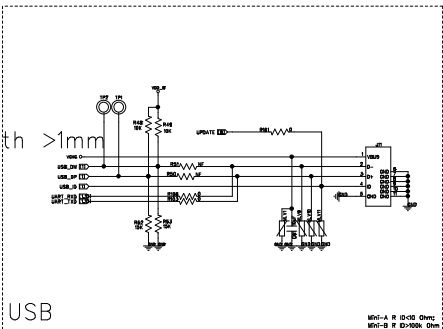
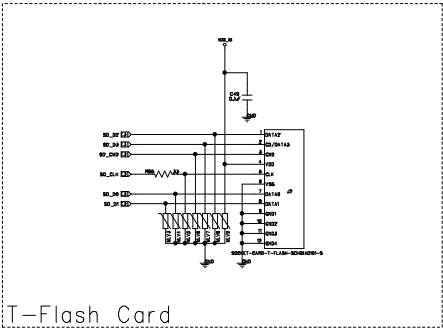
4

3

2

1

REVISION RECORD			
LT#	REV. NO.	APPROVAL	DATE



TITLE & INFO			
DOCUMENT NO.			
DESIGNER	DATE	DEVELOPER	
CHK	DATE	COMPANY	
APPROVED	DATE	CONFIDENTIAL AND PROPRIETARY	
			SHEET 1 OF 1



