

D

D

C

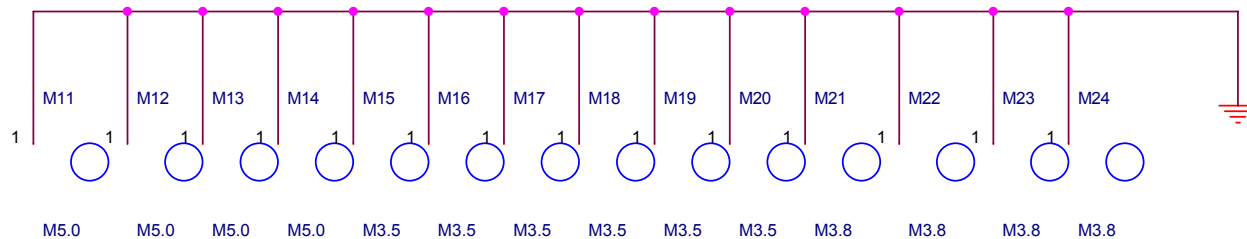
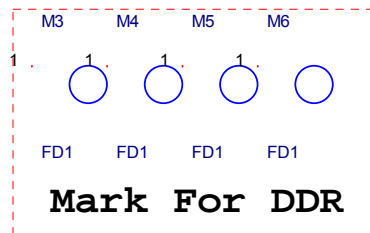
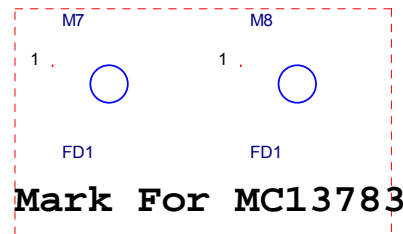
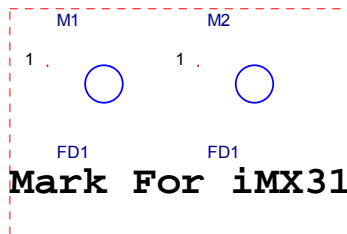
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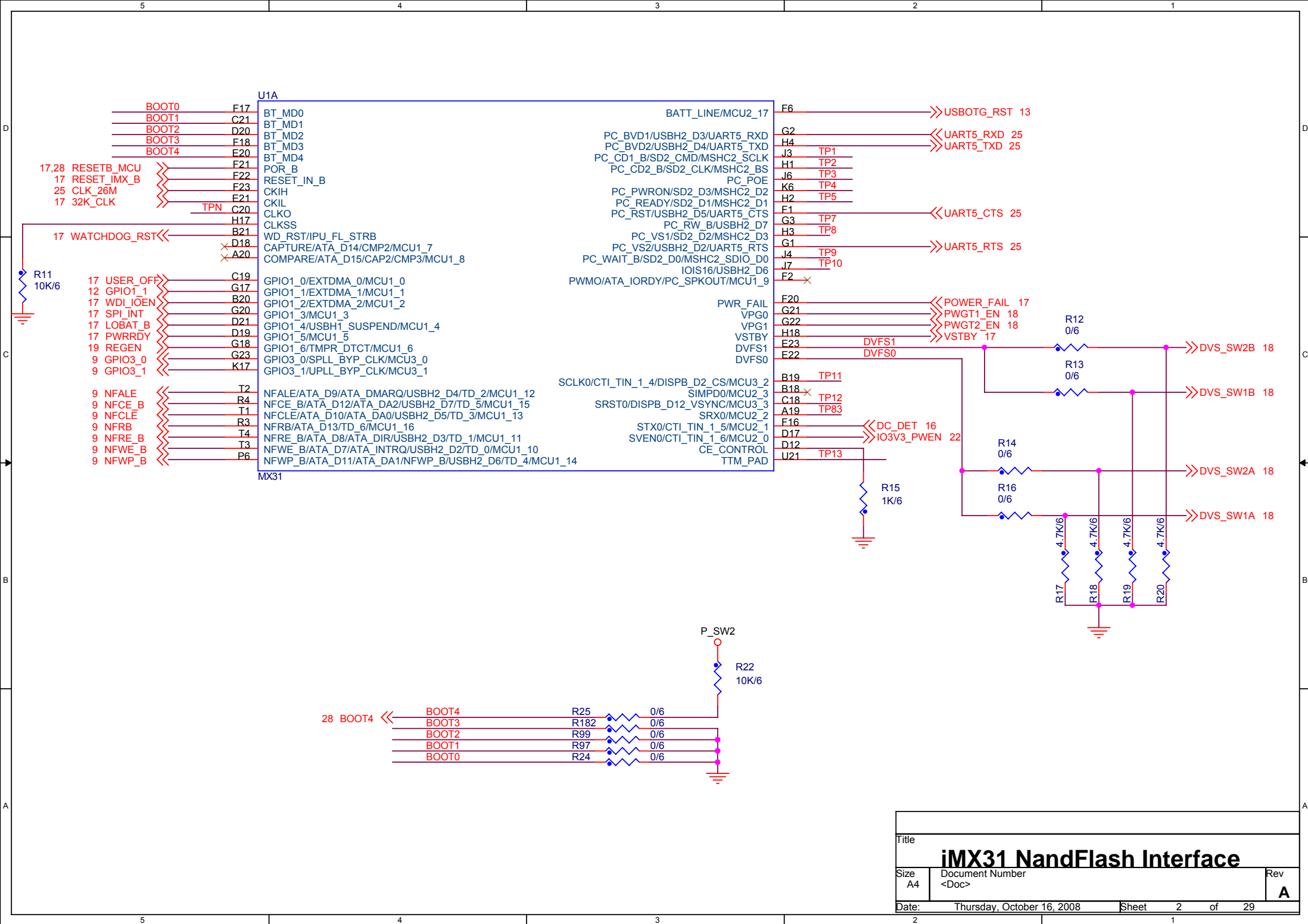
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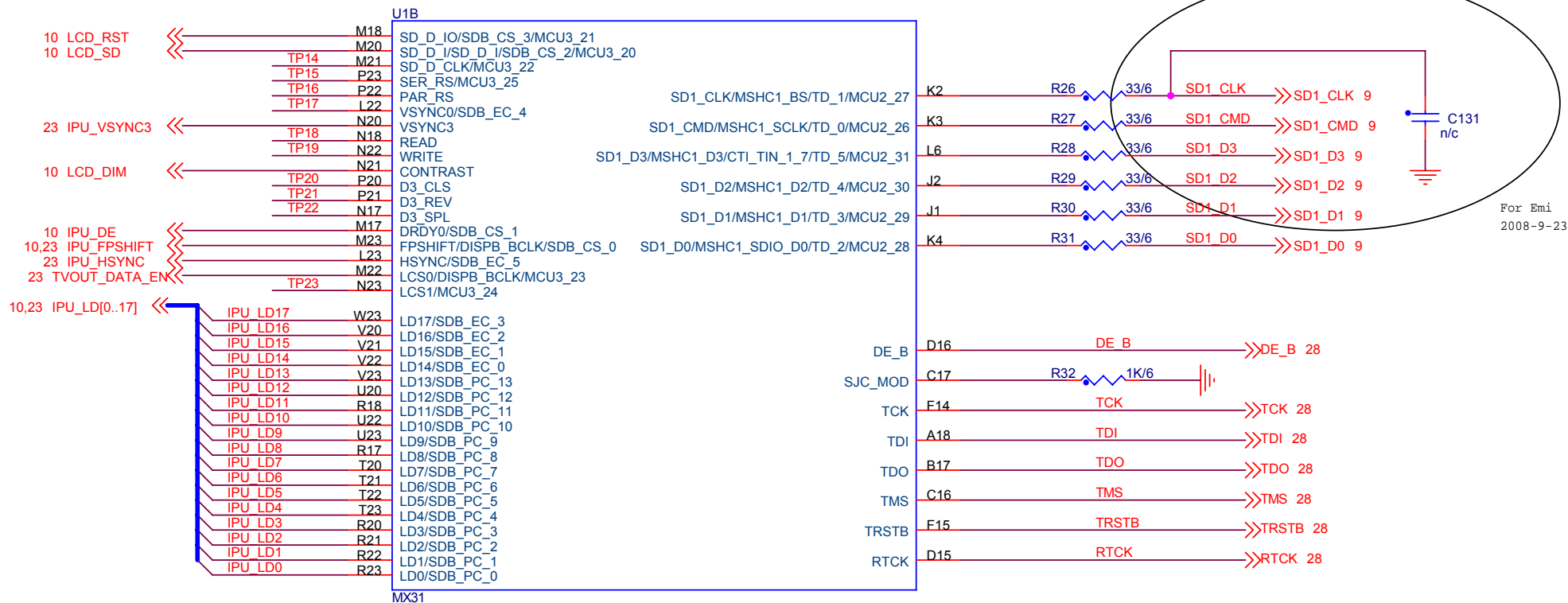
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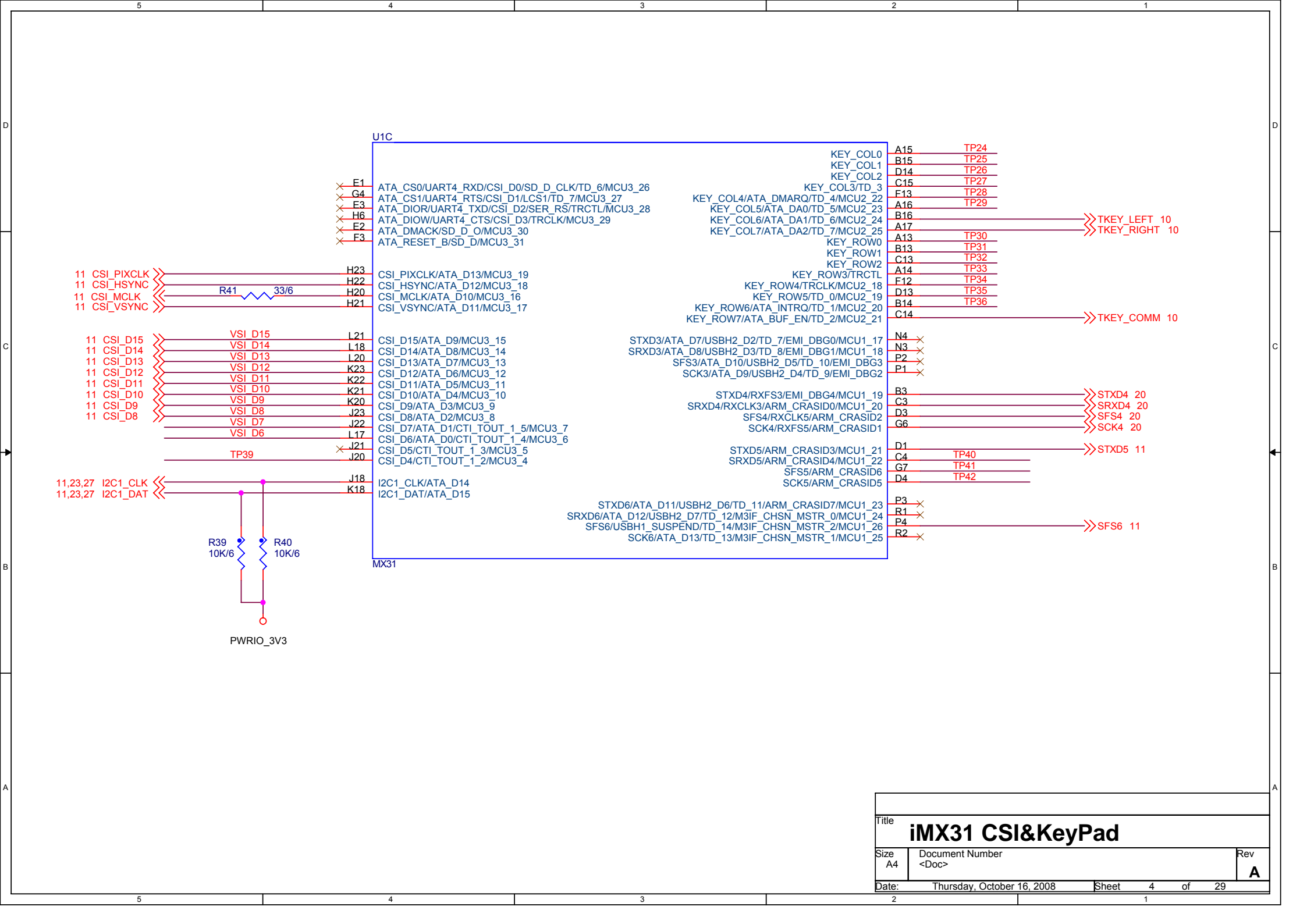
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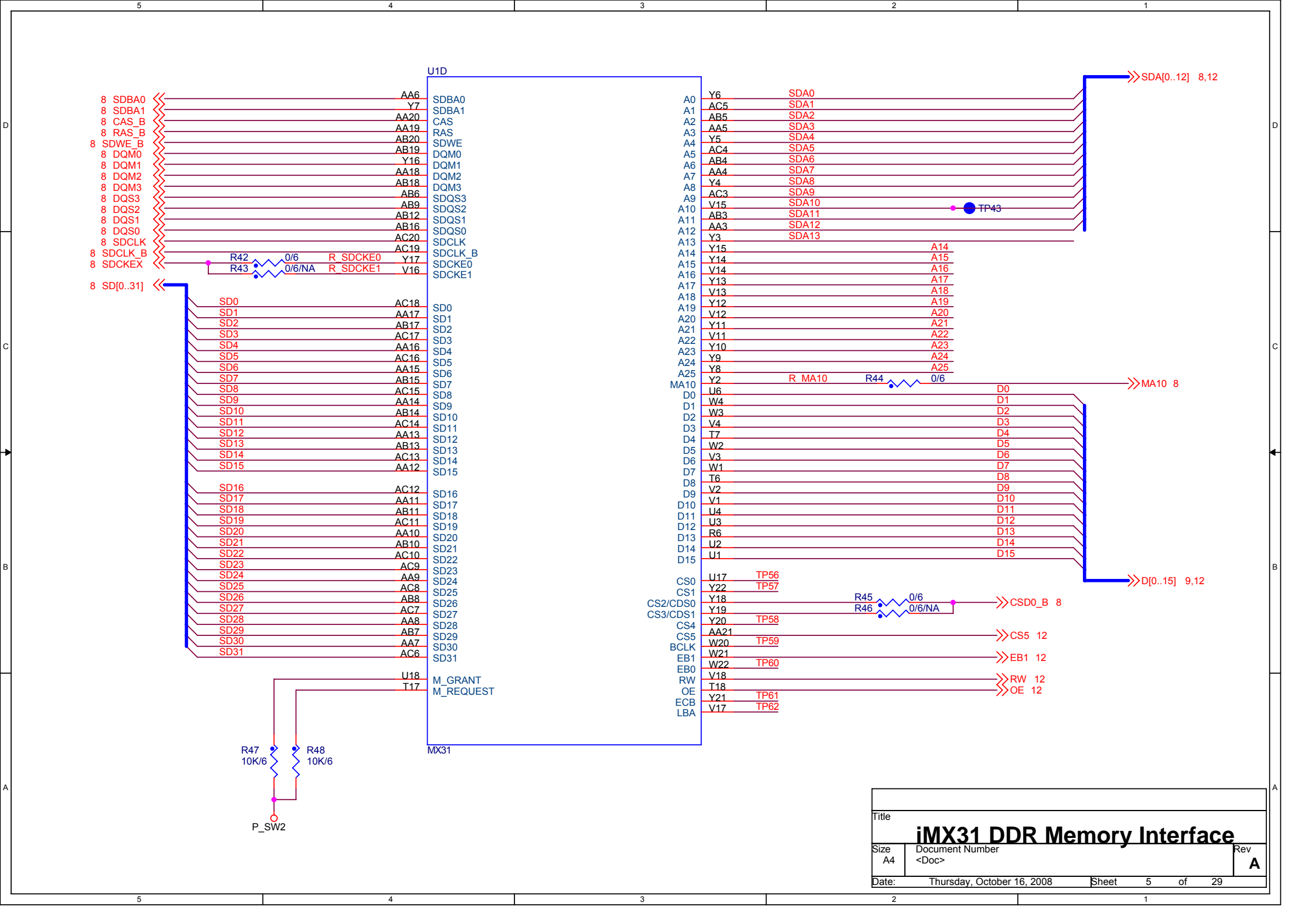


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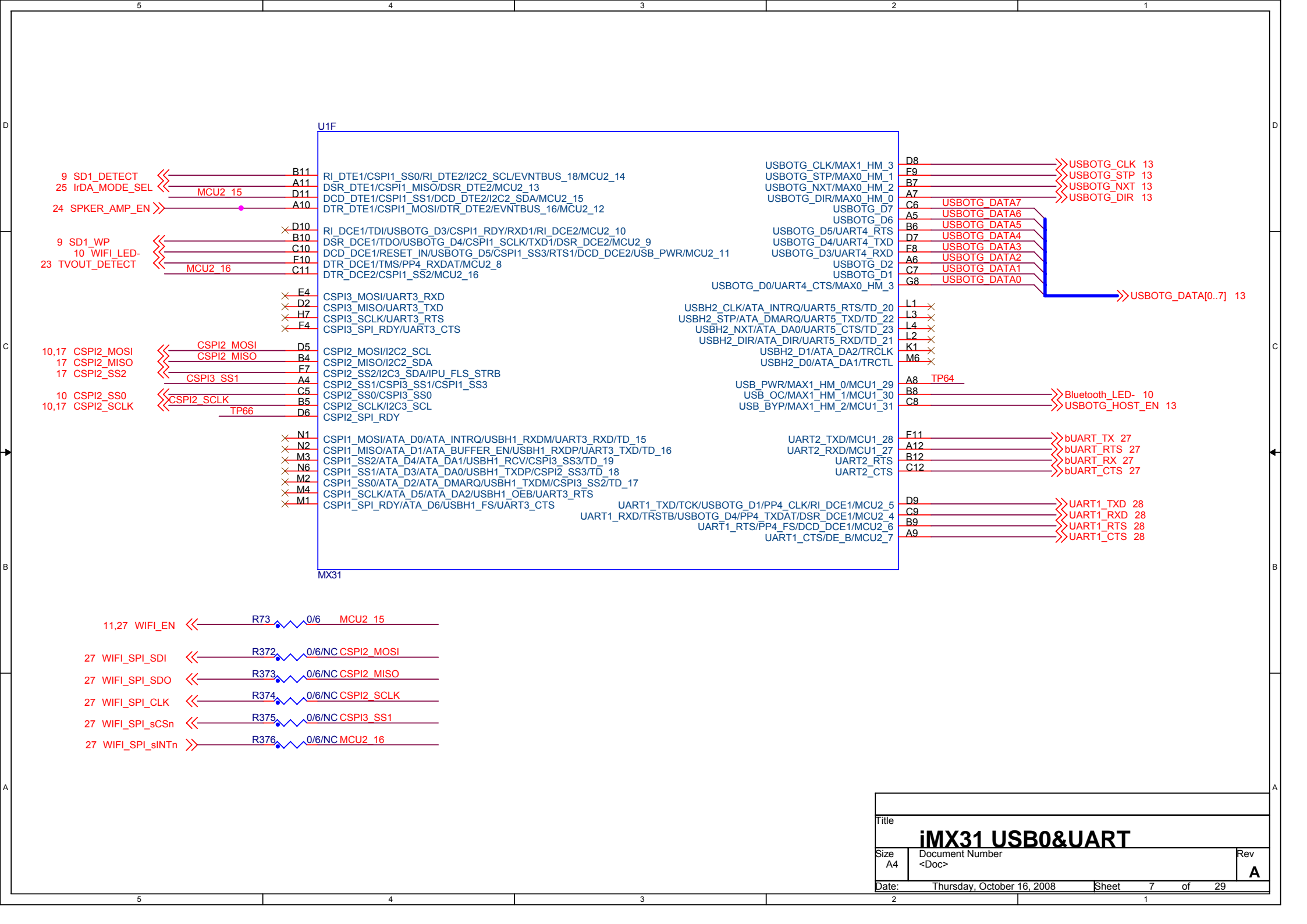


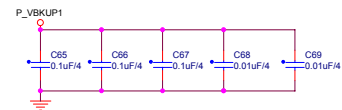
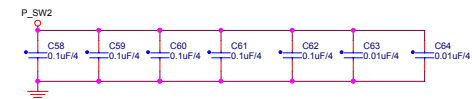
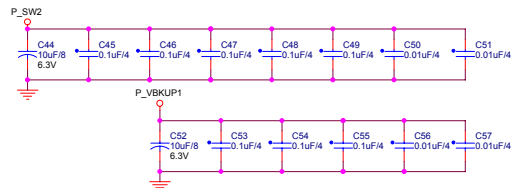
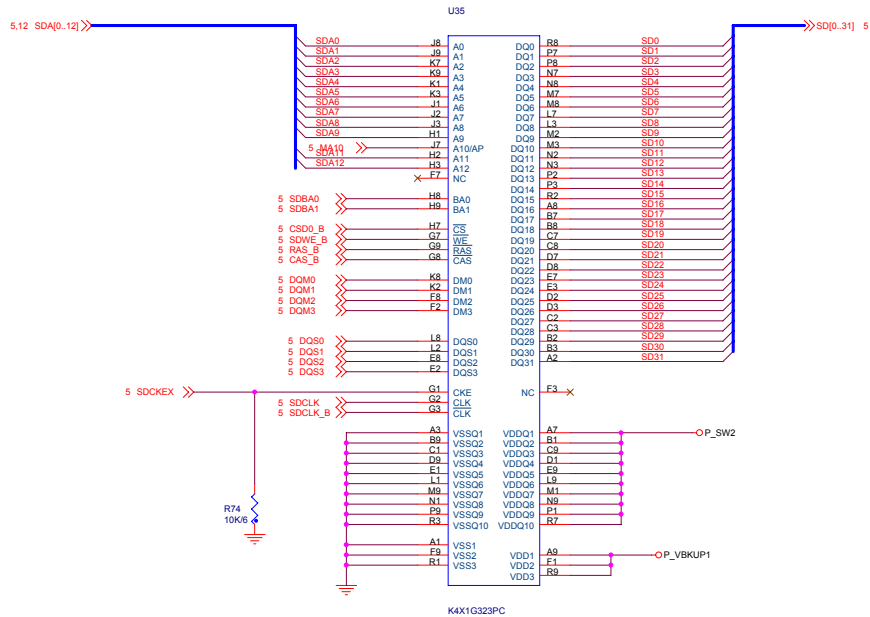




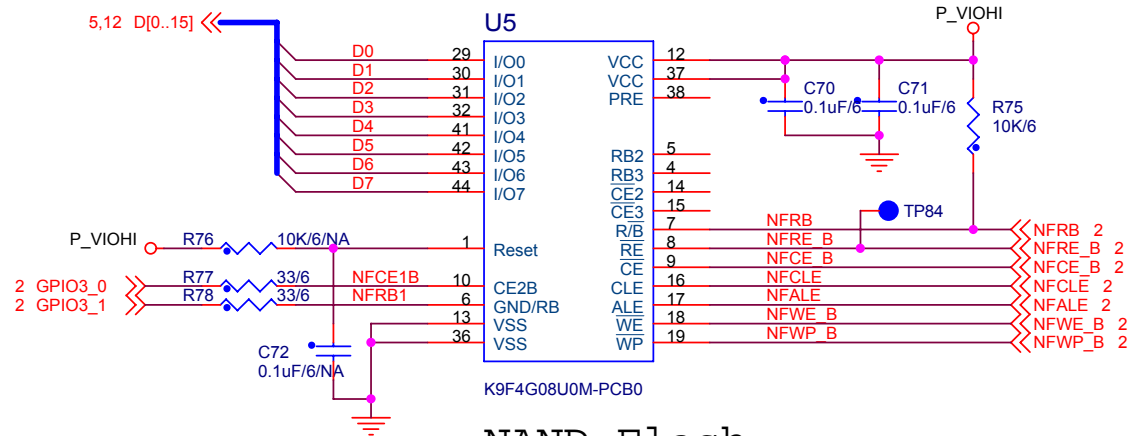




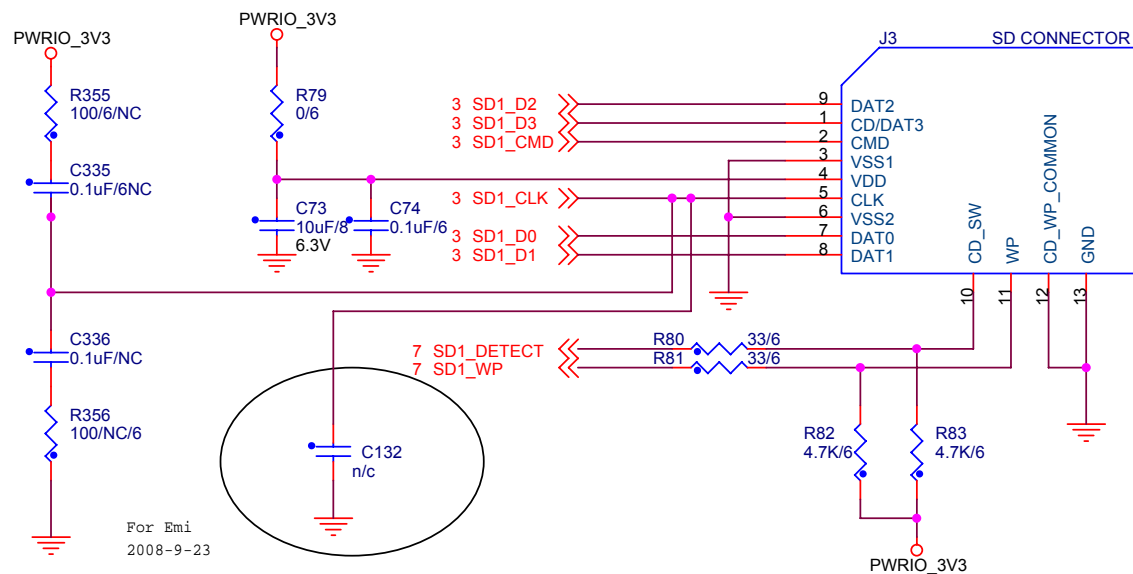
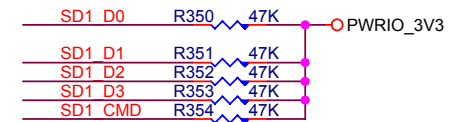






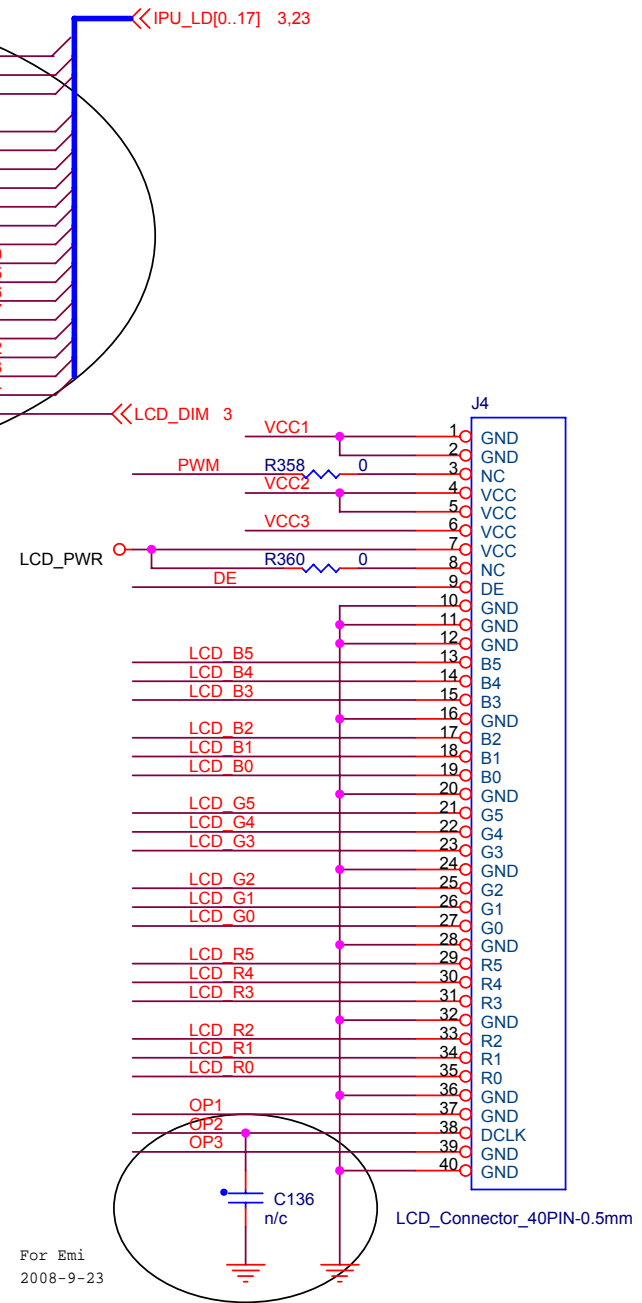
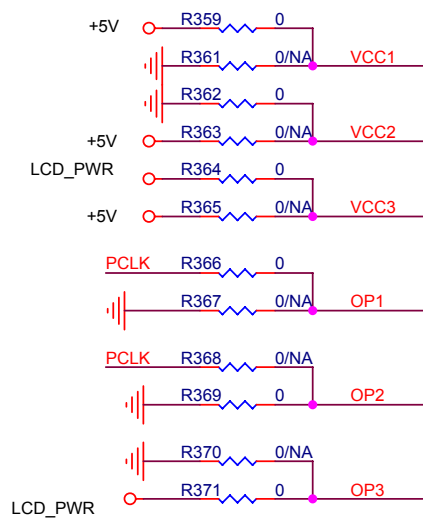
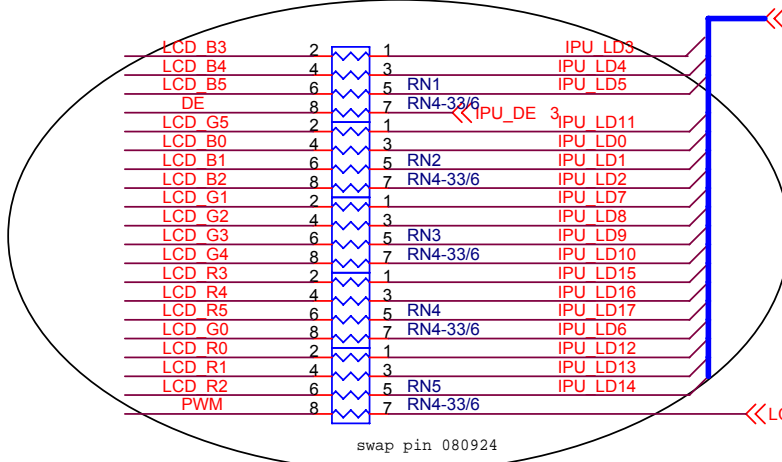
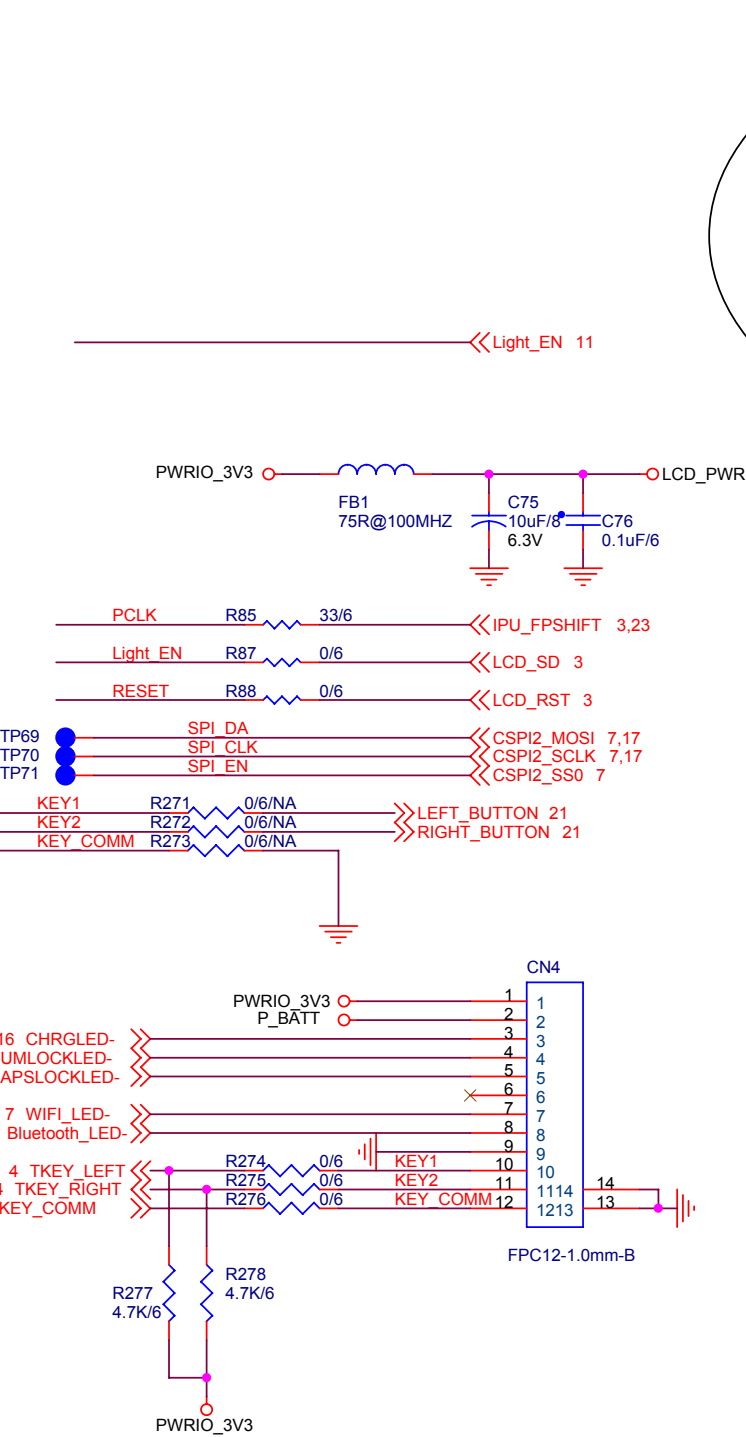


## NAND Flash

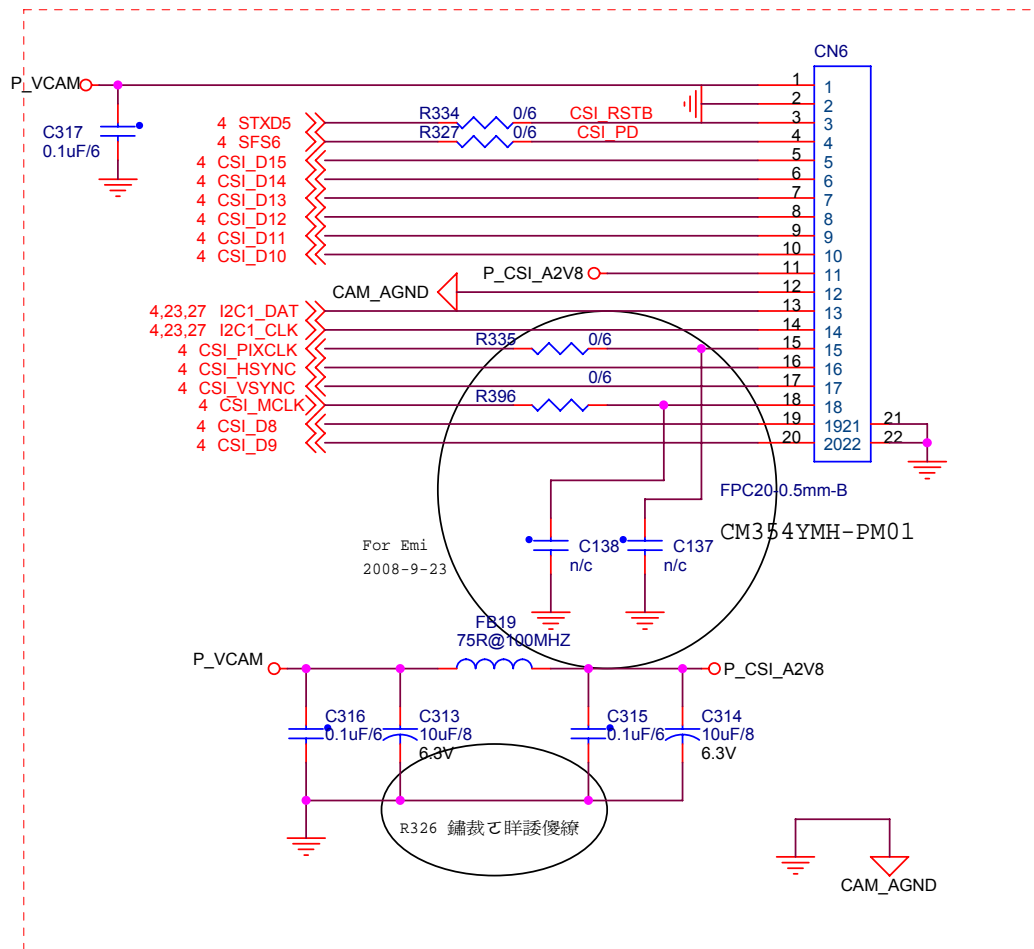
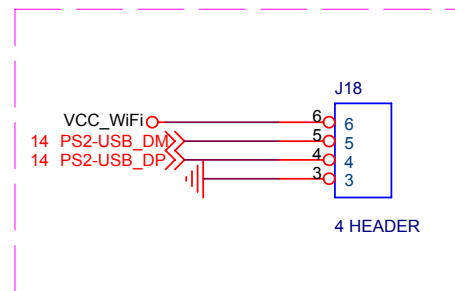
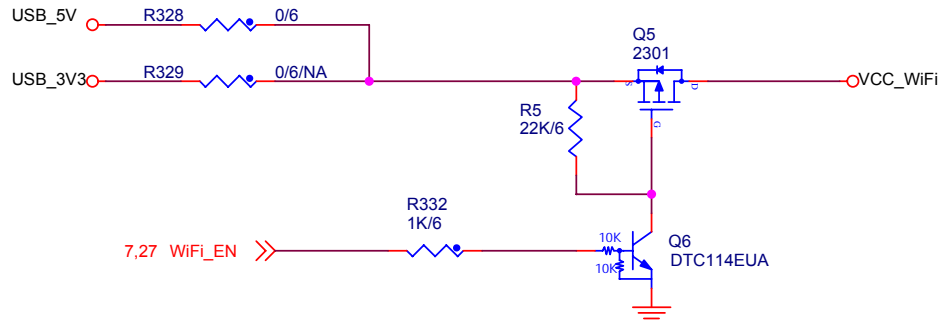


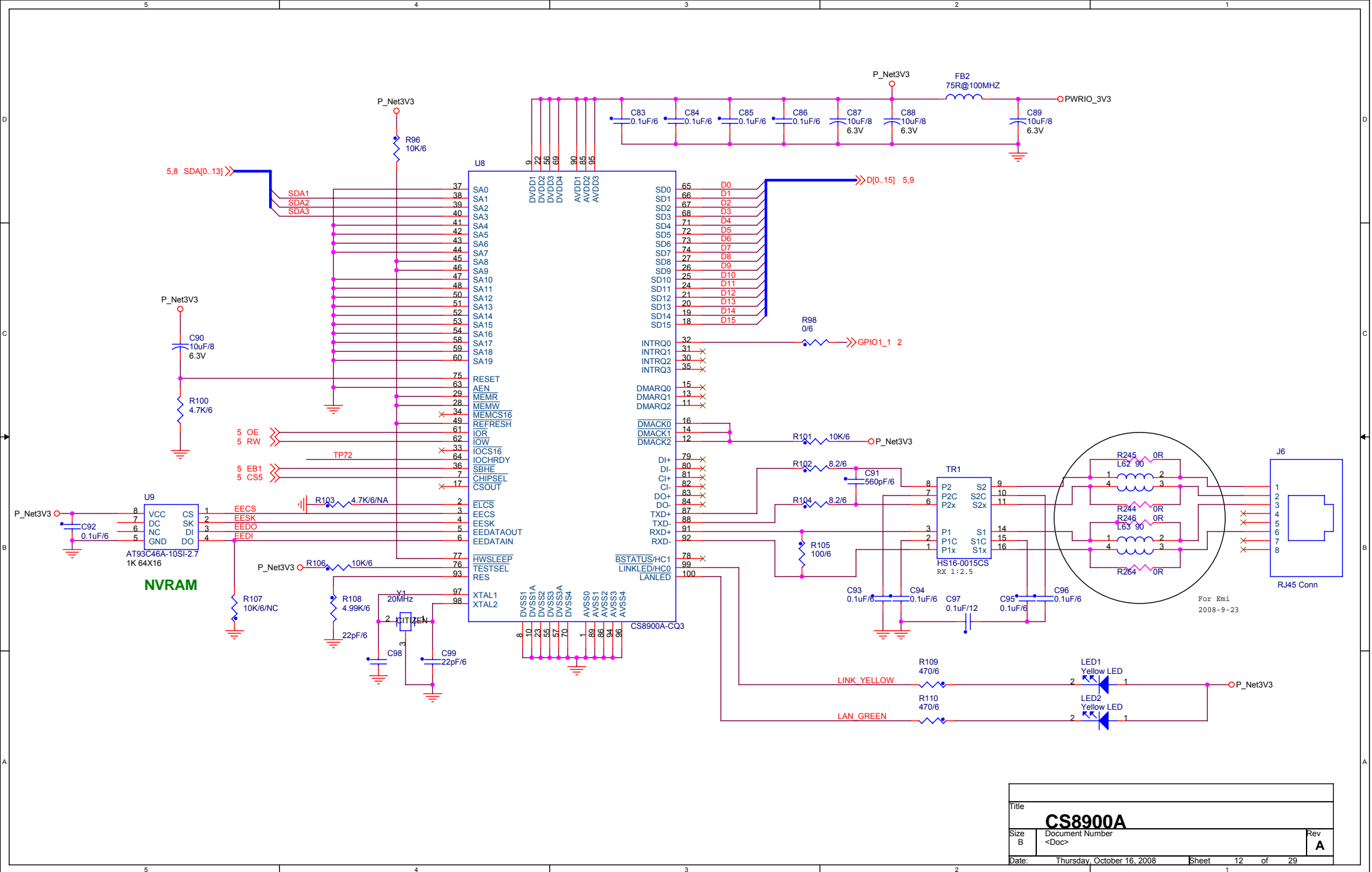
For Emi  
2008-9-23

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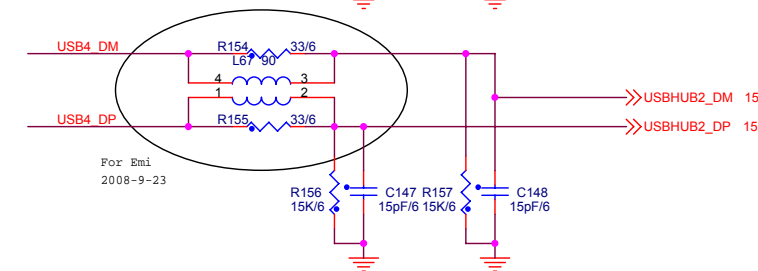
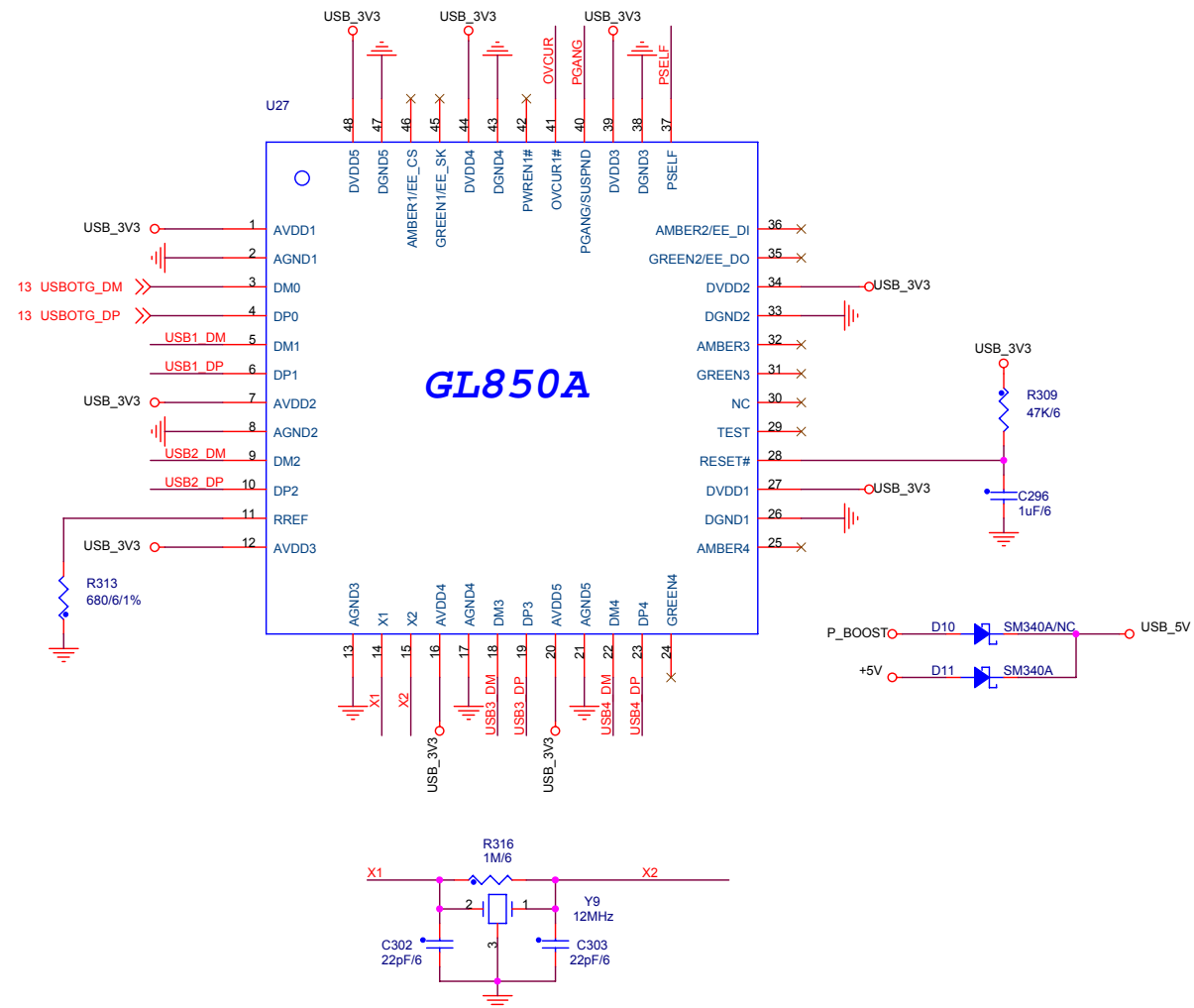
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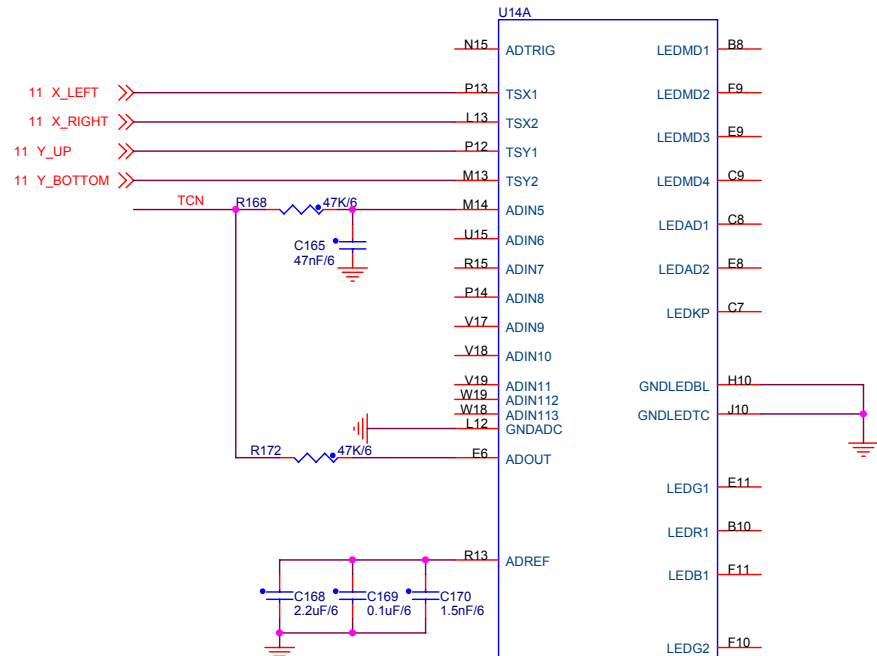
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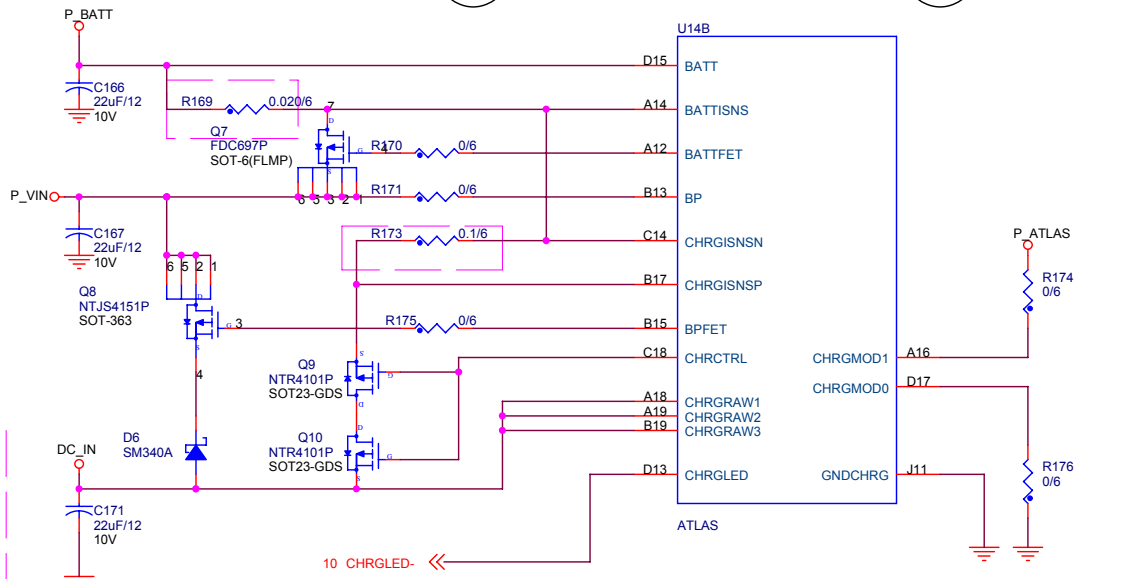
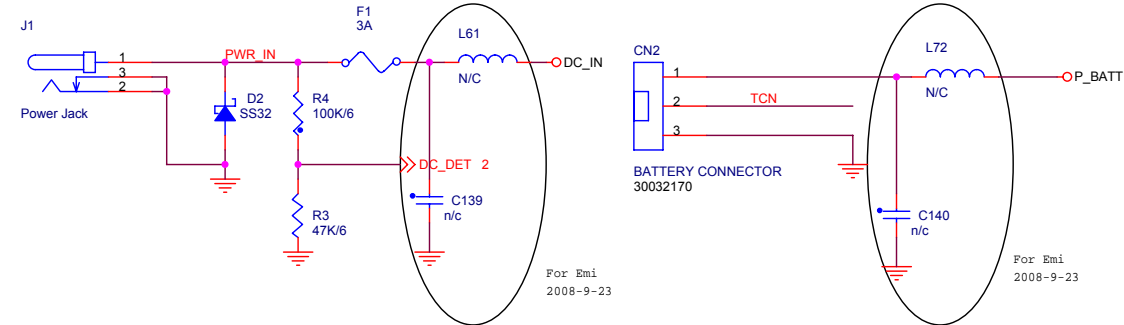
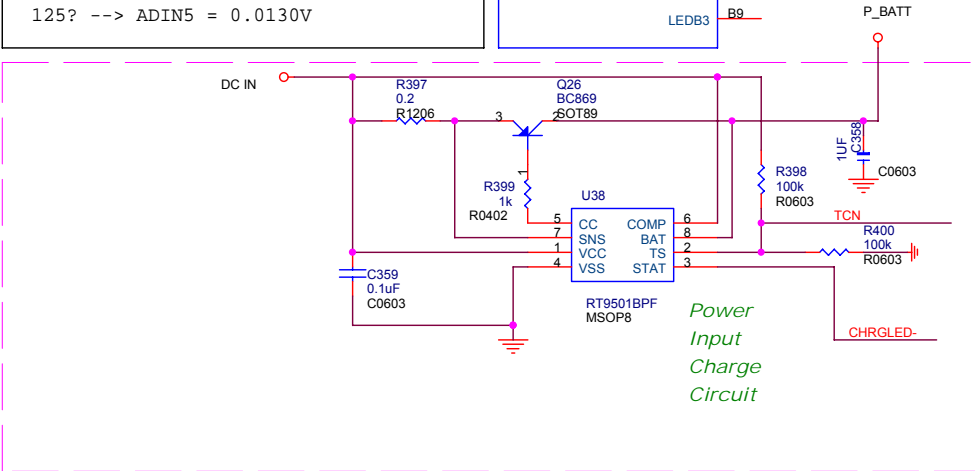
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ADIN5 Input versus temperature with Low Pass Filter  
 $F_s = 450\text{MHz}$

-20? --> ADIN5 = 1.89V  
 25? --> ADIN5 = 0.48V  
 60? --> ADIN5 = 0.11V  
 80? --> ADIN5 = 0.057V  
 100? --> ADIN5 = 0.047V  
 125? --> ADIN5 = 0.0130V

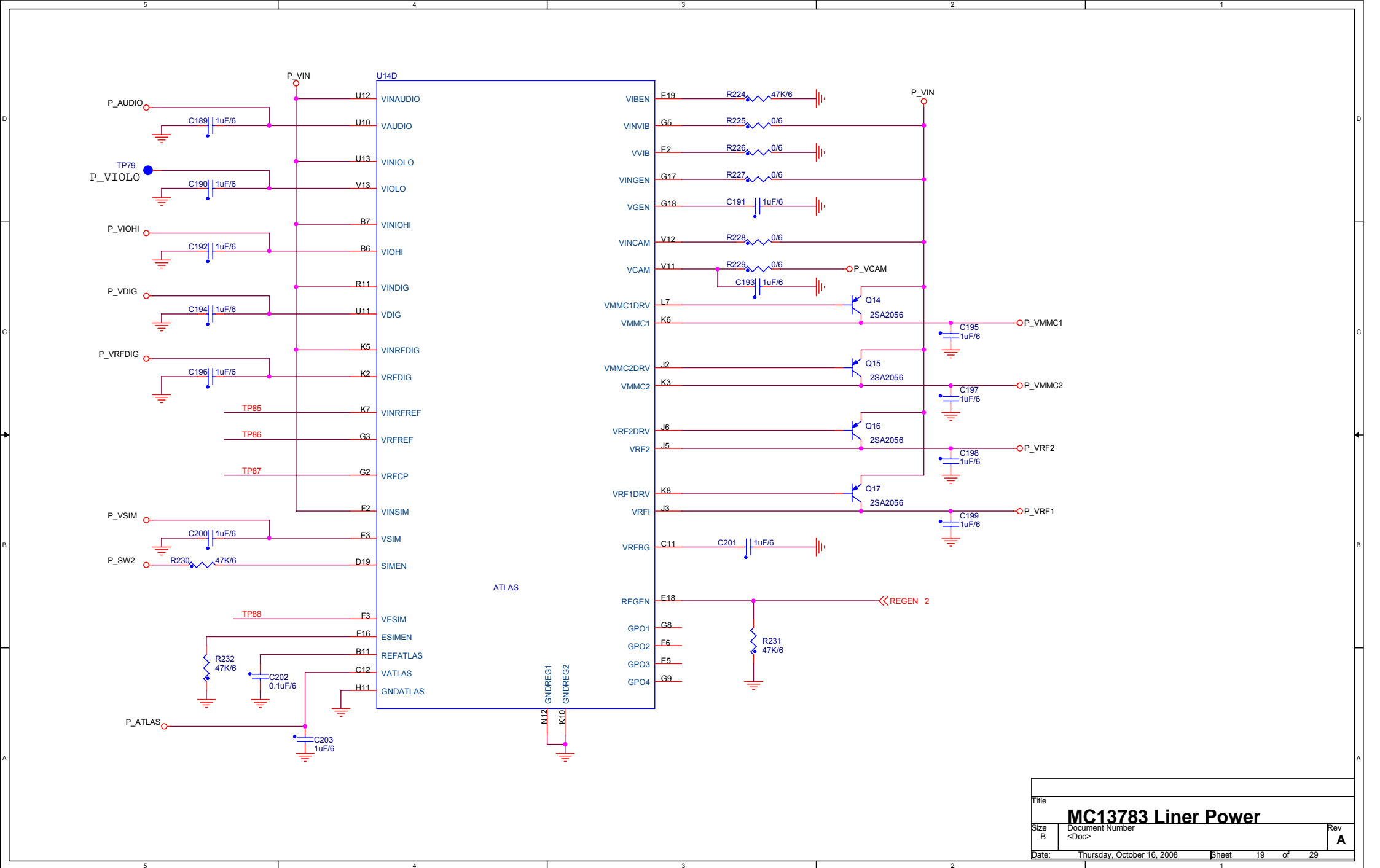


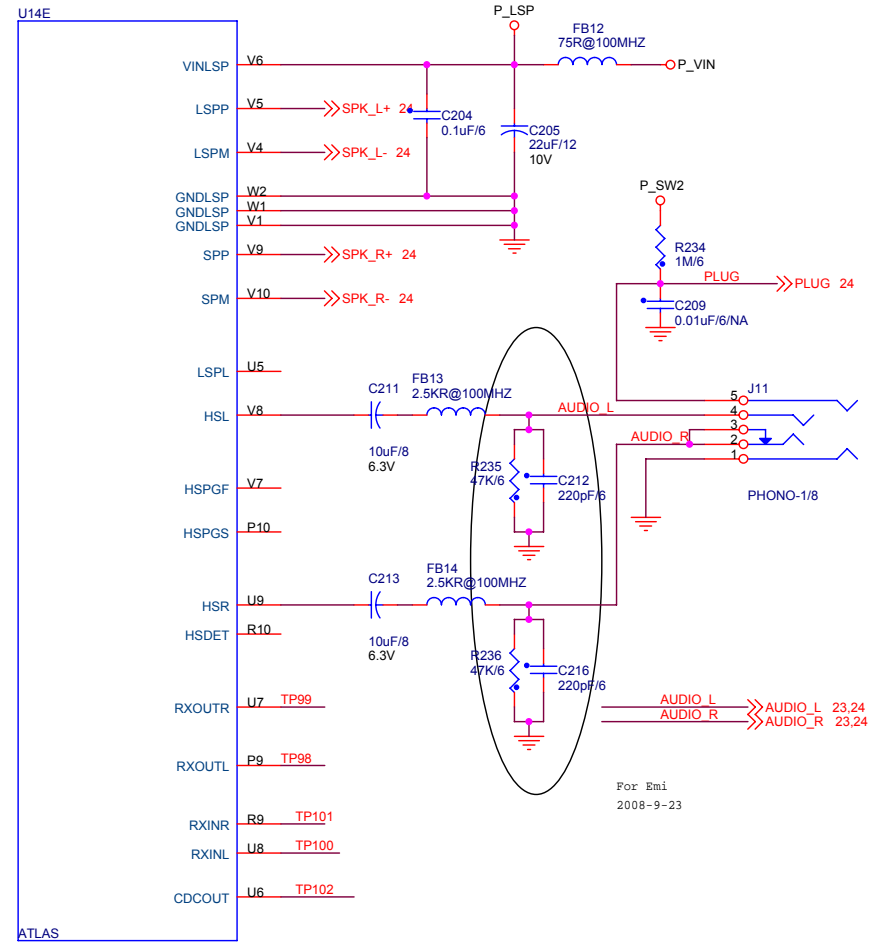
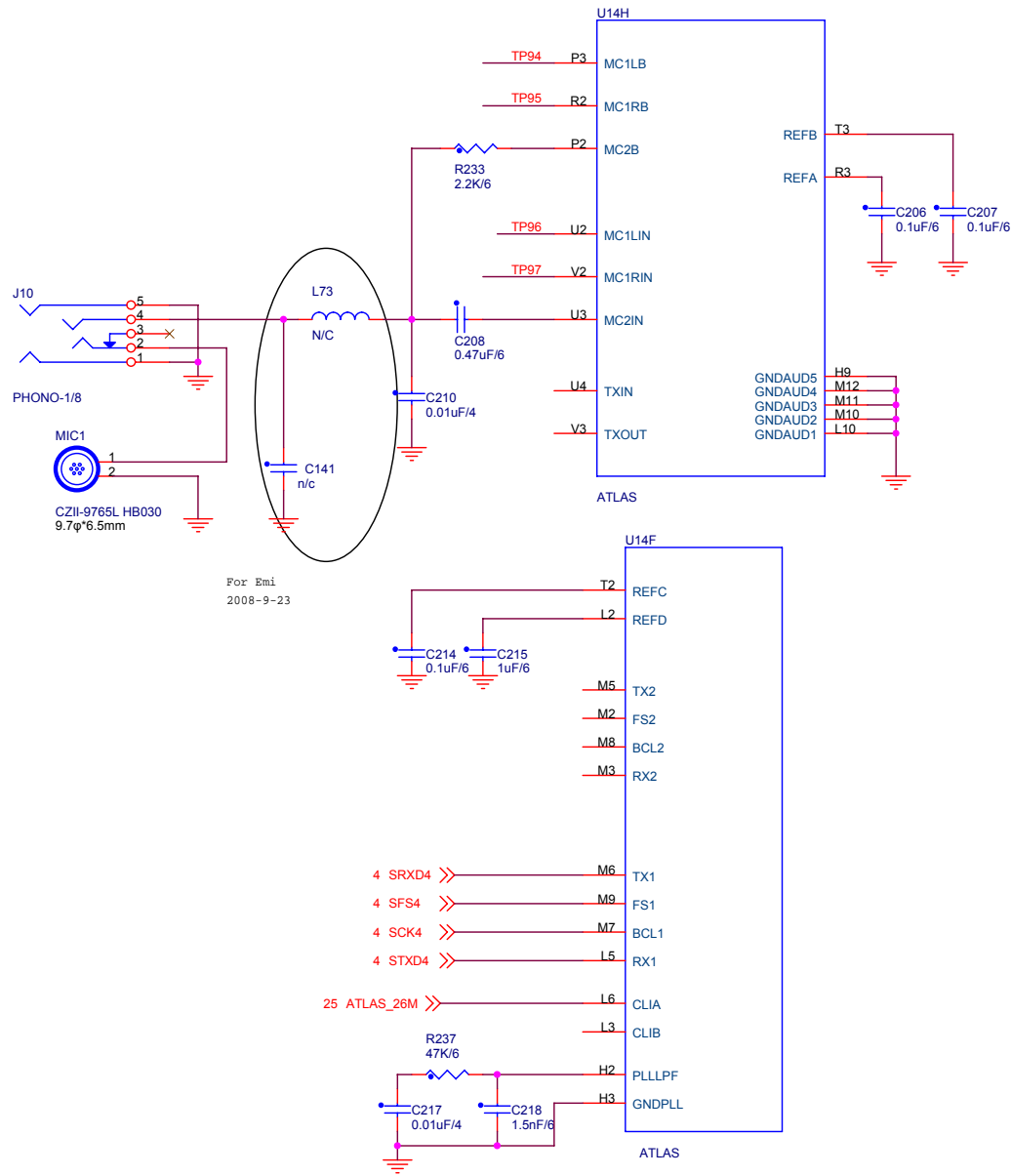
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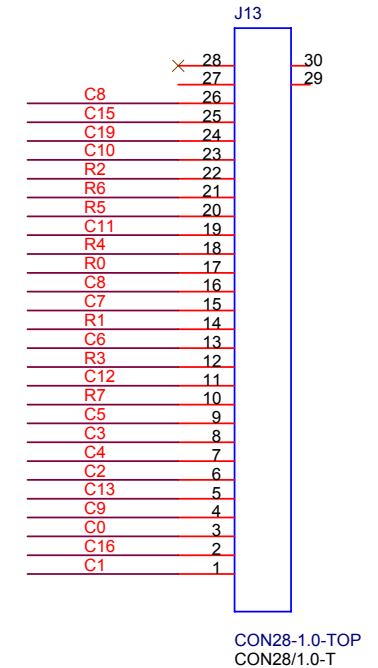
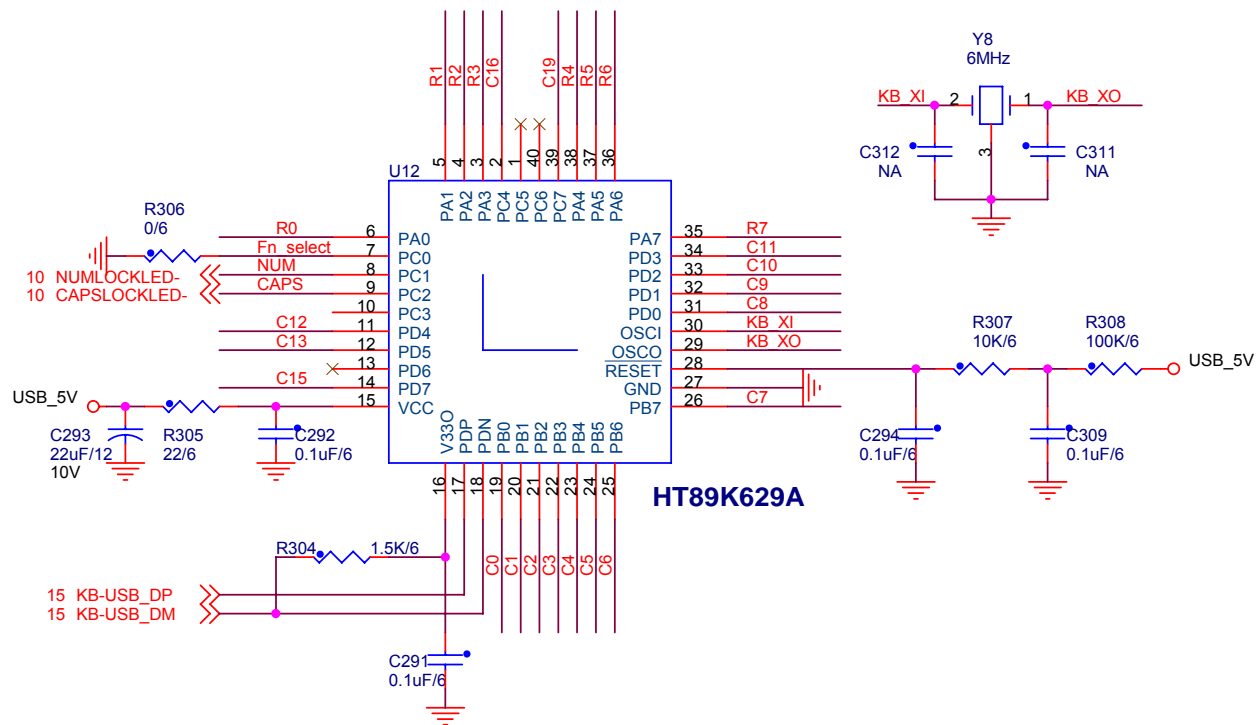
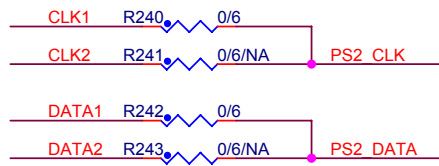
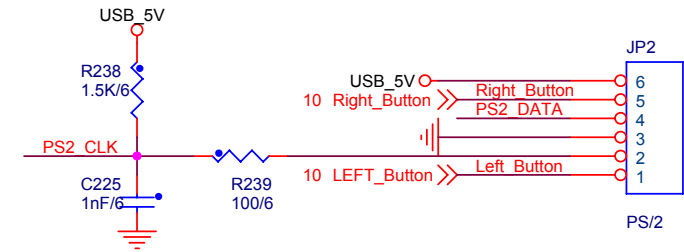
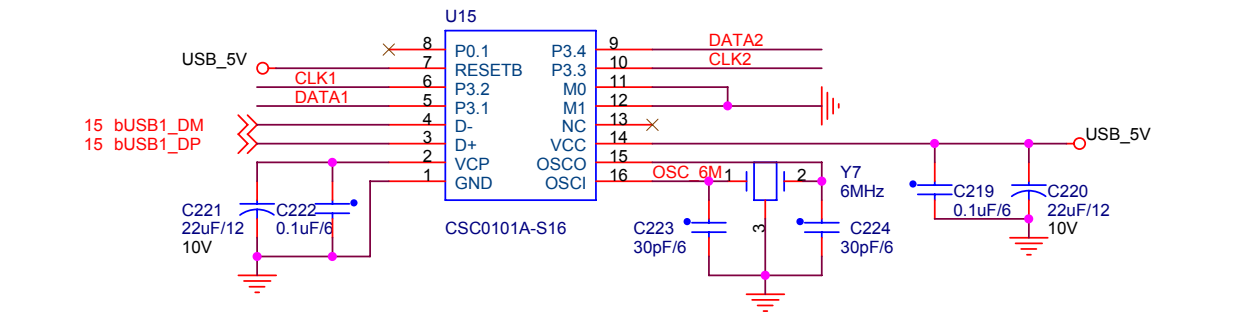


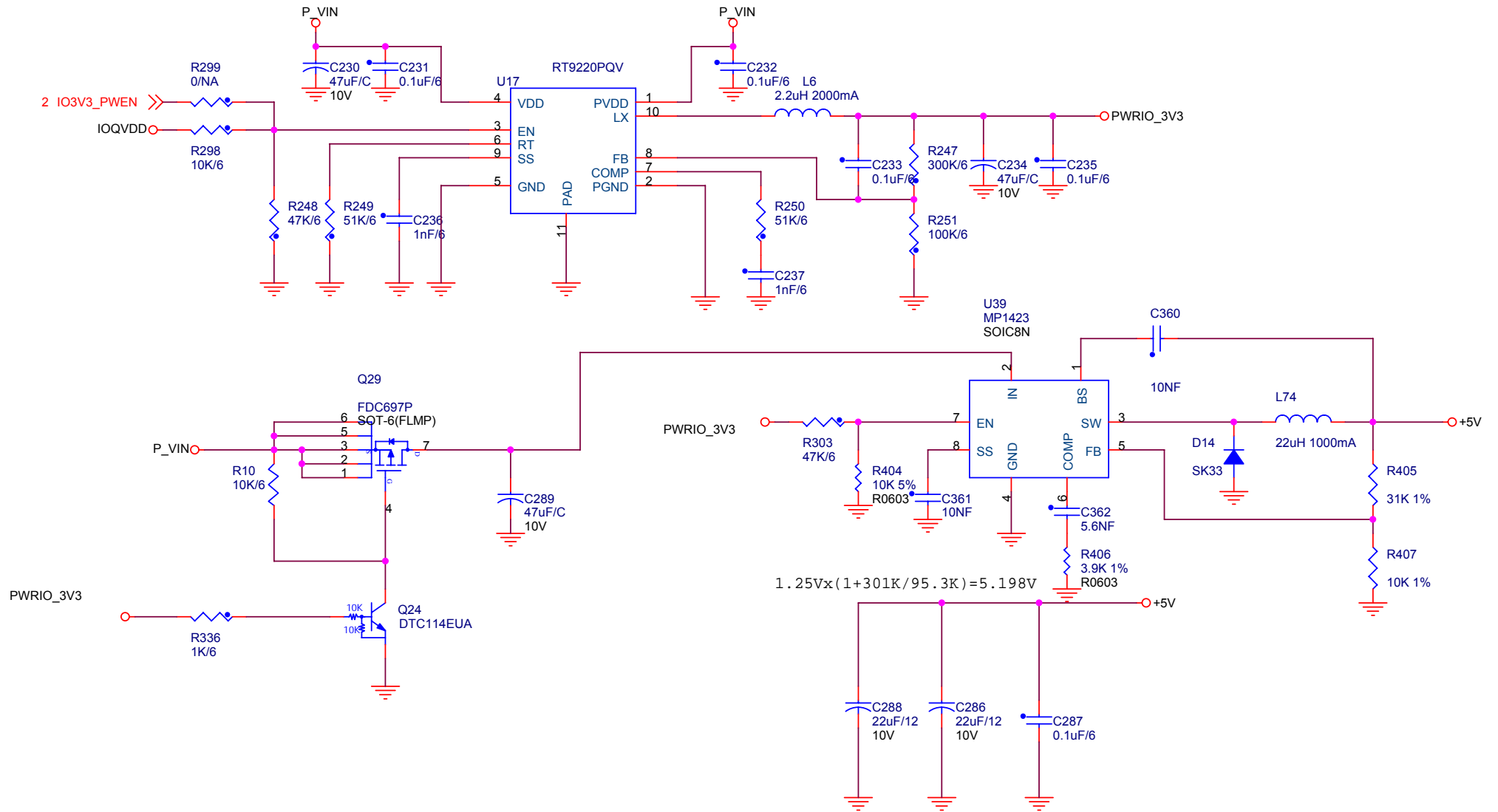




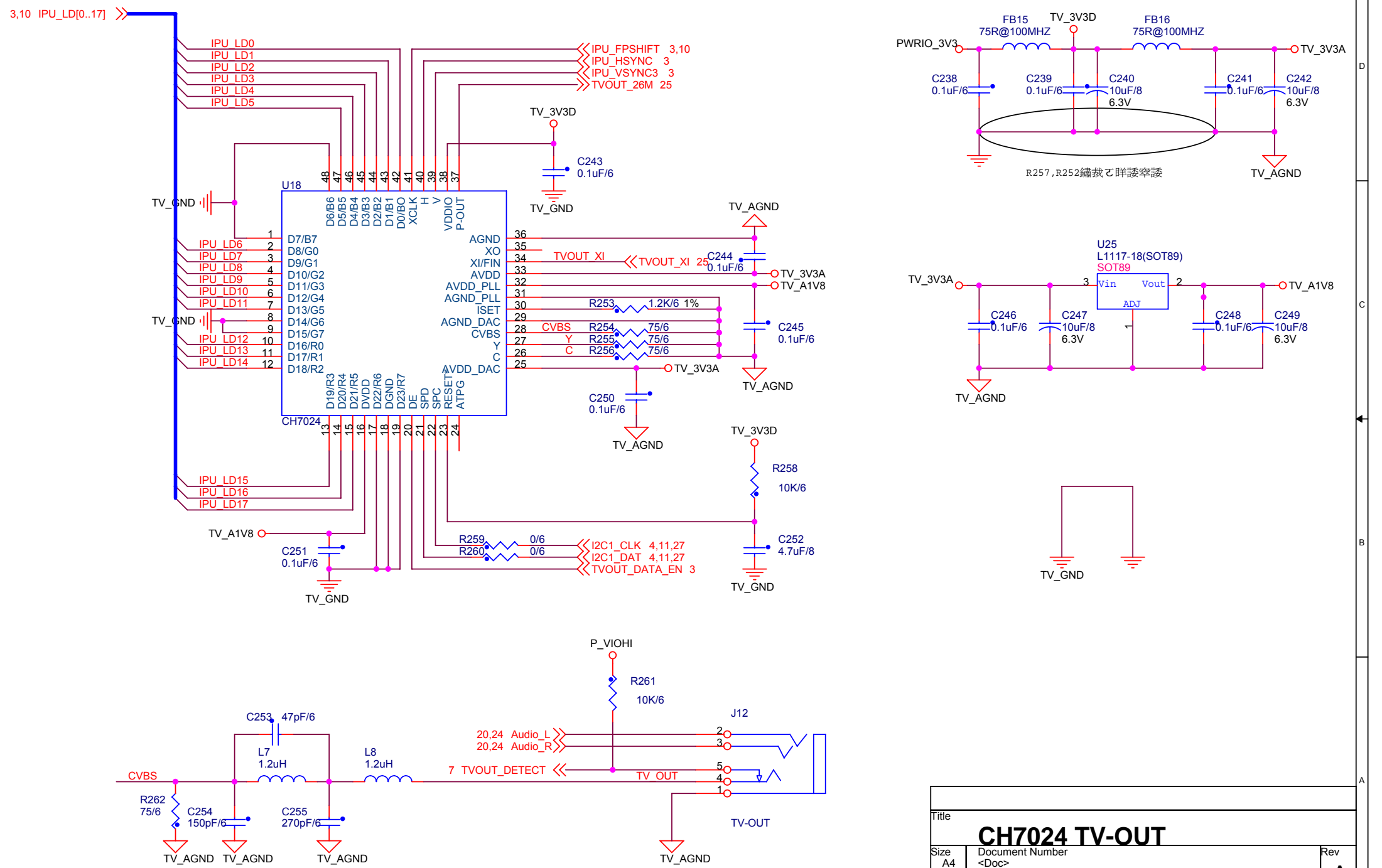


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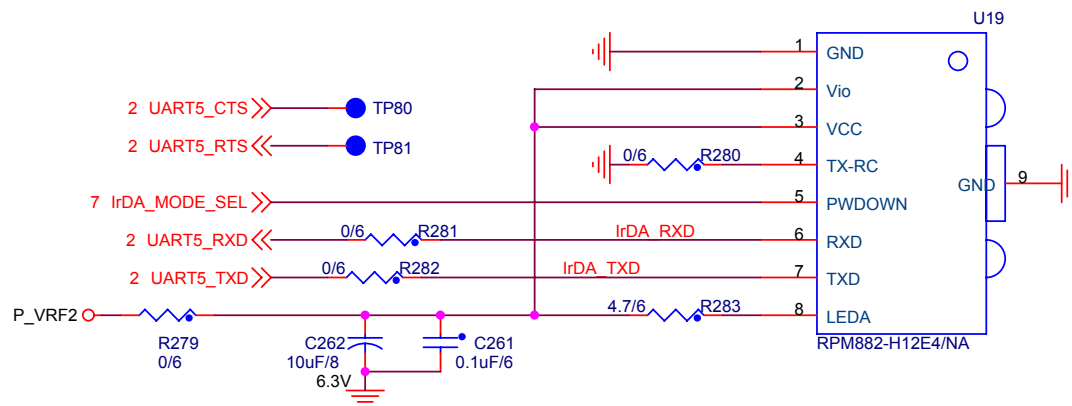
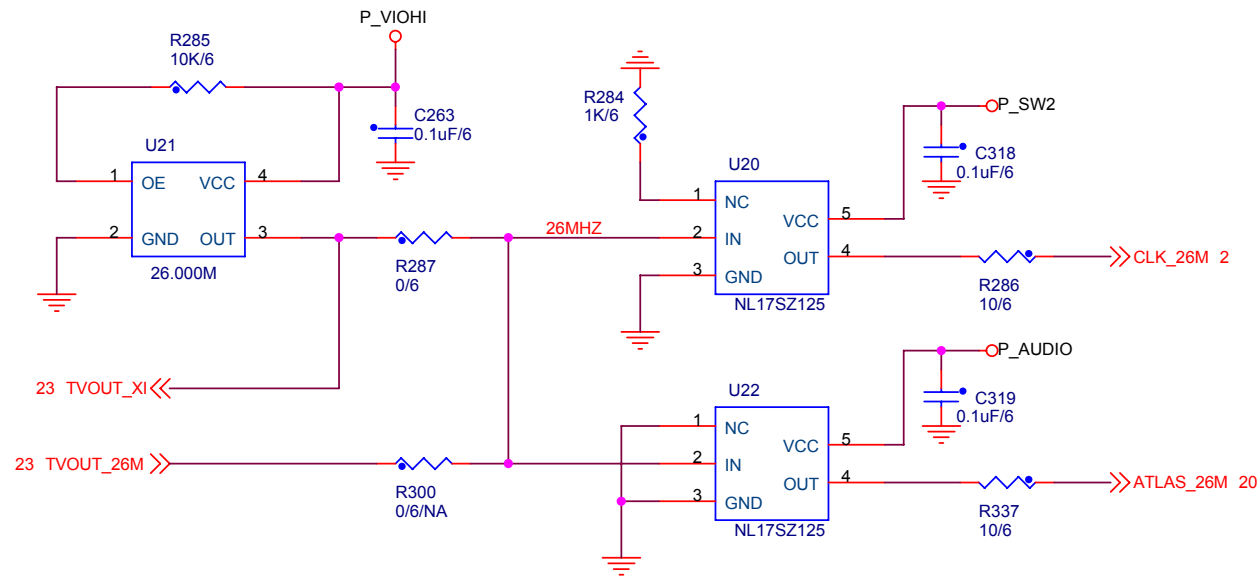
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Size	Document Number	Rev
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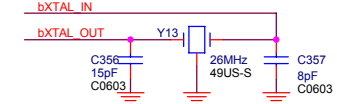
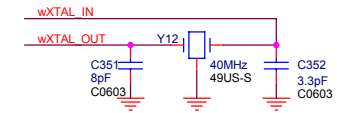
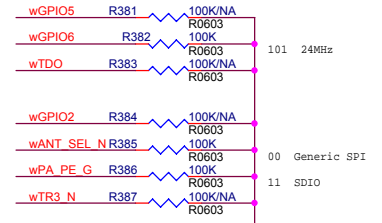
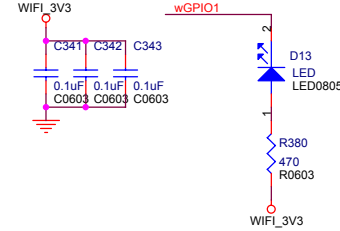
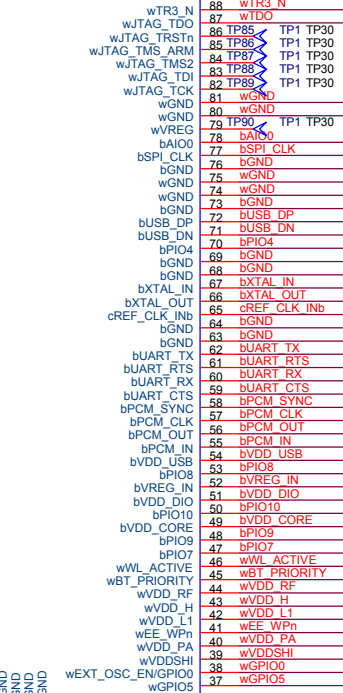
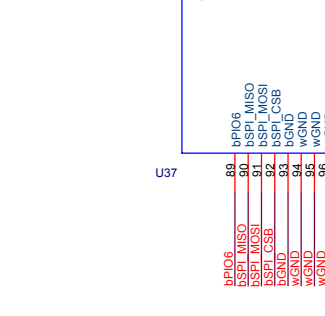
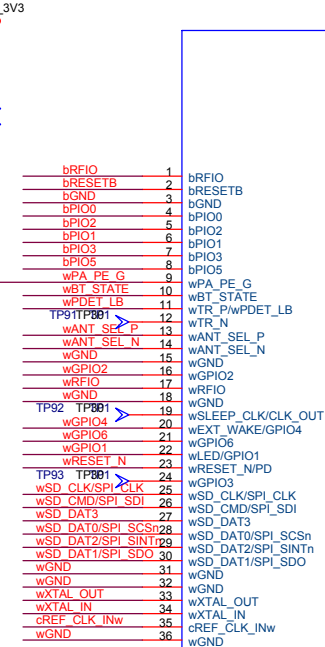
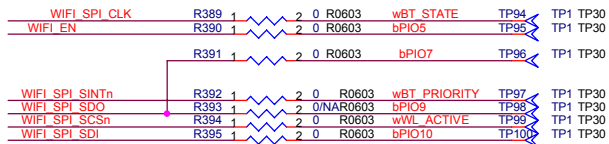
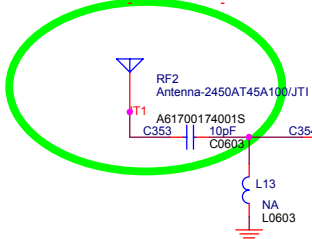
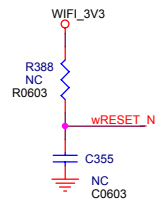
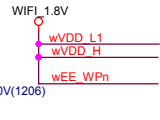
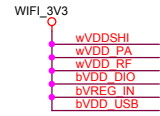
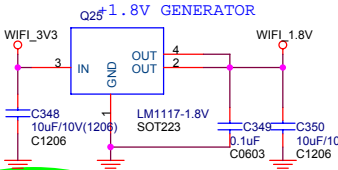
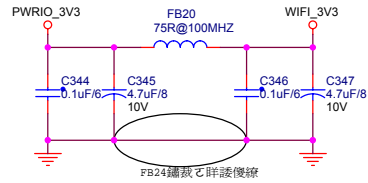
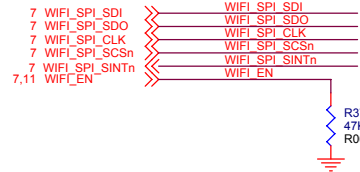
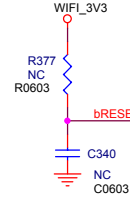
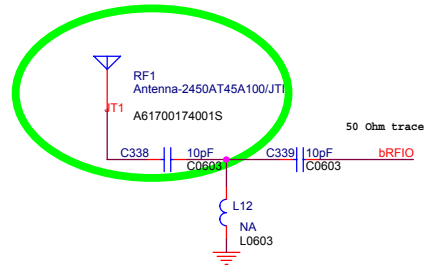
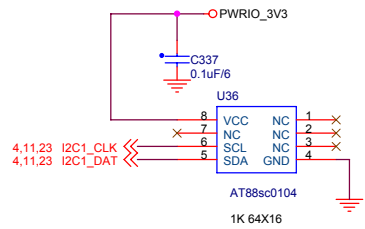




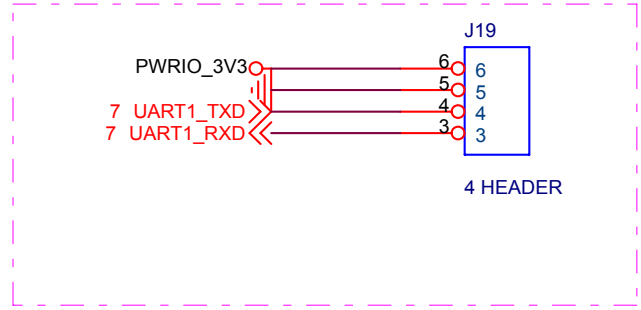
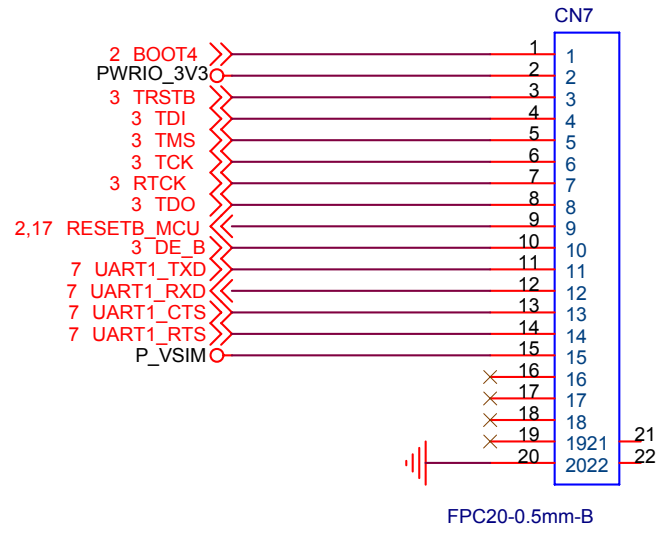
铝隱祥玆璃

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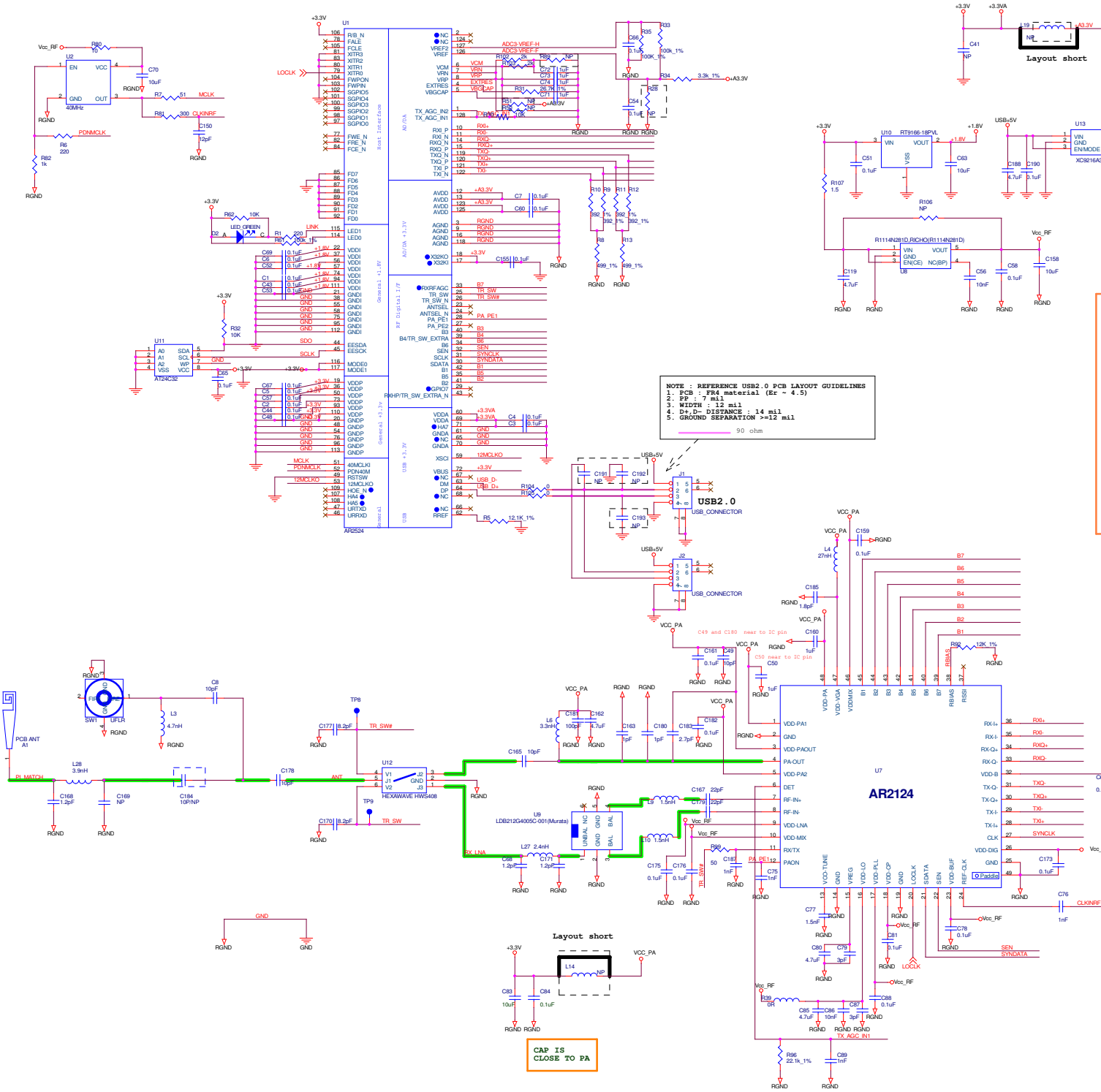




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**PCB STACK-UP FOR USB DONGLE**

LAYER 1	1 OZ Cu
LAYER 2	FR4--7 mil
LAYER 3	1 OZ Cu
LAYER 4	FR4--20 mil
LAYER 5	1 OZ Cu
LAYER 6	FR4--7 mil
LAYER 7	1 OZ Cu

50 Ohm RF thickness line 13 mil width  
Note: the PCB material: FR4 Er= 4.5  
PCB Total thickness=1mm

TITLE: F158W		
DRN:	DWG NO:	
CKD:	REV:	
APPD:	SHEET 1 OF 1	
Amendment History		
Date	Release	Description
2008/07/4	A1	first release
Section ALL Circuit		
Size	Document Number	Rev
Custom		02
Date:	47-01-08	Sheet 1 of 1