

HPA

WARR
IF SEA

AP1875H1F11

RFOUT

RFIN

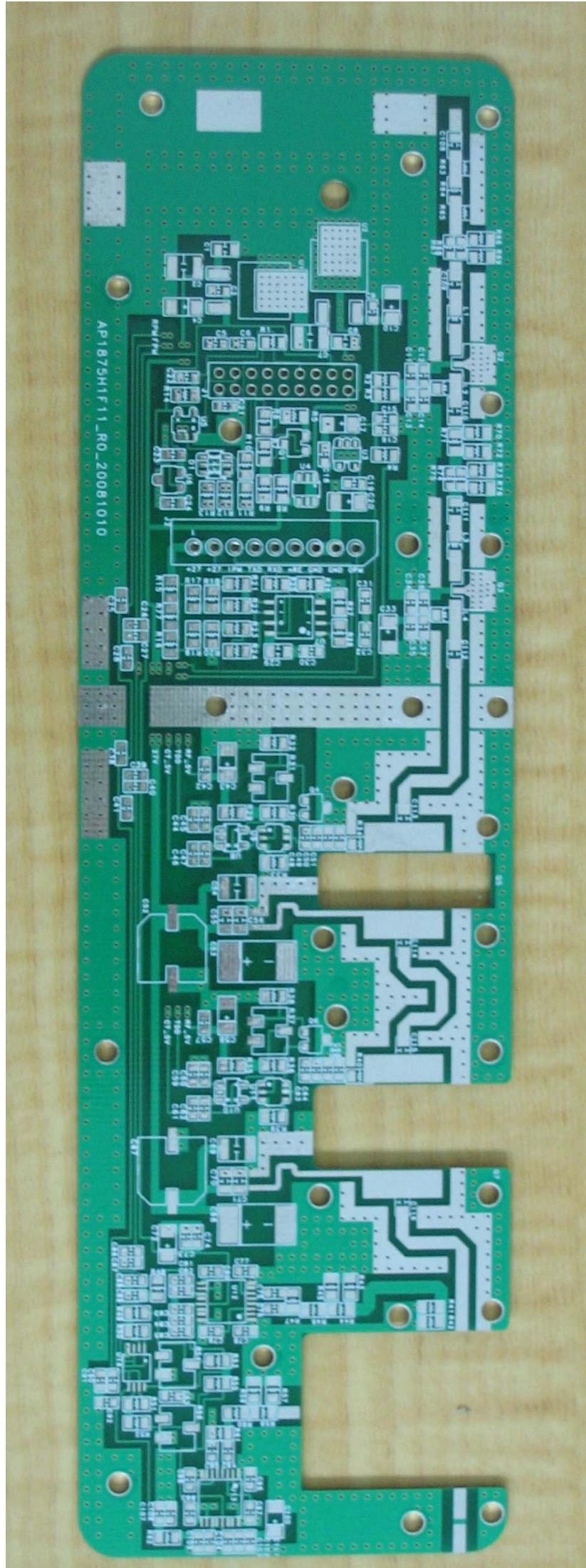
TY VOID
REMOVED

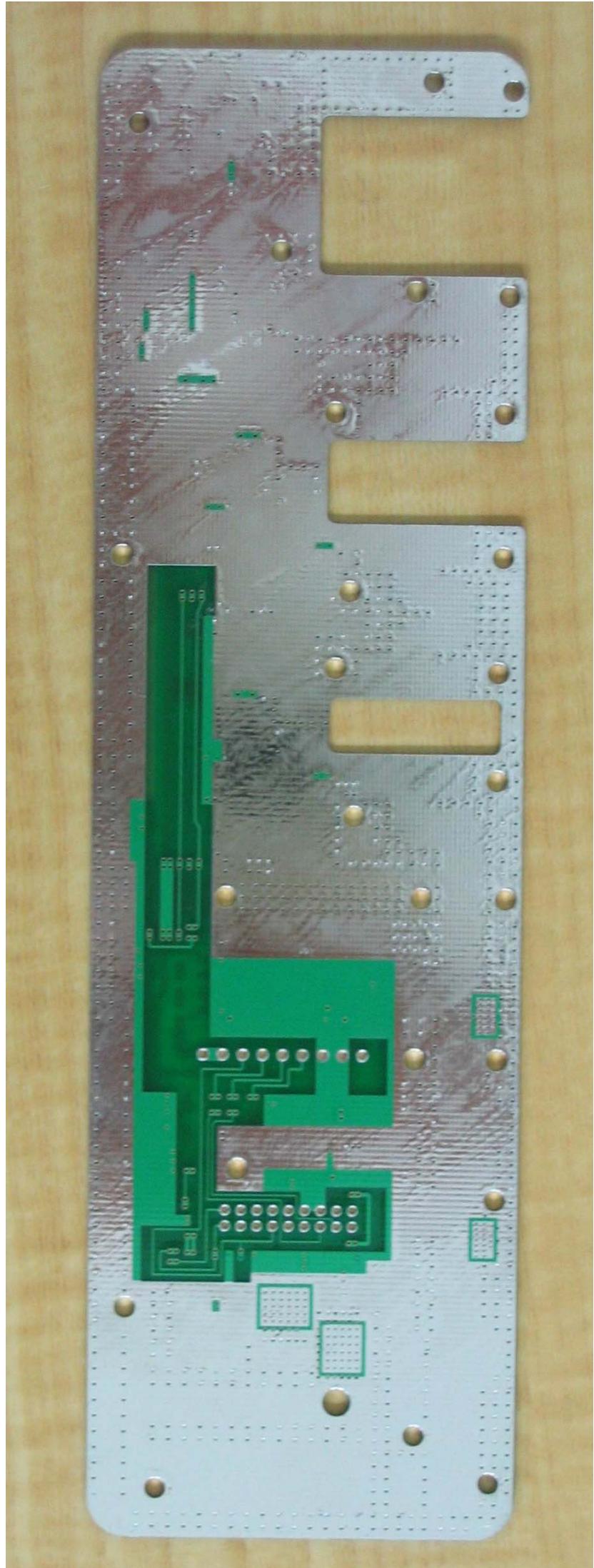
- 1,2. VCC
- 3. Input Power Monitor
- 4. TXD
- 5. RXD
- 6. nRESET
- 7,8. GND
- 9. Output Power Monitor

WARRANTY
IF SEAL R









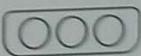




AP1960H30F11

WARRAN
IF SEAL R

N.C.
GND
VCC



- 1 Forward Power Monitor
- 2 Return Power Monitor
- 3 N.C.
- 4 TXD
- 5 RXD
- 6 RESET
- 7,8,9 GND

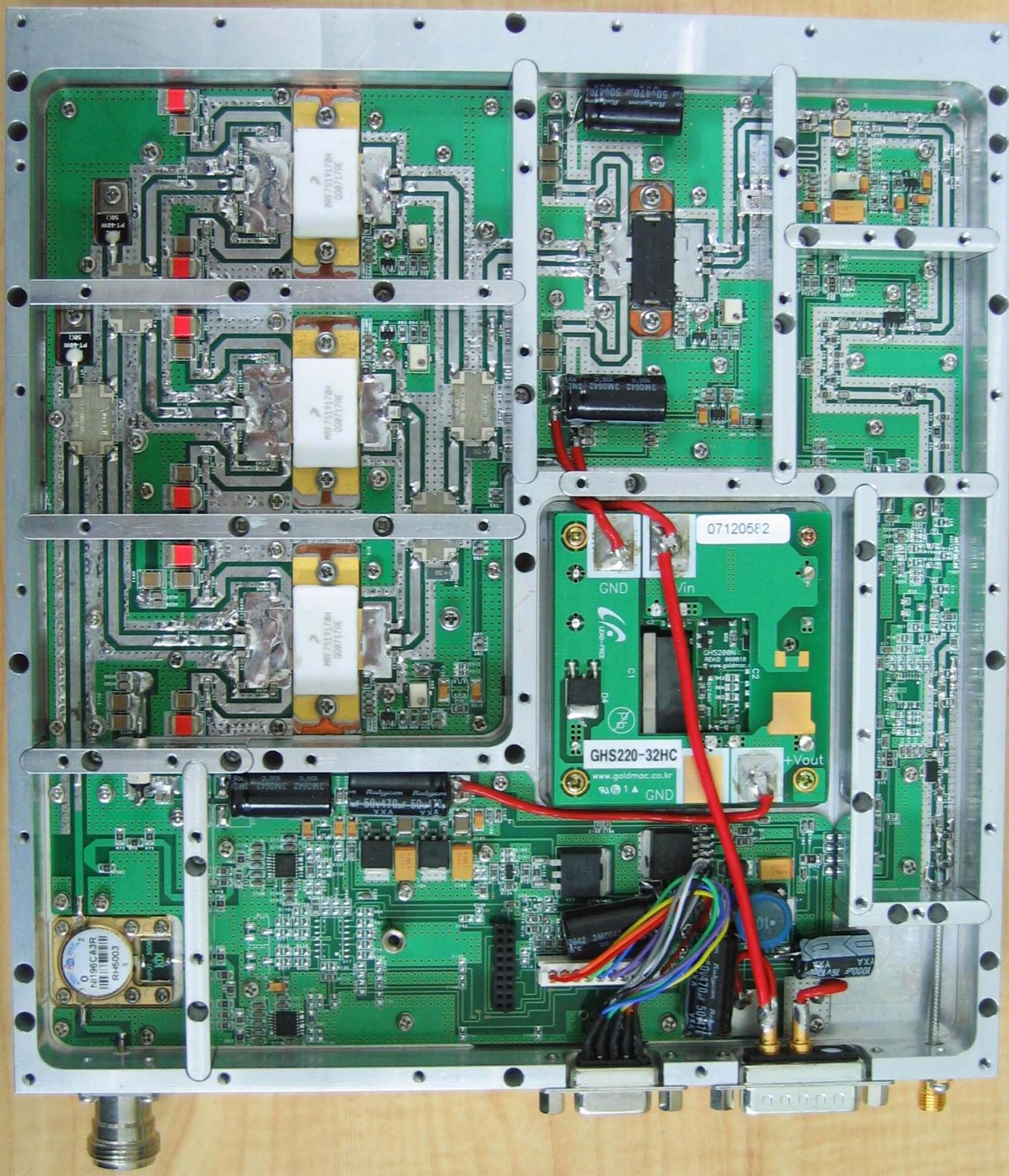


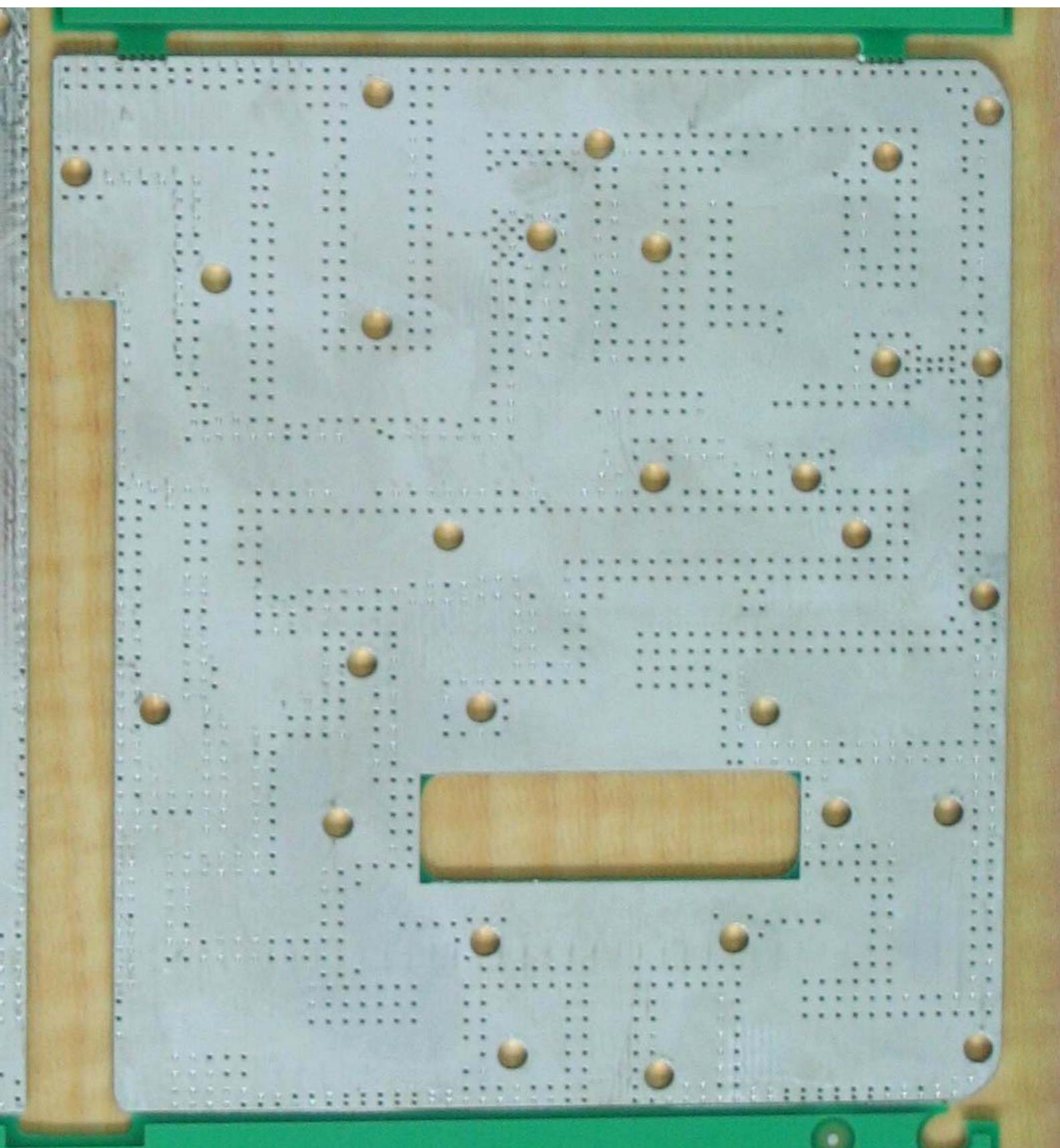
RFIN ▶

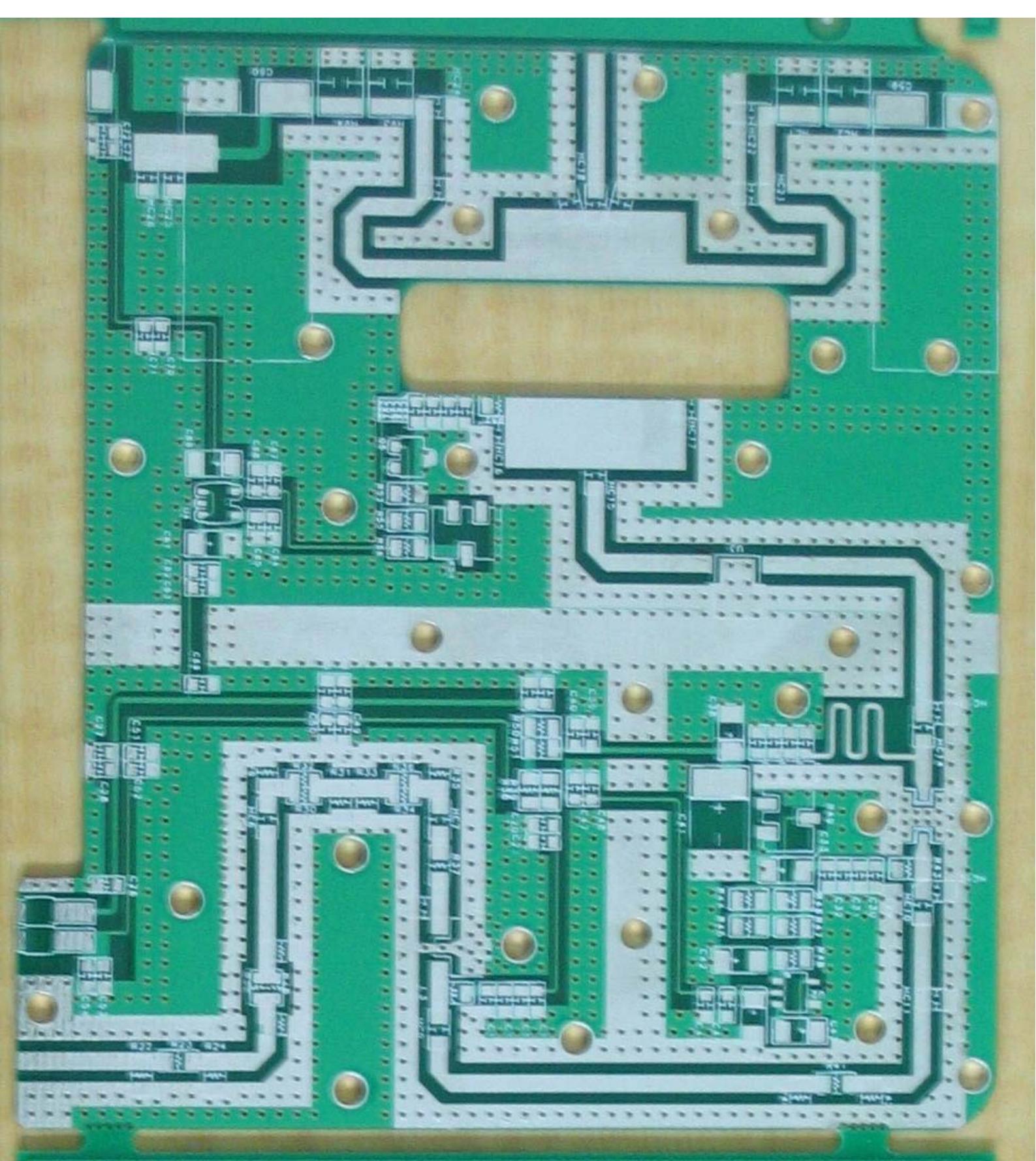
RFOUT ▶

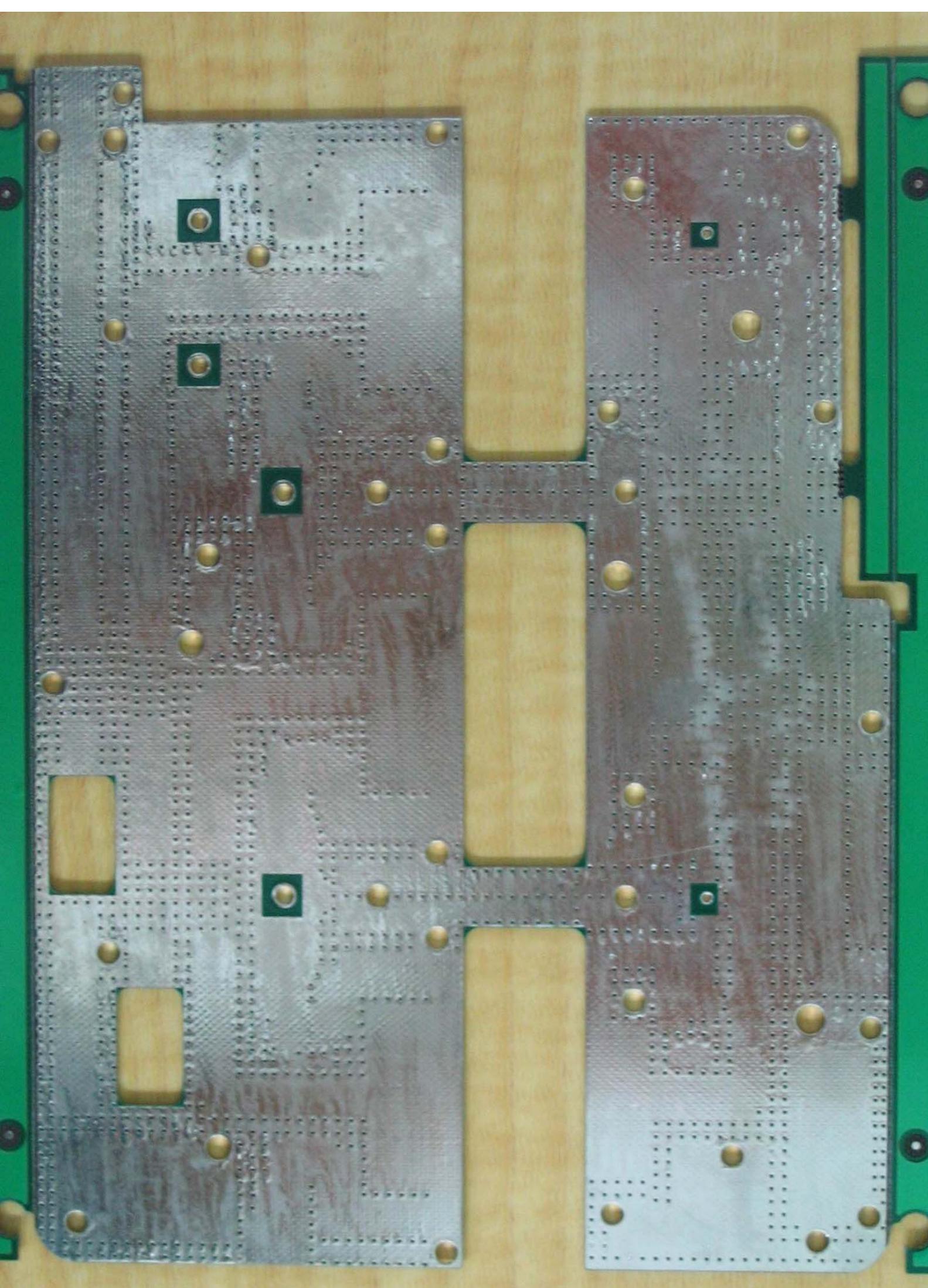
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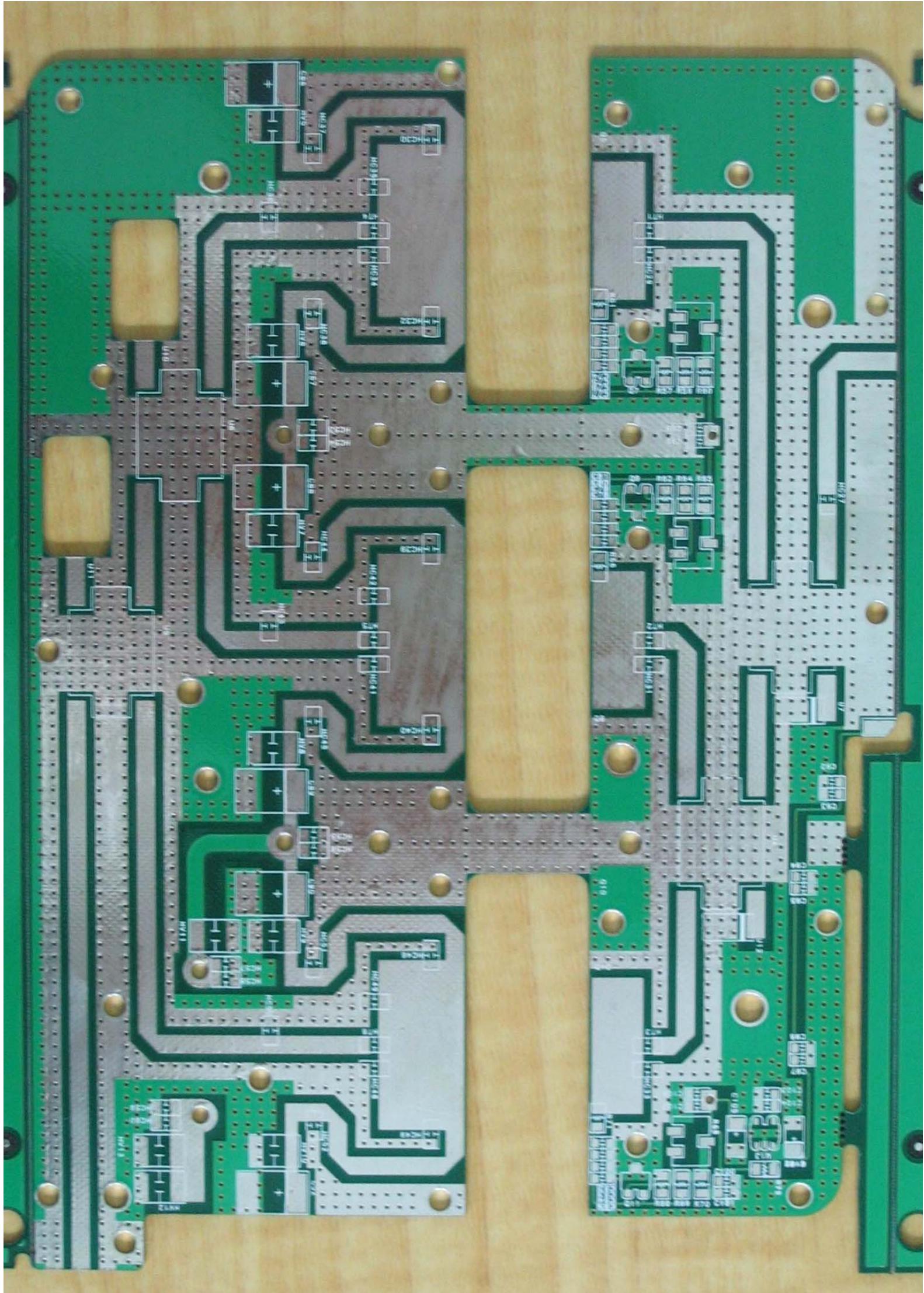
WARRAN
IF SEAL R

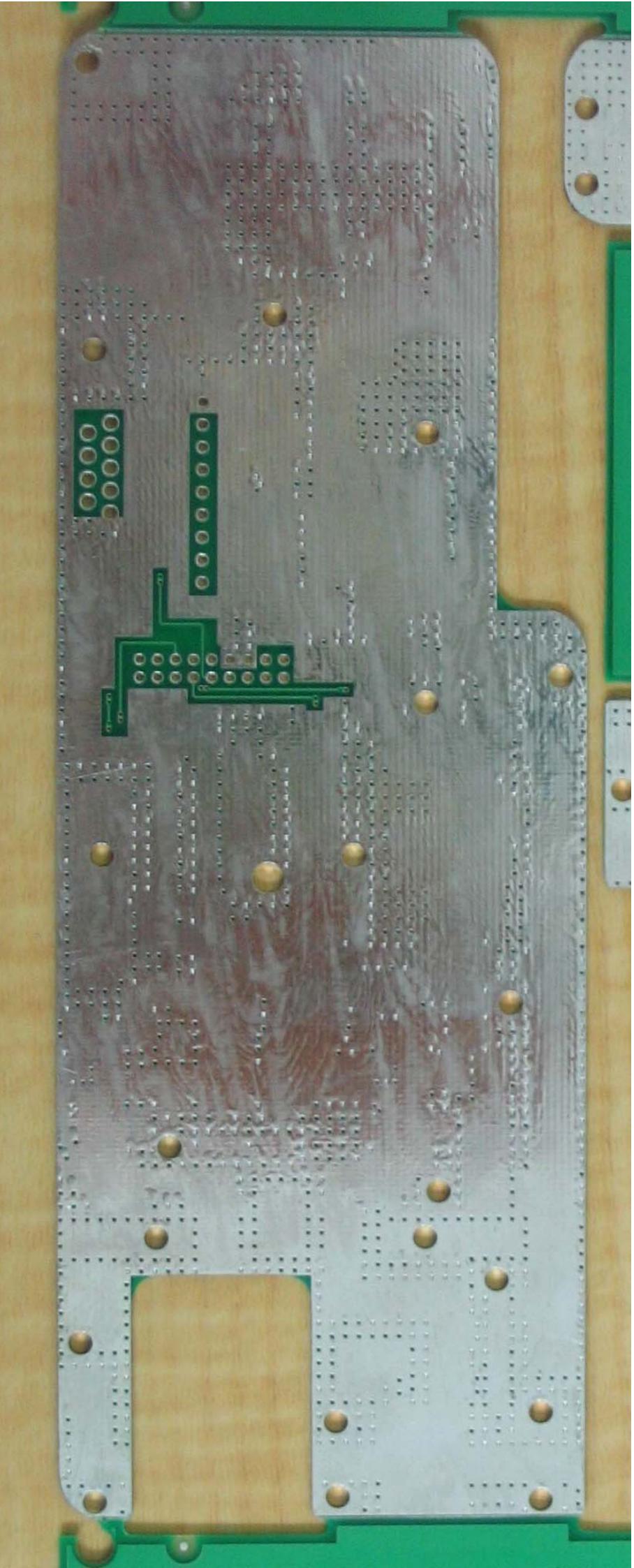


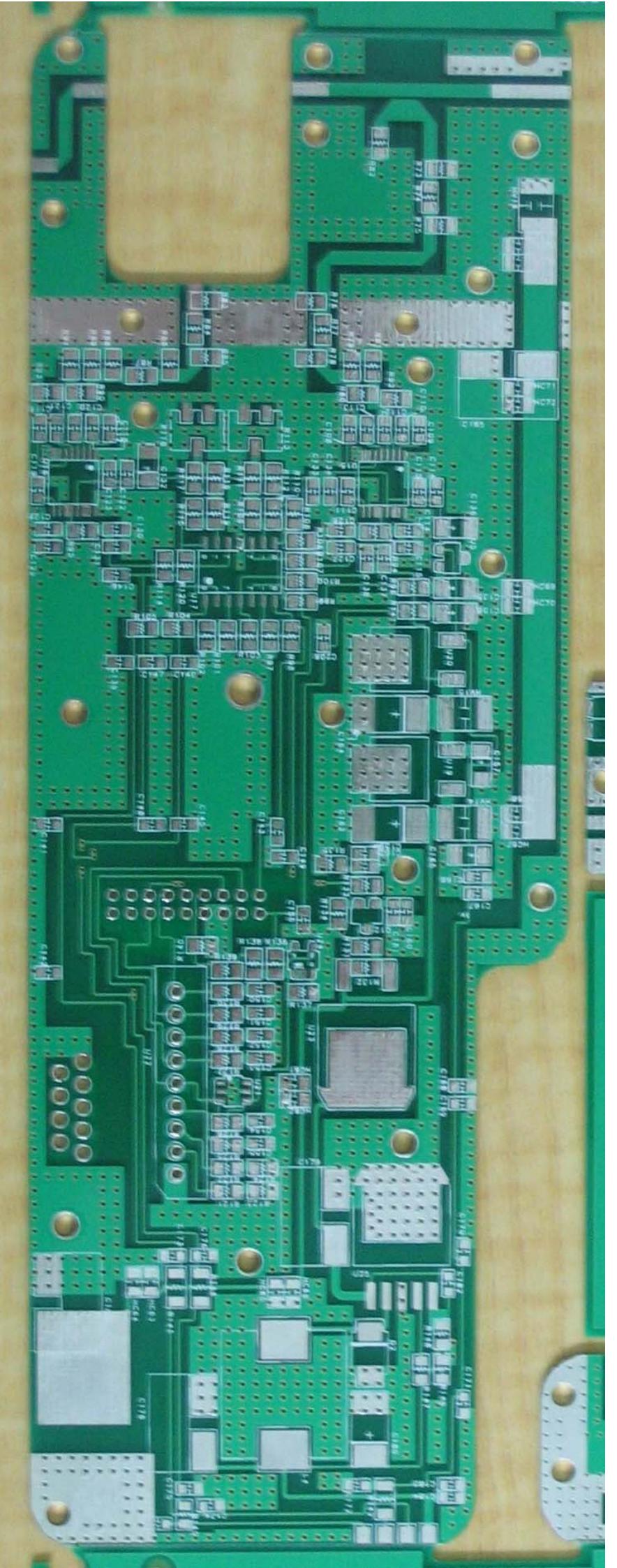


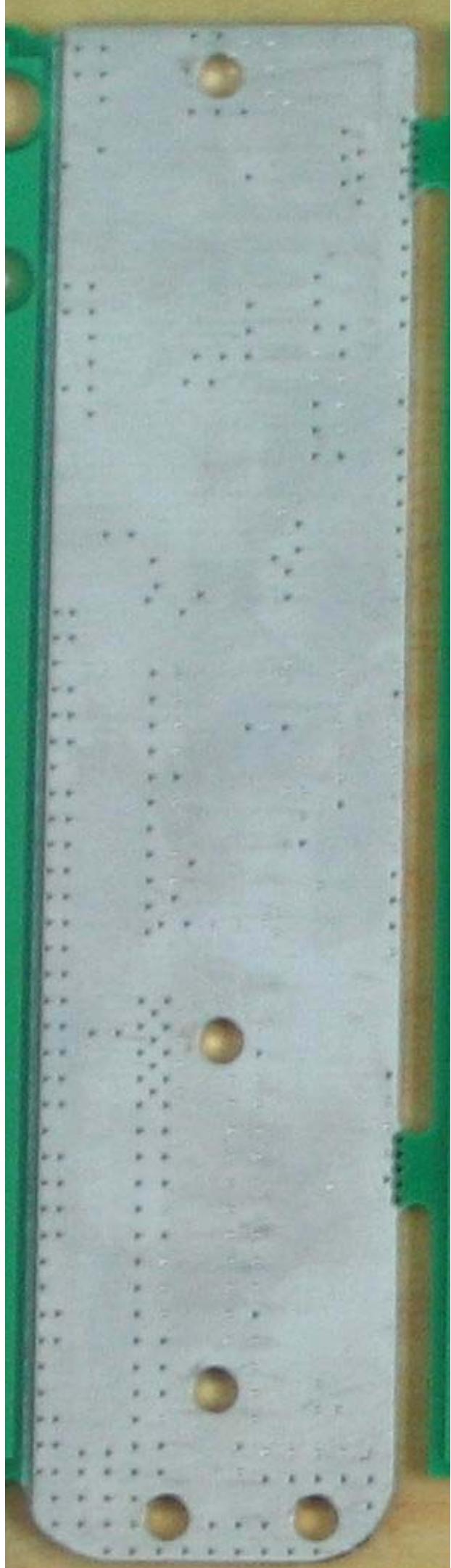


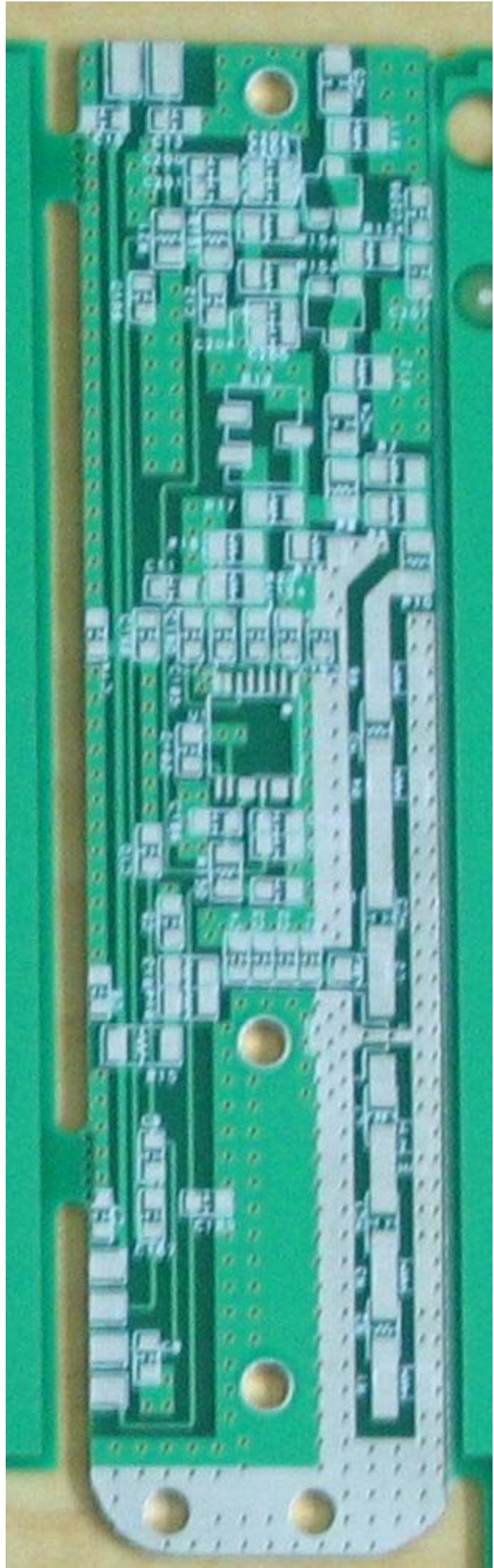




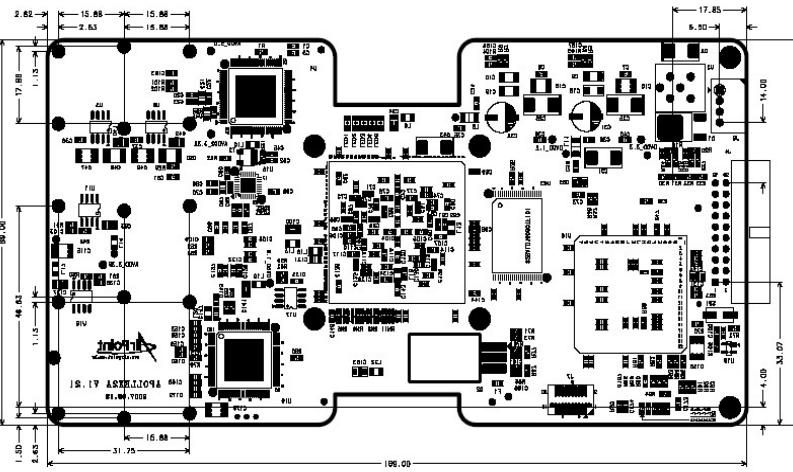
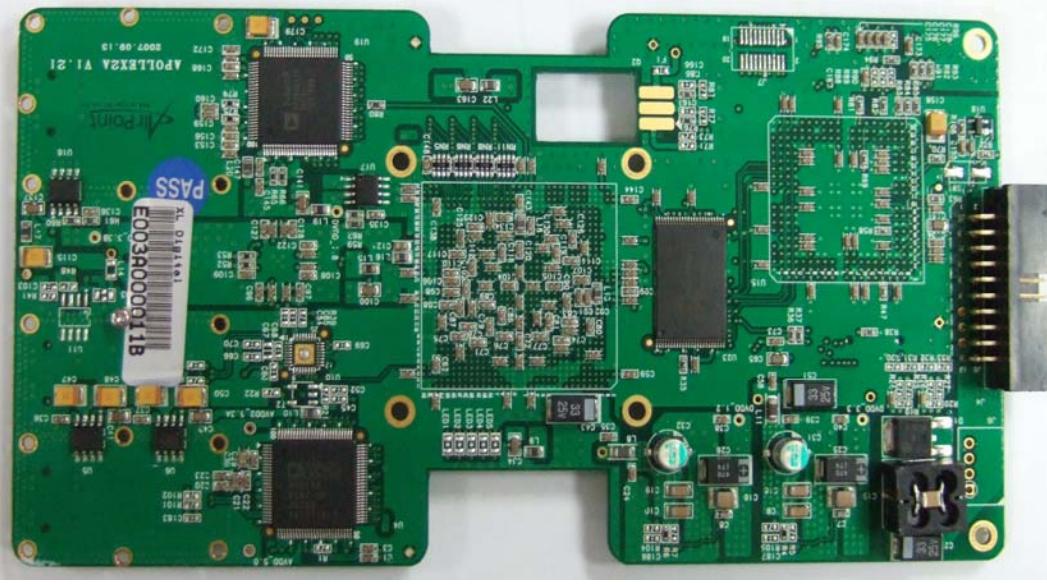


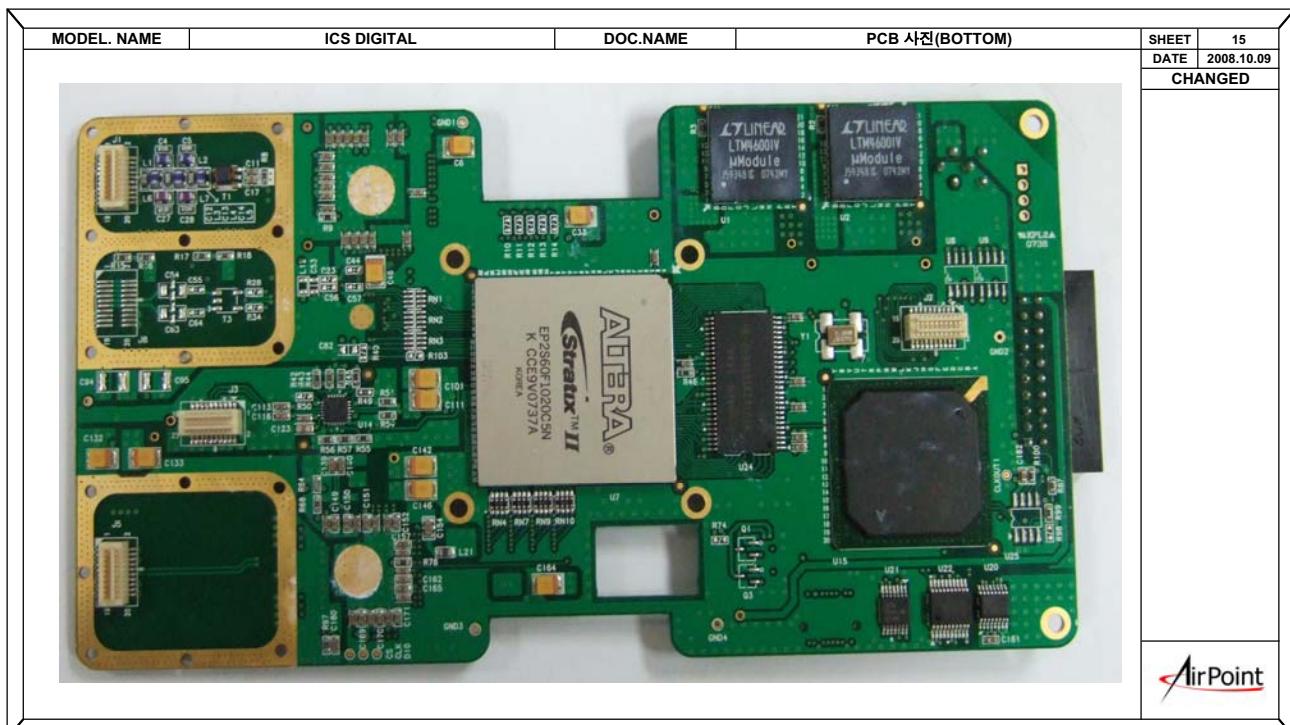




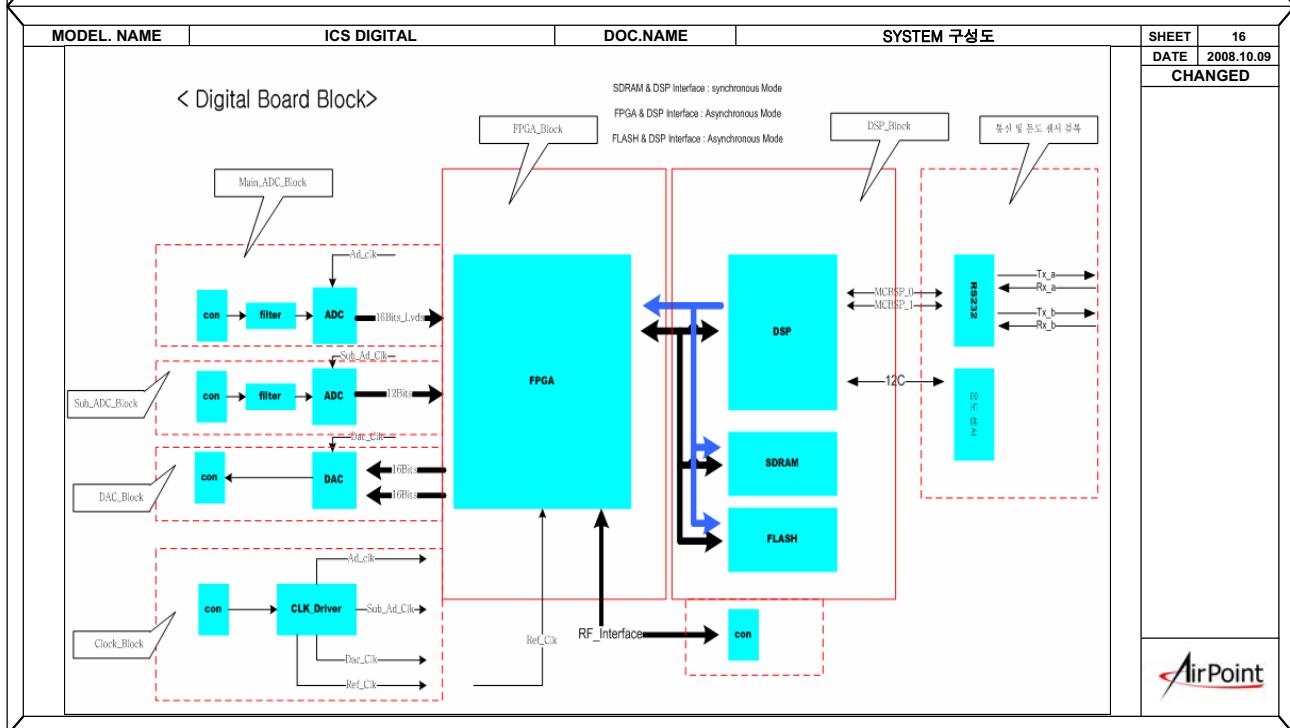


ICS

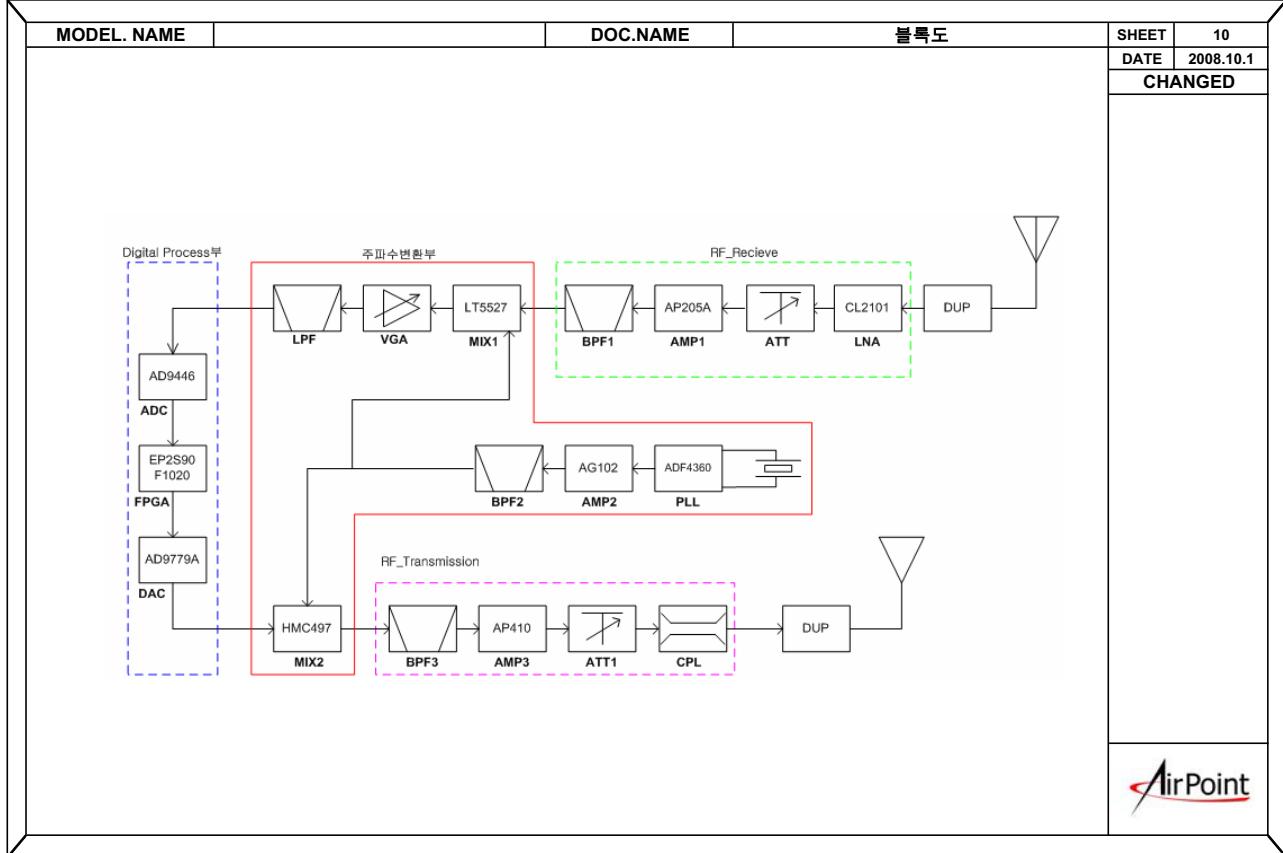
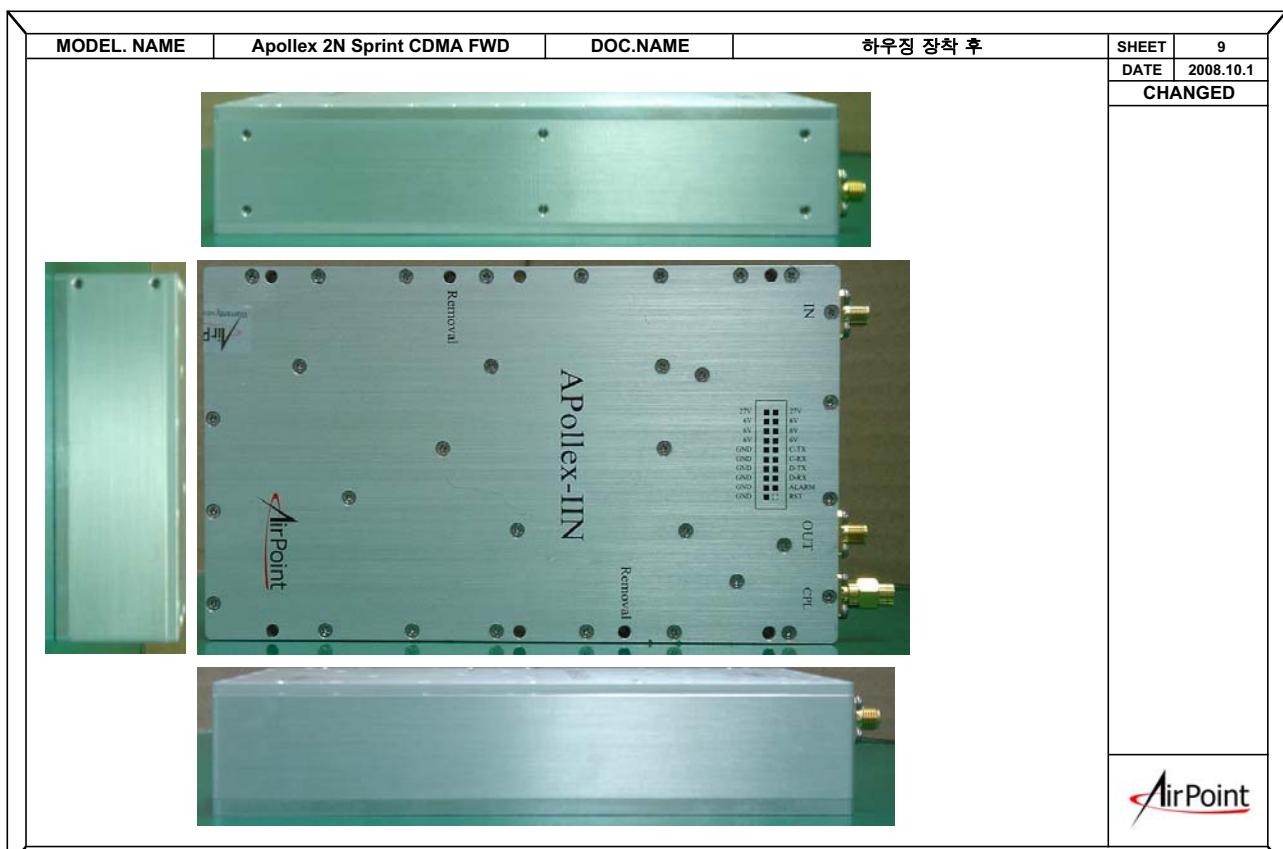
MODEL. NAME	ICS DIGITAL	DOC. NAME	PCB 도면	SHEET	13		
				DATE	2008.10.09		
				CHANGED			
							
							
MODEL. NAME	ICS DIGITAL	DOC. NAME	PCB 사진(TOP)	SHEET	14		
				DATE	2008.10.09		
				CHANGED			
							
							

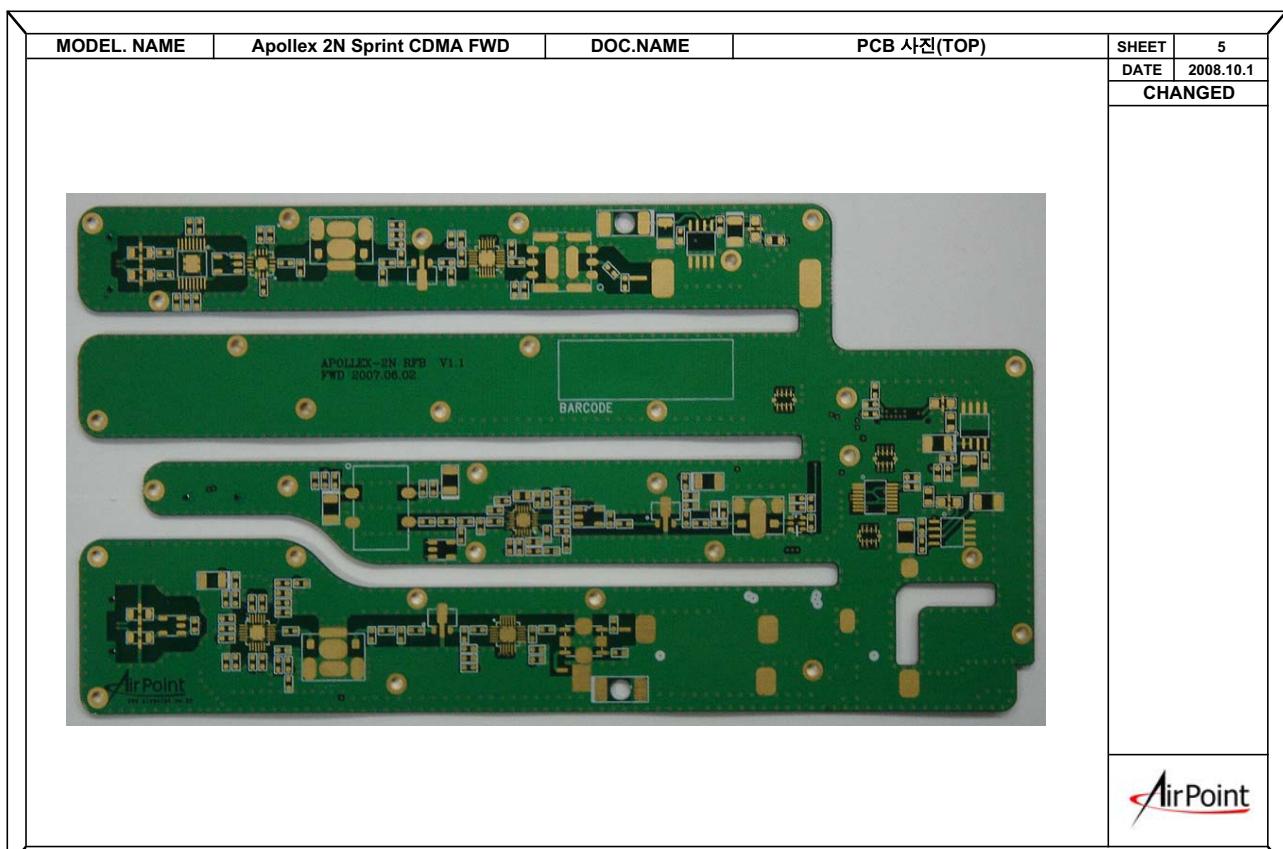


AirPoint

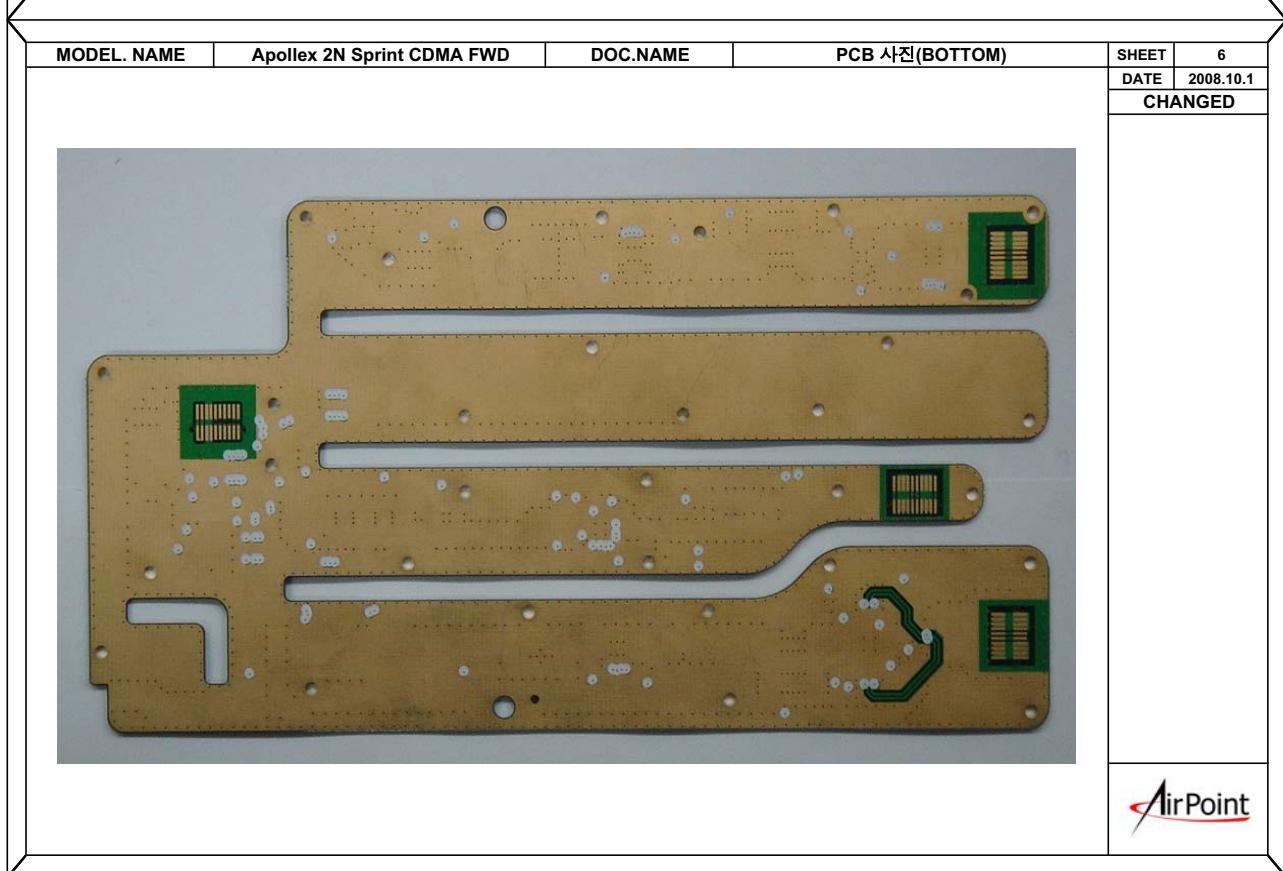


AirPoint



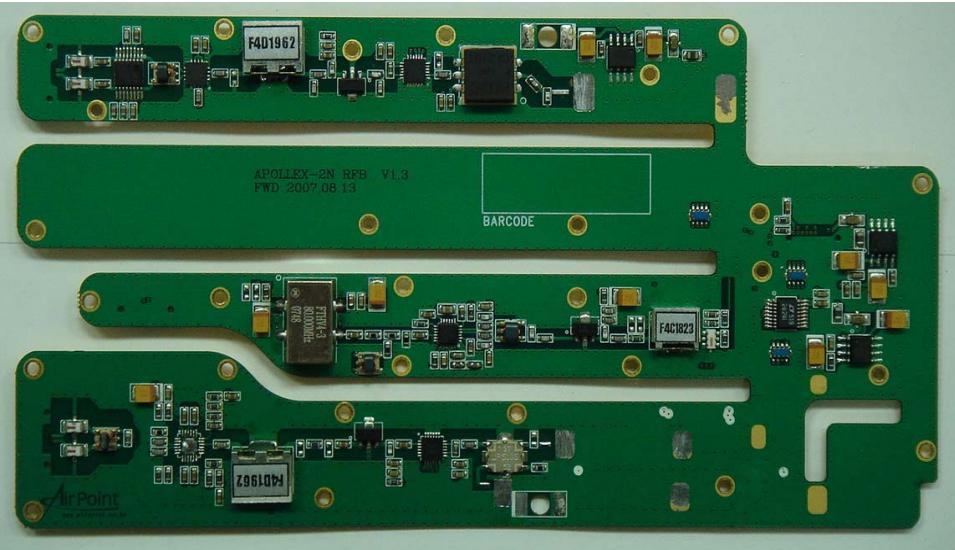


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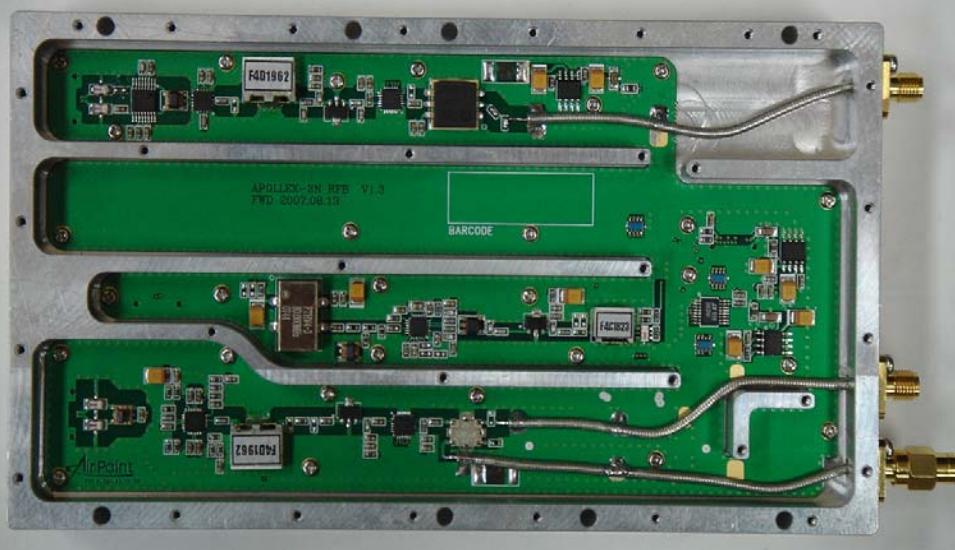
AirPoint

MODEL. NAME	Apollex 2N Sprint CDMA FWD	DOC.NAME	부품 실장 후	SHEET	7
				DATE	2008.10.1
				CHANGED	

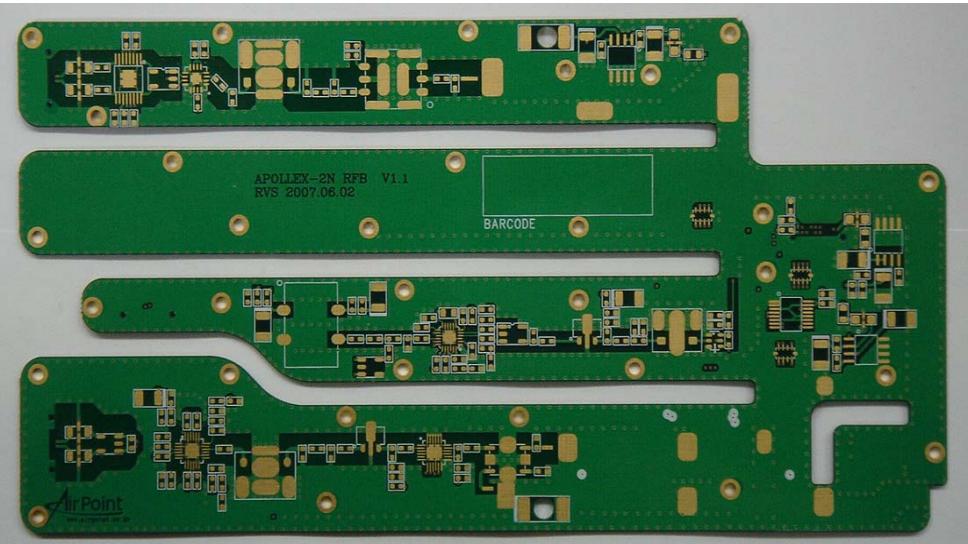


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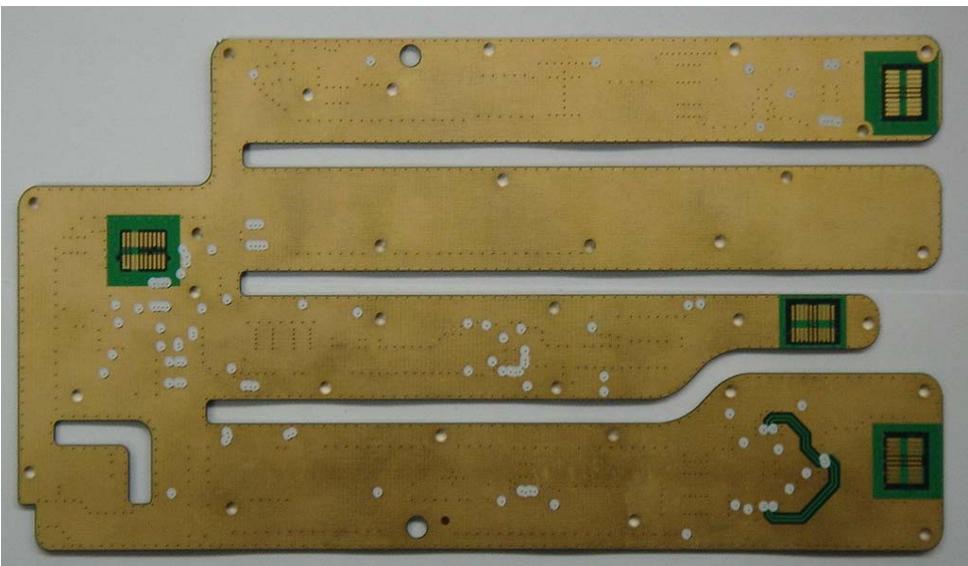
MODEL. NAME	Apollex 2N Sprint CDMA FWD	DOC.NAME	하우징 장착 후	SHEET	8
				DATE	2008.10.1
				CHANGED	



AirPoint

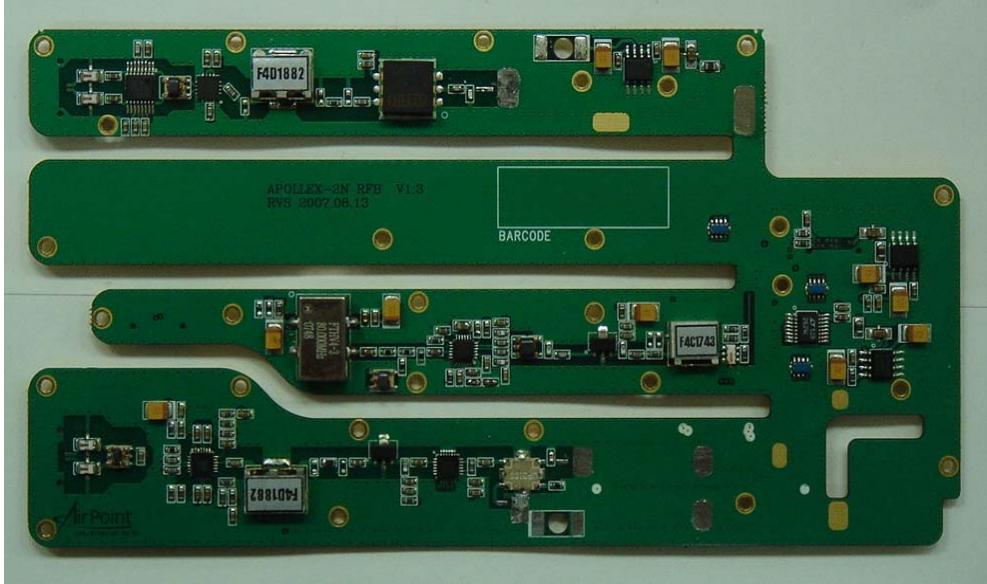
MODEL. NAME	Apollex 2N Sprint CDMA RVS	DOC.NAME	PCB 사진(TOP)	SHEET	5
				DATE	2008.10.1

AirPoint

MODEL. NAME	Apollex 2N Sprint CDMA RVS	DOC.NAME	PCB 사진(BOTTOM)	SHEET	6
				DATE	2008.10.1

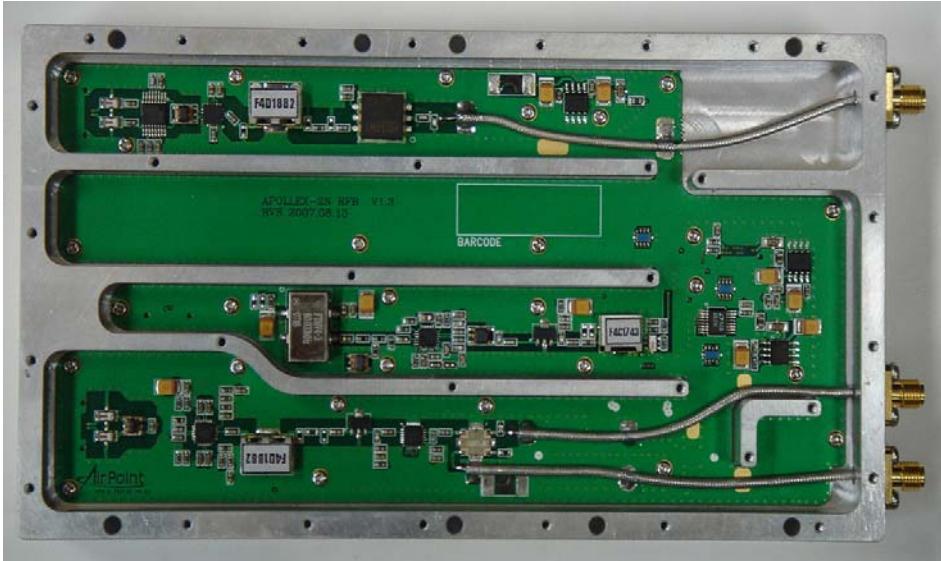
AirPoint

MODEL. NAME	Apollex 2N Sprint CDMA RVS	DOC.NAME	부품 실장 후	SHEET	7
				DATE	2008.10.1
				CHANGED	



AirPoint

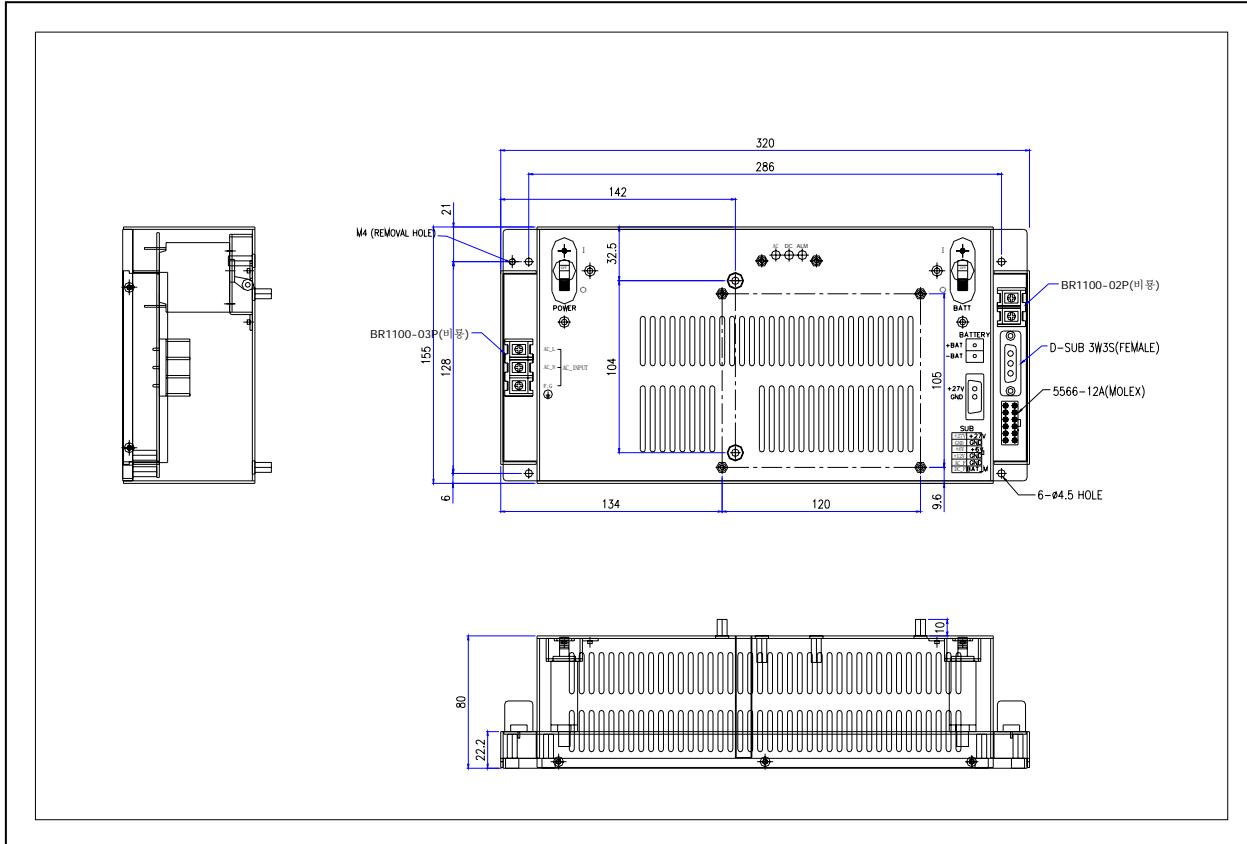
MODEL. NAME	Apollex 2N Sprint CDMA RVS	DOC.NAME	하우징 장착 후	SHEET	8
				DATE	2008.10.1
				CHANGED	



AirPoint

PSU

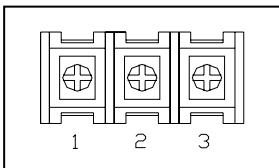
■ Mechanical Specification



Terminal pin number assignment :

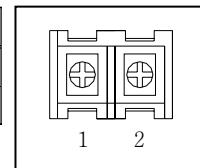
** Input Terminal

Pin No.	Assignment
1	AC_L
2	AC_N
3	F.G



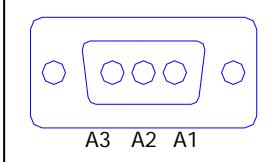
** BATT Terminal

Pin No.	Assignment
1	+BAT
2	-BAT



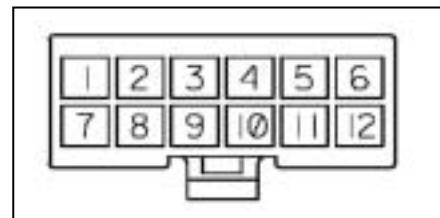
** MAIN Connector

Pin No.	Assignment
1	+27V
2	GND
3	N.C



** SUB Connector

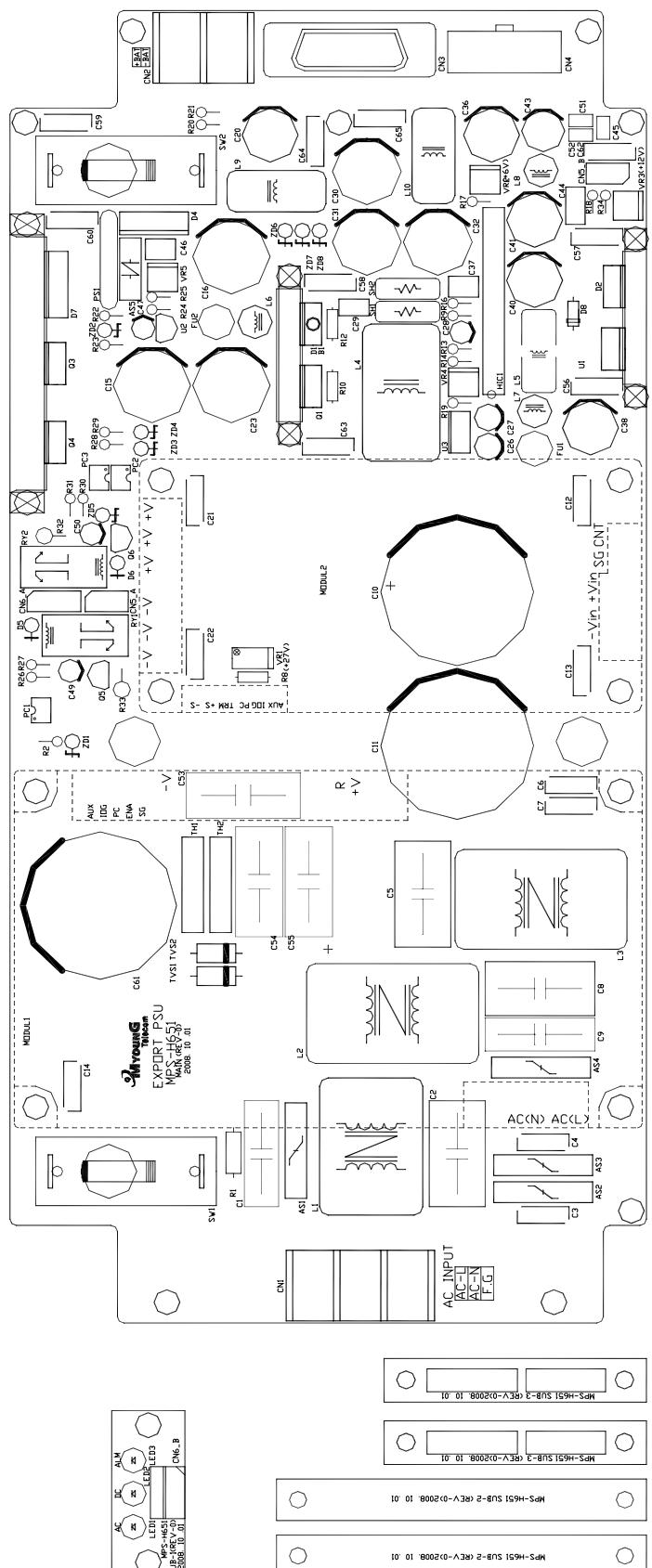
Pin No.	Assignment	Pin No.	Assignment
1	DC_F	7	BAT_M
2	AC_F	8	GND
3	DC OUTPUT (+12V)	9	GND
4	DC OUTPUT (+6V)	10	DC OUTPUT (+6V)
5	GND	11	GND
6	DC OUTPUT (+27V)	12	DC OUTPUT (+27V)



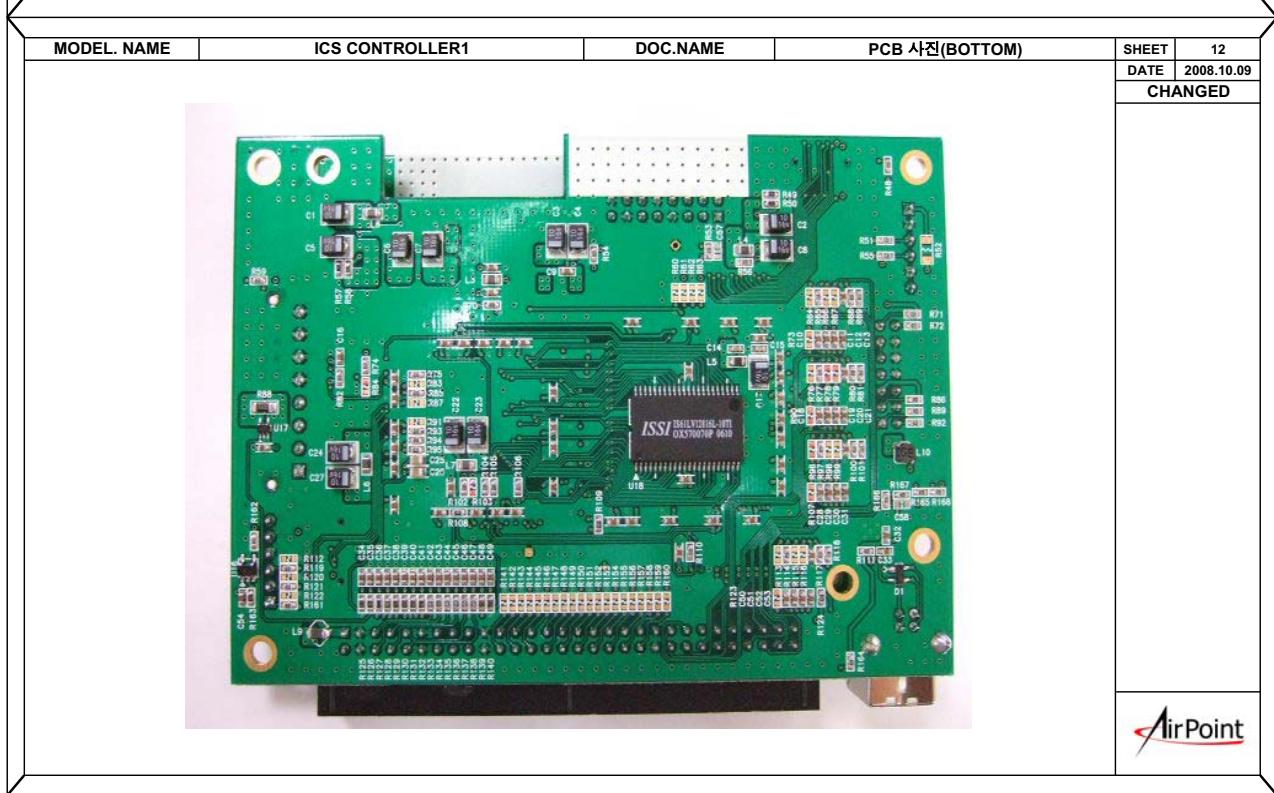
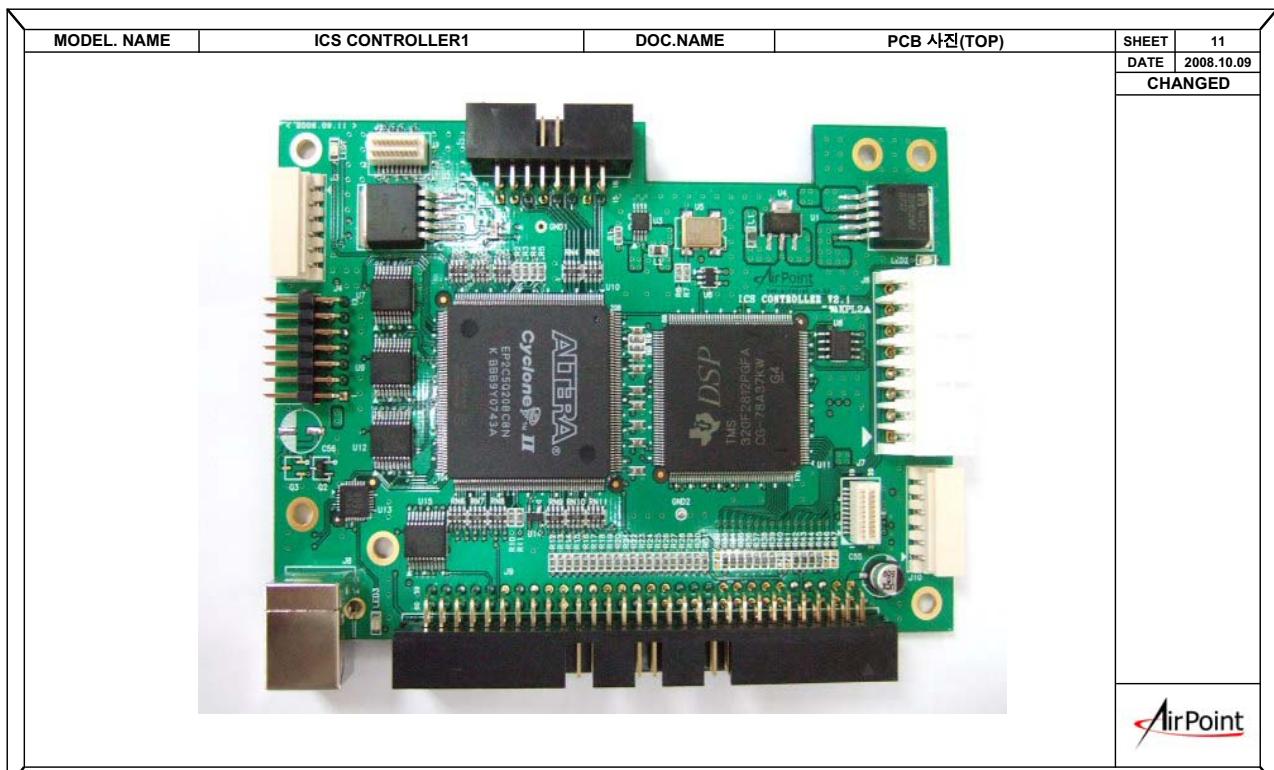
MPS-H651 PCB(TOP SILK)

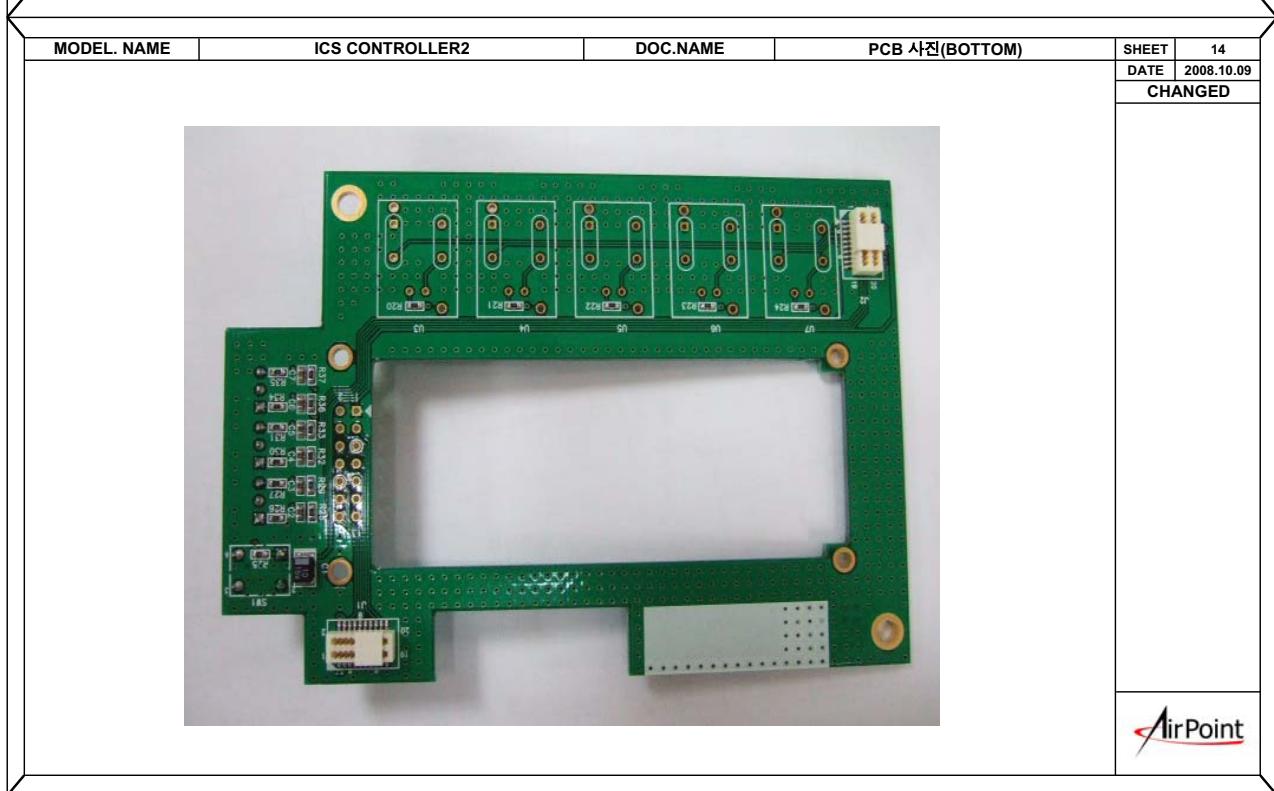
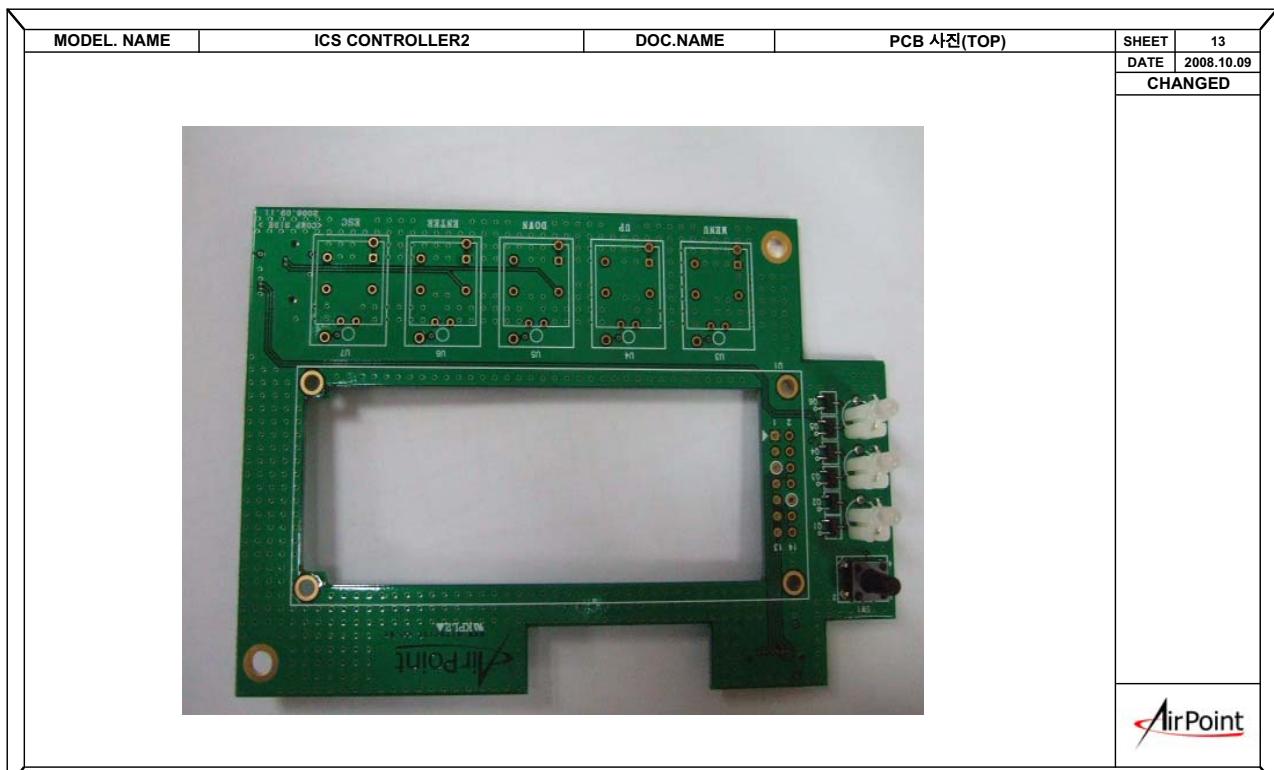
DATE	2008.10.27	MODEL NO.	MPS-H651
ISSUE	REV-0	MODEL NAME	미국수출용 PSU
DESIGN	CHECKED	DRAWING NO.	H651-C001

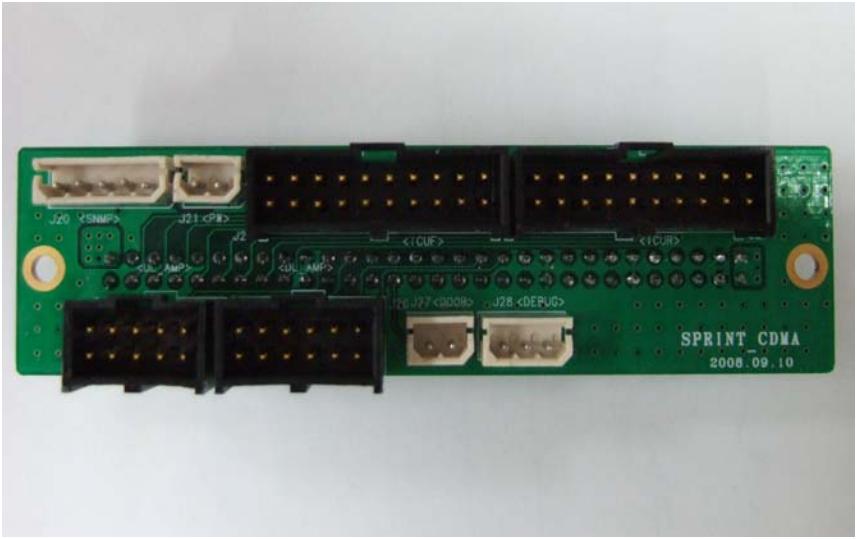
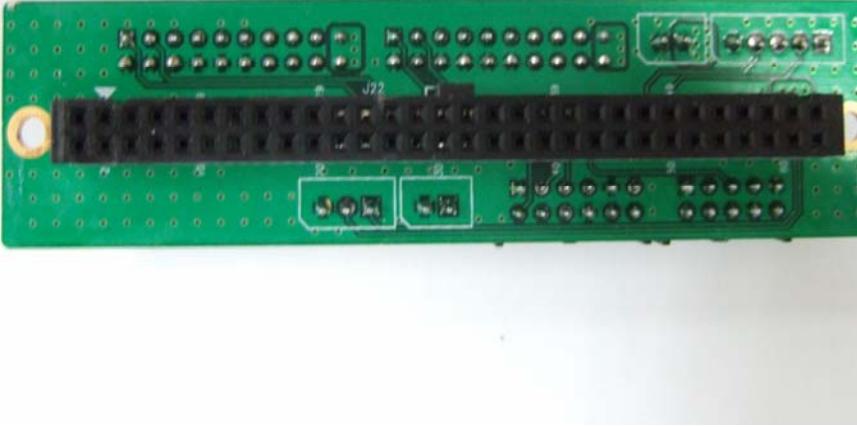
TOP SILK

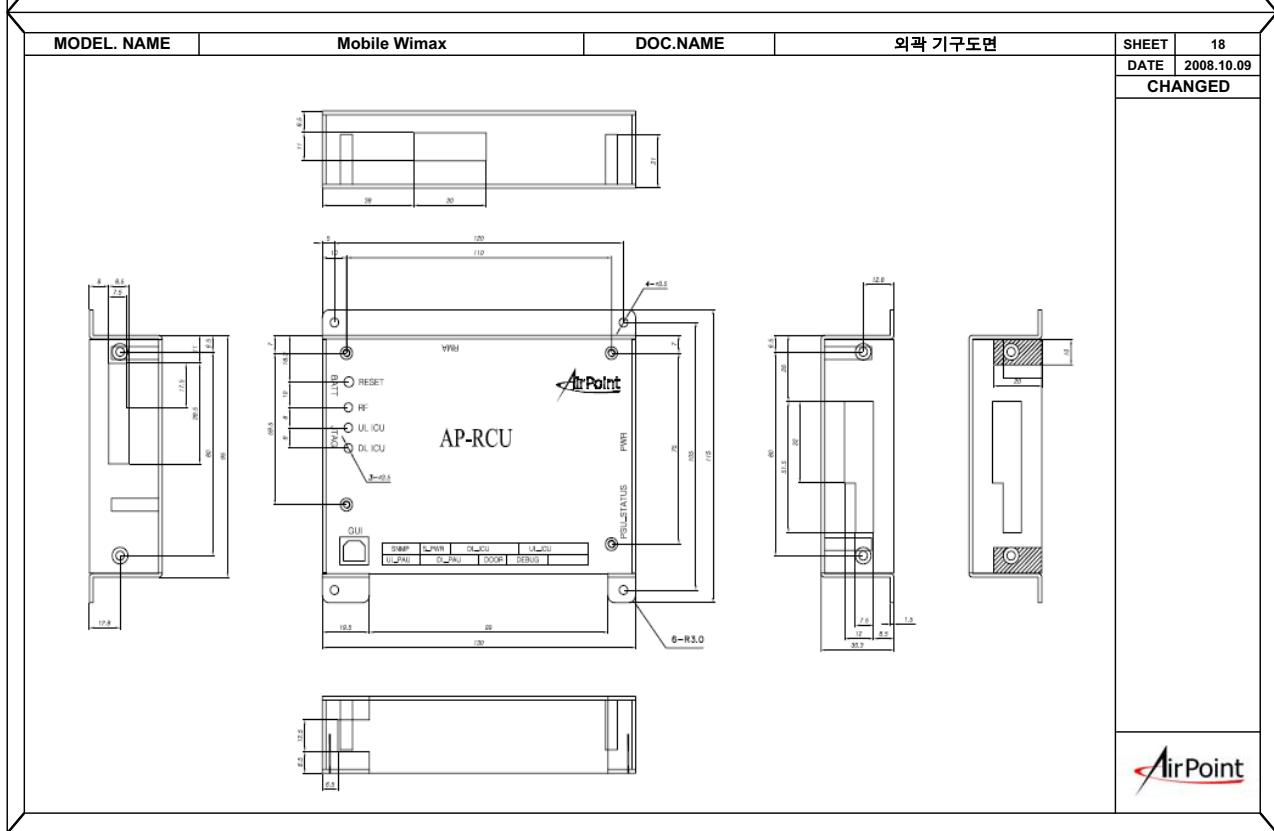
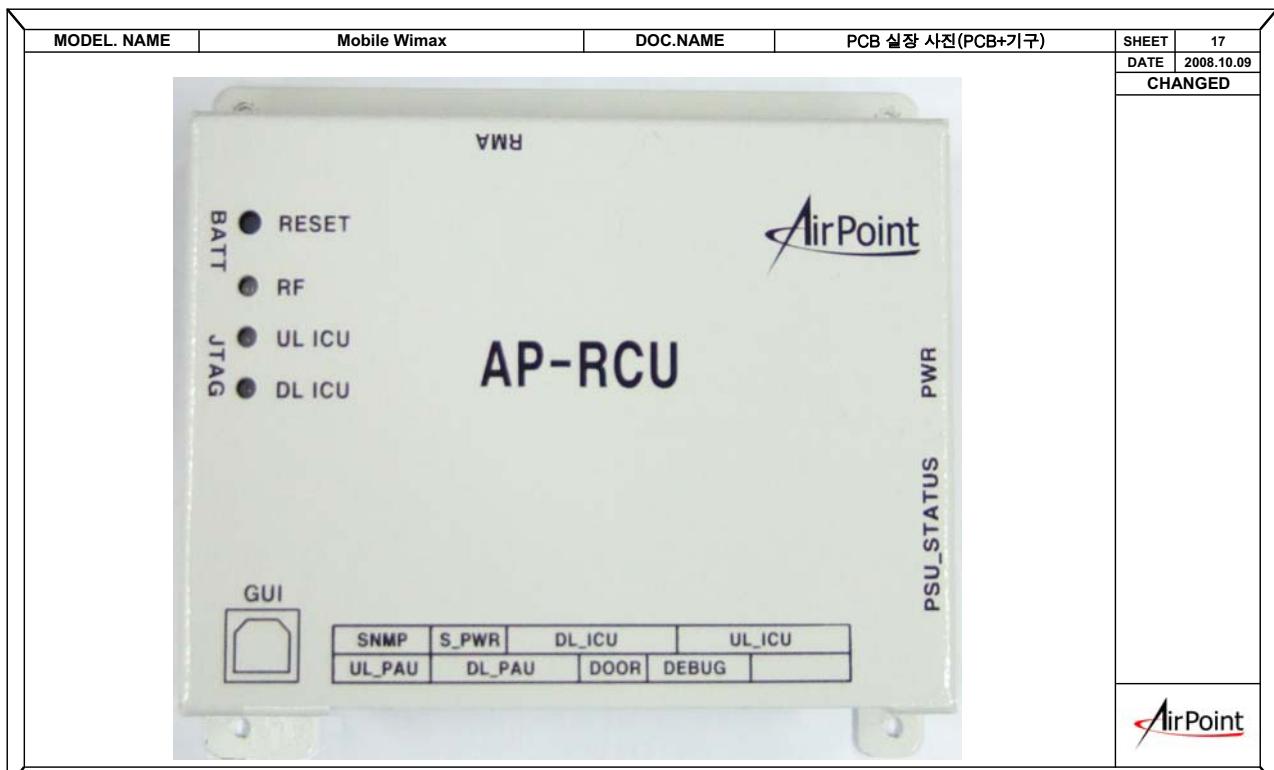


RCU

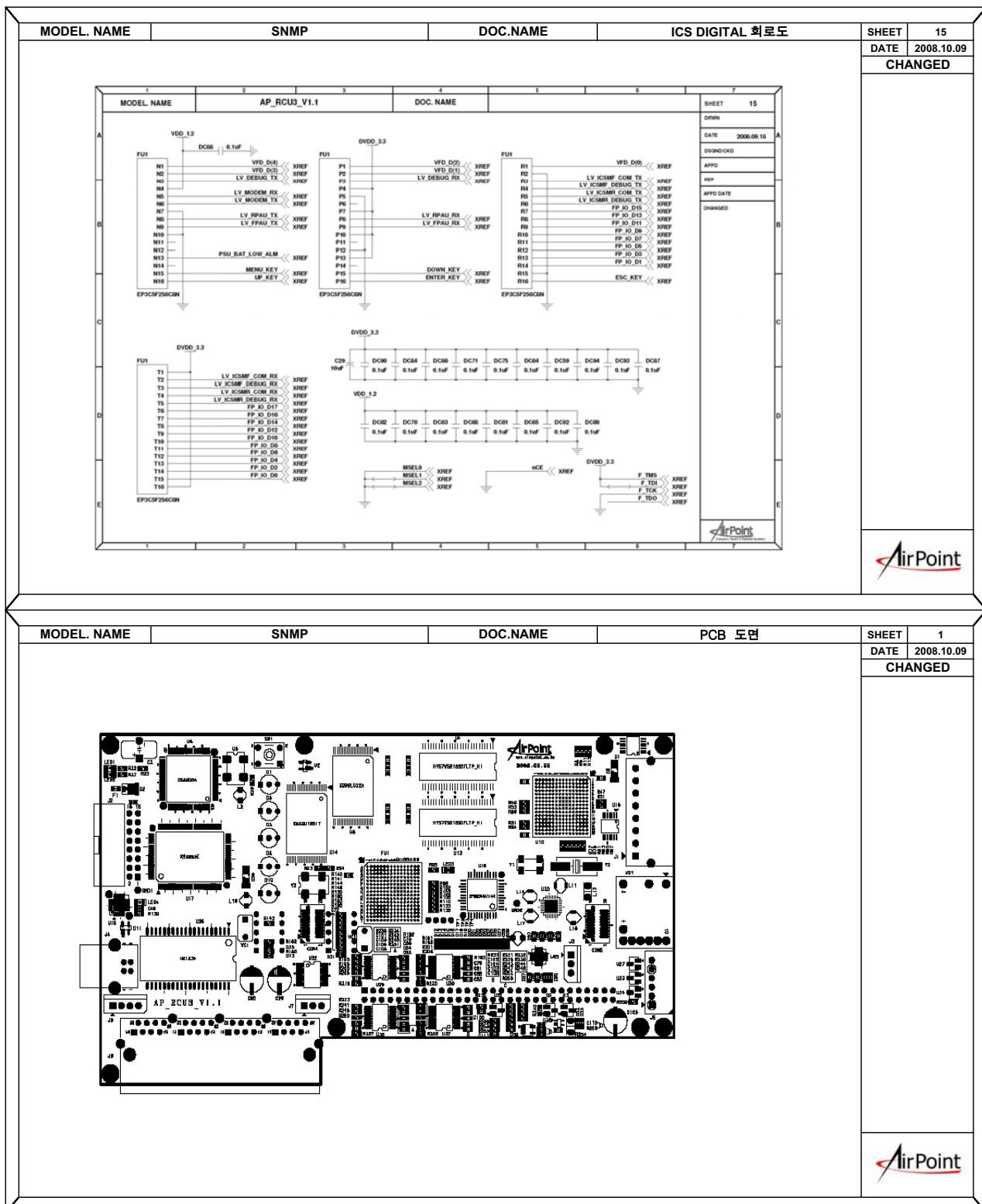


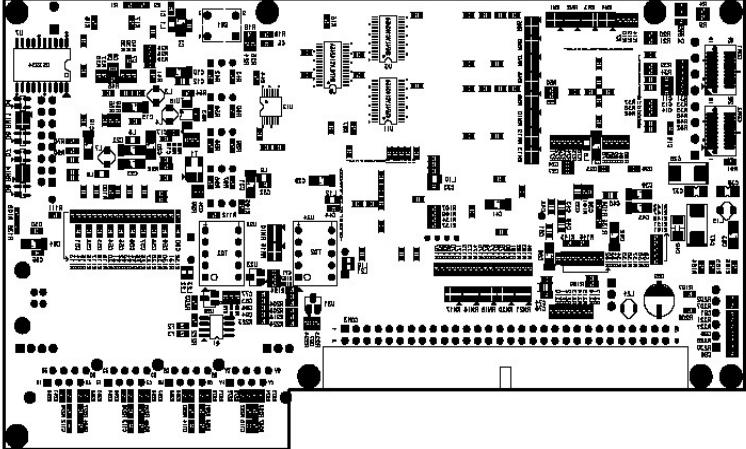


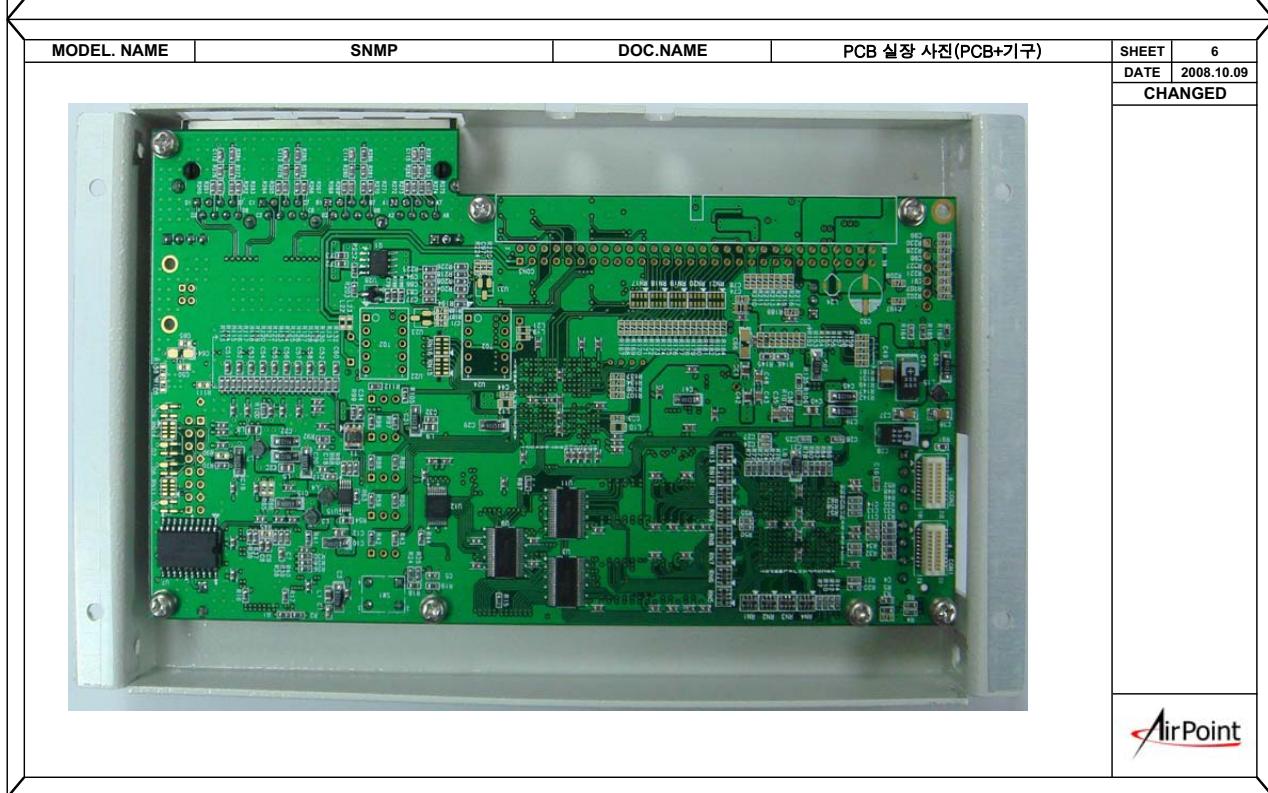
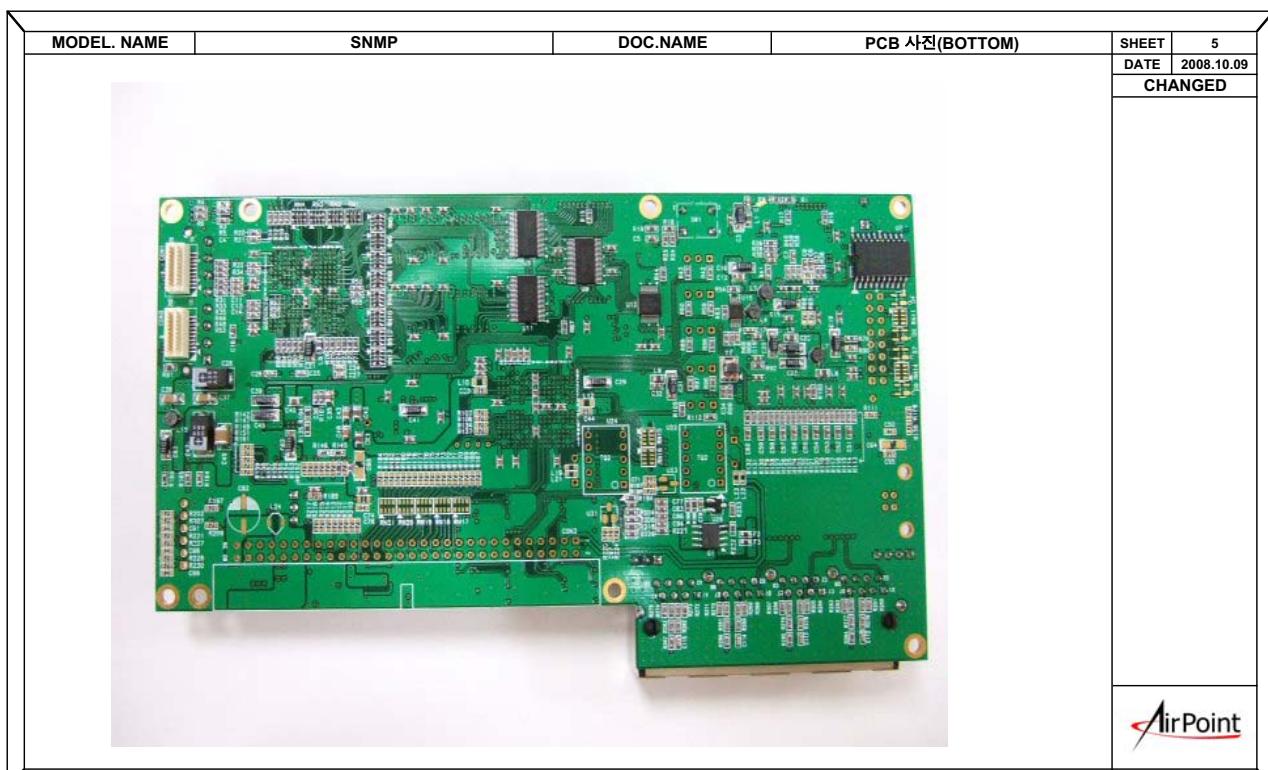
MODEL. NAME	FRONT BOARD	DOC.NAME	I/O PCB 사진(TOP)	SHEET	15		
				DATE	2008.10.09		
				CHANGED			
							
							
MODEL. NAME	FRONT BOARD	DOC.NAME	I/O PCB 사진(BOTTOM)	SHEET	16		
				DATE	2008.10.09		
				CHANGED			
							
							



SNMP



MODEL. NAME	SNMP	DOC.NAME	PCB 도면	SHEET	2
				DATE	2008.10.09
CHANGED					
					
					
MODEL. NAME	SNMP	DOC.NAME	PCB 사진(TOP)	SHEET	4
				DATE	2008.10.09
CHANGED					
					
					



MODEL. NAME	SNMP	DOC.NAME	제품 외관도(기구외관도)	SHEET	7
				DATE	2008.10.09
				CHANGED	