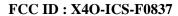


ATTACHMENT F.

- INTERNAL PHOTOS -

Report No.: HCTR1002FR09 1/20

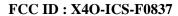




[AMP TOP]

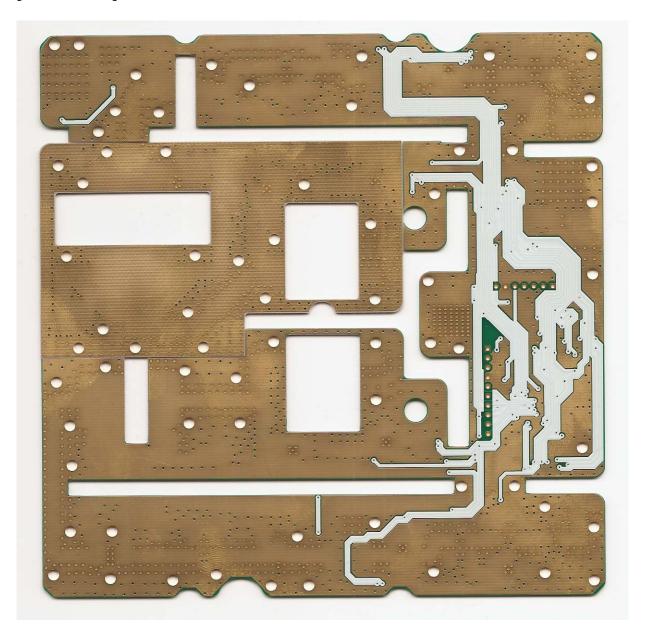


Report No.: HCTR1002FR09 2/20





[AMP Bottom]

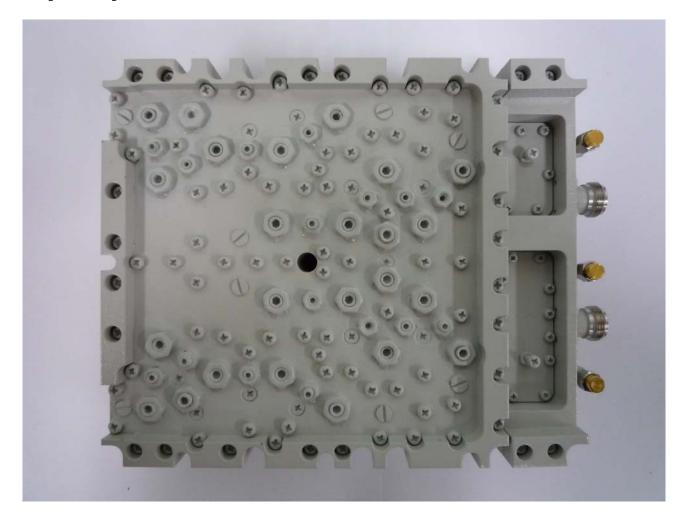


Report No.: HCTR1002FR09 3/20

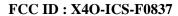




[Duplexer Top]



Report No.: HCTR1002FR09 4/20







Report No.: HCTR1002FR09 5/20





[ICM DSP Module]



Report No.: HCTR1002FR09 6/20



[ICM DSP Top]



Report No.: HCTR1002FR09 7/20





[ICM DSP Bottom]

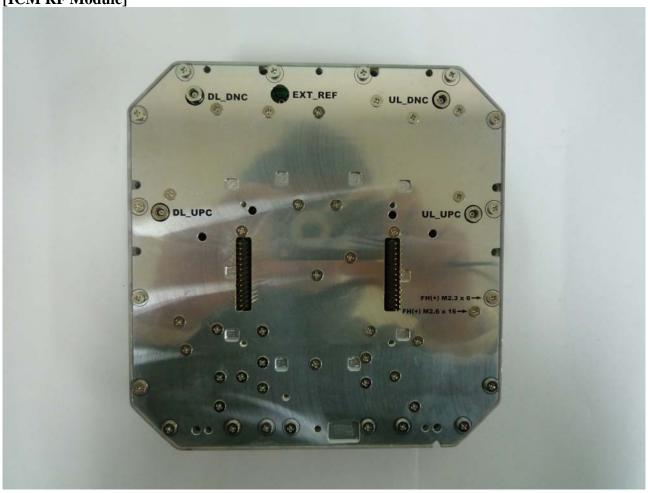


Report No.: HCTR1002FR09 8/20







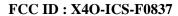


Report No.: HCTR1002FR09 9/20

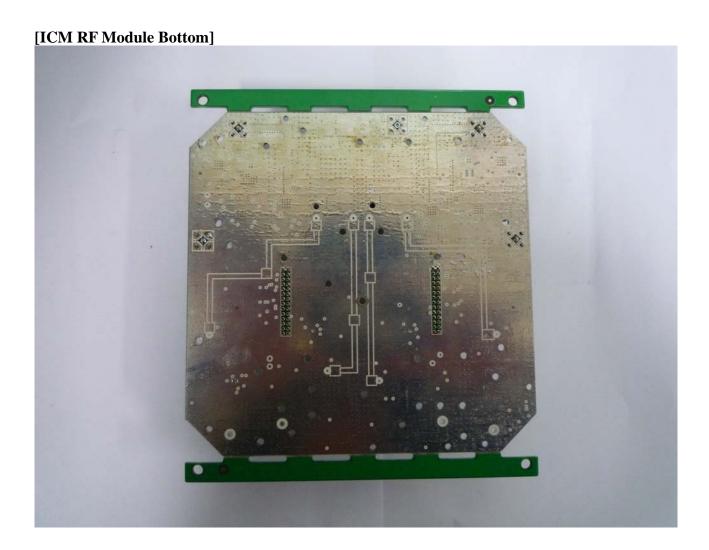




Report No.: HCTR1002FR09 10/20







Report No.: HCTR1002FR09 11/20







Report No.: HCTR1002FR09 12/20



[Interface Top]



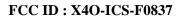
Report No.: HCTR1002FR09 13/20







Report No.: HCTR1002FR09 14/20







Report No.: HCTR1002FR09 15/20

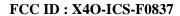




[PSU Inside 1]



Report No.: HCTR1002FR09 16/20









Report No.: HCTR1002FR09 17/20







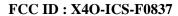
Report No.: HCTR1002FR09 18/20



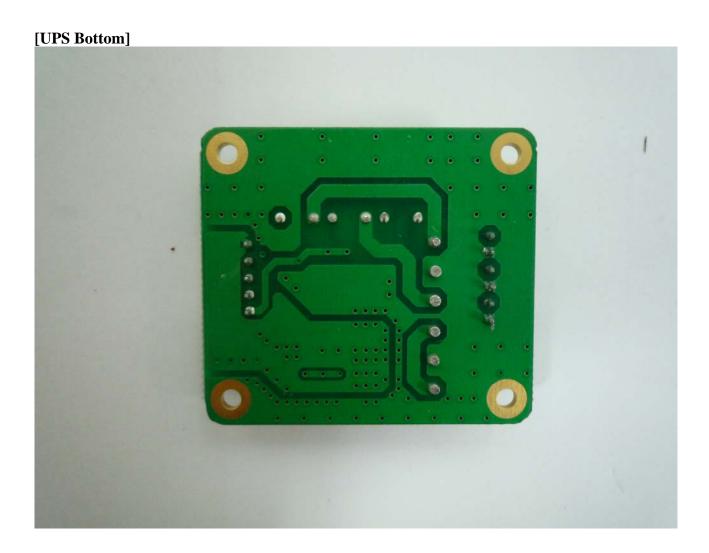




Report No.: HCTR1002FR09 19/20







Report No.: HCTR1002FR09 20/20