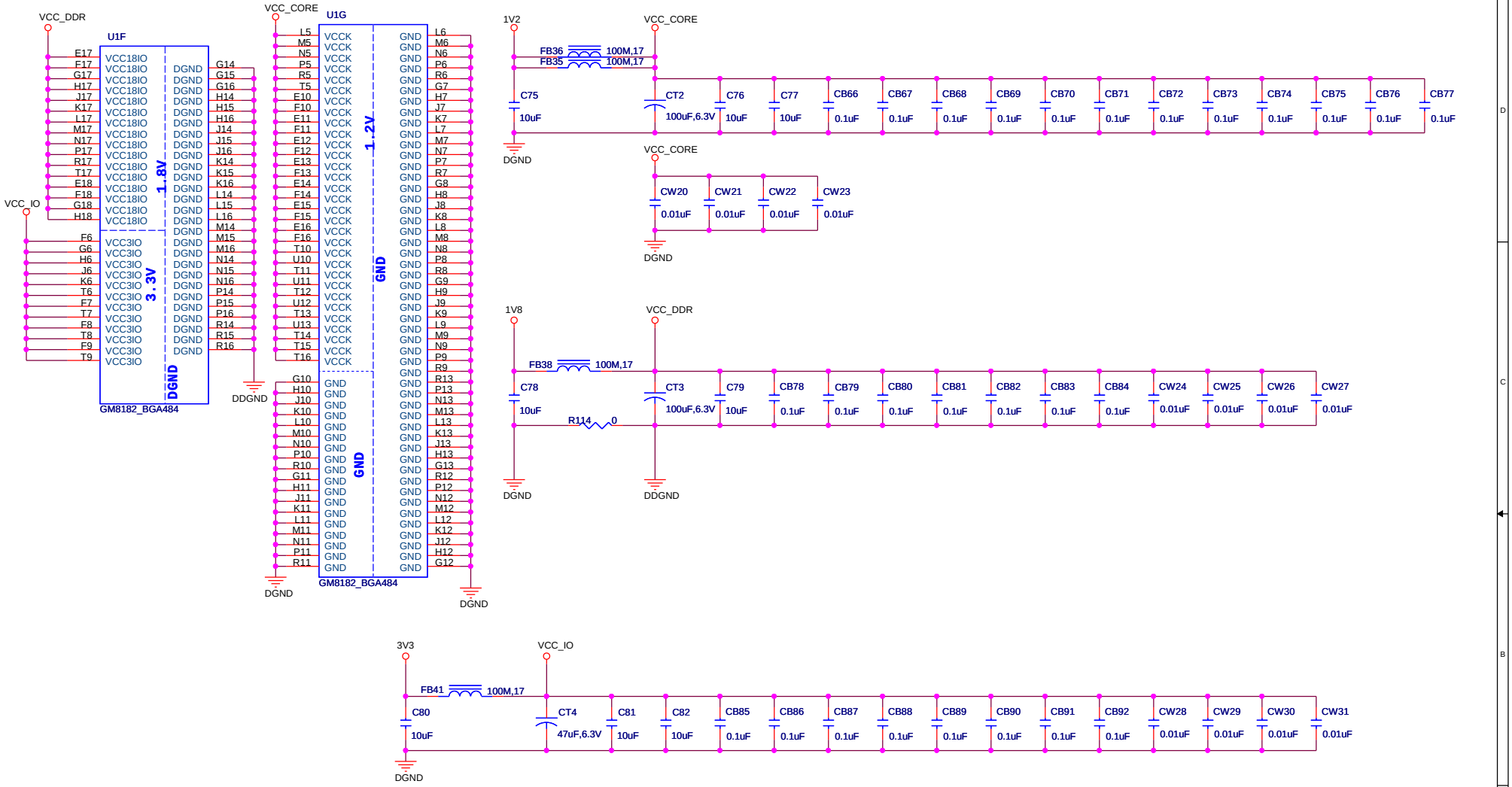

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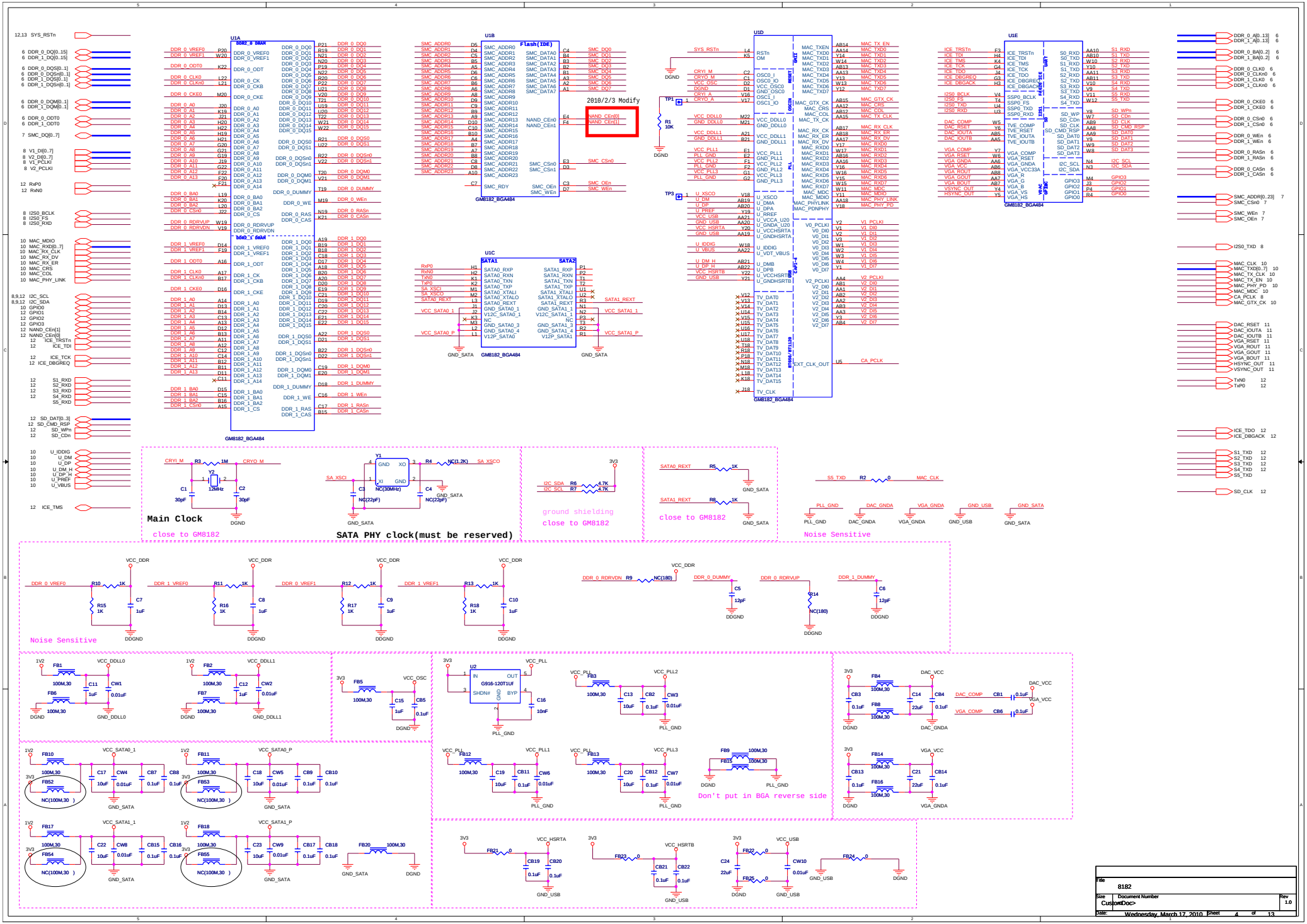

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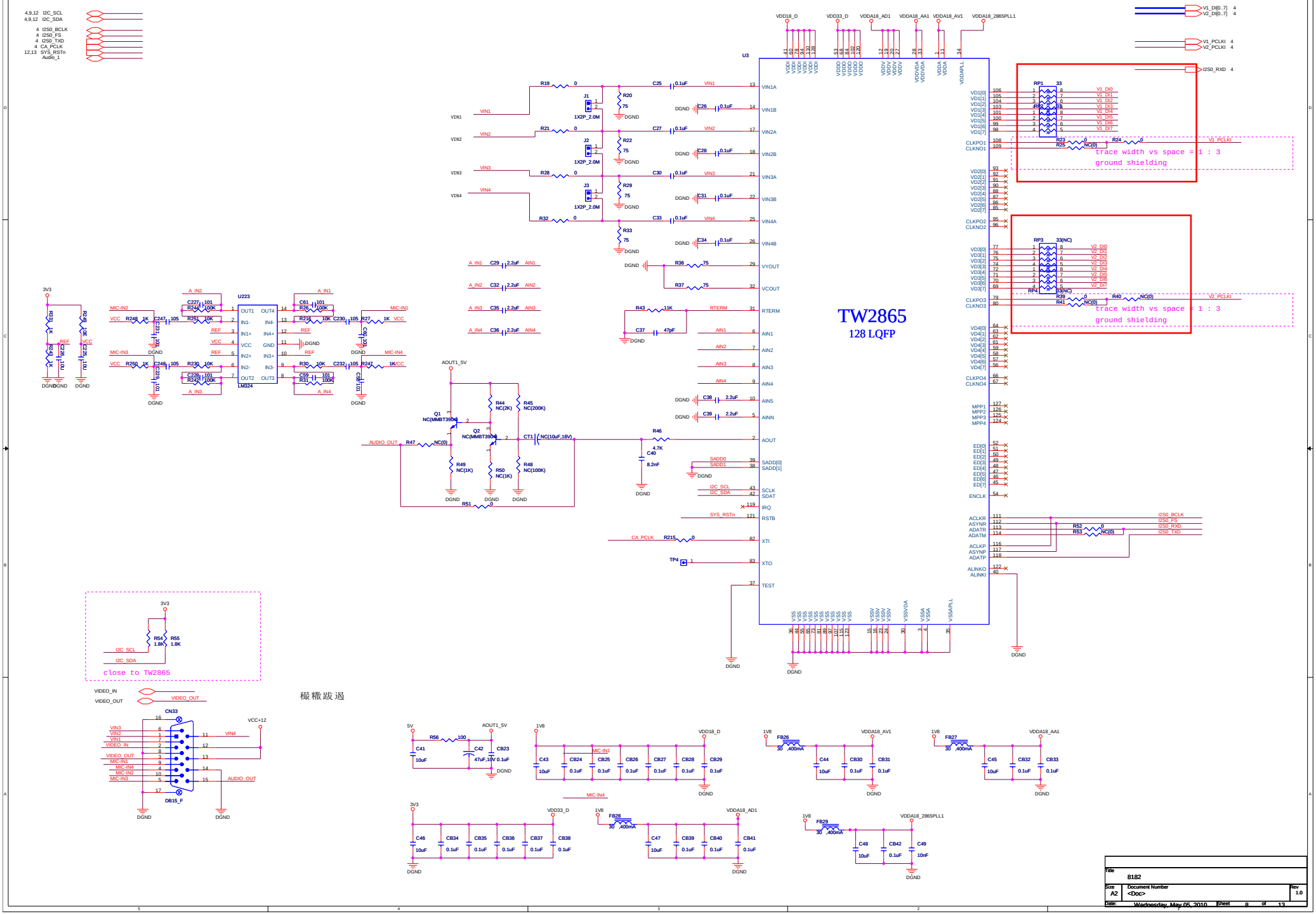

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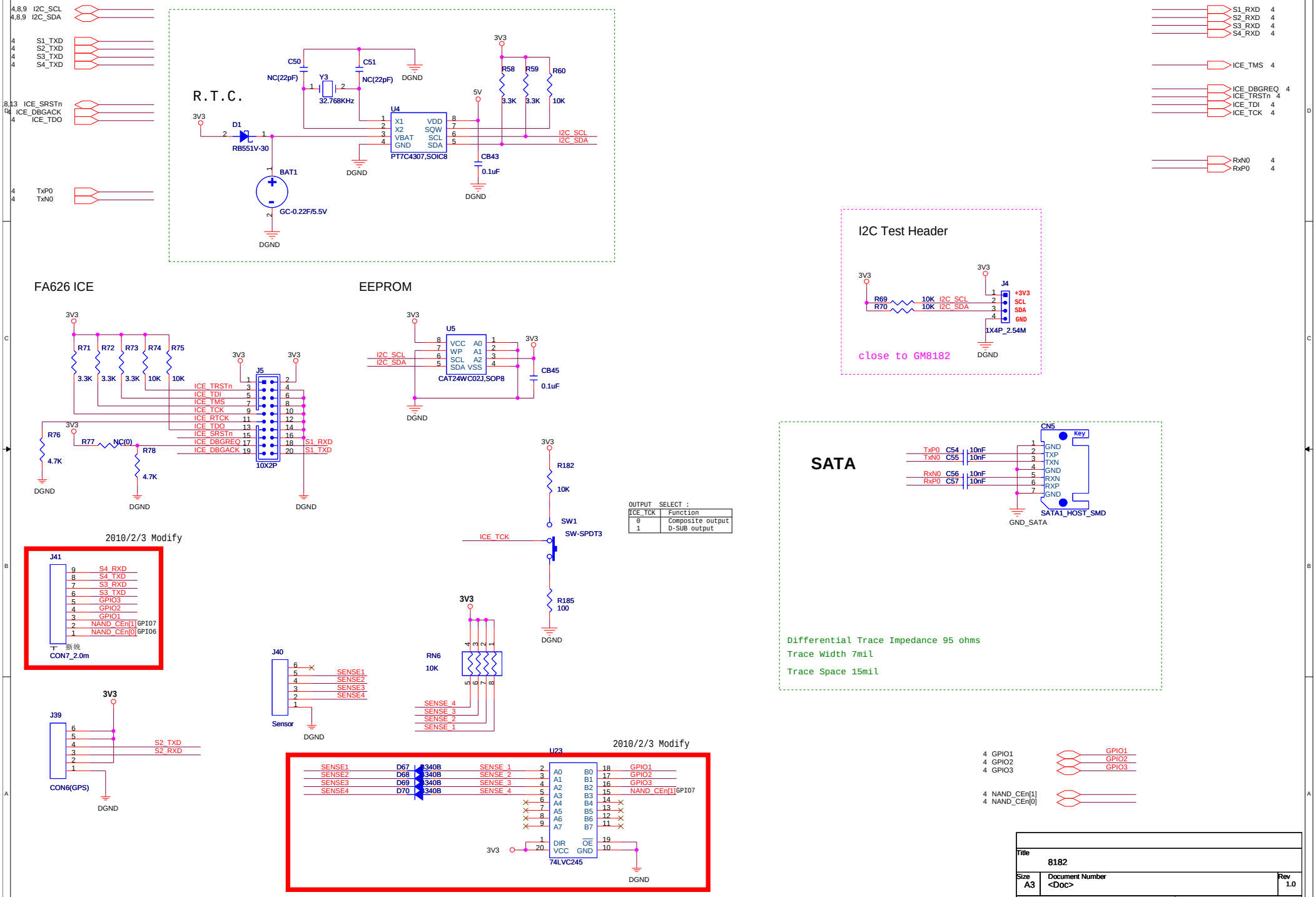

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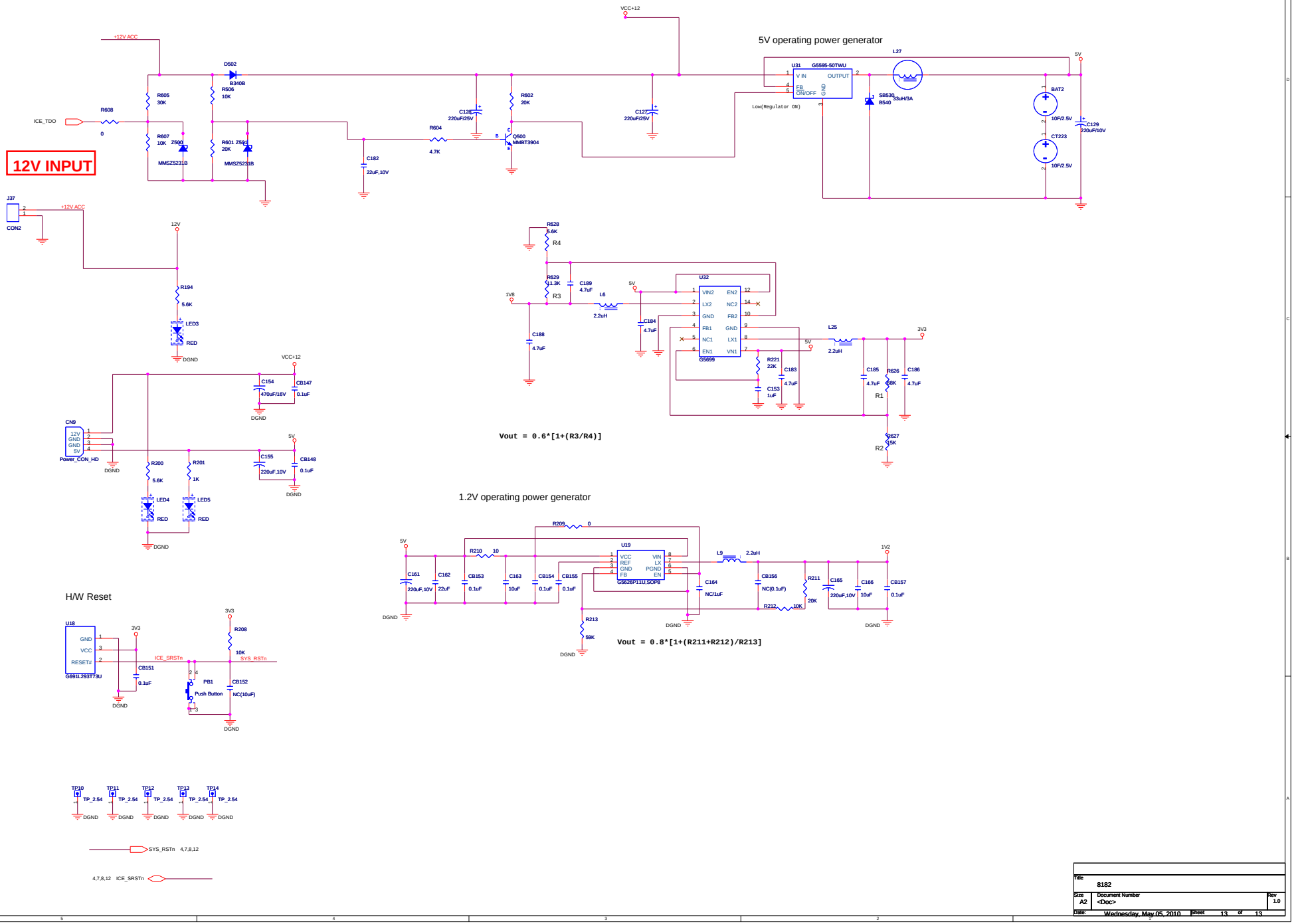

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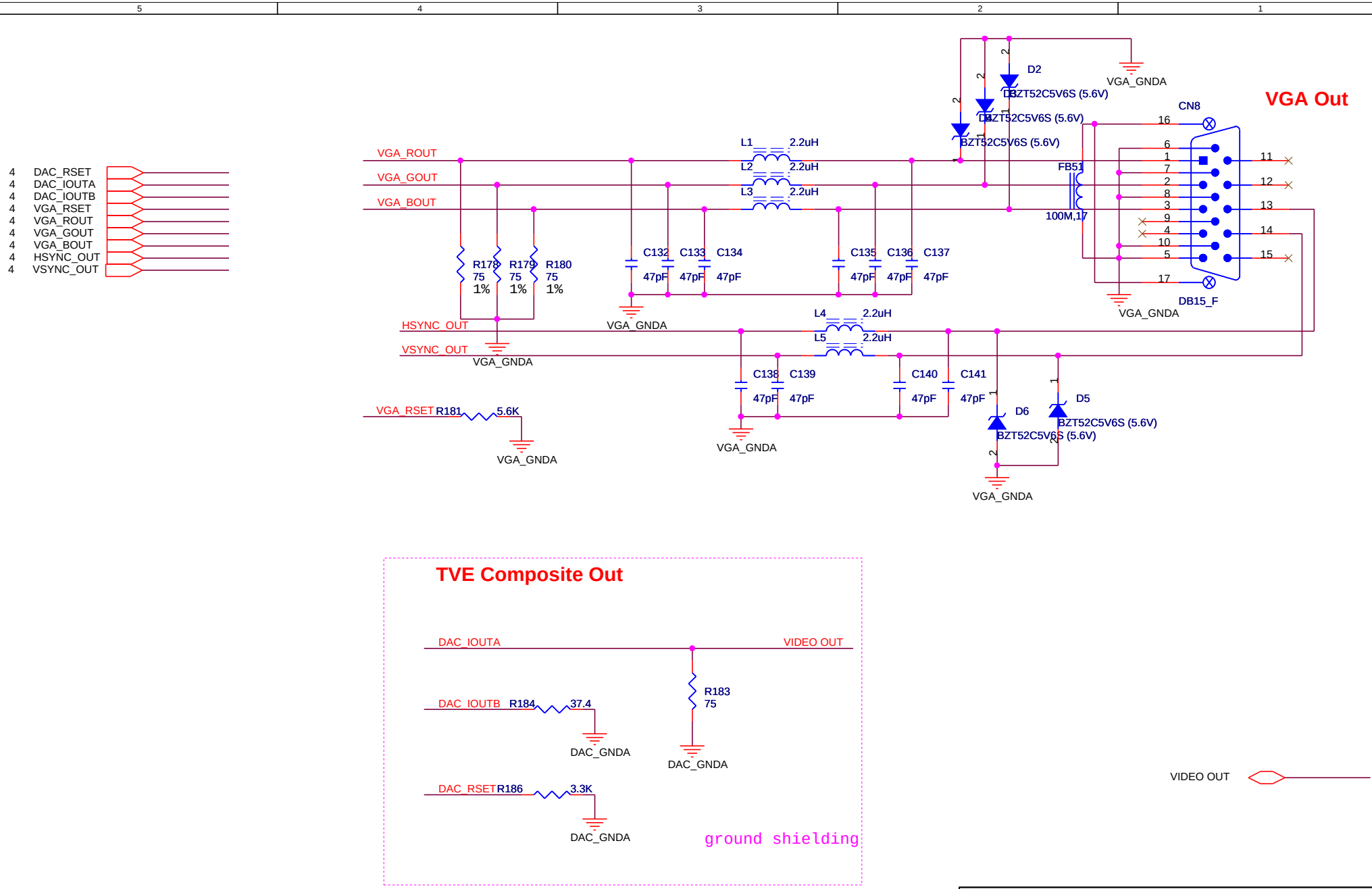

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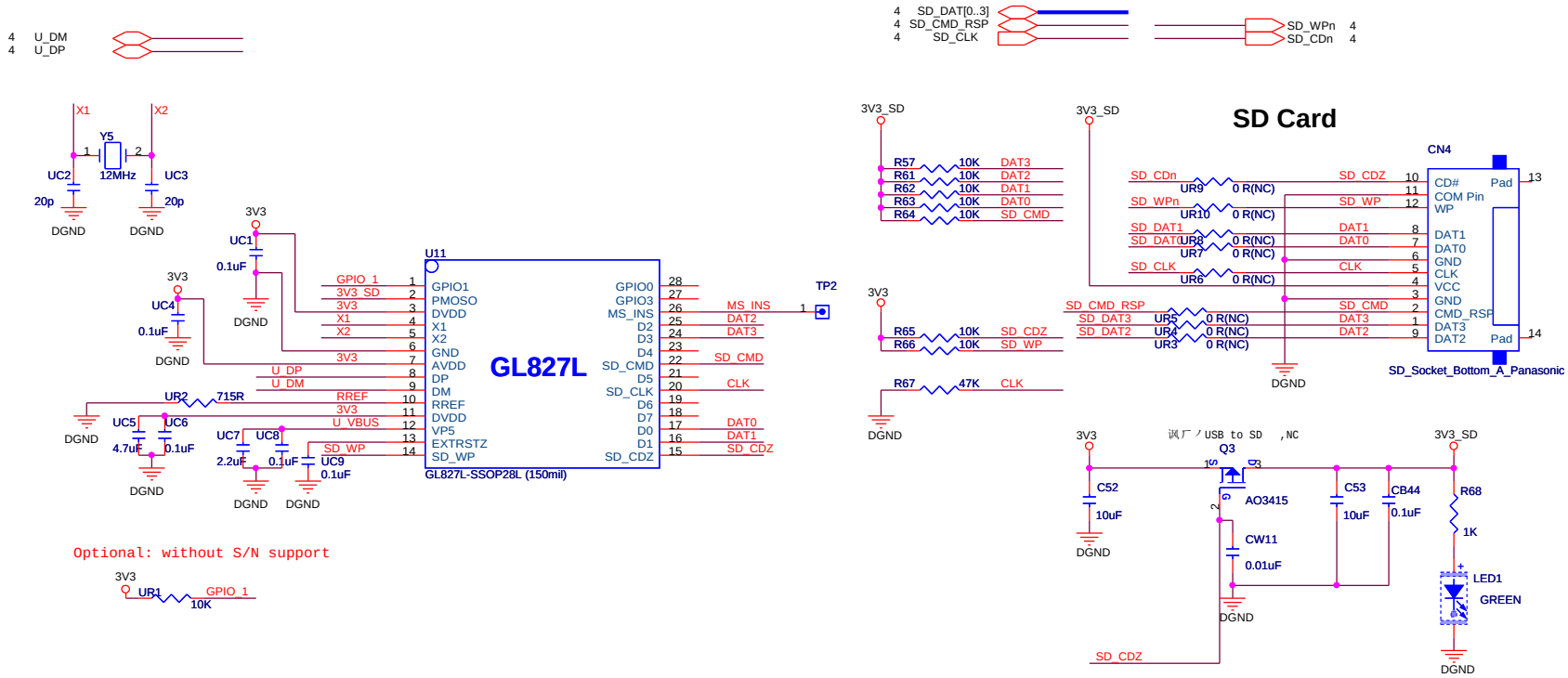

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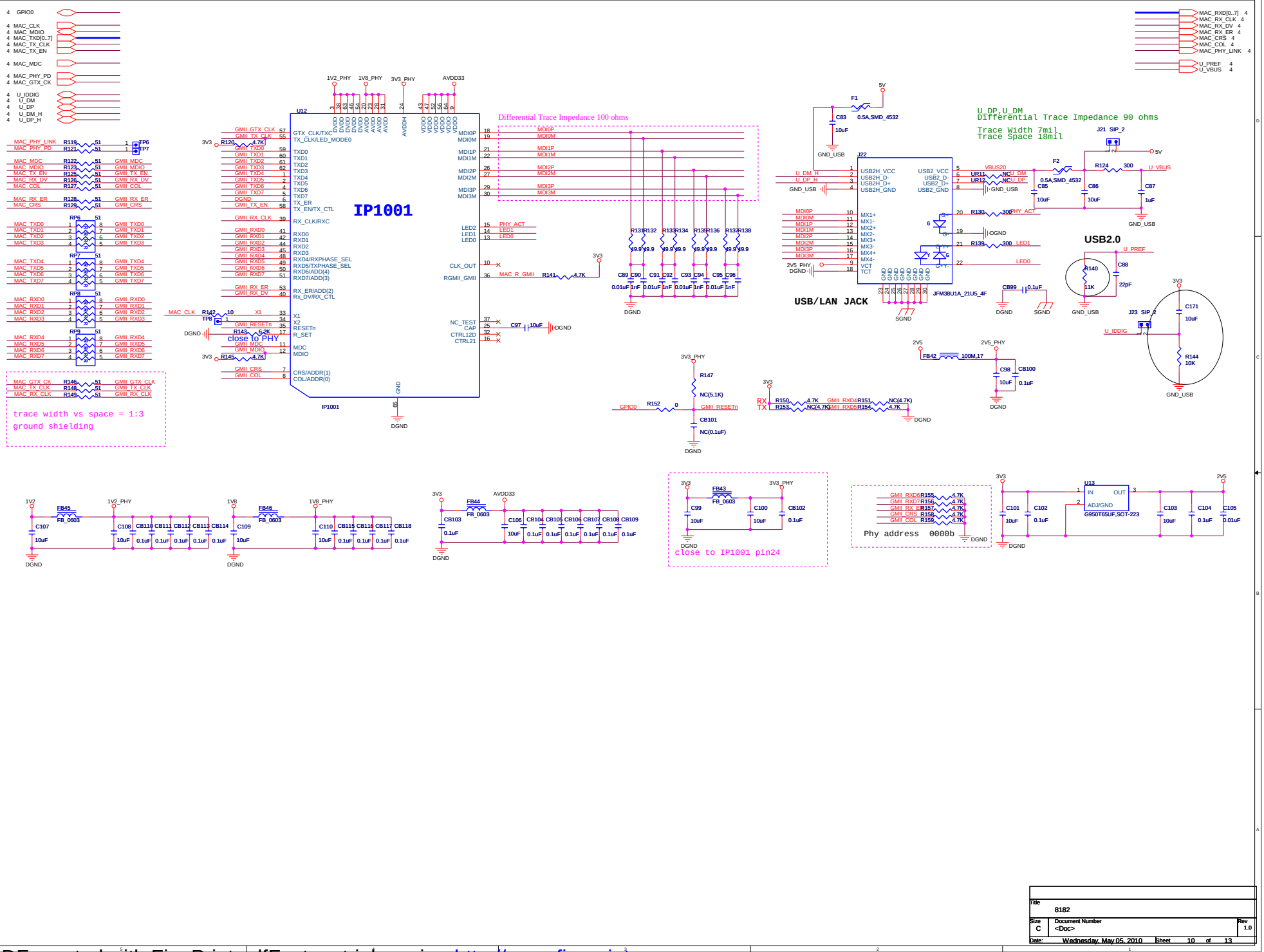

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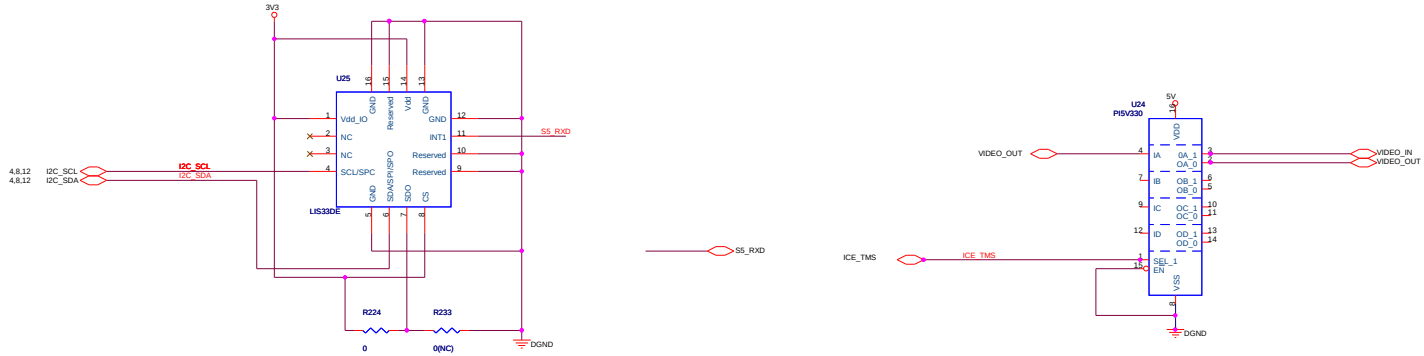

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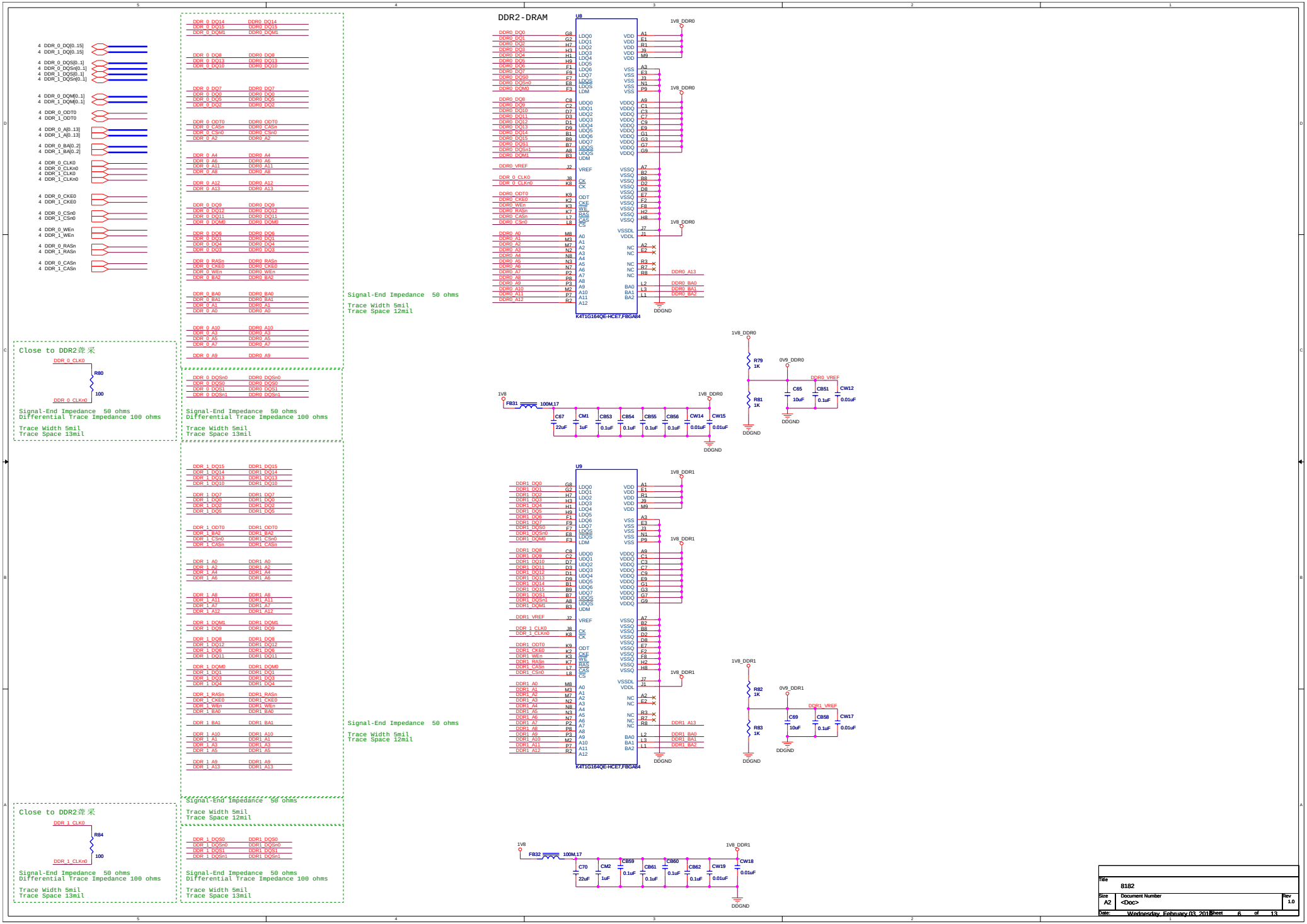

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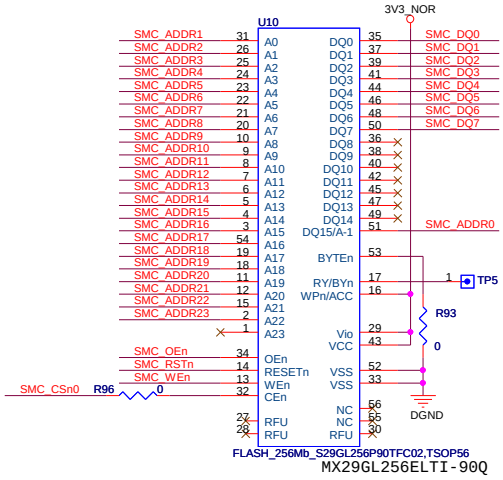
4 SMC_DQ[0..7]
4 SMC_ADDR[0..23]
4 SMC_CSn0

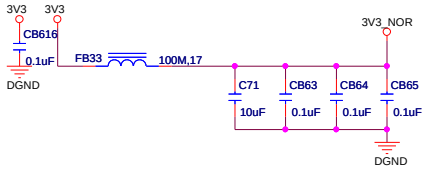
4 SMC_OEn
4 SMC_WEn

12,13 SYS_RSTn

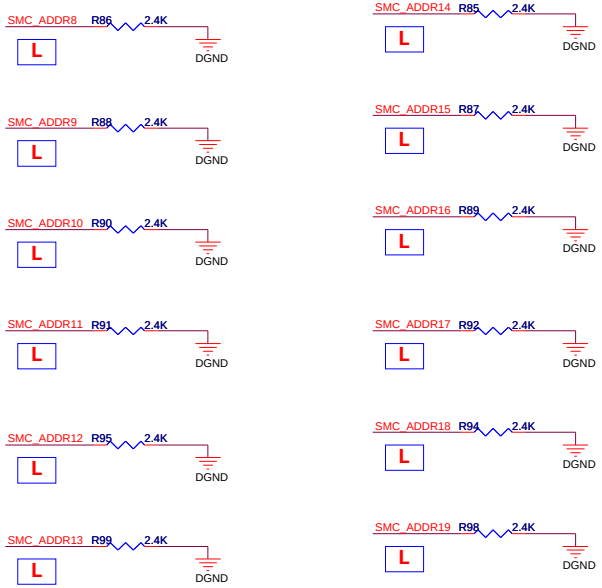
NOR Flash 32Mx8 (byte mode)



SYS_RSTn R100 0 SMC_RSTn



Boot Jumper Setting



Debug/Normal :

A10	Function
0	Normal
1	Debug

Timer Test :

A11	Function
0	Disable
1	Enable

Boot from NOR/NAND :

A13	Function
0	Boot from NOR
1	Boot from NAND

PLL Setting :

A9	A8	Function
0	0	408MHZ
0	1	1200MHZ
1	0	400MHZ(ASYNC)
0	1	600MHZ(ASYNC)

NAND flash type

A19	A18	A17	A16	A15	A14	Function
0	0	0	0	1	1	Firmware update
1	1	1	1	1	1	CPU test mode
X	X	X	X	0	0	Address cycle 3
X	X	X	X	0	1	Address cycle 4
X	X	X	X	1	0	Address cycle 5
X	X	X	0	X	X	Page size 0x200
X	X	X	1	X	X	Page size 0x800
X	0	0	X	X	X	Block size 16 pages
X	0	1	X	X	X	Block size 32 pages
X	1	0	X	X	X	Block size 64 pages
X	1	1	X	X	X	Block size 128 pages
0	X	X	X	X	X	8 Bits
1	X	X	X	X	X	16 Bits

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