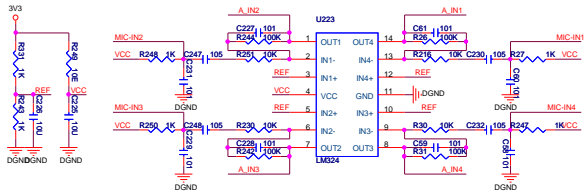
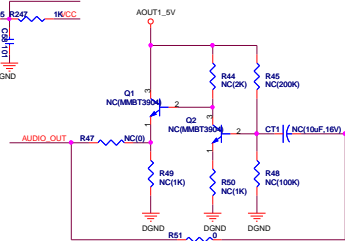


4,9,12 I2C_SCL
4,9,12 I2C_SDA
4 I2S0_BCLK
4 I2S0_FS
4 I2S0_TXD
4 CA_PCLK
12,13 SYS_RSTn
Audio_1

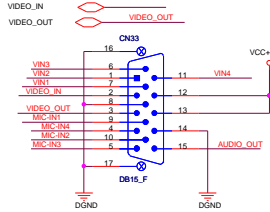
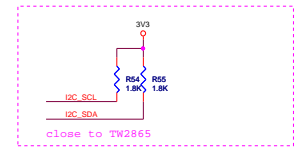


A_IN1 C29 2.2uF AIN1
A_IN2 C32 2.2uF AIN2
A_IN3 C35 2.2uF AIN3
A_IN4 C36 2.2uF AIN4

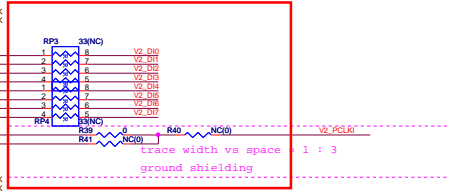
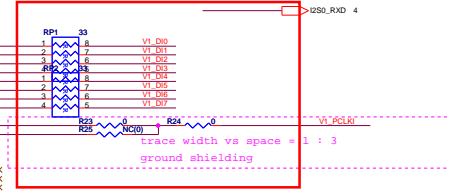
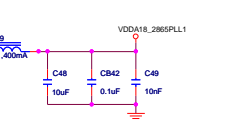
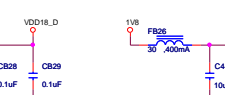
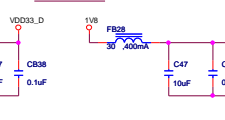
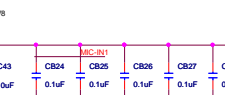
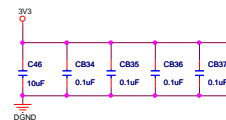
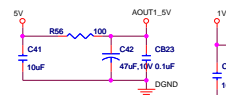


AOUT1_VS
AOUT2_VS
AOUT3_VS
AOUT4_VS
AOUT5_VS
AOUT6_VS
AOUT7_VS
AOUT8_VS
AOUT9_VS
AOUT10_VS
AOUT11_VS
AOUT12_VS
AOUT13_VS
AOUT14_VS
AOUT15_VS
AOUT16_VS
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AOUT85_VS
AOUT86_VS
AOUT87_VS
AOUT88_VS
AOUT89_VS
AOUT90_VS
AOUT91_VS
AOUT92_VS
AOUT93_VS
AOUT94_VS
AOUT95_VS
AOUT96_VS
AOUT97_VS
AOUT98_VS
AOUT99_VS
AOUT100_VS

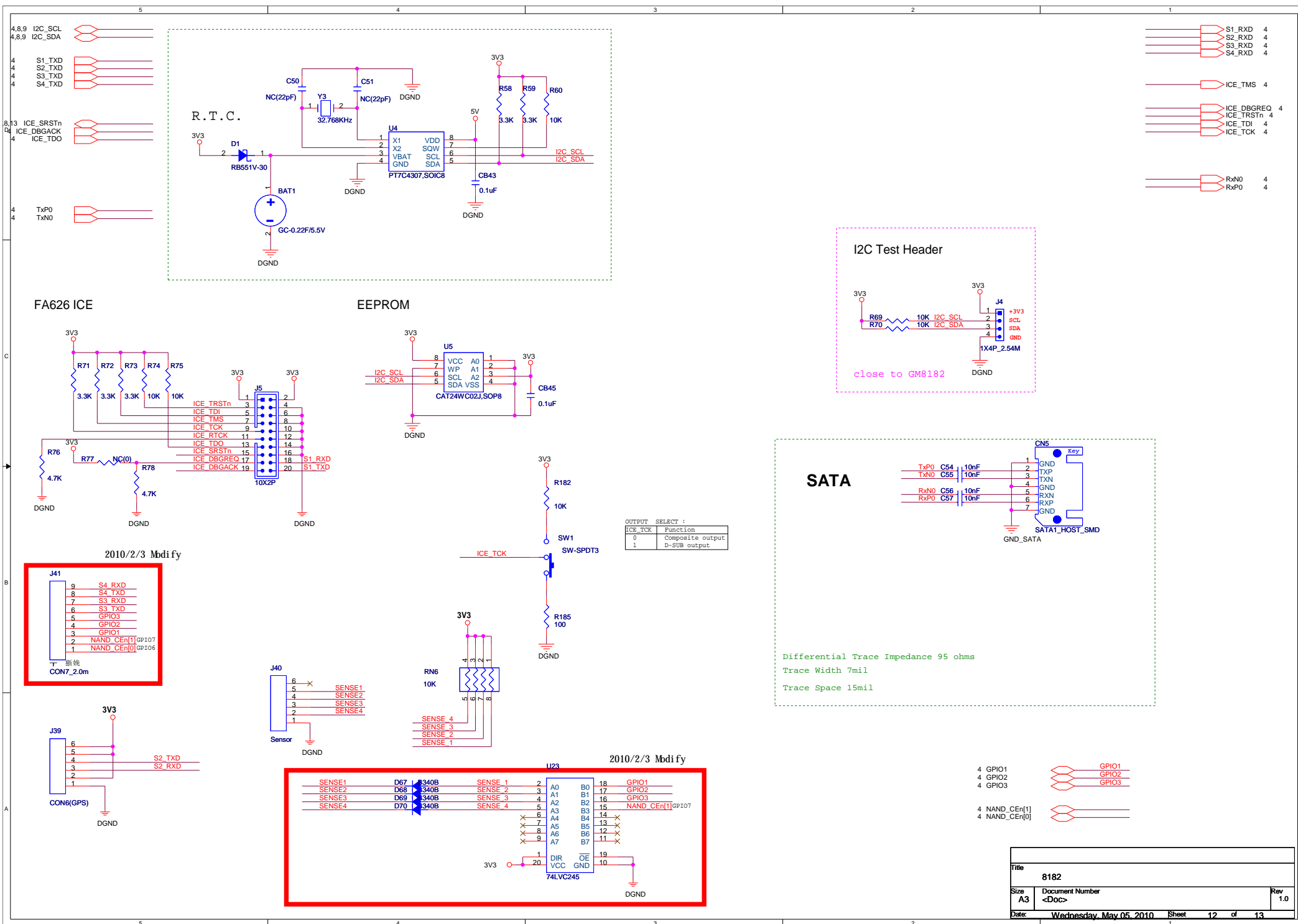
TW2865
128 LQFP



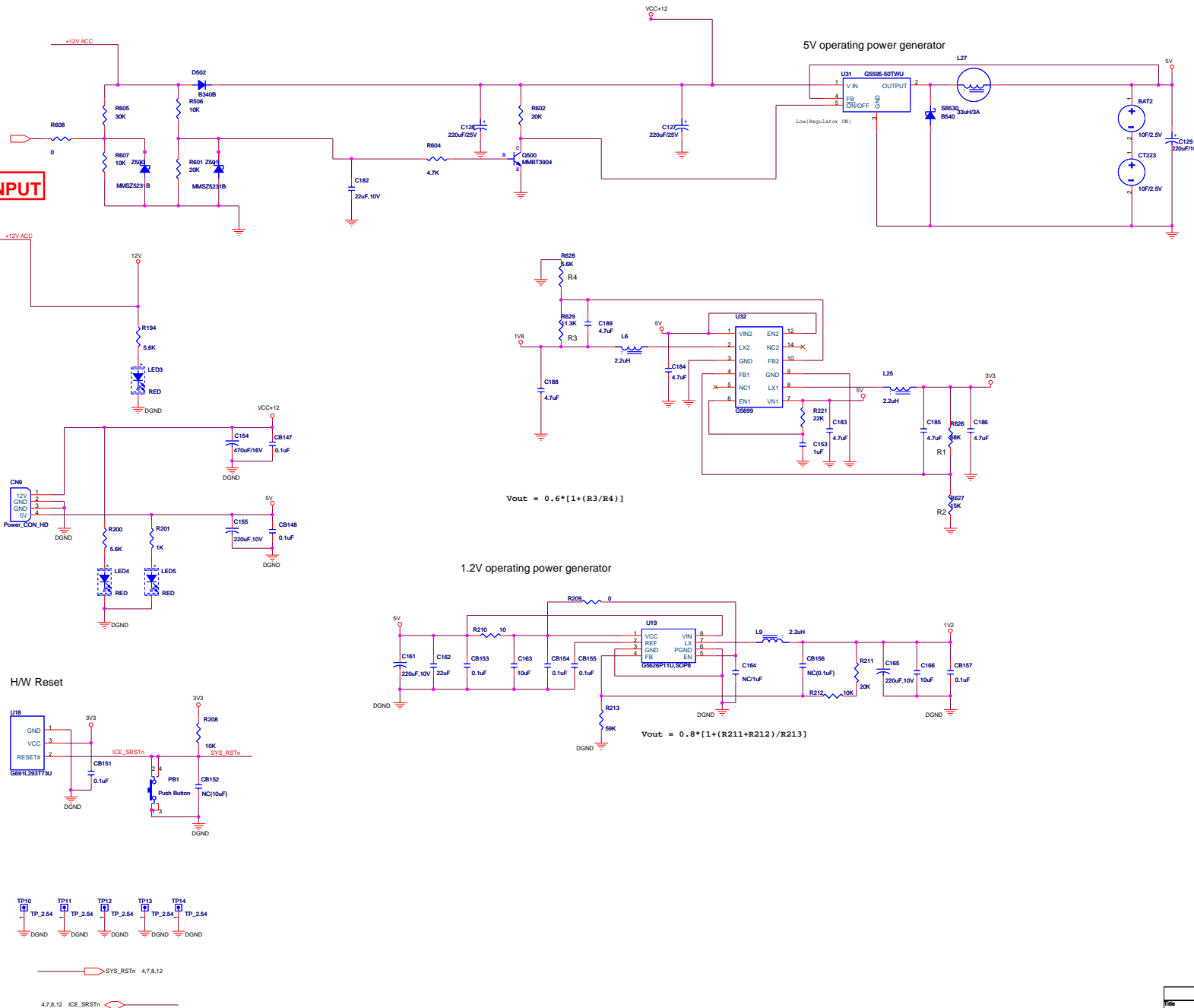
極權跋過



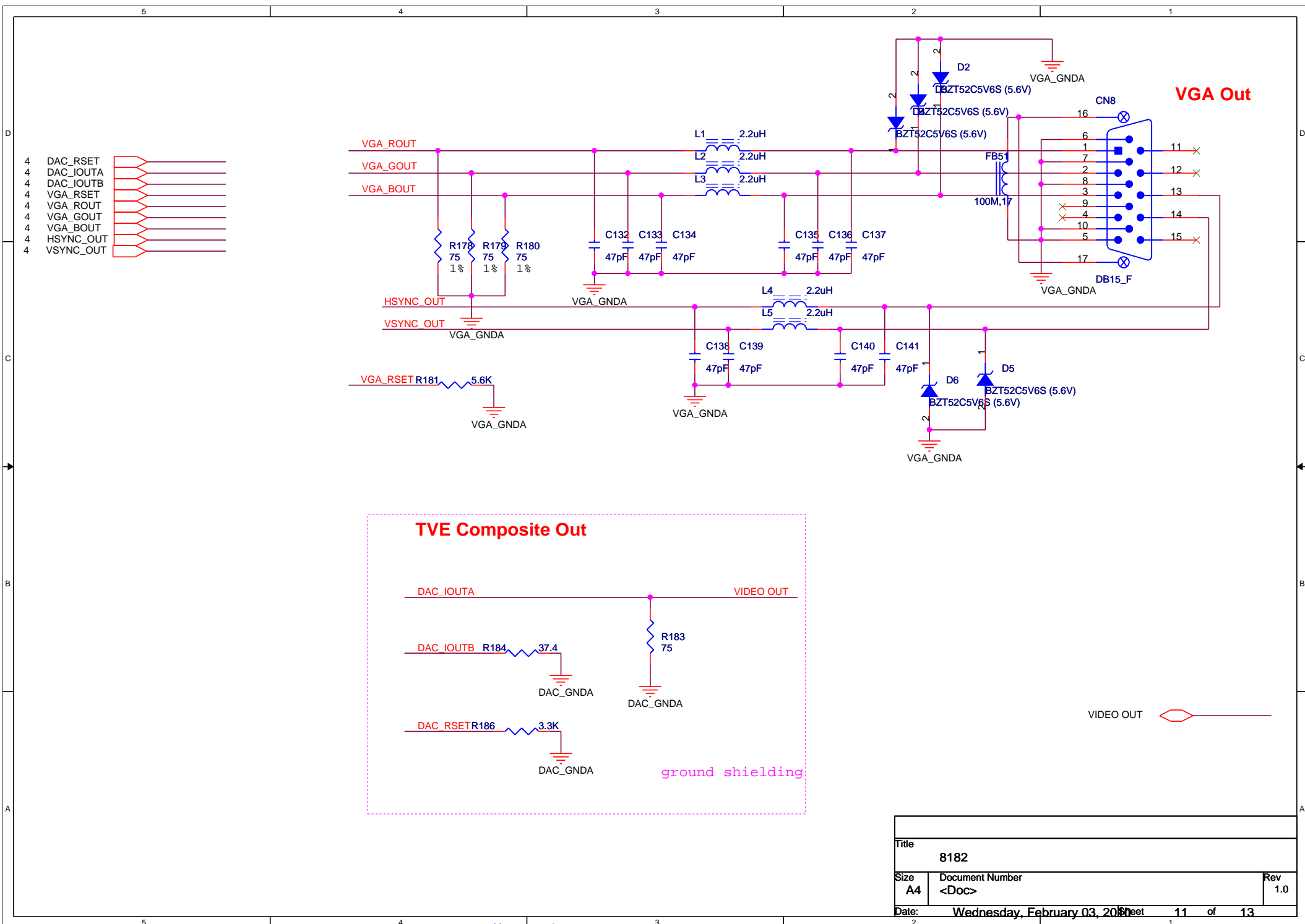
File	8182	Rev	1.0
Size	Document Number		
A2	<Doc>		
Date	Wednesday, May 05, 2010	Sheet	8 of 13



Title			8182
Size	Document Number	Rev	
A3	<Doc>	1.0	
Date:	Wednesday, May 05, 2010	Sheet	12 of 13

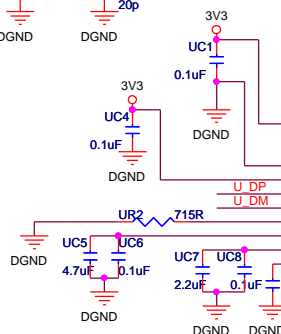
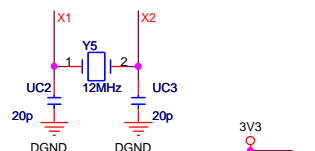


File	8182	
Size	Document Number	Rev
A2	<Doc>	1.0
Date	Wednesday, May 05, 2010	Sheet 13 of 13

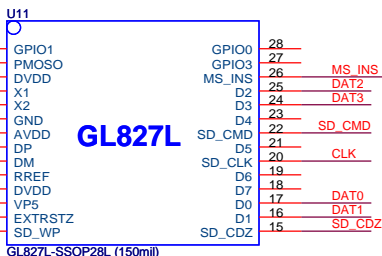


Title		
8182		
Size	Document Number	Rev
A4	<Doc>	1.0
Date: Wednesday, February 03, 2011		
Sheet 11 of 13		

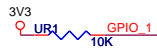
4 U_DM
4 U_DP



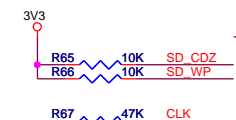
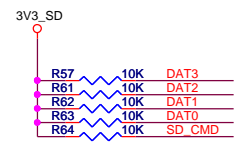
GL827L



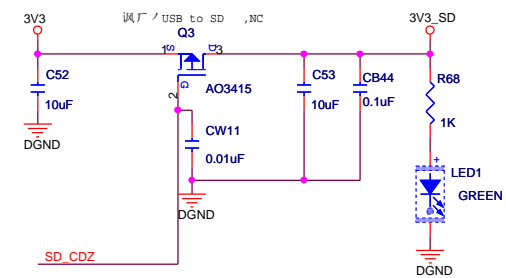
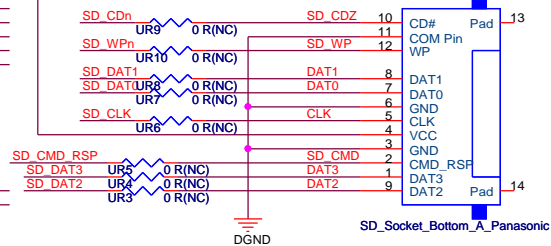
Optional: without S/N support



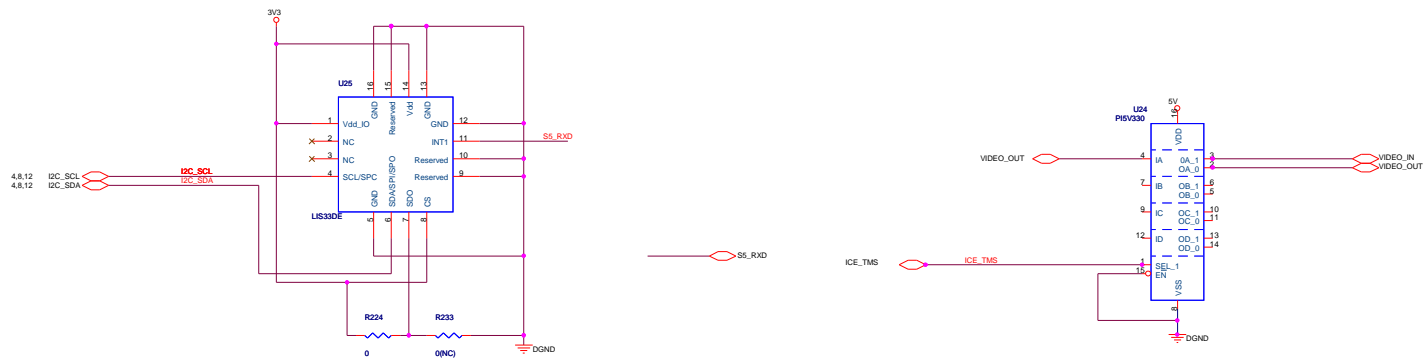
4 SD_DAT[0..3]
4 SD_CMD_RSP
4 SD_CLK



SD Card



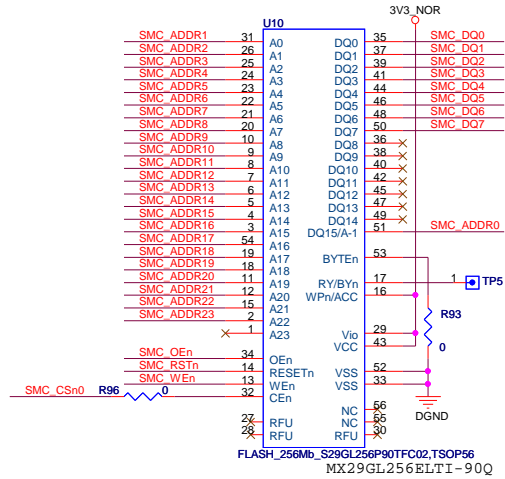
Title		
8182		
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4 SMC_DQ[0..7]
4 SMC_ADDR[0..23]
4 SMC_CSn0
4 SMC_OEn
4 SMC_WEn
12,13 SYS_RSTn

NOR Flash 32Mx8
(byte mode)



SYS_RSTn R100 0 SMC_RSTn

Boot Jumper Setting

SMC_ADDR8 R86 2.4K
L

SMC_ADDR9 R88 2.4K
L

SMC_ADDR10 R90 2.4K
L

SMC_ADDR11 R91 2.4K
L

SMC_ADDR12 R95 2.4K
L

SMC_ADDR13 R99 2.4K
L

Debug/Normal :	
A10	Function
0	Normal
1	Debug

Timer Test :	
A11	Function
0	Disable
1	Enable

Boot from NOR/NAND :	
A13	Function
0	Boot from NOR
1	Boot from NAND

PLL Setting :	
A9 A8	Function
0 0	408MHz
0 1	1200MHz
1 0	400MHz (ASYNCR)
0 1	600MHz (ASYNCR)

SMC_ADDR14 R85 2.4K
L

SMC_ADDR15 R87 2.4K
L

SMC_ADDR16 R89 2.4K
L

SMC_ADDR17 R92 2.4K
L

SMC_ADDR18 R94 2.4K
L

SMC_ADDR19 R98 2.4K
L

SMC_ADDR20 R101 2.4K
L

SMC_ADDR21 R102 2.4K
L

SMC_ADDR22 R105 2.4K
L

SMC_ADDR23 R107 2.4K
L

NAND flash type						Function
A19	A18	A17	A16	A15	A14	
0	0	0	0	1	1	Firmware update
1	1	1	1	1	1	CPU test mode
X	X	X	X	0	0	Address cycle 3
X	X	X	X	0	1	Address cycle 4
X	X	X	X	1	0	Address cycle 5
X	X	X	0	X	X	Page size 0x200
X	X	X	1	X	X	Page size 0x800
X	0	0	X	X	X	Block size 16 pages
X	0	1	X	X	X	Block size 32 pages
X	1	0	X	X	X	Block size 64 pages
X	1	1	X	X	X	Block size 128 pages
0	X	X	X	X	X	8 Bits
1	X	X	X	X	X	16 Bits

Title		
8182		
Size	Document Number	Rev
A3	<Doc>	1.0
Date:		
Wednesday, February 03, 2016		
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