## Suanmper

## DVS-0828FINAL.DSN

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Release Version	Description

Owing to the GM8182 is a high speed IC with high speed DDR2-SDRAM interface, improper PCB layout will cause the function fail.

From our experience, we found that the GM8182 performance and stability is strongly related to the PCB layout.

In order to ensure your system will work when making your own EVB at the first time, please copy the Power/Ground and DDR layout of our sample design into your system.

If your PCB layout of the Power/Ground and DDR part is not identical to our sample design, we may refuse to support your EVB debugging.

GM8182是一顆高速SoC晶片,並搭配高速之DD2 SDRAM介面,不正確的pcB繞線。會造成EVB系統工作不正常, 從我們多次設計GM8182系統經驗,發現GM8183的性能與穩定性,會與pcB繞線品質有強烈之關係。 因此我們會提供客戶Layout 與cerber file做為參考 為了確保客戶在製作屬於自己的SVB時,第一次就能成功, 我們要求客戶之PCB繞線。TGBA POWER (FOUND ADDRS SDRAM部分,要與原駁相同。 假若客戶之PCB繞線與原販要求不同時,基於人力考量,我們可能拒絕幫助客戶的PCB值錯工作

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## Revision

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			1				7
			2				8
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2009.10.21		Modify USB power and gnd Bead. Modify GM8182 SATA power and gnd.	4	2009.10.21		Modify USB Vref Resistor to 11K. Modify OTG IDDIG circuit.	10
			5				11
2009.10.27	1V1	1. Modify DDR series resistor.	6				12

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