

16 DATA INPUT/OUTPUTS

25 ADDRESS INPUT

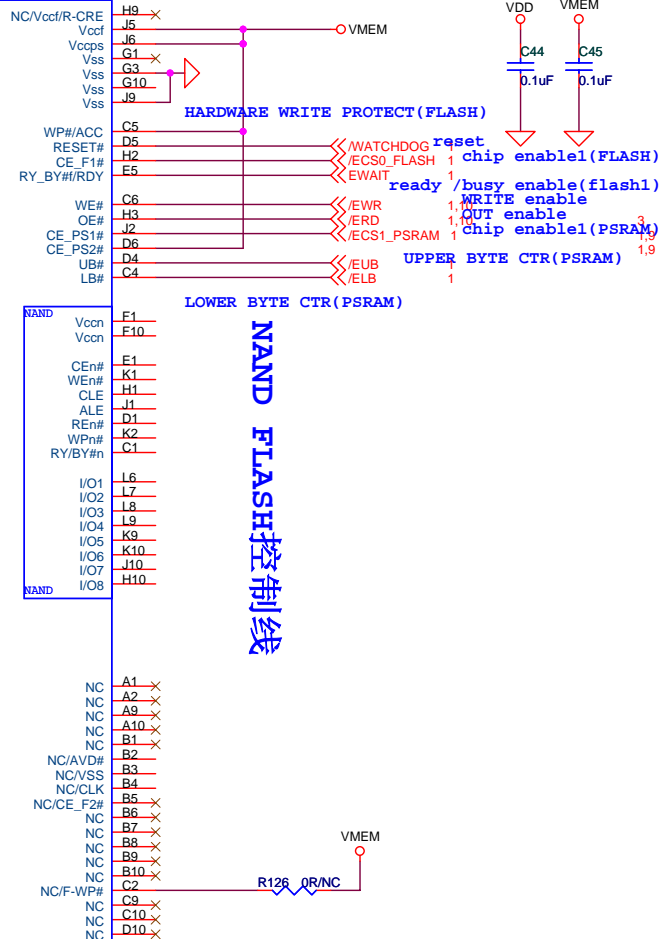
2.8V

TV00579002CAGD/ S71PL256N/ S71WS256N/ S71WS512P/S71PL254JB0

Spansion PLN Family

N88支持SPANSION/TOSHIBA FLASH

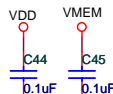
R-CRE: control register enable;
for cellularRAM only



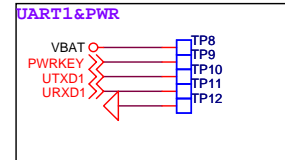
LOWER BYTE CTR(PSRAM)

NAND FLASH控制线

HARDWARE WRITE PROTECT(FLASH)



reset
chip enable1(FLASH)
ready/busy enable(FLASH)
WRITE enable
OUT enable
chip enable1(PSRAM)
UPPER BYTE CTR(PSRAM)



测试调试端口以及夹具制作

Title		
Q1		
Size	Document Number	Rev
B	FLASH	V1.0
Date:	Monday, September 21, 2009	Sheet 5 of 11

