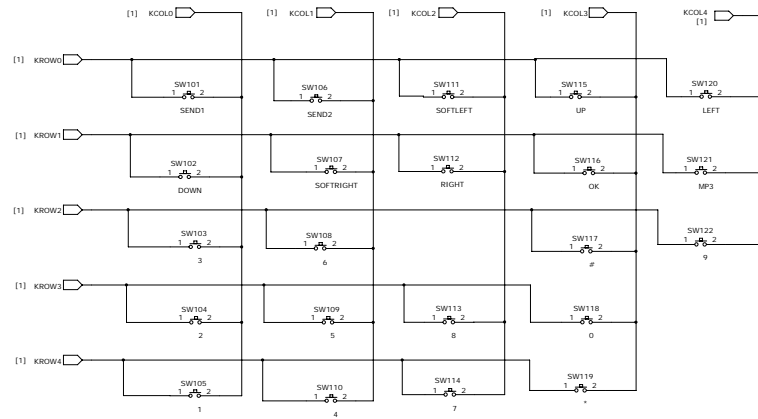
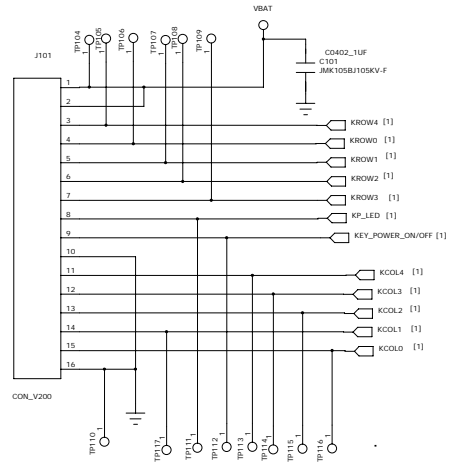
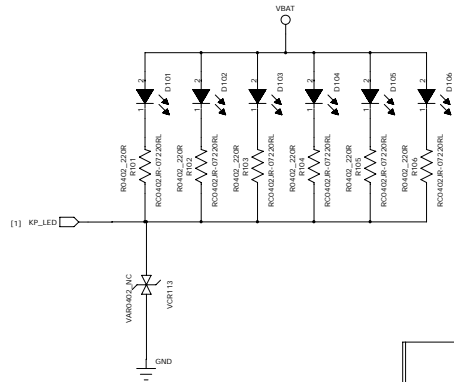
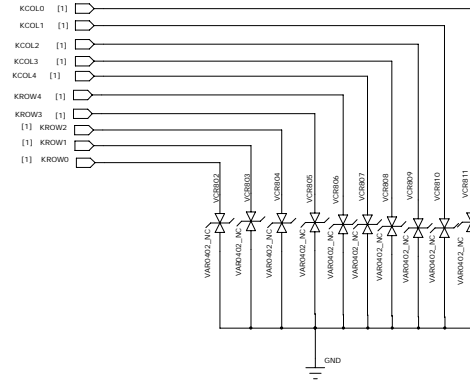
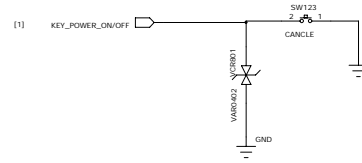
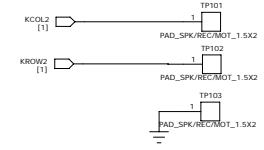


Key pad

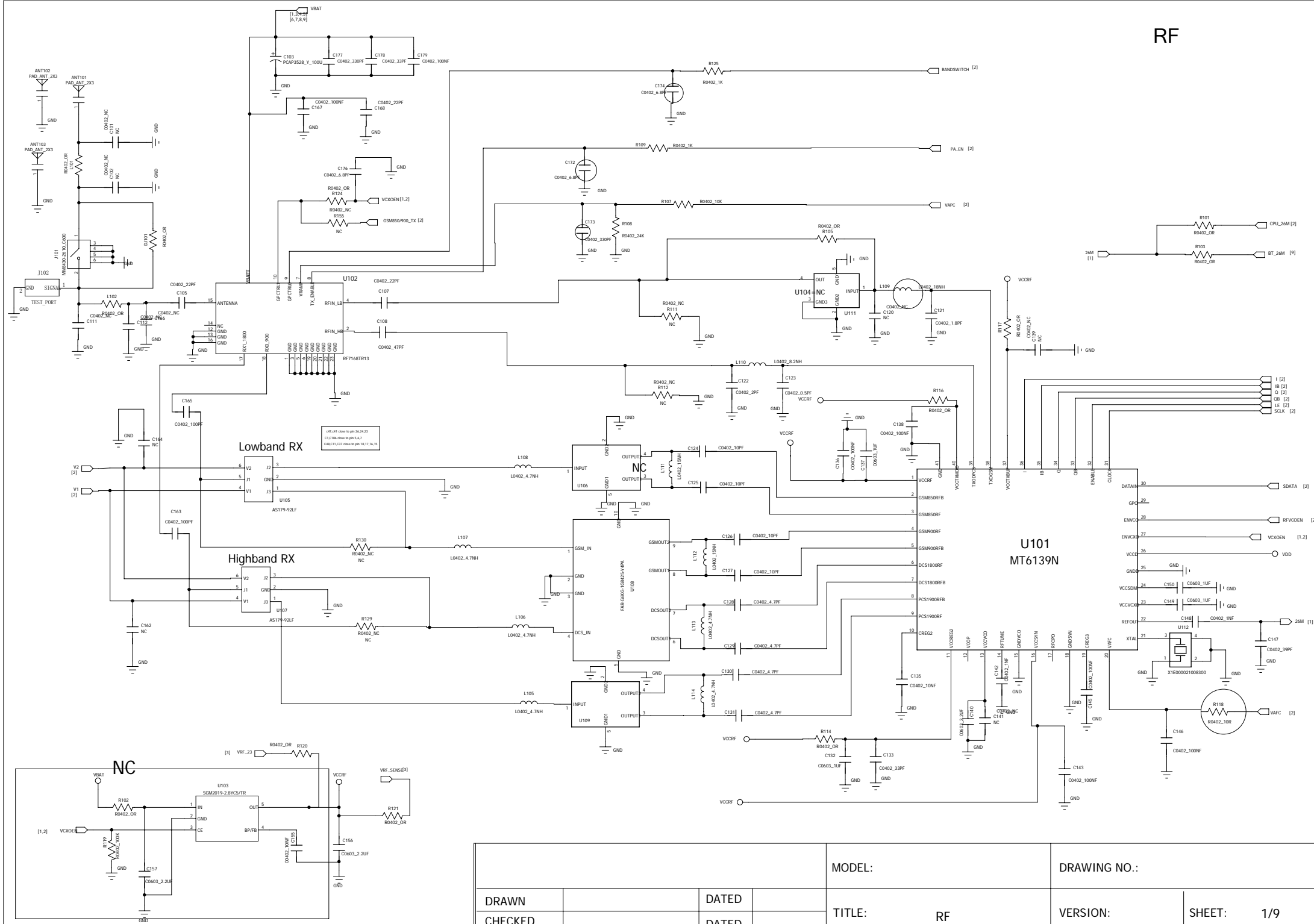


SIDEKEY



			MODEL:	DRAWING NO.:	
DRAWN		DATED	TITLE:	VERSION:	SHEET: 1/1
CHECKED		DATED			

RF



				MODEL:	DRAWING NO.:	
DRAWN		DATED		TITLE: RF	VERSION:	SHEET: 1/9
CHECKED		DATED				


The diagram illustrates the pinmux configuration for the STM32MP137. It shows the mapping of various pins to specific peripheral functions. The pins are organized into columns, and the peripheral functions are listed on the right. The connections are as follows:

- GPIOA:** PA0 (GPIOA[0]), PA1 (GPIOA[1]), PA2 (GPIOA[2]), PA3 (GPIOA[3]), PA4 (GPIOA[4]), PA5 (GPIOA[5]), PA6 (GPIOA[6]), PA7 (GPIOA[7]), PA8 (GPIOA[8]), PA9 (GPIOA[9]), PA10 (GPIOA[10]), PA11 (GPIOA[11]), PA12 (GPIOA[12]), PA13 (GPIOA[13]), PA14 (GPIOA[14]), PA15 (GPIOA[15]).
- GPIOB:** PB0 (GPIOB[0]), PB1 (GPIOB[1]), PB2 (GPIOB[2]), PB3 (GPIOB[3]), PB4 (GPIOB[4]), PB5 (GPIOB[5]), PB6 (GPIOB[6]), PB7 (GPIOB[7]), PB8 (GPIOB[8]), PB9 (GPIOB[9]), PB10 (GPIOB[10]), PB11 (GPIOB[11]), PB12 (GPIOB[12]), PB13 (GPIOB[13]), PB14 (GPIOB[14]), PB15 (GPIOB[15]).
- GPIOC:** PC0 (GPIOC[0]), PC1 (GPIOC[1]), PC2 (GPIOC[2]), PC3 (GPIOC[3]), PC4 (GPIOC[4]), PC5 (GPIOC[5]), PC6 (GPIOC[6]), PC7 (GPIOC[7]), PC8 (GPIOC[8]), PC9 (GPIOC[9]), PC10 (GPIOC[10]), PC11 (GPIOC[11]), PC12 (GPIOC[12]), PC13 (GPIOC[13]), PC14 (GPIOC[14]), PC15 (GPIOC[15]).
- GPIOD:** PD0 (GPIOD[0]), PD1 (GPIOD[1]), PD2 (GPIOD[2]), PD3 (GPIOD[3]), PD4 (GPIOD[4]), PD5 (GPIOD[5]), PD6 (GPIOD[6]), PD7 (GPIOD[7]), PD8 (GPIOD[8]), PD9 (GPIOD[9]), PD10 (GPIOD[10]), PD11 (GPIOD[11]), PD12 (GPIOD[12]), PD13 (GPIOD[13]), PD14 (GPIOD[14]), PD15 (GPIOD[15]).
- GPIOE:** PE0 (GPIOE[0]), PE1 (GPIOE[1]), PE2 (GPIOE[2]), PE3 (GPIOE[3]), PE4 (GPIOE[4]), PE5 (GPIOE[5]), PE6 (GPIOE[6]), PE7 (GPIOE[7]), PE8 (GPIOE[8]), PE9 (GPIOE[9]), PE10 (GPIOE[10]), PE11 (GPIOE[11]), PE12 (GPIOE[12]), PE13 (GPIOE[13]), PE14 (GPIOE[14]), PE15 (GPIOE[15]).
- GPIOF:** PF0 (GPIOF[0]), PF1 (GPIOF[1]), PF2 (GPIOF[2]), PF3 (GPIOF[3]), PF4 (GPIOF[4]), PF5 (GPIOF[5]), PF6 (GPIOF[6]), PF7 (GPIOF[7]), PF8 (GPIOF[8]), PF9 (GPIOF[9]), PF10 (GPIOF[10]), PF11 (GPIOF[11]), PF12 (GPIOF[12]), PF13 (GPIOF[13]), PF14 (GPIOF[14]), PF15 (GPIOF[15]).
- GPIOG:** PG0 (GPIOG[0]), PG1 (GPIOG[1]), PG2 (GPIOG[2]), PG3 (GPIOG[3]), PG4 (GPIOG[4]), PG5 (GPIOG[5]), PG6 (GPIOG[6]), PG7 (GPIOG[7]), PG8 (GPIOG[8]), PG9 (GPIOG[9]), PG10 (GPIOG[10]), PG11 (GPIOG[11]), PG12 (GPIOG[12]), PG13 (GPIOG[13]), PG14 (GPIOG[14]), PG15 (GPIOG[15]).
- EXTI:** EXTI0 (EXTI[0]), EXTI1 (EXTI[1]), EXTI2 (EXTI[2]), EXTI3 (EXTI[3]), EXTI4 (EXTI[4]), EXTI5 (EXTI[5]), EXTI6 (EXTI[6]), EXTI7 (EXTI[7]), EXTI8 (EXTI[8]), EXTI9 (EXTI[9]), EXTI10 (EXTI[10]), EXTI11 (EXTI[11]), EXTI12 (EXTI[12]), EXTI13 (EXTI[13]), EXTI14 (EXTI[14]), EXTI15 (EXTI[15]).
- I2C:** I2C1 (I2C1[0]), I2C1 (I2C1[1]), I2C1 (I2C1[2]), I2C1 (I2C1[3]), I2C1 (I2C1[4]), I2C1 (I2C1[5]), I2C1 (I2C1[6]), I2C1 (I2C1[7]), I2C1 (I2C1[8]), I2C1 (I2C1[9]), I2C1 (I2C1[10]), I2C1 (I2C1[11]), I2C1 (I2C1[12]), I2C1 (I2C1[13]), I2C1 (I2C1[14]), I2C1 (I2C1[15]).
- SPI:** SPI1 (SPI1[0]), SPI1 (SPI1[1]), SPI1 (SPI1[2]), SPI1 (SPI1[3]), SPI1 (SPI1[4]), SPI1 (SPI1[5]), SPI1 (SPI1[6]), SPI1 (SPI1[7]), SPI1 (SPI1[8]), SPI1 (SPI1[9]), SPI1 (SPI1[10]), SPI1 (SPI1[11]), SPI1 (SPI1[12]), SPI1 (SPI1[13]), SPI1 (SPI1[14]), SPI1 (SPI1[15]).
- UART:** UART1 (UART1[0]), UART1 (UART1[1]), UART1 (UART1[2]), UART1 (UART1[3]), UART1 (UART1[4]), UART1 (UART1[5]), UART1 (UART1[6]), UART1 (UART1[7]), UART1 (UART1[8]), UART1 (UART1[9]), UART1 (UART1[10]), UART1 (UART1[11]), UART1 (UART1[12]), UART1 (UART1[13]), UART1 (UART1[14]), UART1 (UART1[15]).
- CAN:** CAN1 (CAN1[0]), CAN1 (CAN1[1]), CAN1 (CAN1[2]), CAN1 (CAN1[3]), CAN1 (CAN1[4]), CAN1 (CAN1[5]), CAN1 (CAN1[6]), CAN1 (CAN1[7]), CAN1 (CAN1[8]), CAN1 (CAN1[9]), CAN1 (CAN1[10]), CAN1 (CAN1[11]), CAN1 (CAN1[12]), CAN1 (CAN1[13]), CAN1 (CAN1[14]), CAN1 (CAN1[15]).
- USB:** USB1 (USB1[0]), USB1 (USB1[1]), USB1 (USB1[2]), USB1 (USB1[3]), USB1 (USB1[4]), USB1 (USB1[5]), USB1 (USB1[6]), USB1 (USB1[7]), USB1 (USB1[8]), USB1 (USB1[9]), USB1 (USB1[10]), USB1 (USB1[11]), USB1 (USB1[12]), USB1 (USB1[13]), USB1 (USB1[14]), USB1 (USB1[15]).
- Other:** ETH (ETH[0]), ETH (ETH[1]), ETH (ETH[2]), ETH (ETH[3]), ETH (ETH[4]), ETH (ETH[5]), ETH (ETH[6]), ETH (ETH[7]), ETH (ETH[8]), ETH (ETH[9]), ETH (ETH[10]), ETH (ETH[11]), ETH (ETH[12]), ETH (ETH[13]), ETH (ETH[14]), ETH (ETH[15]).

The schematic diagram illustrates the power supply section of the MT6223PA1AN-L. It features a +2.8V input connected to a network of capacitors (C204, C203, C205, C202, C208, C209, C210) and inductors (B8001, B8002). The input is filtered by C204 and C203, then passes through a series inductor B8001 and a parallel combination of C202 and C210. The output is filtered by C208 and C209, and then by C210. The input is also connected to a network of capacitors (C204, C203, C205, C202, C208, C209, C210) and inductors (B8001, B8002). The input is filtered by C204 and C203, then passes through a series inductor B8001 and a parallel combination of C202 and C210. The output is filtered by C208 and C209, and then by C210.

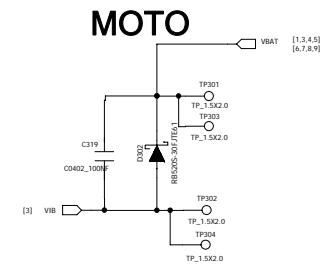
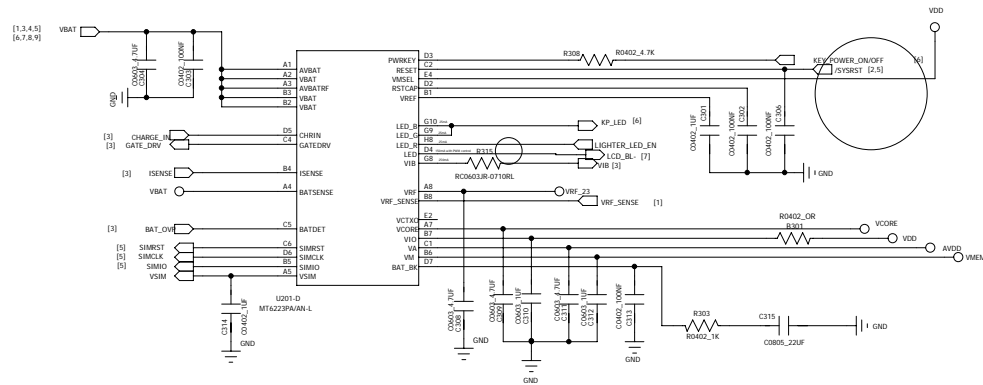
[illegible]

26M到CPU_26M和BT_26M的电路放到第一页

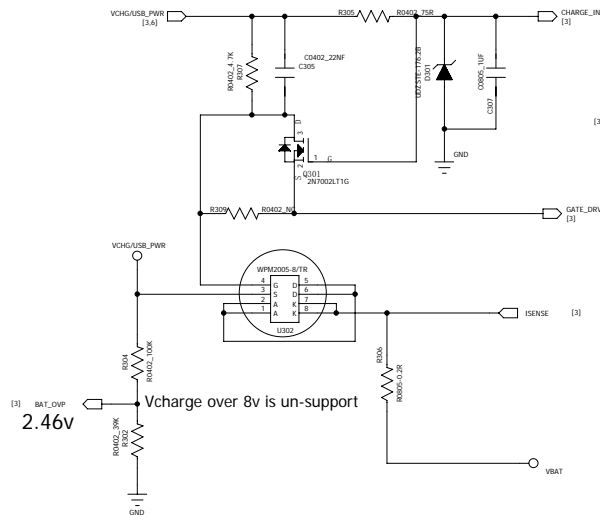


				MODEL:	DRAWING NO.:	
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CHECKED		DATED				

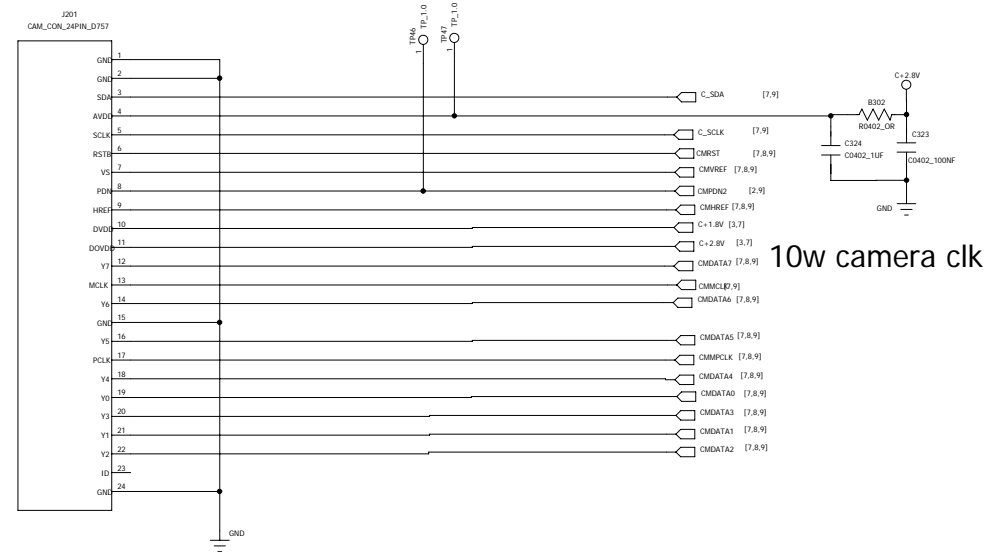
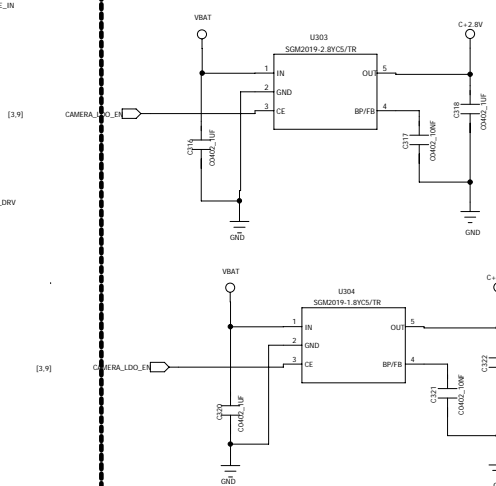
MT6223_BUILT_IN_POWER MANAGE IC



CHARGE

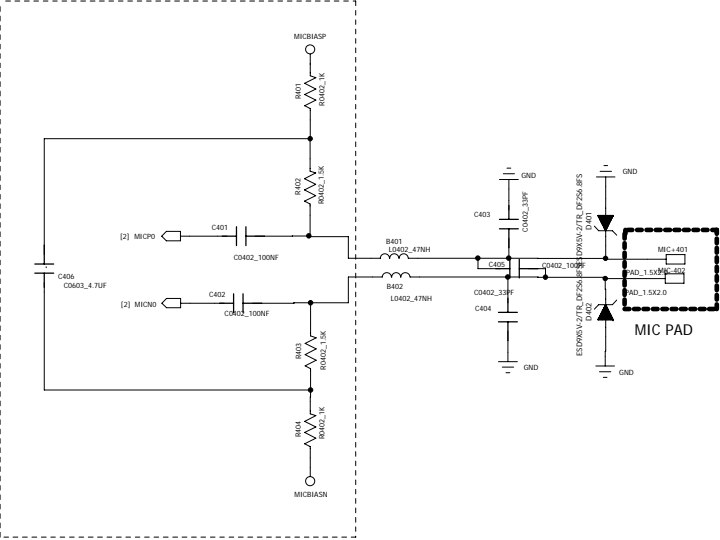


CAMERA POWER

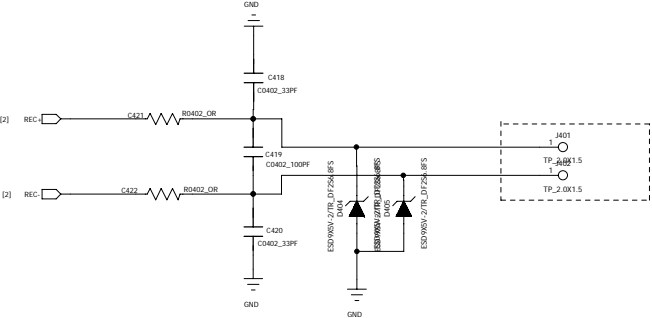
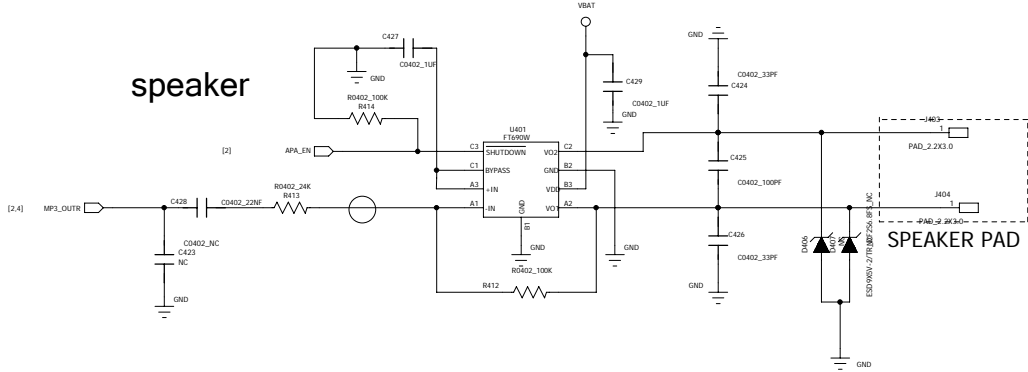
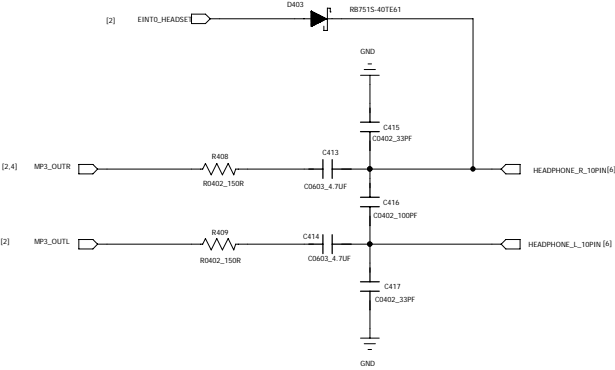
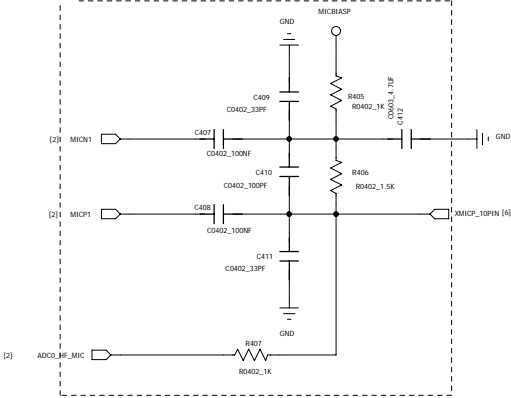


			MODEL:	DRAWING NO.:	
DRAWN		DATED	TITLE: BB-POWER	VERSION: 1.0	SHEET: 3/9
CHECKED		DATED			

MIC
Place these part near baseband

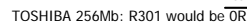


EAR-MIC
Place these parts near to CPU



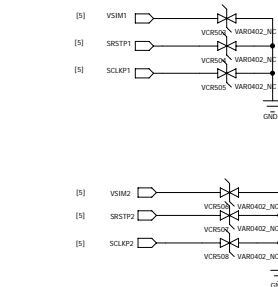
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DRAWN		DATED	TITLE: BB-AUDIO	VERSION:	SHEET: 4/9
CHECKED		DATED			

MCP:NOR,PSRAM

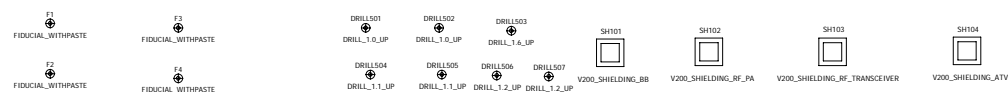


MT6225A can't support full 1.8V product stably, while MT6225B had removed this bug, can support full 1.8V product without LDO. And MT6223 have no information about it.

TV00570002CDGB	NC R502 R503 R505 R507,LDO OR R504 R506
M36LOT7050T3ZSP 1.8V CORE,2.8V IO	NC R502 R507 R504 LDO OR R503 R505 R506

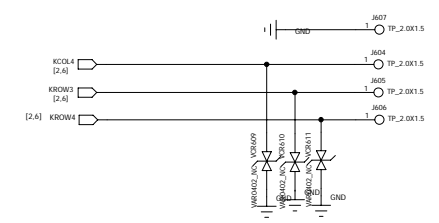
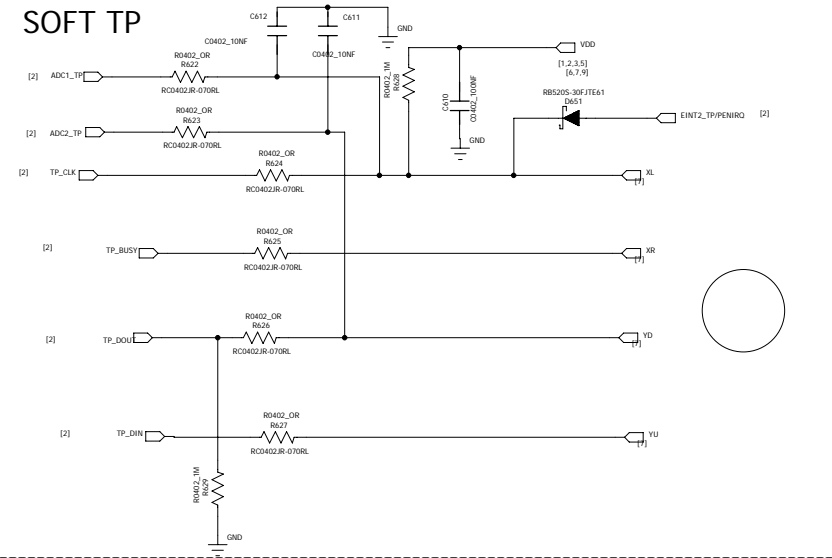


SHIELDING CASE



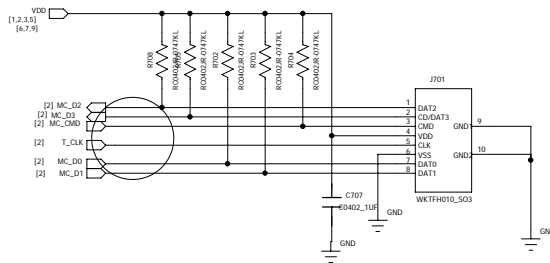
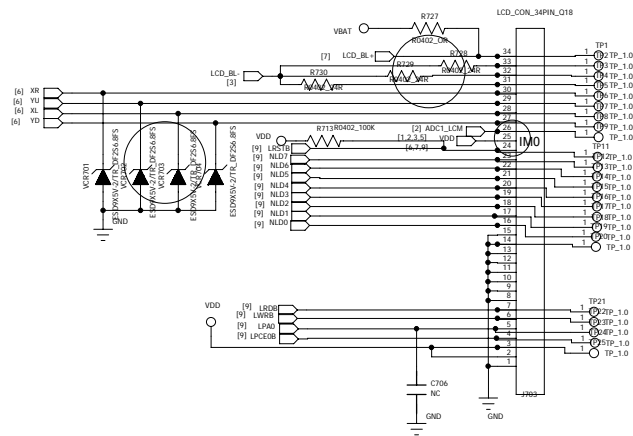
			MODEL:	DRAWING NO.:	
DRAWN		DATED	TITLE: MEMORY.SIM	VERSION:	SHEET: 5/9
CHECKED		DATED			

I/O CONNECTOR

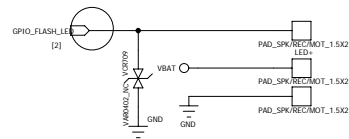


			MODEL:	DRAWING NO.:	
DRAWN		DATED	TITLE: I/O,G-SENSOR,BAT-CON,KEYPAD	VERSION:	SHEET: 6/9
CHECKED		DATED			

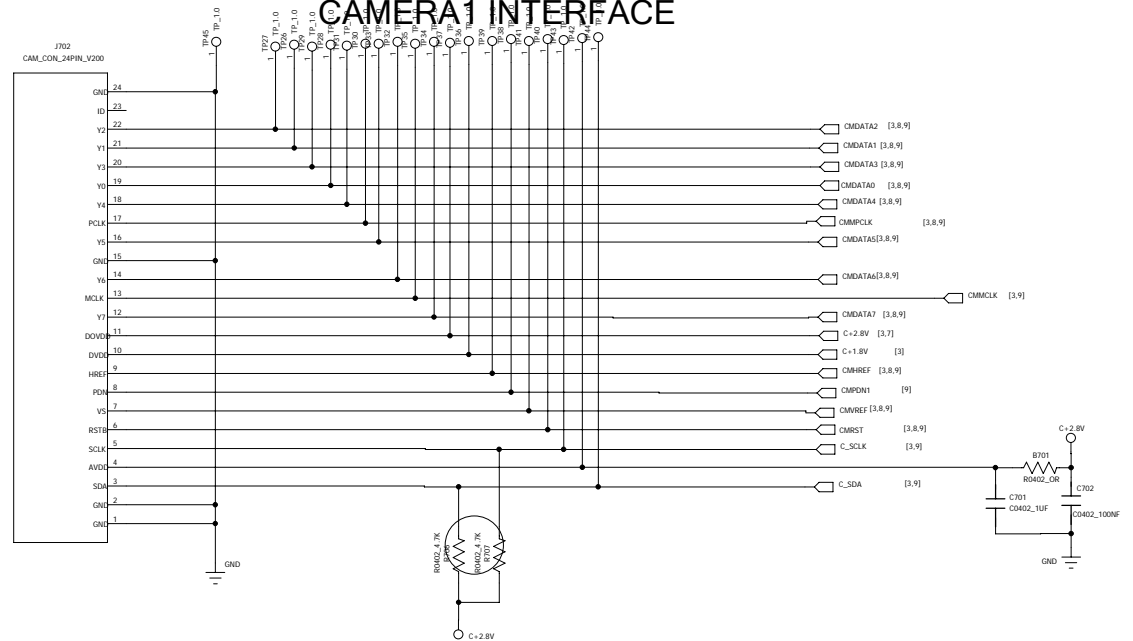
LCM INTERFACE 8-BITS LCD



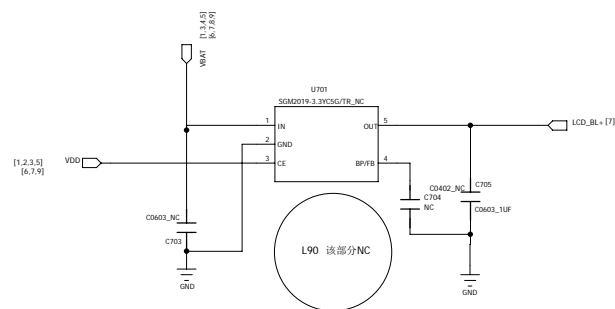
FLASH LED



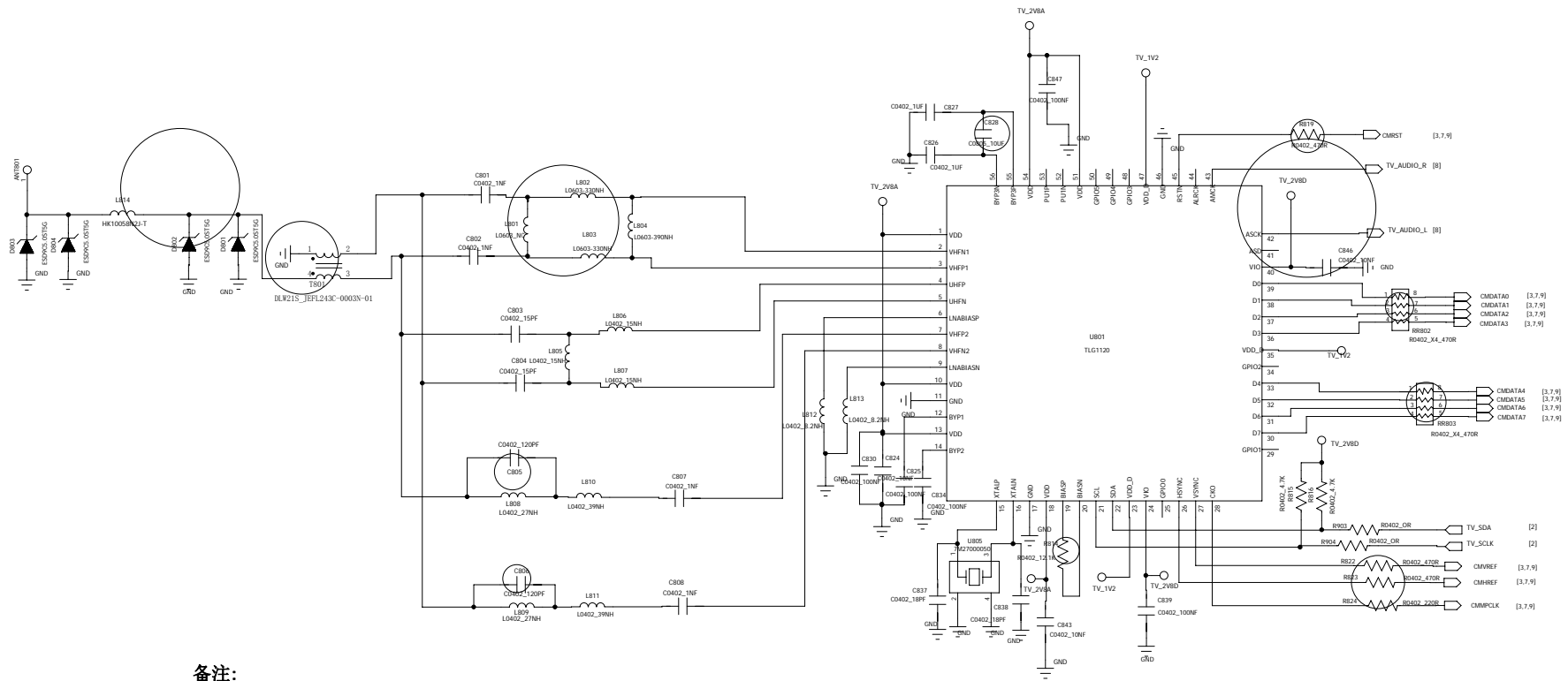
CAMERA1 INTERFACE



LCM BACKLIGHT



				MODEL:	DRAWING NO.:	
DRAWN		DATED		TITLE: LCM . T-Flash,CAMERA	VERSION:	SHEET: 7/9
CHECKED		DATED				

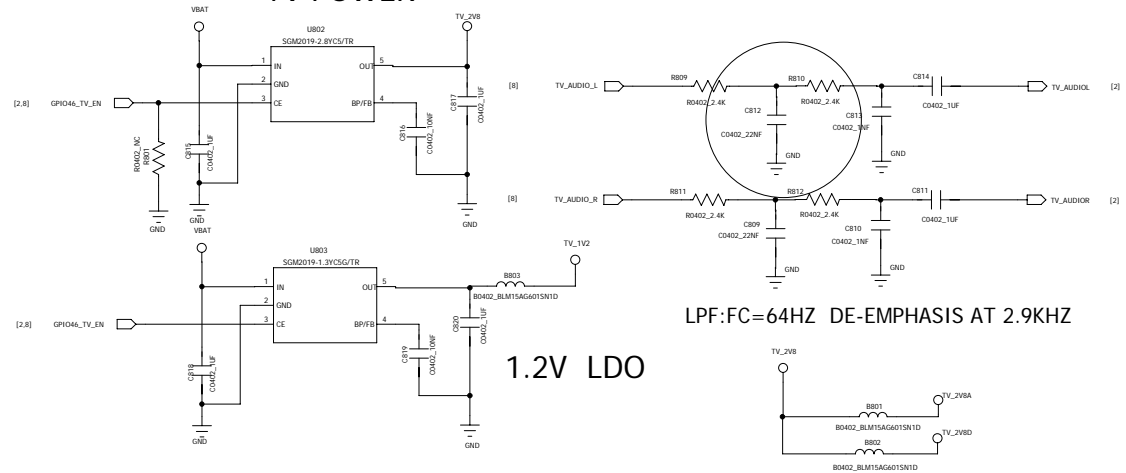


备注:

L801,L802,L803,L804,L805,L806,L807,L810,L811,L812,L813用HIGH-Q电感(Q>30)

L808,L809用LOW-Q电感(8-10)

TV POWER

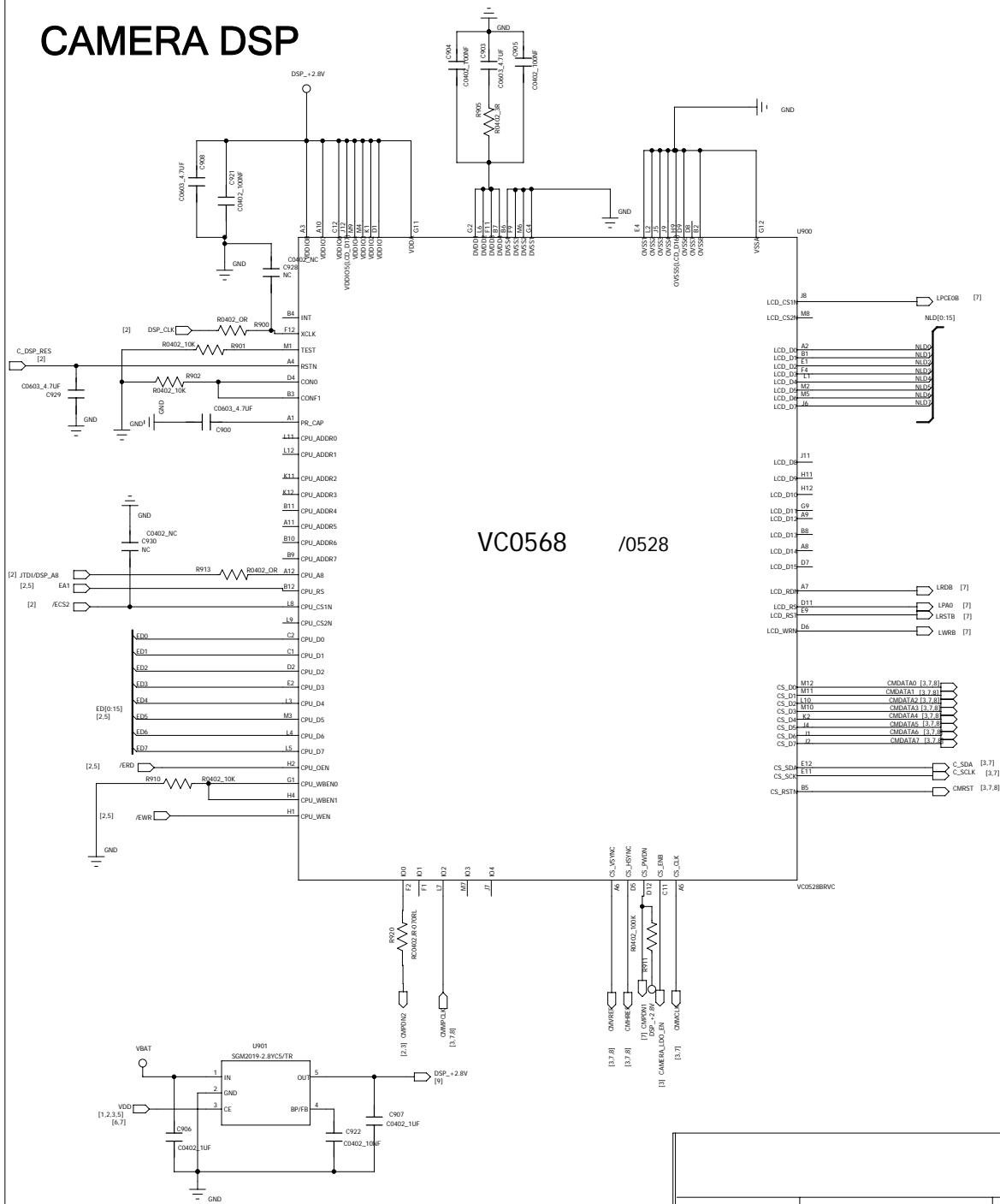


LPF:FC=64HZ DE-EMPHASIS AT 2.9KHZ

1.2V LDO

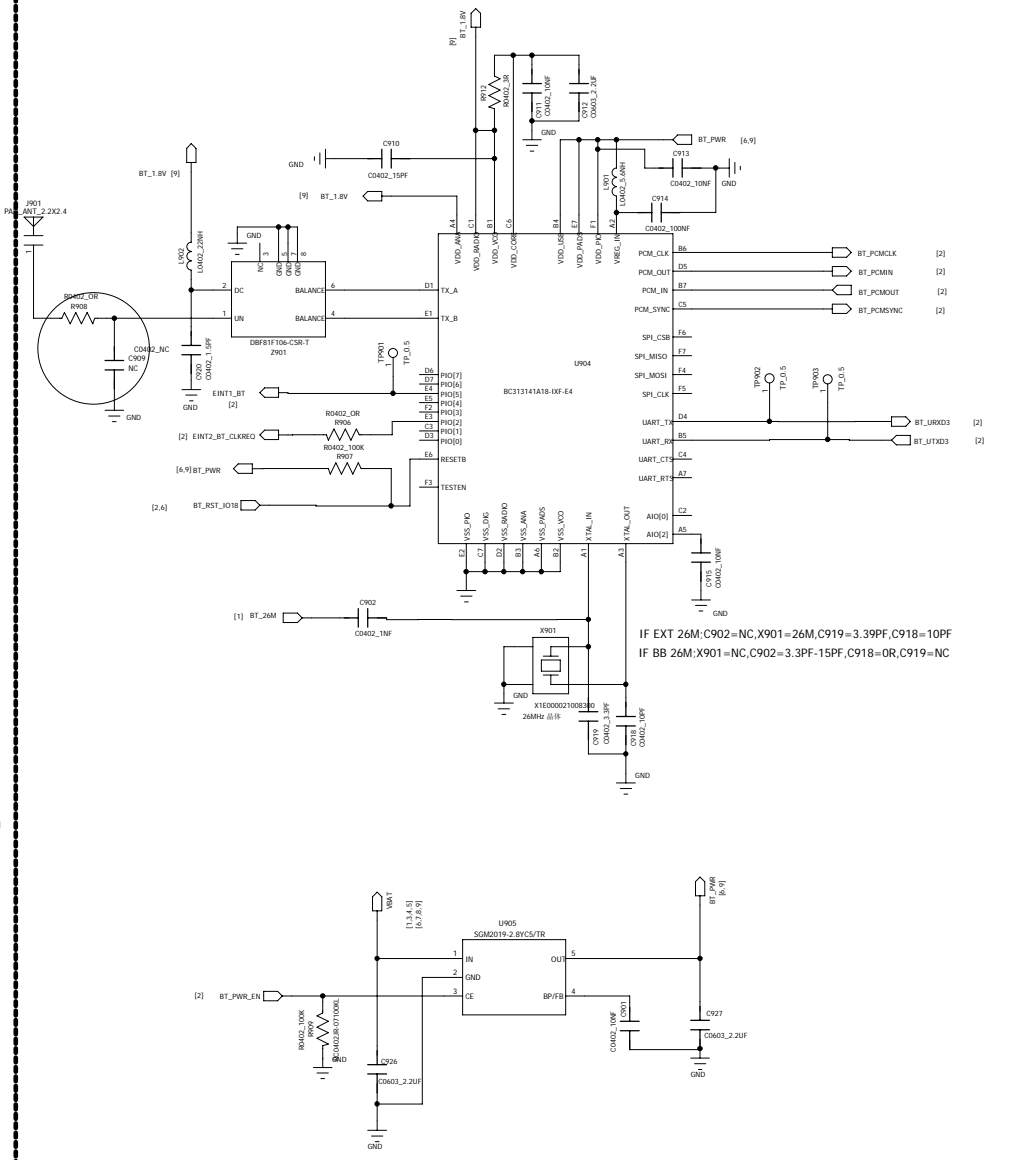
		MODEL:		DRAWING NO.:	
DRAWN		DATED	TITLE: ATV	VERSION:	SHEET: 8/9
CHECKED		DATED			

CAMERA DSP



VC0568 /0528

BLUETOOTH



IF EXT 26M: C902=NC, X901=26M, C919=3.39PF, C918=10PF
IF BB 26M: X901=NC, C902=3.3PF-15PF, C918=OR, C919=NC

			MODEL:	DRAWING NO.:	
DRAWN		DATED	TITLE: DSP,BT	VERSION:	SHEET: 9/9
CHECKED		DATED			