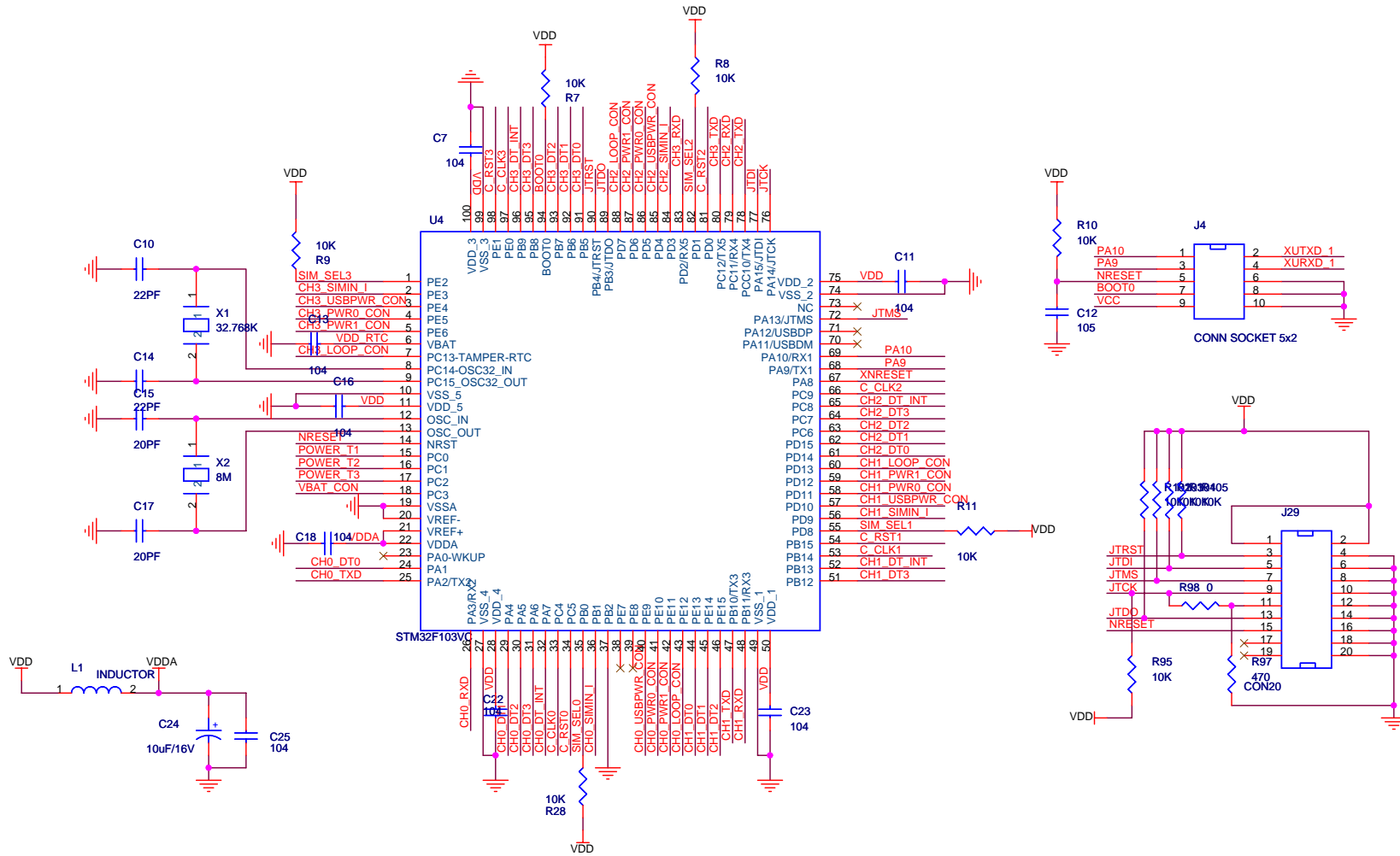
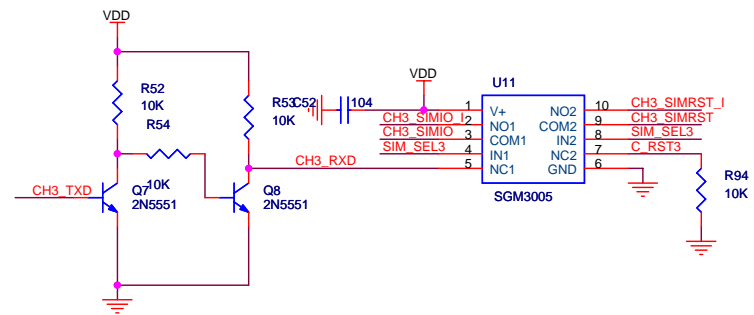
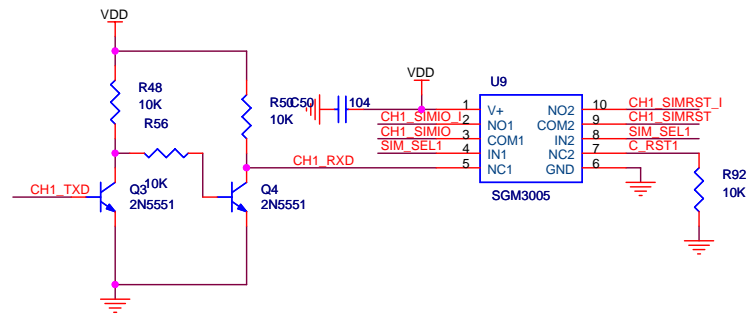
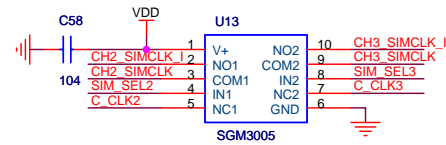
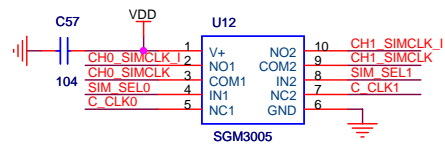
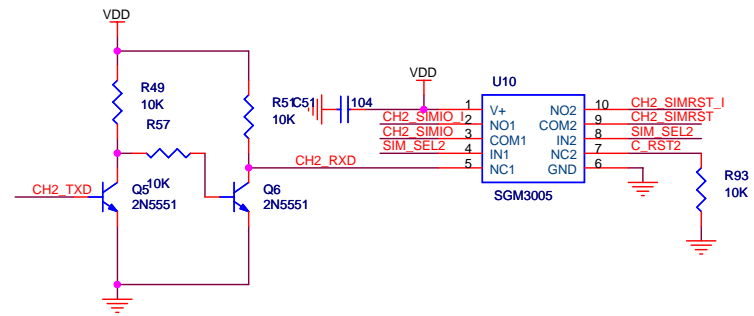
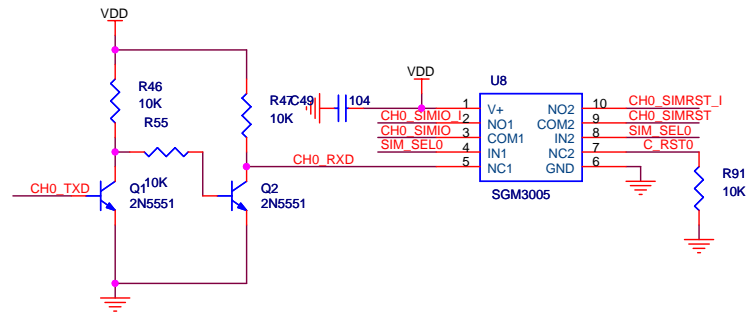
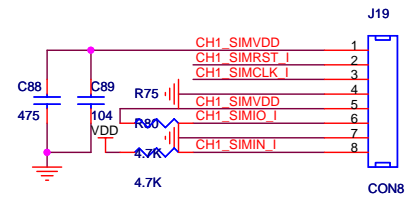
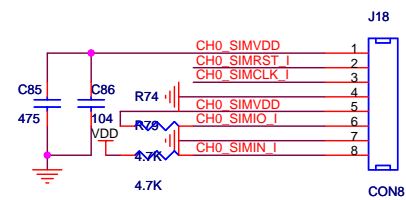
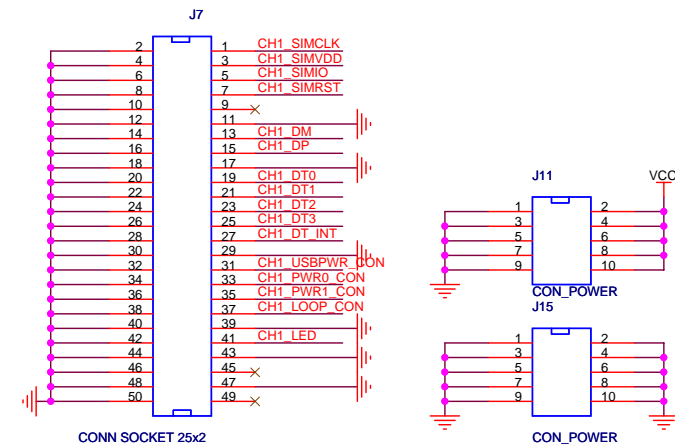
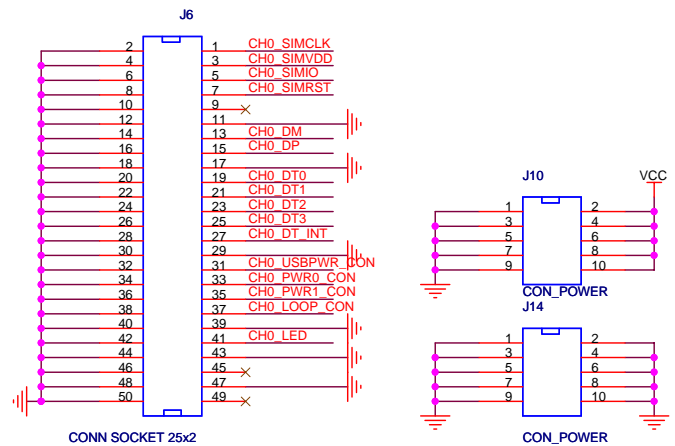
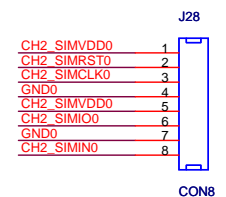
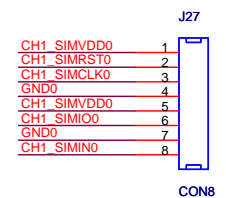
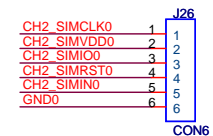
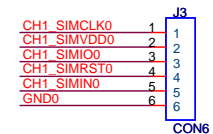
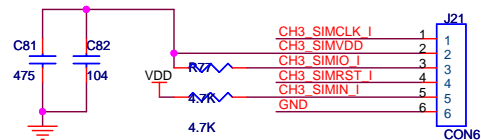
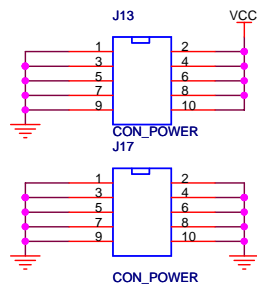
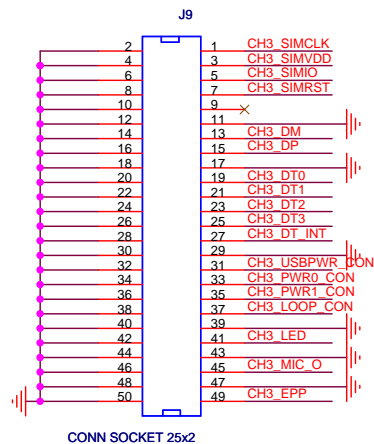
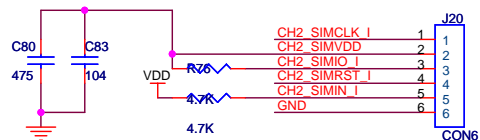
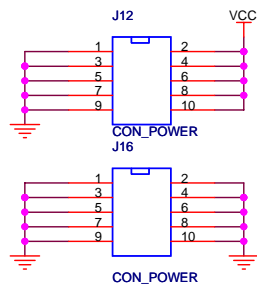
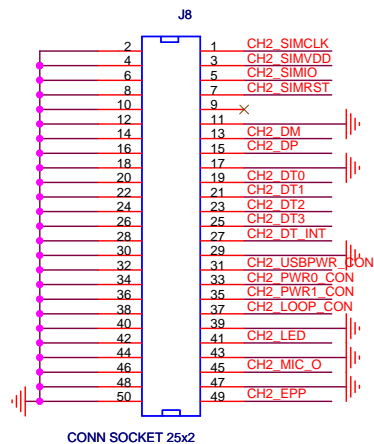


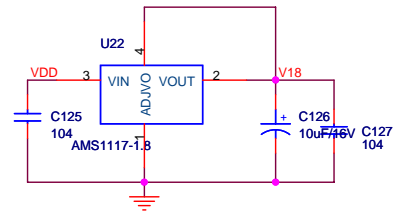
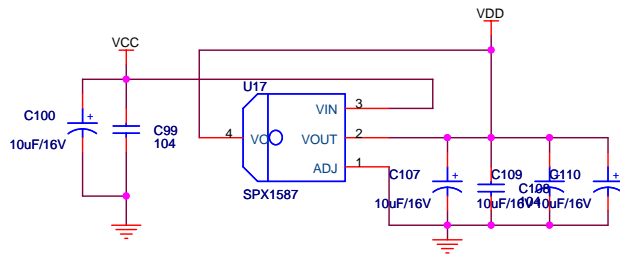
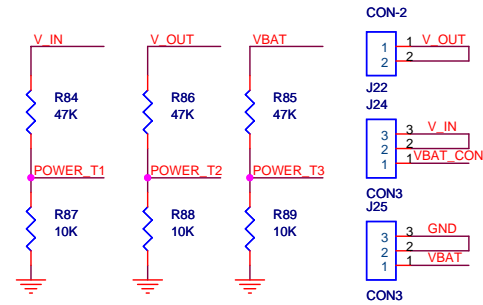
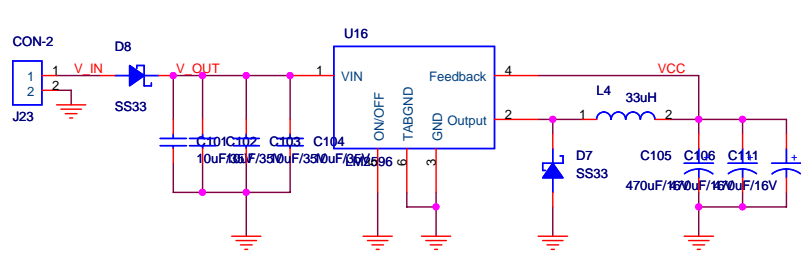
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Ningbo Putian communication technology CO.,LTD		
Size	Document Number	Rev
B	<DK-Em4-mainboard>	<RevCode>
Date:	Saturday, January 16, 2010	Sheet 4 of 9

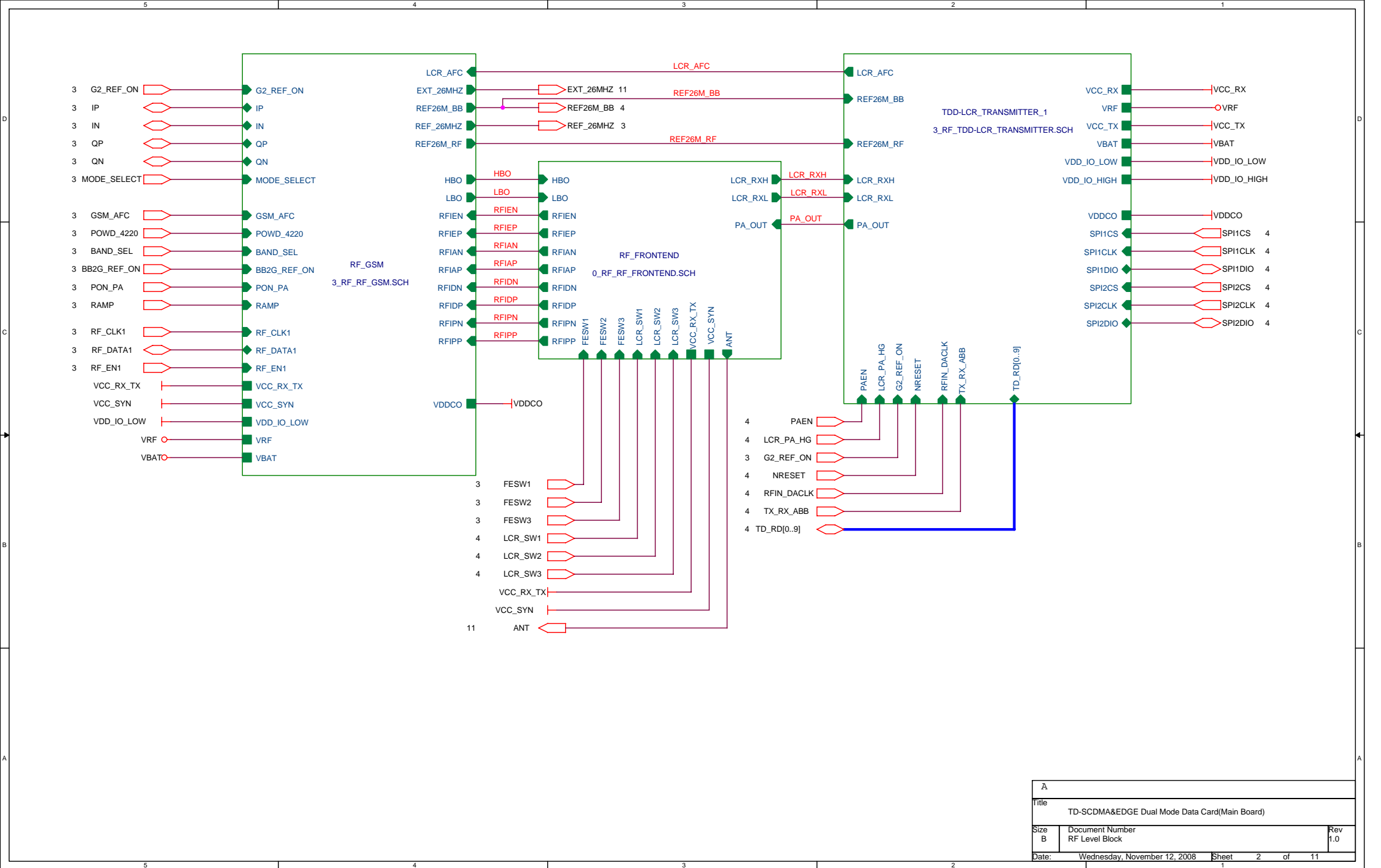


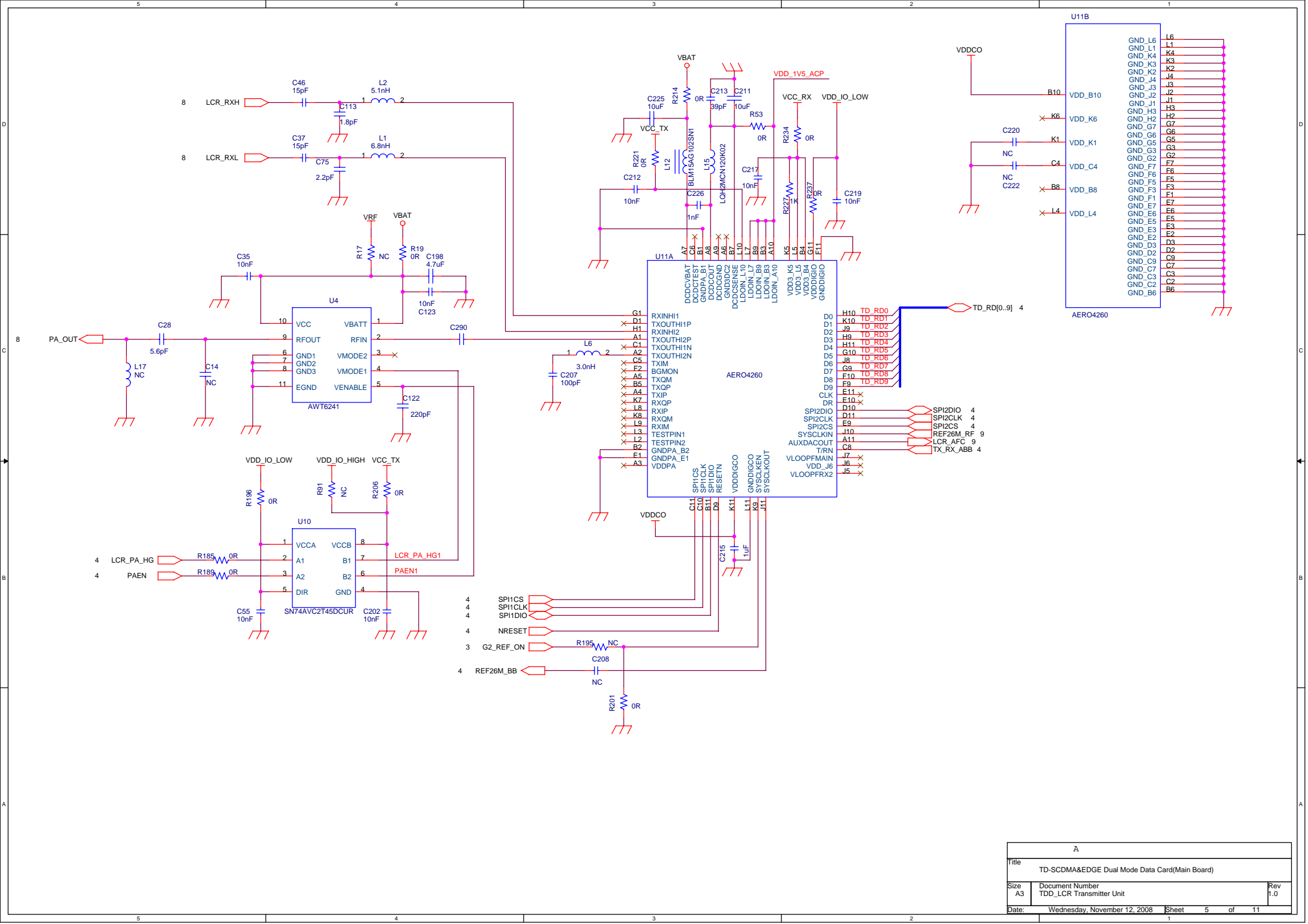




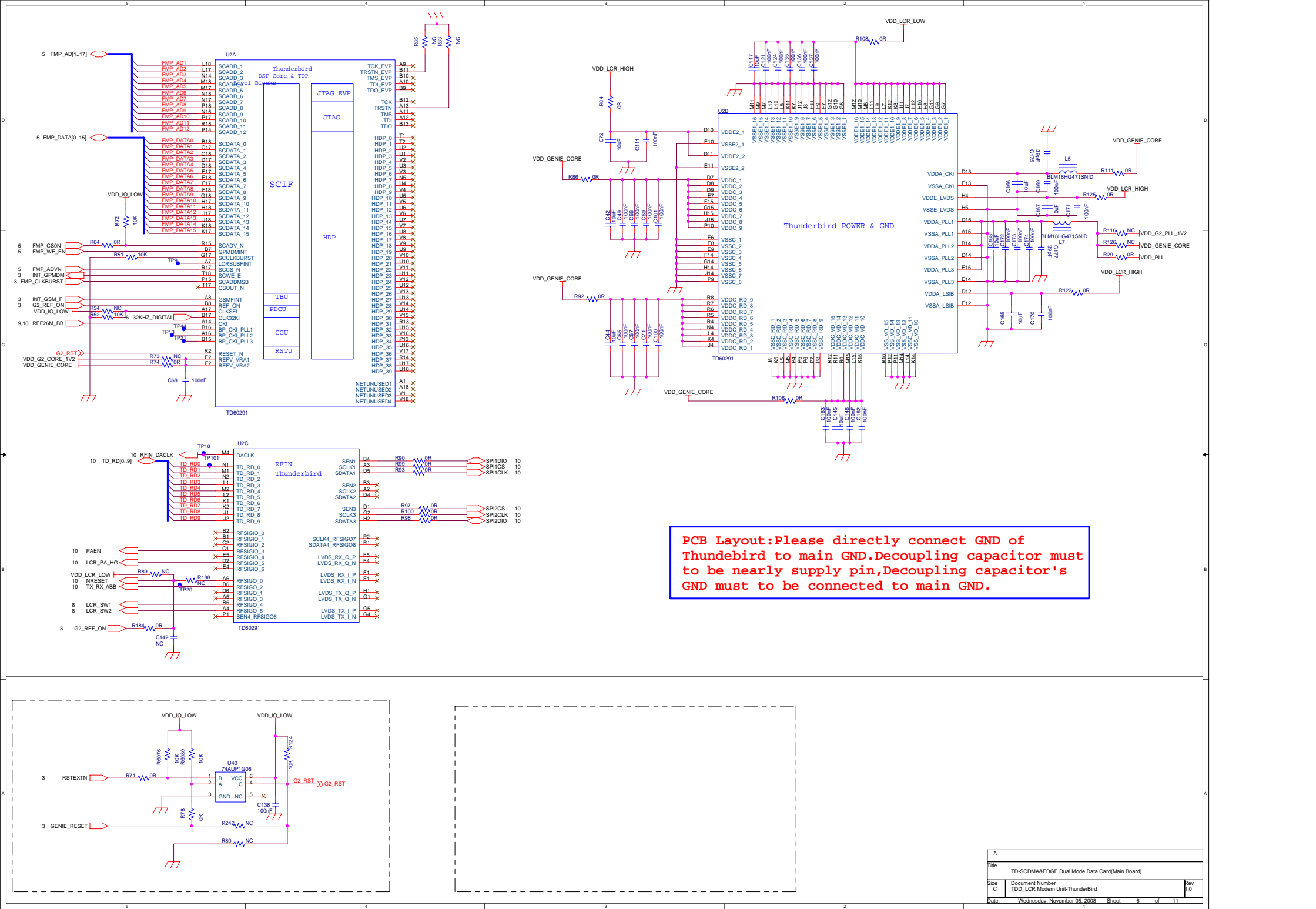


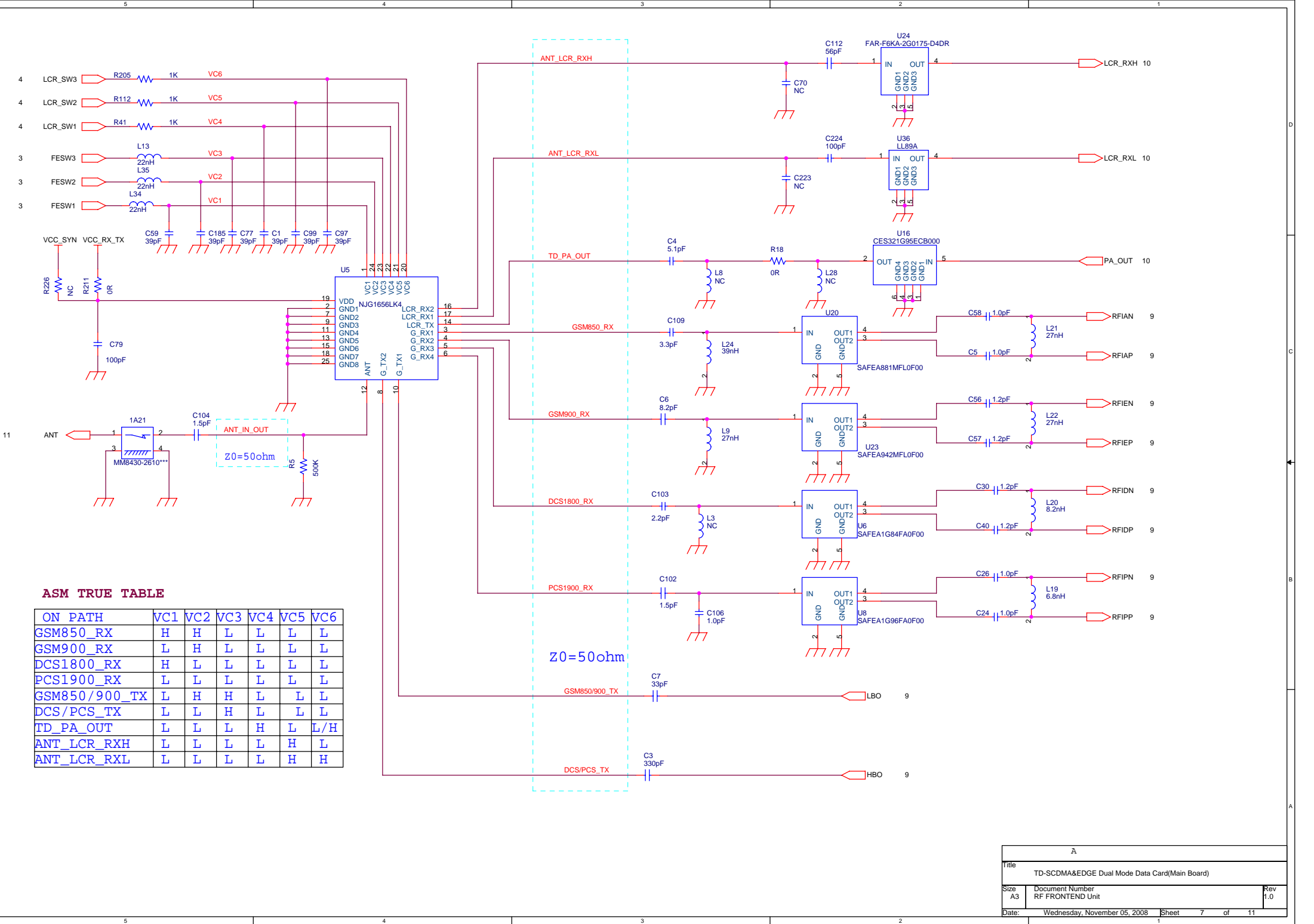






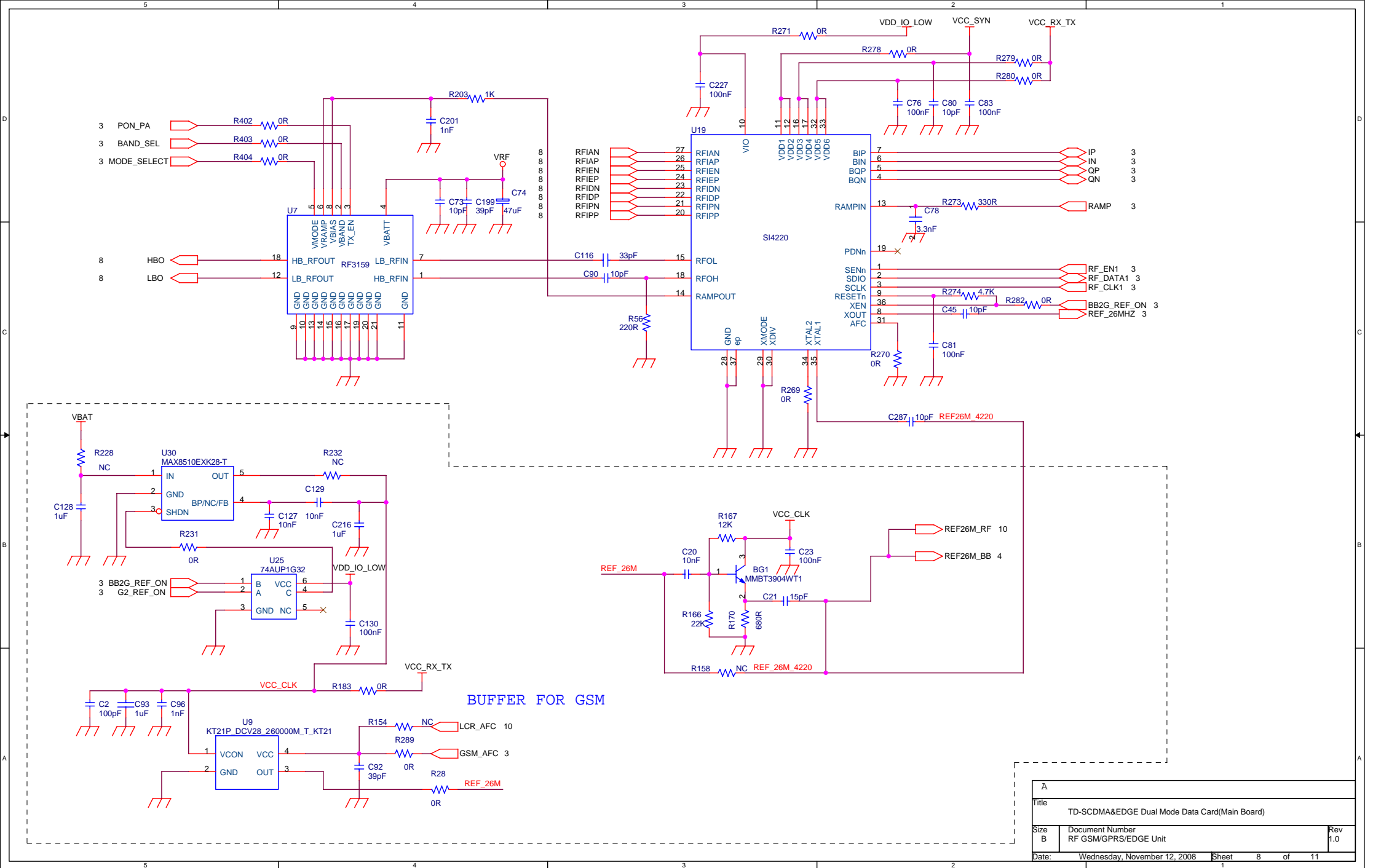
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Title			
TDD_LCR Transmitter Unit			
Size	Document Number	Rev	
A3	TDD_LCR Transmitter Unit	1.0	
Date:	Wednesday, November 12, 2008	Sheet	5 of 11

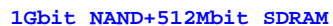




ASM TRUE TABLE

ON PATH	VC1	VC2	VC3	VC4	VC5	VC6
GSM850_RX	H	H	L	L	L	L
GSM900_RX	L	H	L	L	L	L
DCS1800_RX	H	L	L	L	L	L
PCS1900_RX	L	L	L	L	L	L
GSM850/900_TX	L	H	H	L	L	L
DCS/PCS_TX	L	L	H	L	L	L
TD_PA_OUT	L	L	L	H	L	L/H
ANT_LCR_RXH	L	L	L	L	H	L
ANT_LCR_RXL	L	L	L	L	H	H





A			
Title TD-SCDMA&EDGE Dual Mode Data Card(Main Board)			
Size C	Document Number System Memory Unit(1Gbit NAND+512Mbit SDRAM)		Rev 1.0
Date:	Wednesday, November 12, 2008	Sheet 9 of 11	

8 FESW1
8 FESW2
8 FESW3
9 REF_28MHZ
9 QP
9 QN
9 IP
9 IN
9 DN010
DN003

11 USBDM
11 USBDP
RTS1
DTR1
11 DSR1
11 CTS1
11 TXD1
11 RXD1
CON_JRSTN
11 USB_VBUS
11 AUDIO_DET
AUXMICN
AUXMICP
MICN
MICP
MIC_BIAS
HFR2
HFR1
EAR2
EAR1
6,11 SDA1
6,11 SCL1
9 GSM_AFC
11 USB_ON
6 SIM_OFF

9 MODE_SELECT
9 BB2Q_REF_ON
9 RF_EN1
9 RF_DATA1
9 RF_CLK1
9 RAMP
9 BAND_SEL
9 PON_PA
9 POWD_4220
11 TEMP_DET
6 USB_SUSPEND

6 IT_PMU
6 RST_626
6 32KHZ_DIGITAL
6 PMU_REF_ON
VDD_SIM
VCC_RX_TX
VCC_SYN
VDD_GSM_CORE
VDD_LCD_DIG
AVDD
VDD_USB
VDD_IO_HIGH
VDD_IO_LOW
VDD_PLL
VDD_HFA
VBAT

11 SIMOCD
11 SIMCLKCD
11 SIMRSTCD
6,9,10 G2_REF_ON
6 32KHZ_DIGITAL
VDD_GENIE_CORE
VDD_LCR_LOW
VDD_LCR_HIGH
VDD_IO_LOW
VINT



SMP_NFL_D[0..7]

FMP_CLKBURST

GENIE_RESET

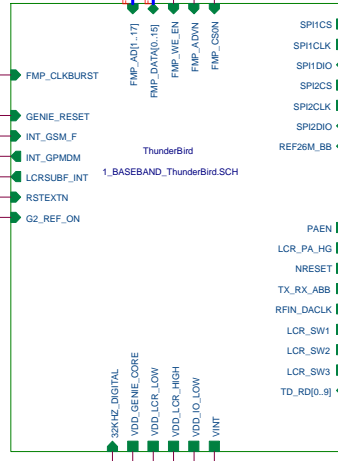
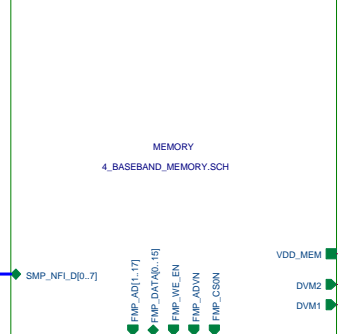
INT_GSM_F

INT_GPMDM

LCRSUBF_INT

RSTEXTN

G2_REF_ON

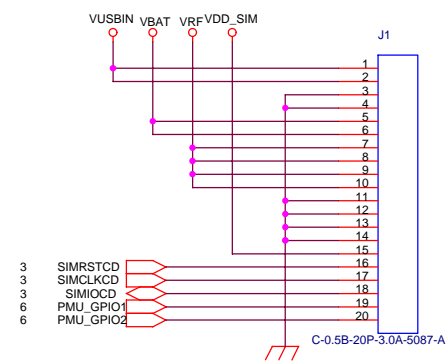


VDD_MEM
DVM2
DVM1

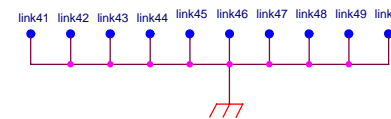
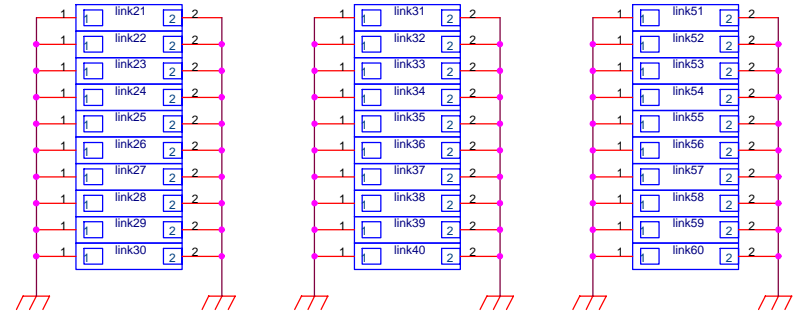
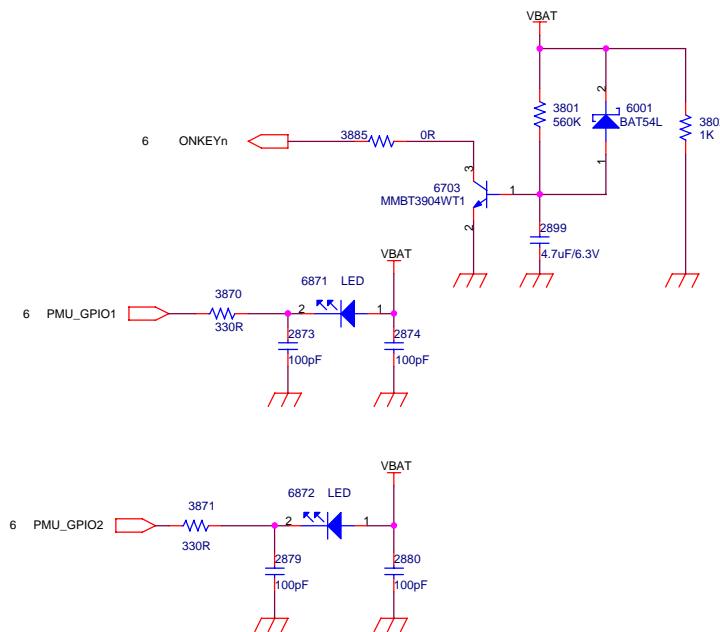
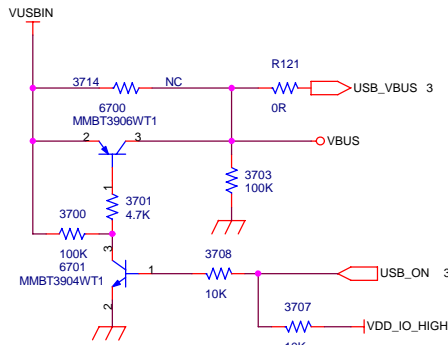
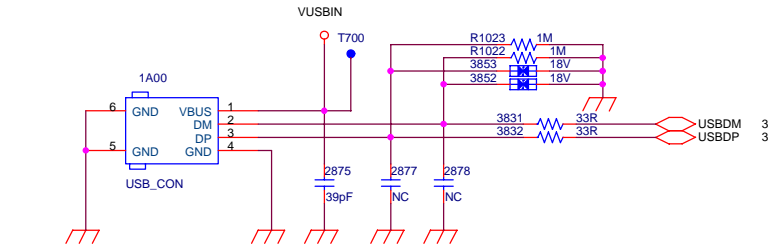
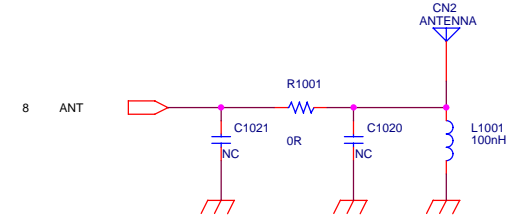
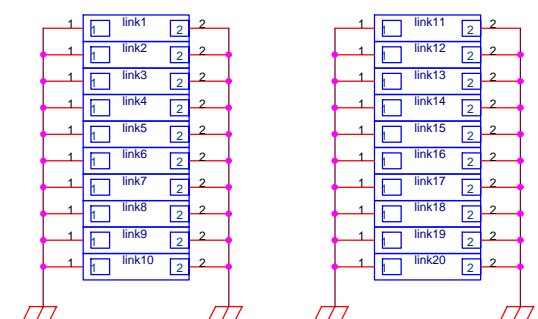
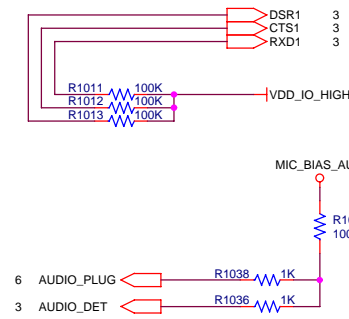
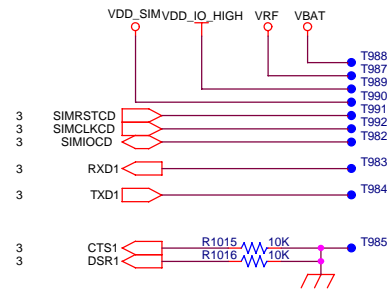
SP1CS
SP1CLK
SP1DIO
SP1DIO
SP2CS
SP2CLK
SP2DIO
REF26M_BB

PAEN
LCR_PA_HG
NRESET
TX_RX_ABB
RFIN_DACLK
LCR_SW1
LCR_SW2
LCR_SW3
TD_RD[0..9]

B2B Connector



Jig Test Point



A		
Title		
TD-SCDMA&EDGE Dual Mode Data Card(Main Board)		
Size	Document Number	Rev
A3	Audio&External Interface Unit	1.0
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01-TITLE

02-MCU(ADDR/DATA)

03-MCU(LCD/CAM/USB/UART/IIS/IIC/TIMER..)

04-MCU(POWER)

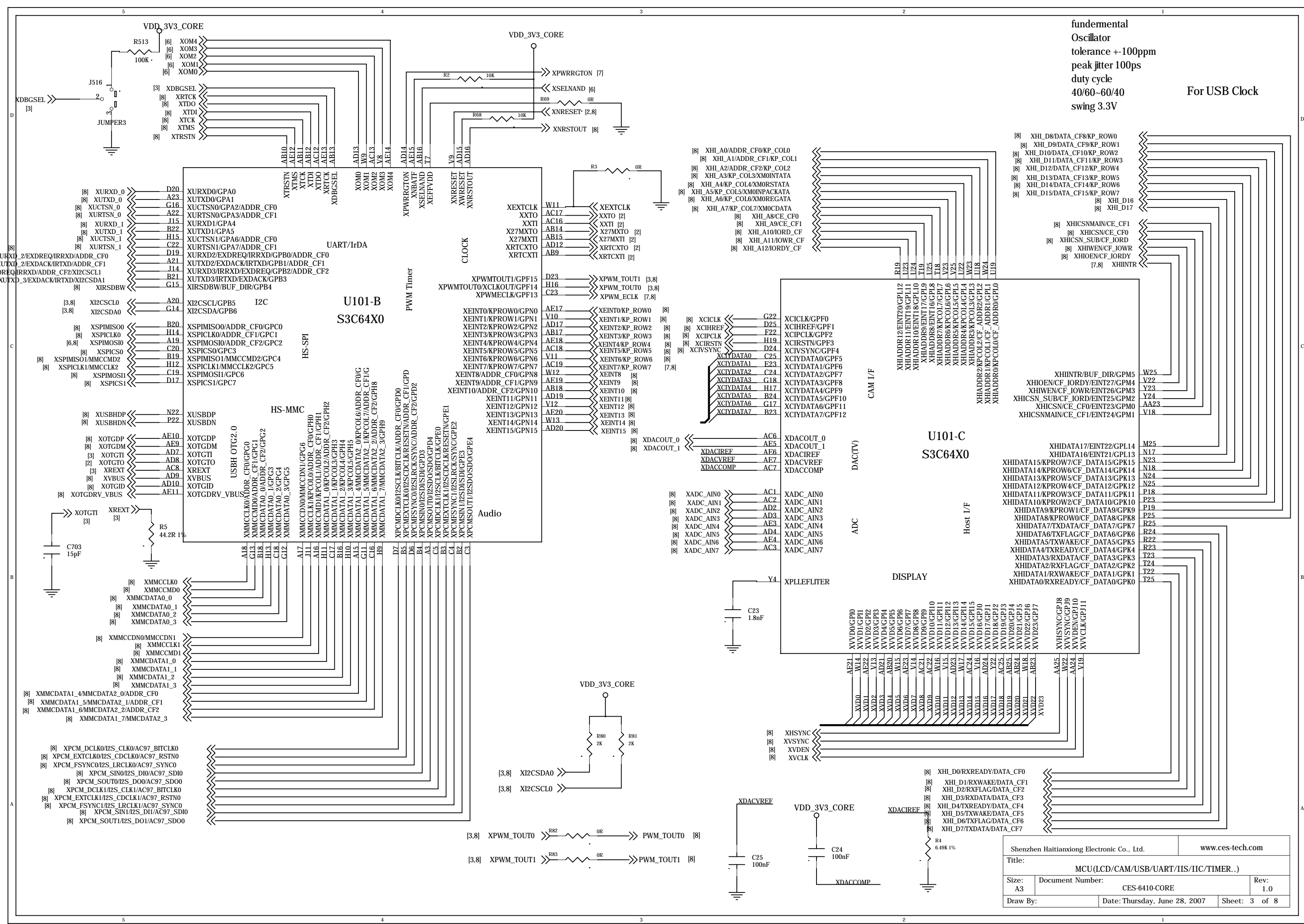
05-mDDR

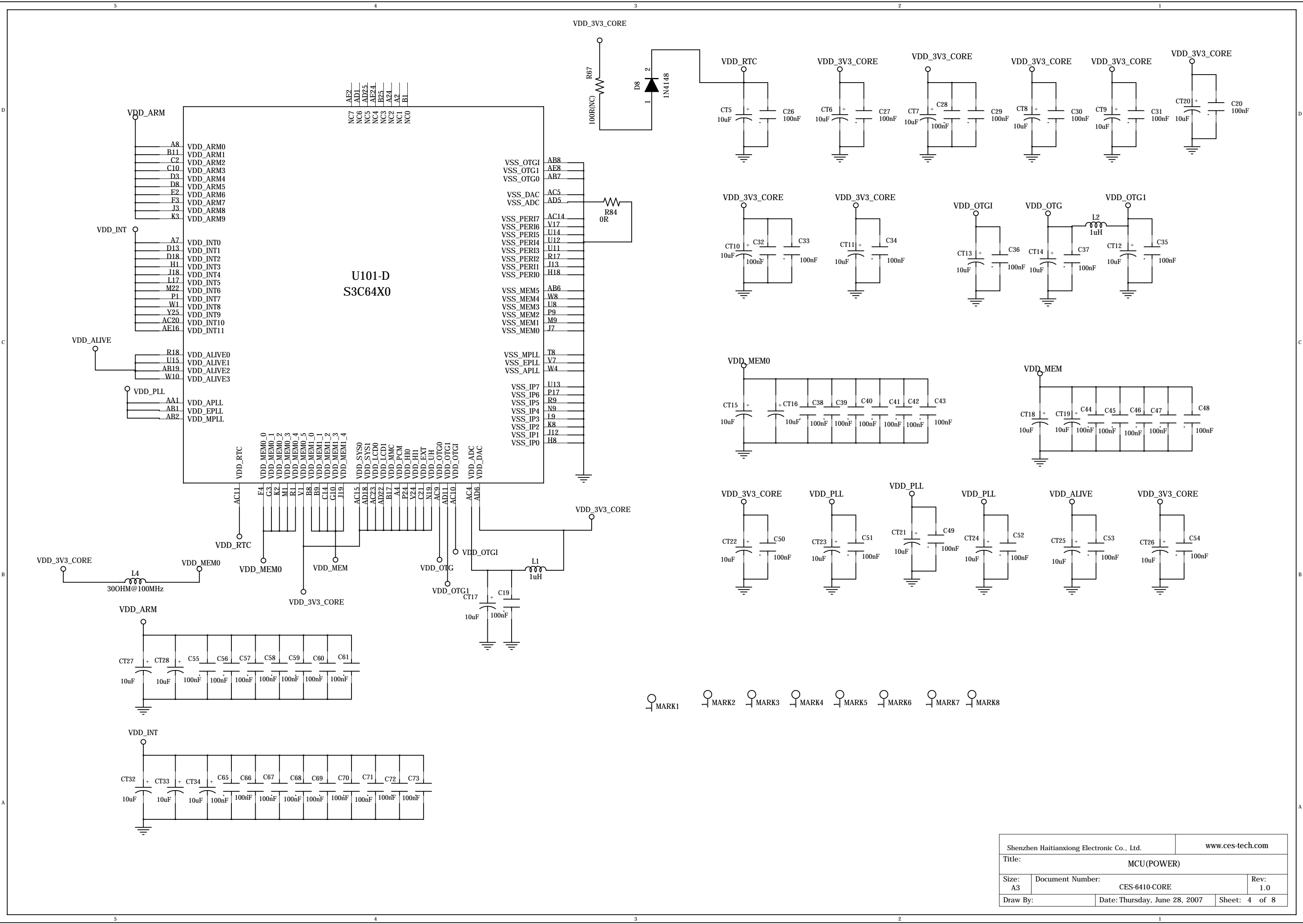
06-NAND FLASH and NOR FLASH

07-POWER

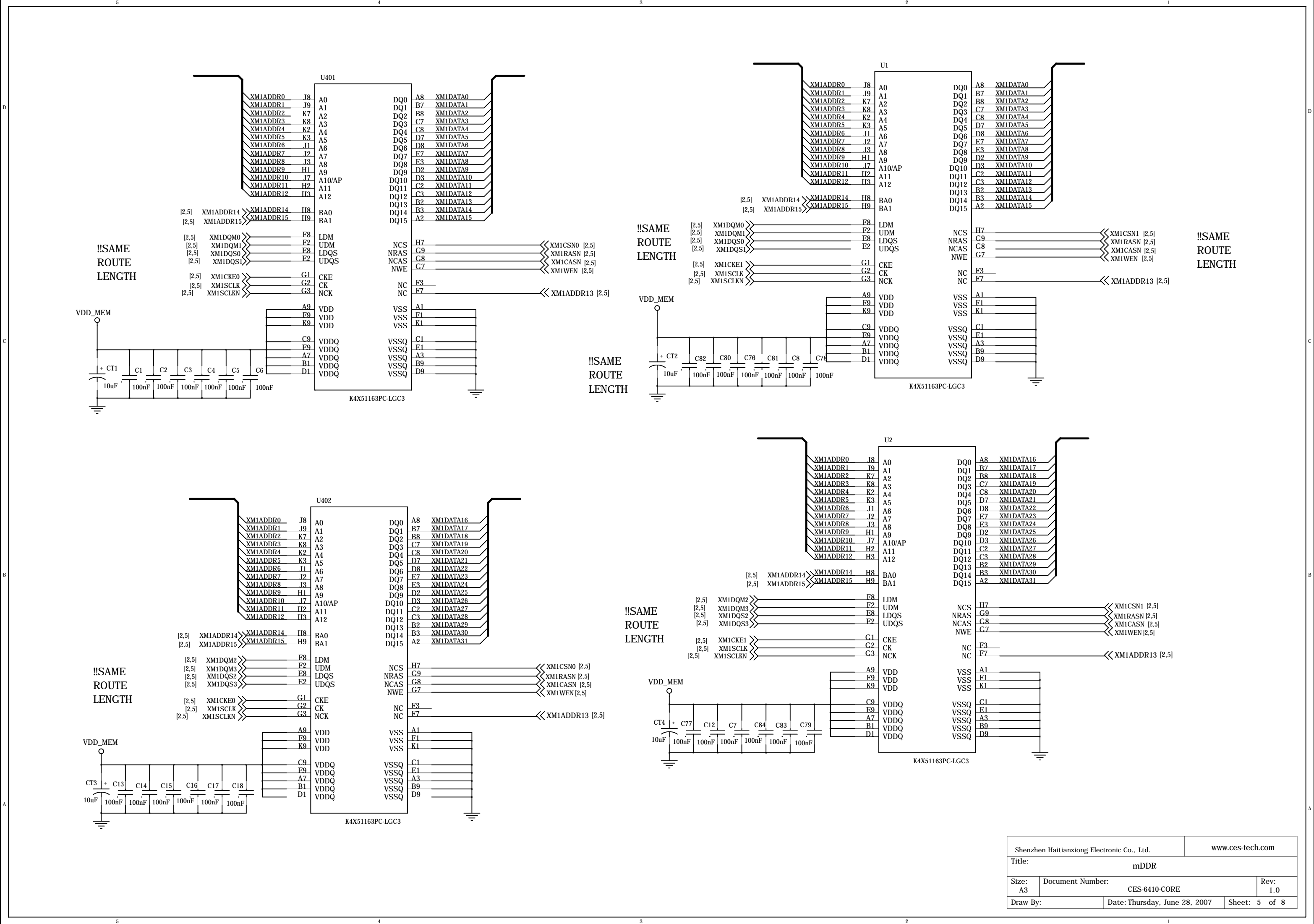
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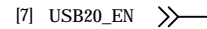
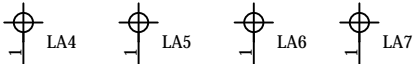
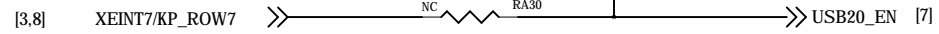
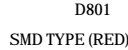
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Title:		TITLE	
Size: A3	Document Number: CES-6410-CORE		Rev: 1.0
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Title: MCU(POWER)			
Size: A3	Document Number: CES-6410-CORE		Rev: 1.0
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Title: POWER

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