

Title		BL-BKB83A		Revision	
Size	Number	20110321		V.1.0	
B	24-Aug-2010	A Sheet of		Drawn By: Design	
Date		File			

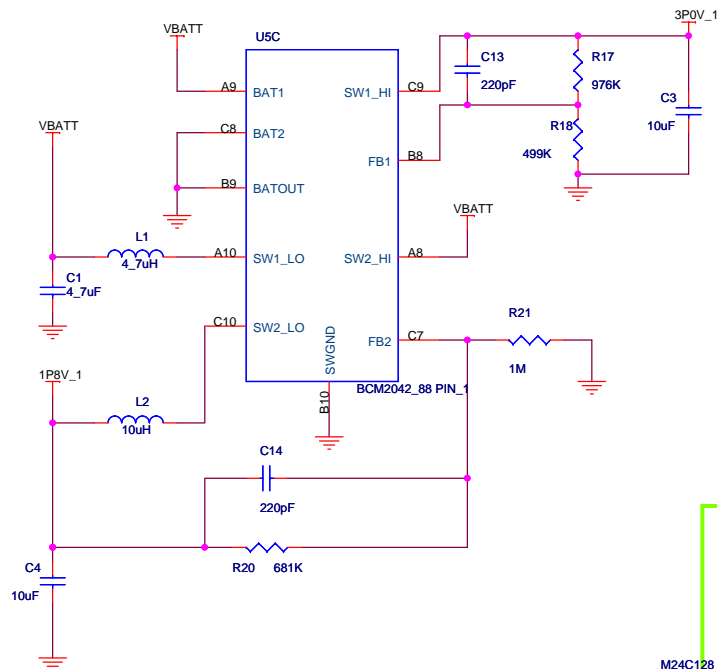
REVISION HISTORY

Revision 0000-P2 -- EEPROM for BCM2042A1 set switcher VDDA output to 1.85v, and using VDDA for on chip LDO input  
Revision 0020-P1 -- Put additional resistor R22 to avoid Xtal startup issue  
Revision 0020-P2 -- update the components value on "pi" low pass filter for 2nd and 3rd harmonic reduction

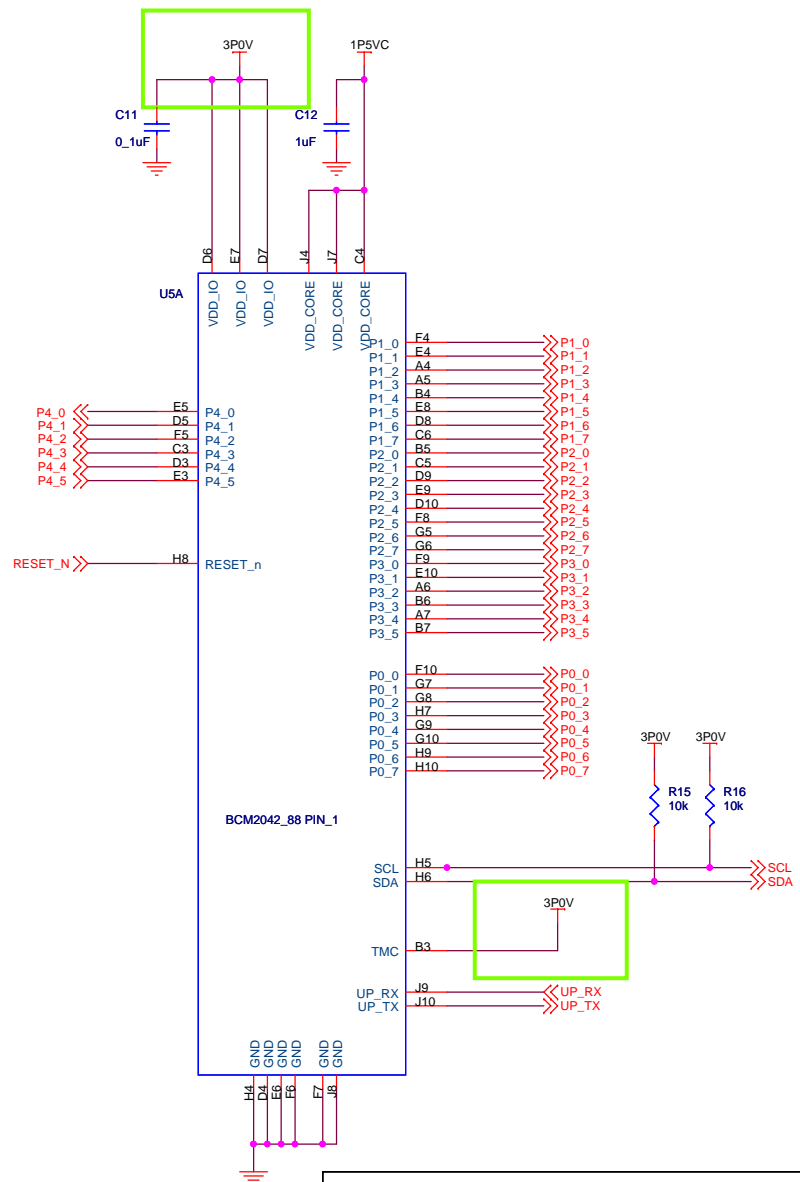
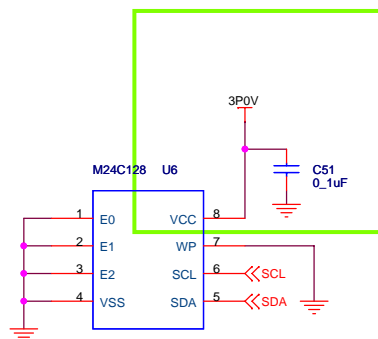


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Title		
BCM92042MDX-B88 --- cover page		
Size	Document Number	Rev
	115056-0020	P1
Date:	Monday, May 08, 2006	Sheet 1 of 4

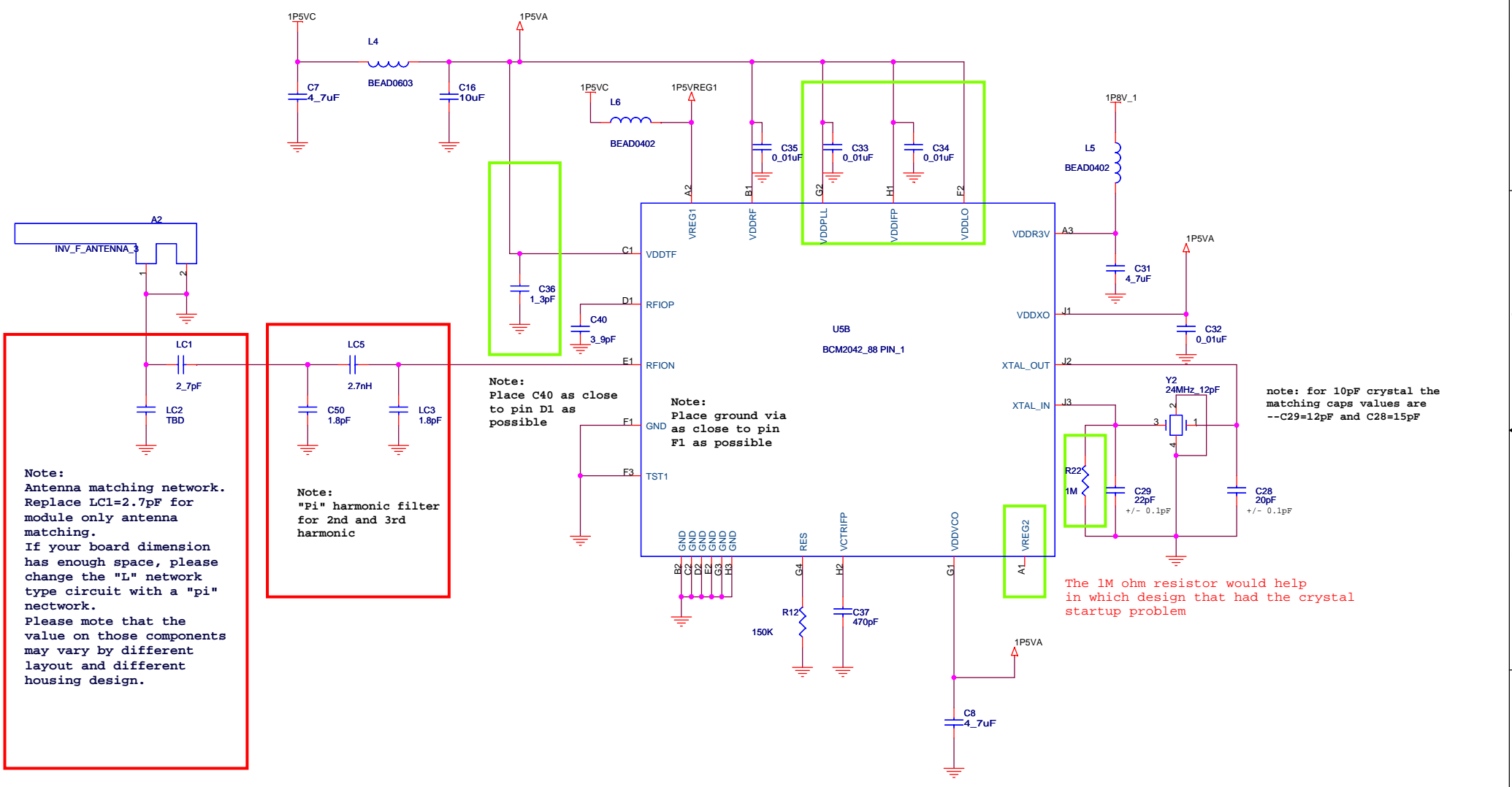


Note: The switching regulator is configured for 2 battery cells in serial.



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Title		
BCM92042MDX-B88 --- BASEBAND		
Size	Document Number	Rev
	115056-0020	P1
Date:	Monday, May 08, 2006	Sheet 2 of 4



Note:  
Antenna matching network.  
Replace LC1=2.7pF for  
module only antenna  
matching.  
If your board dimension  
has enough space, please  
change the "L" network  
type circuit with a "pi"  
network.  
Please note that the  
value on those components  
may vary by different  
layout and different  
housing design.

Note:  
"Pi" harmonic filter  
for 2nd and 3rd  
harmonic

Note:  
Place C40 as close  
to pin D1 as  
possible

Note:  
Place ground via  
as close to pin  
F1 as possible

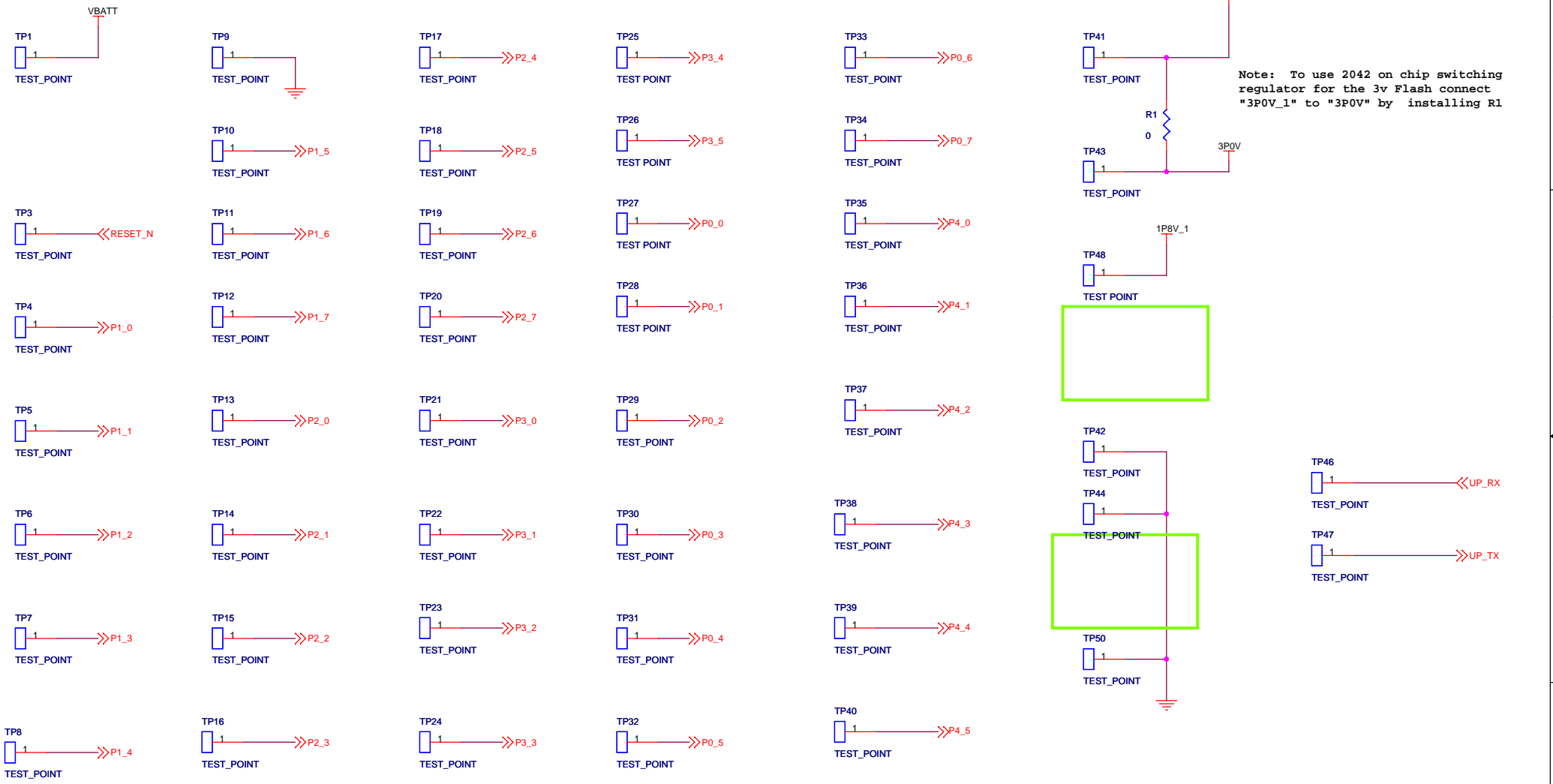
note: for 10pF crystal the  
matching caps values are  
--C29=12pF and C28=15pF

The 1M ohm resistor would help  
in which design that had the crystal  
startup problem



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Title			BCM92042MDX-B88--- RF
Size	Document Number	115056-0020	
Date:	Monday, May 08, 2006	Sheet	3 of 4
		Rev	P1



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Title			BCM92042MDX-B88 ---MODULE I/O	
Size	Document Number		115056-0020	Rev P1
Date:	Saturday, April 01, 2006		Sheet 4 of 4	