

The block diagram illustrates the system architecture. It features a **DVC-T004 Tx Module** containing a **Crystal** with **12MHz** and **32.768KHz** components, and an **ASIC**. This module is connected to a **Camera** and a **Motor** via a **Serial to Parallel Shift Register**. The **Serial to Parallel Shift Register** also receives input from a **PIR** sensor and outputs **H+/H-/V+/V-** signals to the **Camera** and **Motor**. The **PIR** sensor is also connected to the **DVC-T004 Tx Module**. The **DVC-T004 Tx Module** is connected to a **DVC-R004 Rx Module**, which also contains a **Crystal** with **12MHz** and **32.768KHz** components and an **ASIC**. The **DVC-R004 Rx Module** is connected to a **KeyPad** and a **TV encoder**. The **TV encoder** outputs **AV Out**.

2.4G Wireless Digital camera Image with Audio communication
Pan motor control from 2.4G Wireless Receiver
Direct AV out from 2.4G wireless Receiver
Two way sound communication

Camera image data through TX module transfer RF datae and the Receiver module Asic
progressing Audio and Video direct output from TFT display, Pan Key control from RX module

Audio In/Out Processor
Video In/Out Processor
KeyPad control Built In CPU
2.4G wireless module