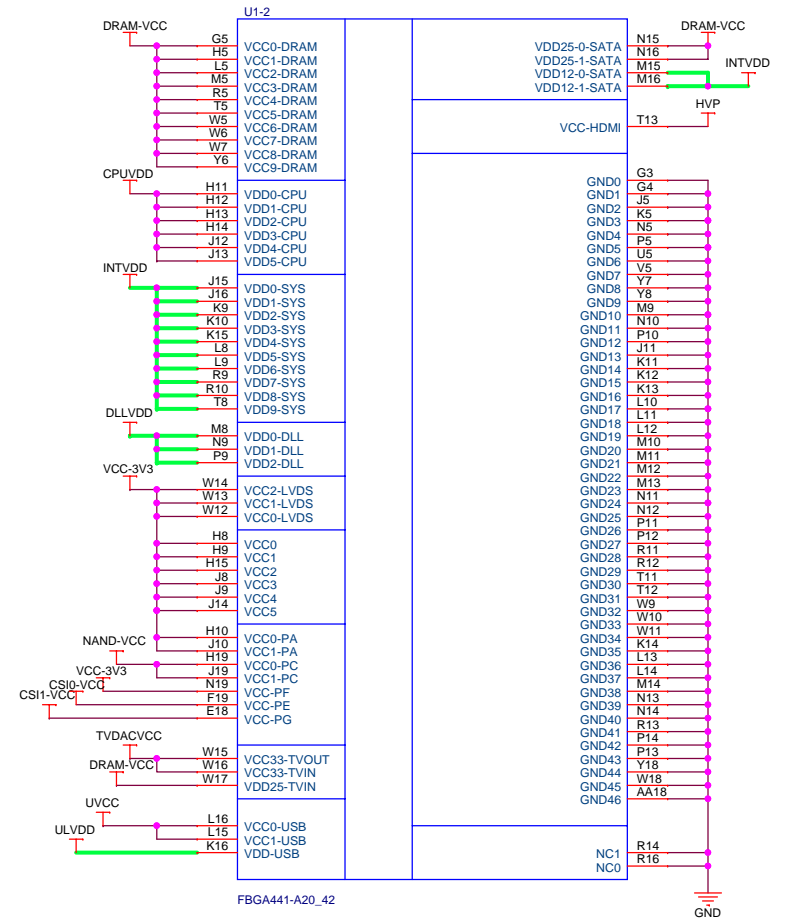
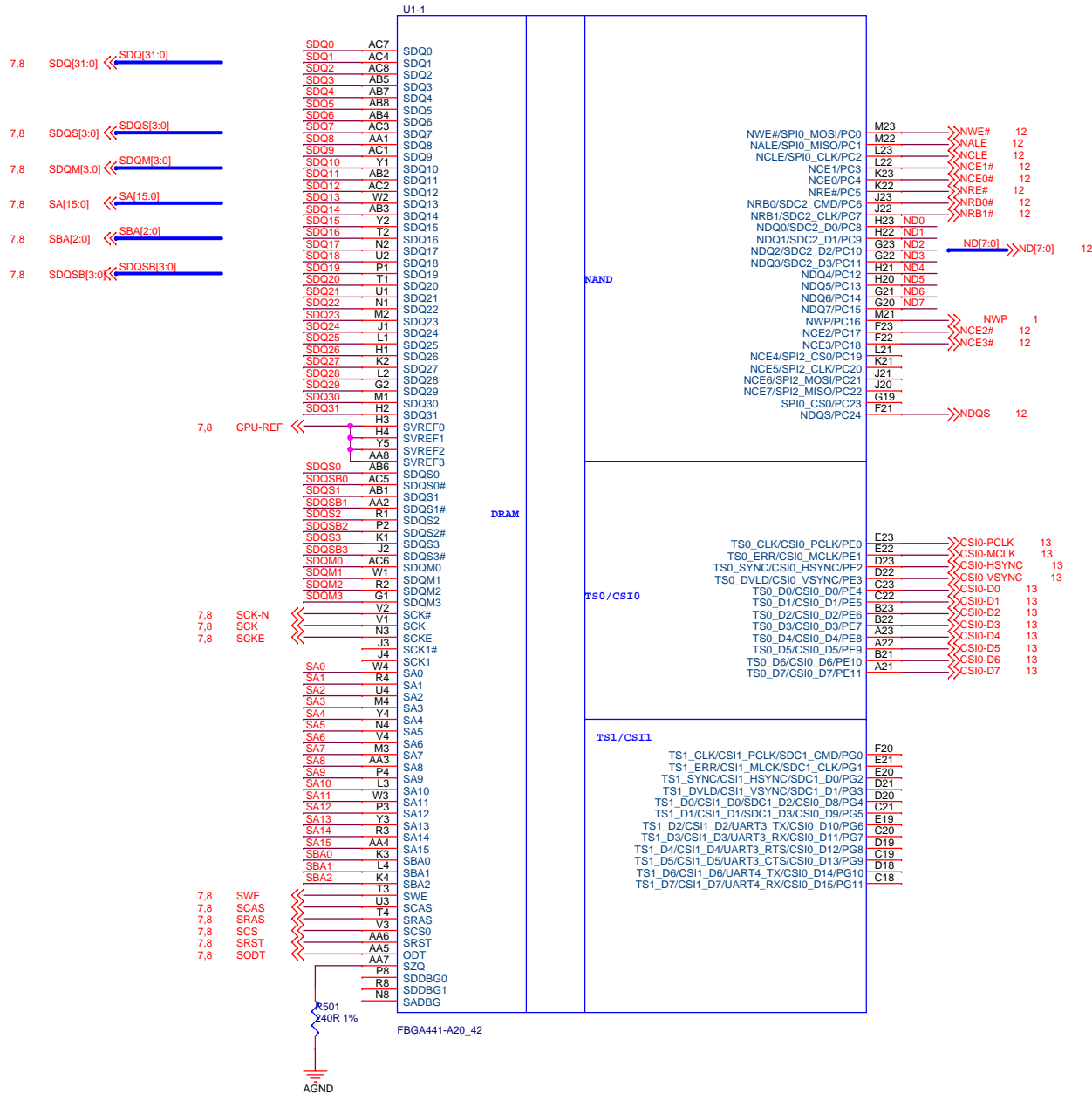


# CPU1

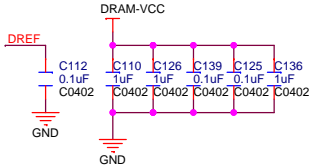
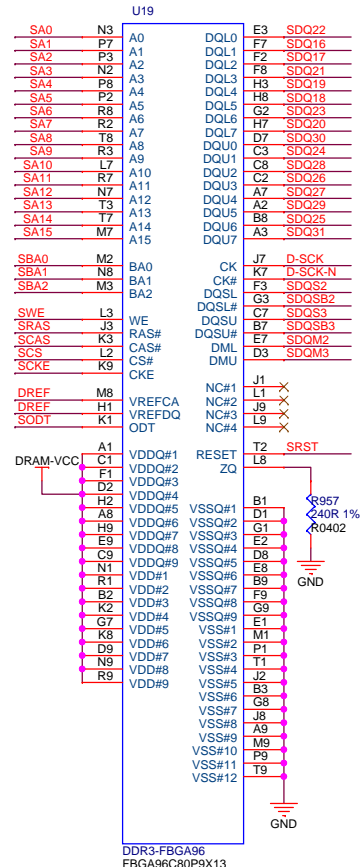
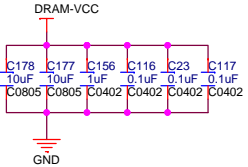
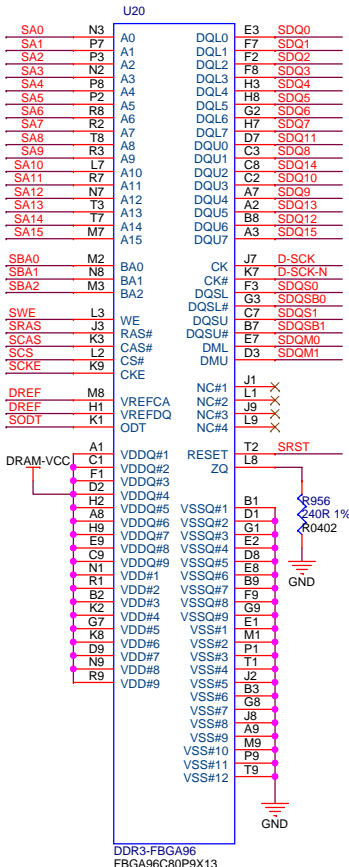
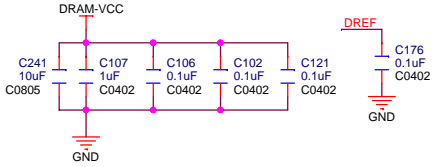
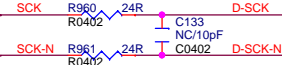
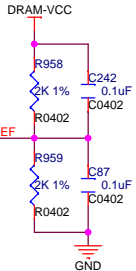
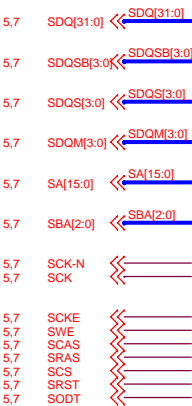


FBGA441-A20\_42

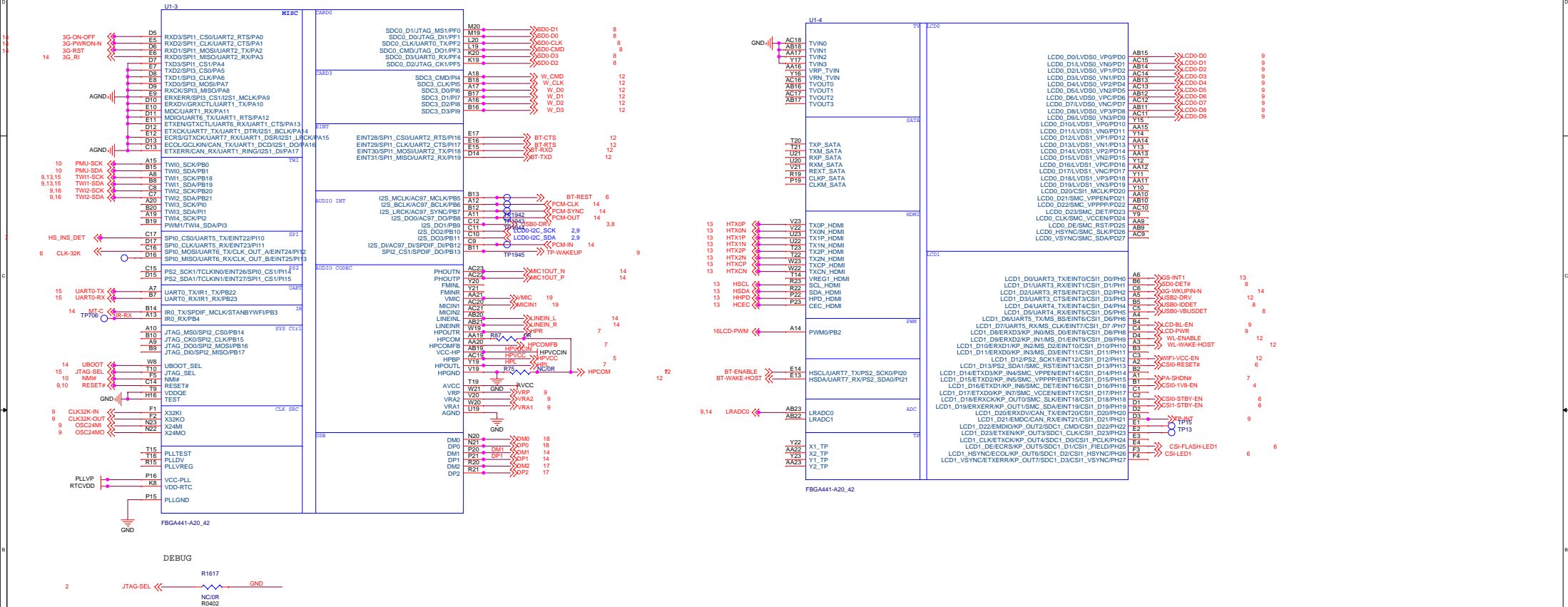
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DDR3-16BITX2



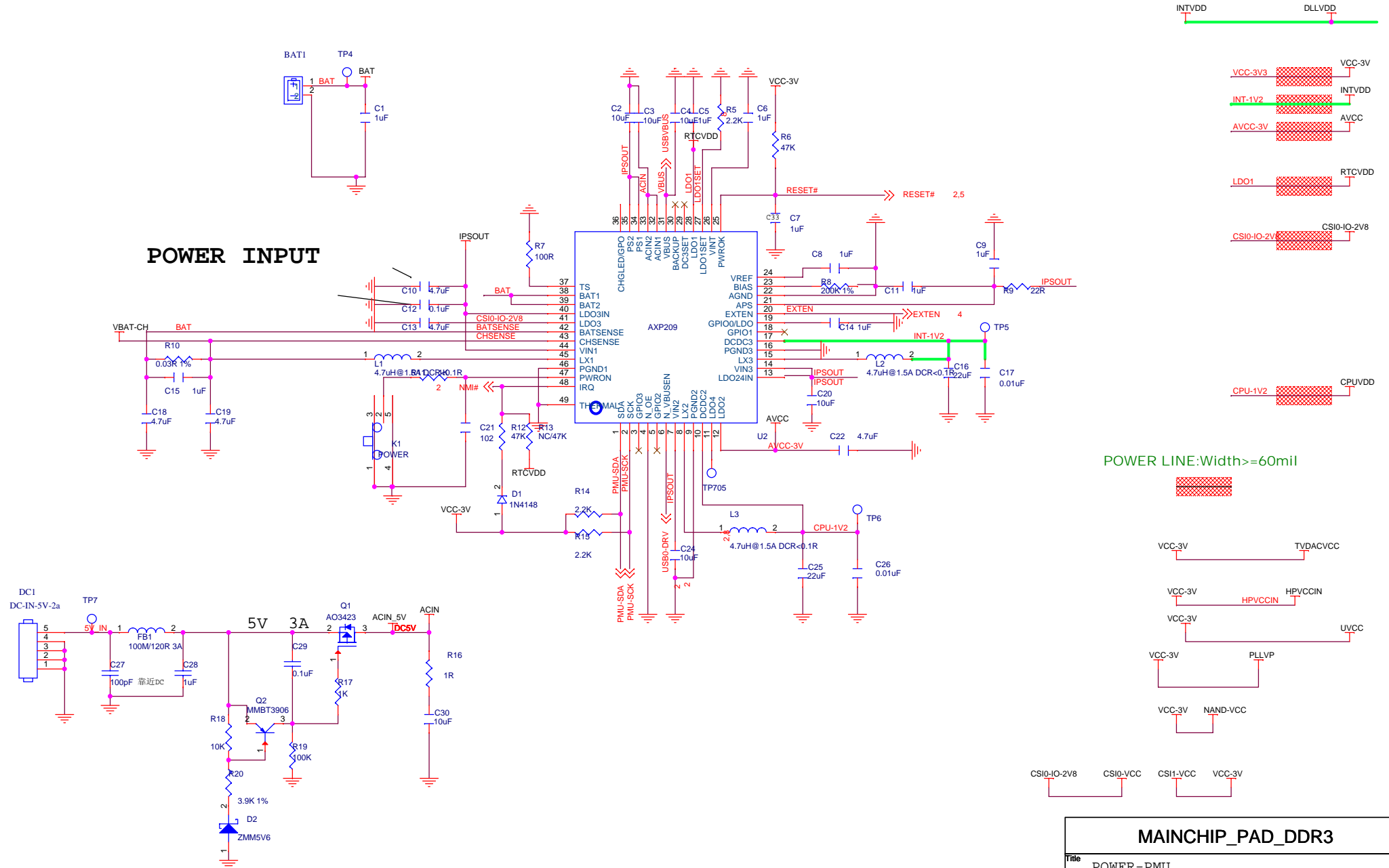
## CPU2



MAINCHIP\_PAD\_DDR3

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CPU2			
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## POWER-PMU



POWER LINE:Width>=60mil

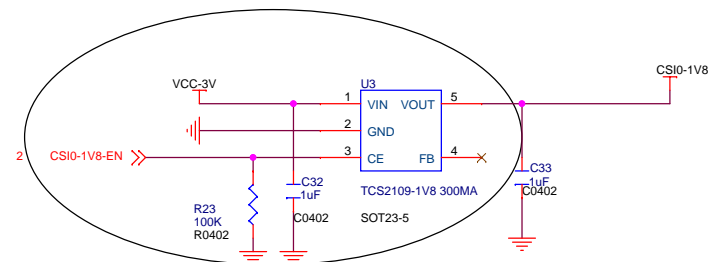
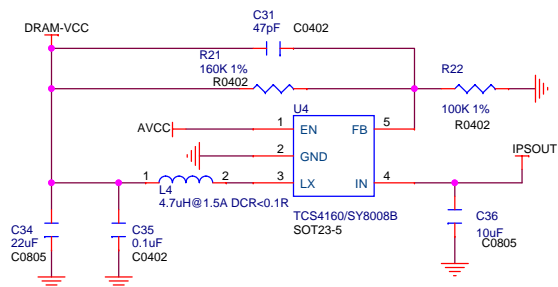
MAINCHIP\_PAD\_DDR3

Title	POWER-PMU
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Size	Document Number	Rev
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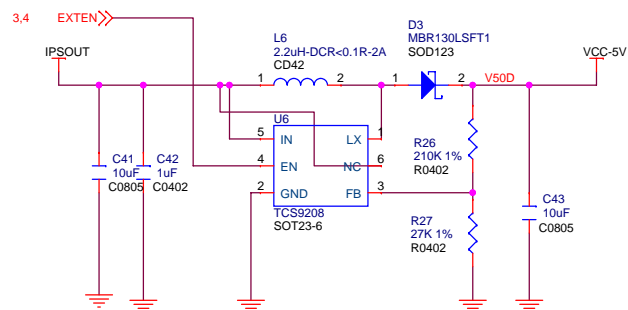
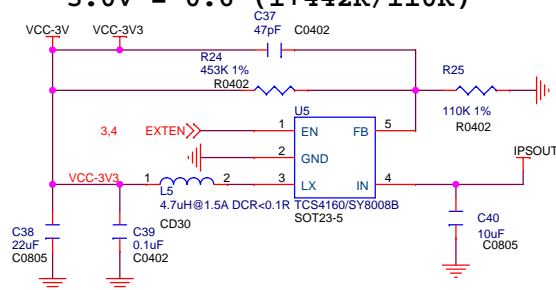
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# POWER-DC/DC



$$V_{out} = 0.6 * (1 + R1/R2)$$

$$3.0V = 0.6 * (1 + 442K/110K)$$



$$V_{out} = 0.6 * (1 + R1/R2)$$

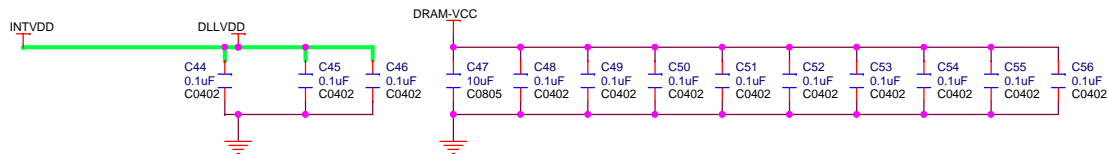
$$5.26V = 0.6 * (1 + 210K/27K)$$

## MAINCHIP\_PAD\_DDR3

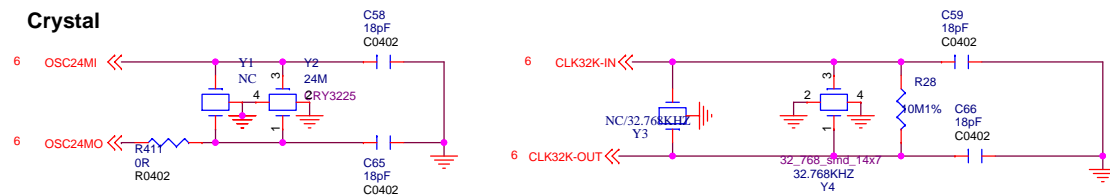
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Size	Document Number	Rev
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# BESIDE CPU

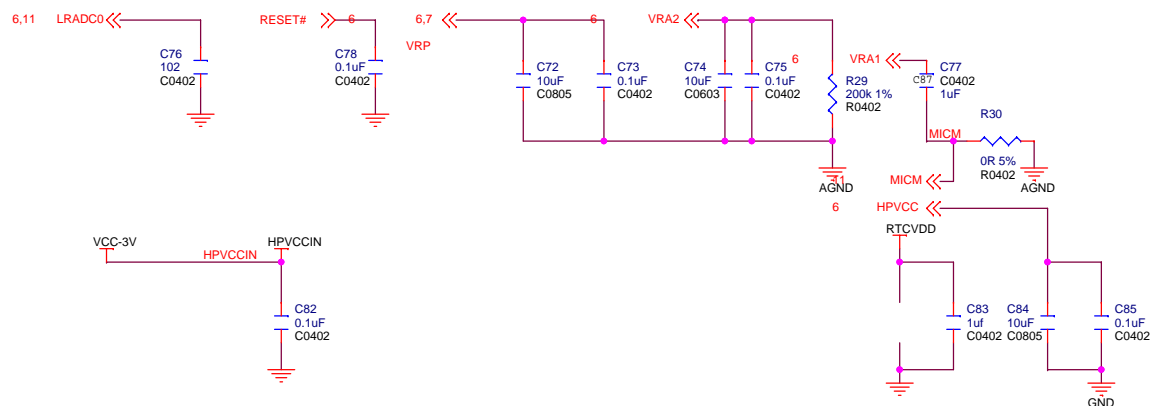
## DRAM



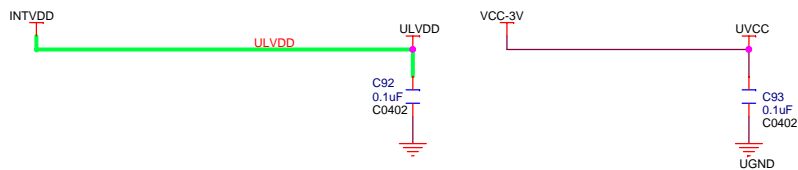
## Crystal



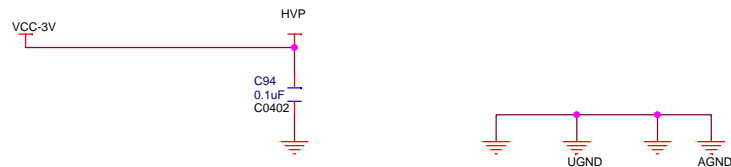
## AUDIO&SYS&TP



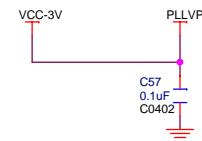
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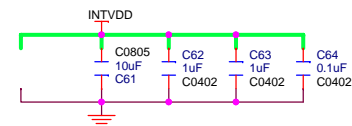
## HDMI



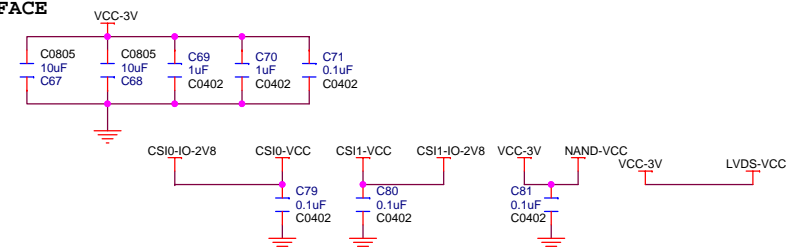
## PLL



## CORE



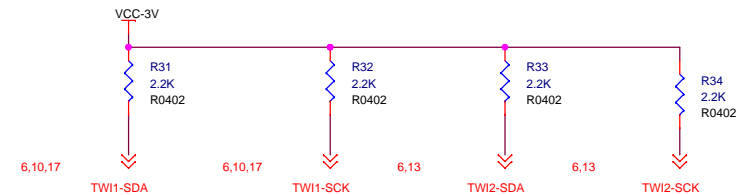
## PIO-INTERFACE



## CPU&TV



## TWI-PULLUP

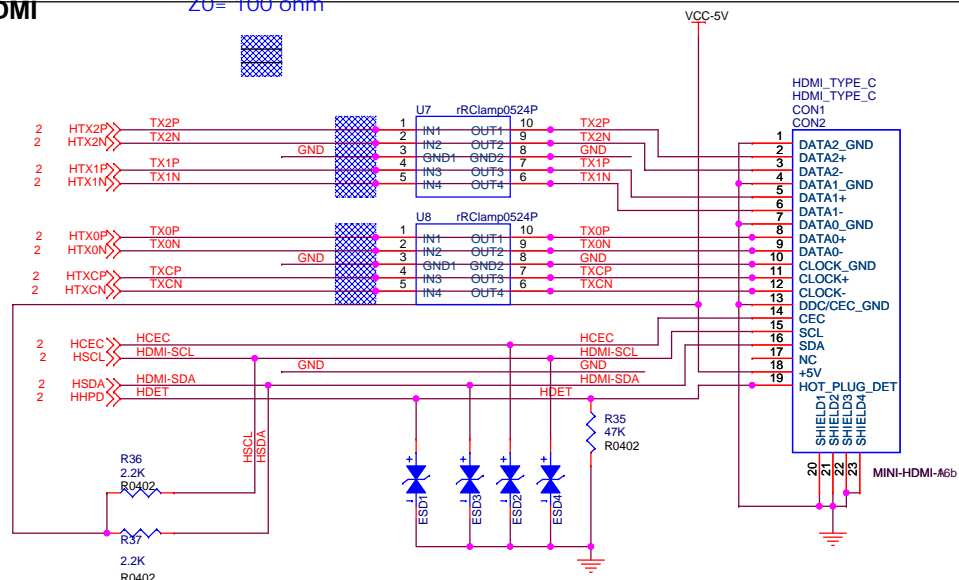


## MAINCHIP\_PAD\_DDR3

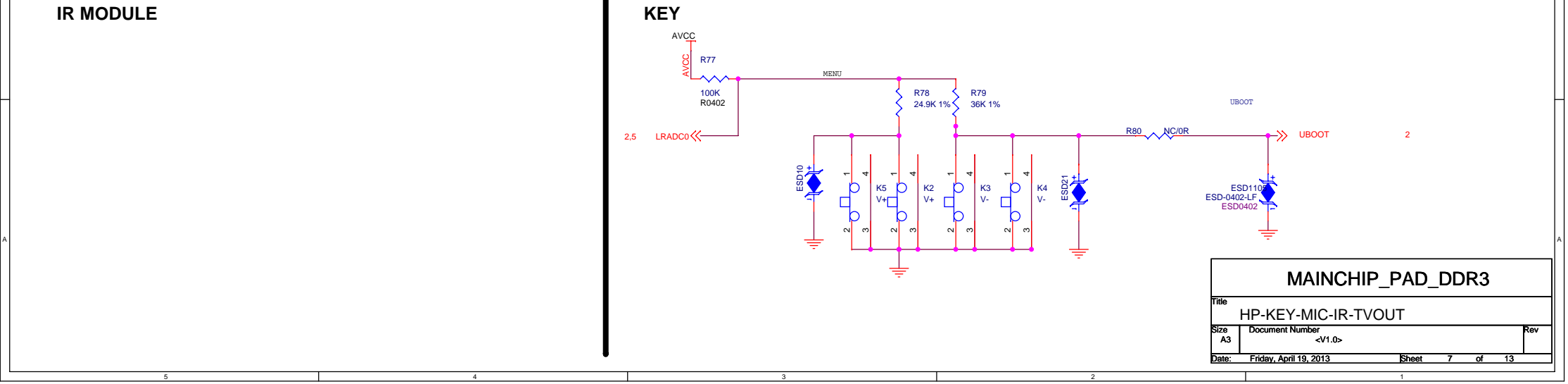
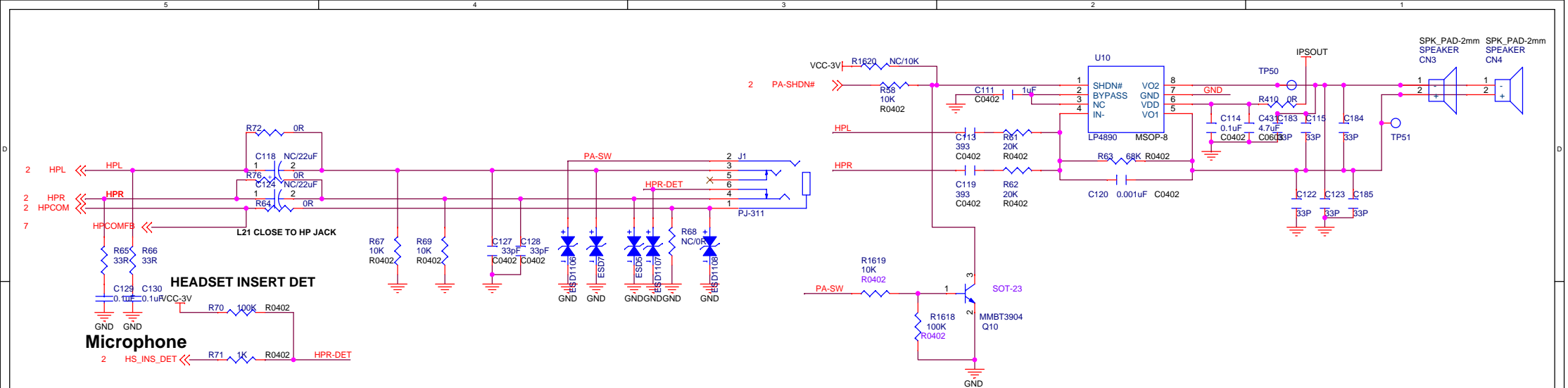
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Size	Document Number	Rev	
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## HDMI

Differential pairs  
Z0= 100 ohm

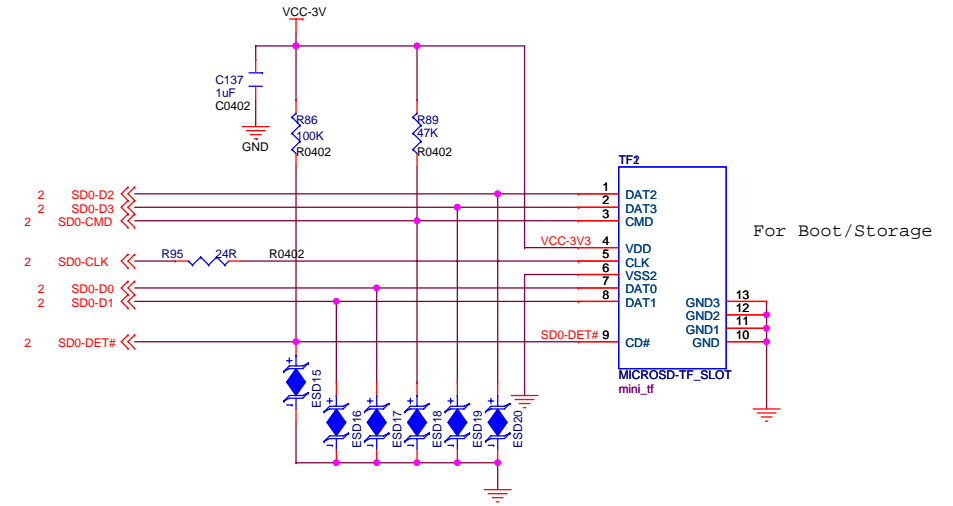
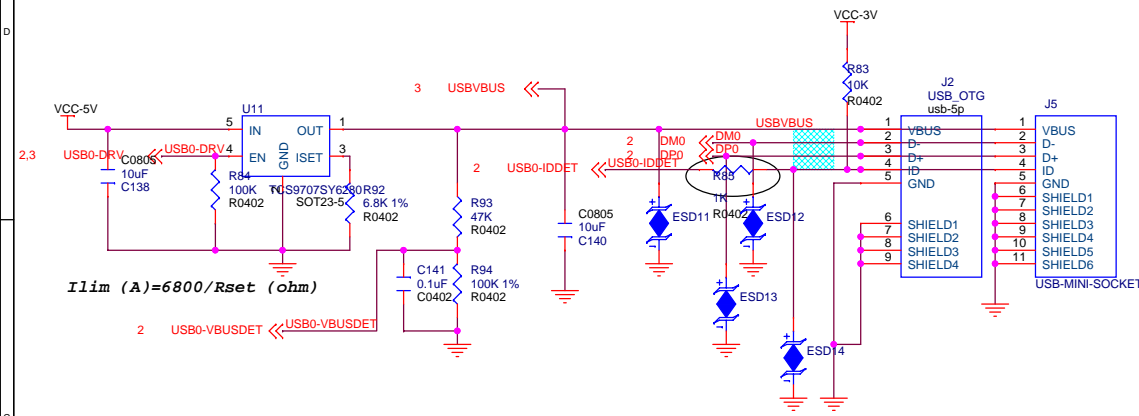
[illegible]

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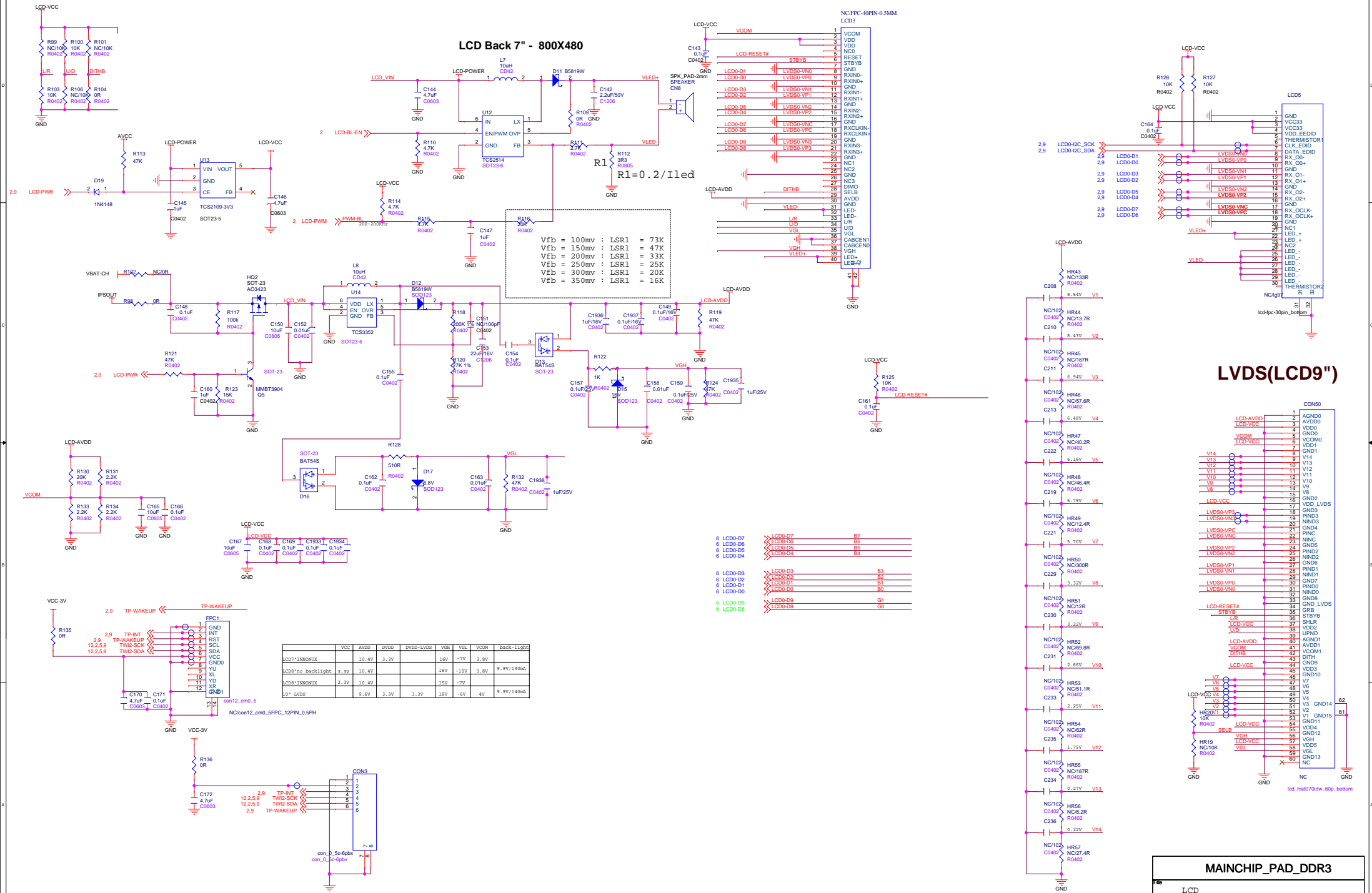
**CARD0**

For Boot/Storage

MAINCHIP\_PAD\_DDR3

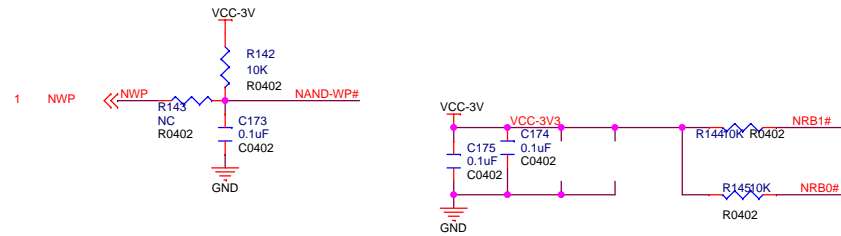
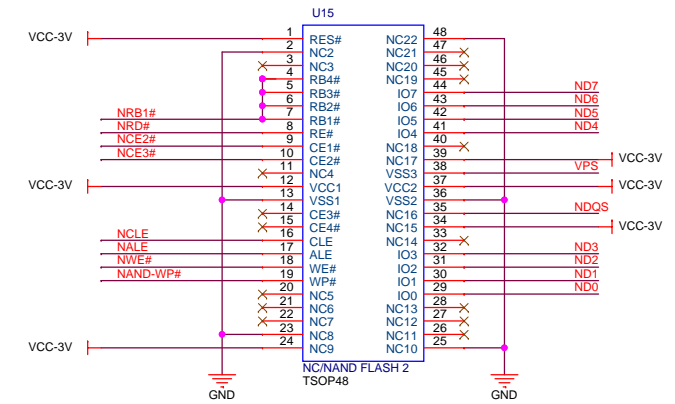
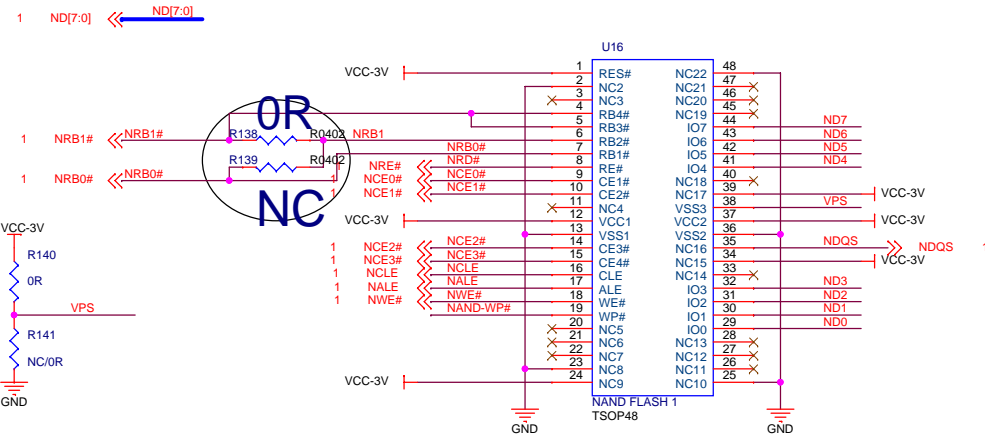
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## LCD

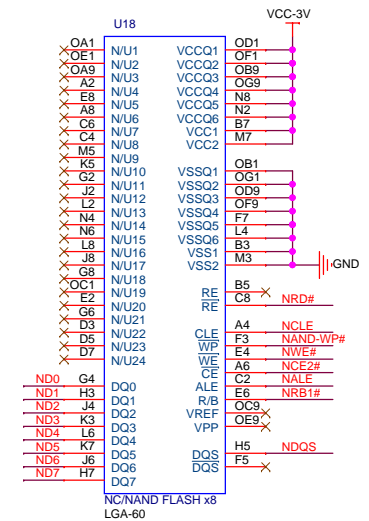
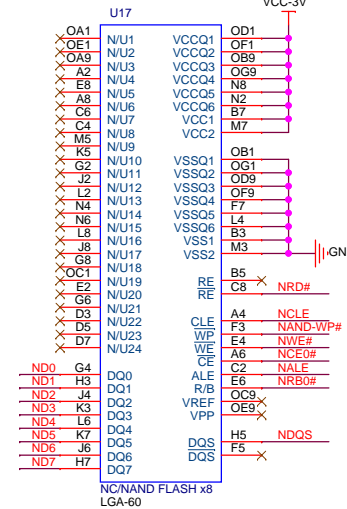


# NAND Flash

## TSOP-48 Nand

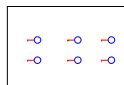


## LGA-60 Nand

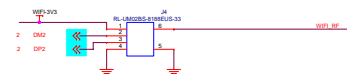


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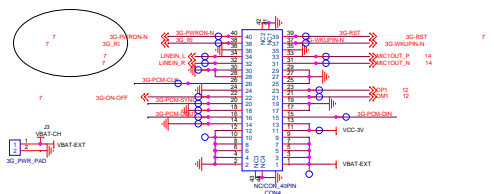
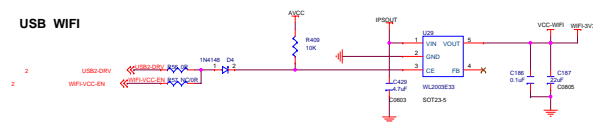
DEBUG

Differential pairs  
70-80 chps

$Z_0 = 90 \text{ ohm}$

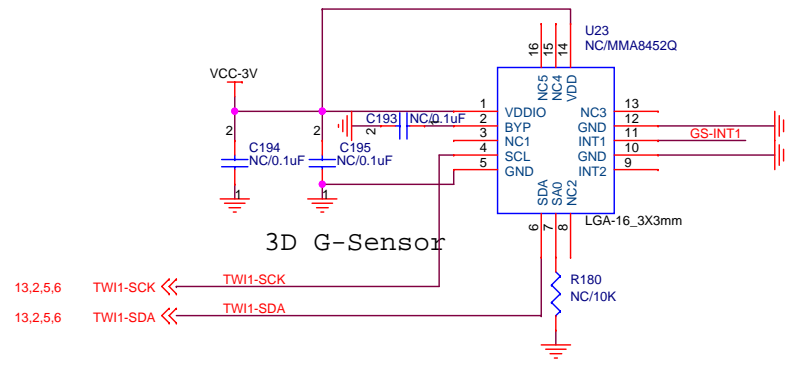
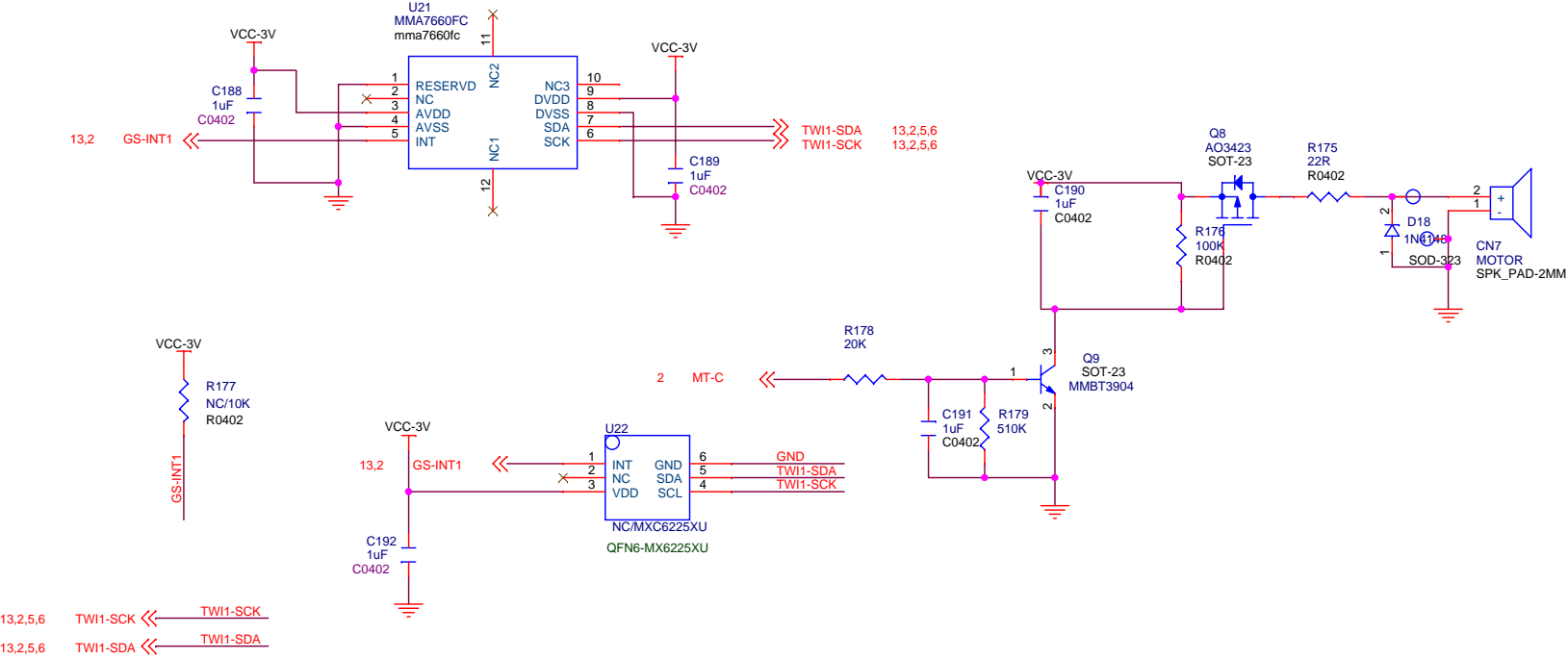


USB WIFI



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Size	Document Number								Rev
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G-SENSOR



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