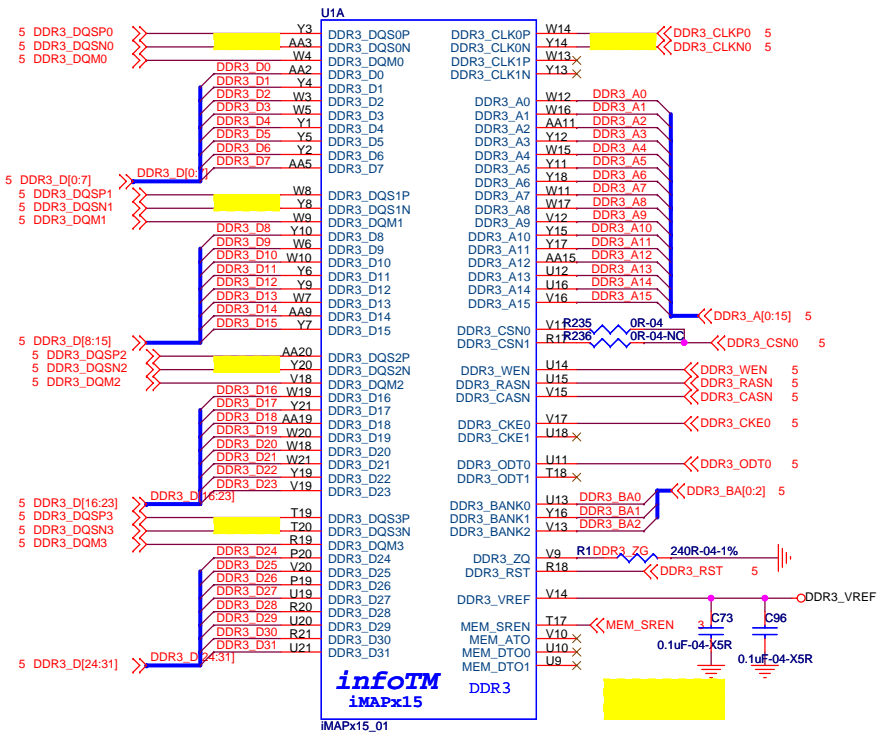
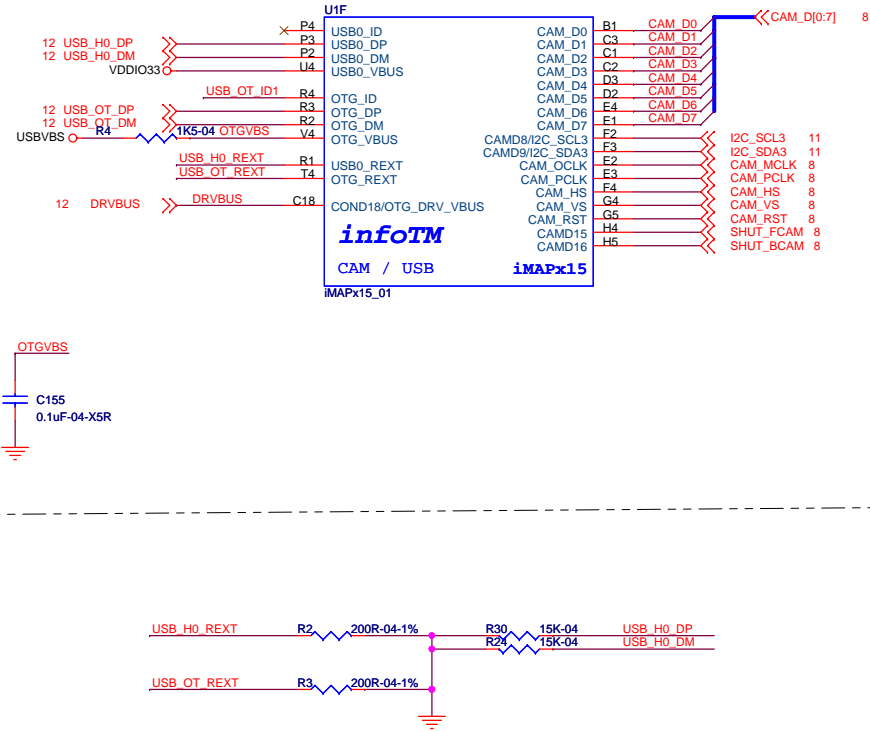


## DDR3 Interface

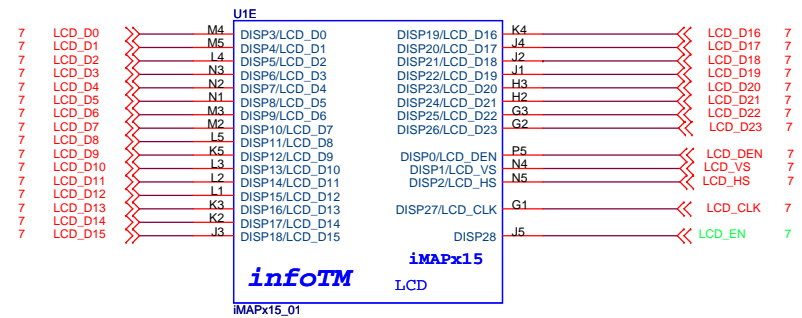


Difference line impedance for 100R

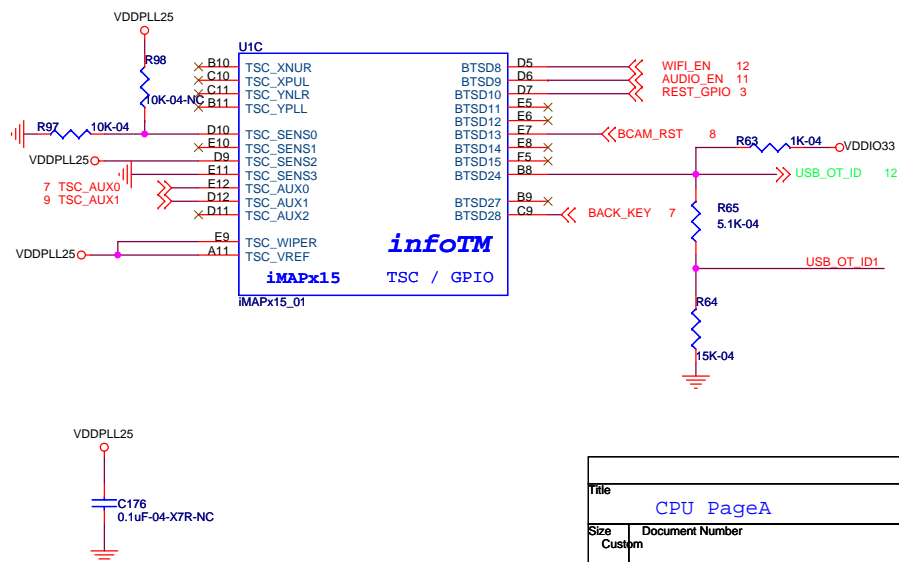
## USB / Ethernet Interface



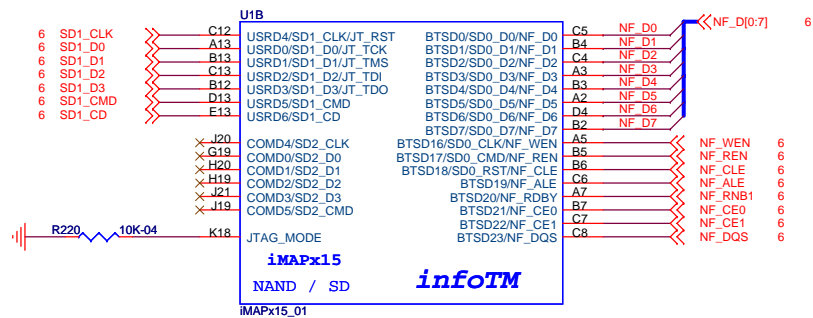
## LCD / HDMI Interface



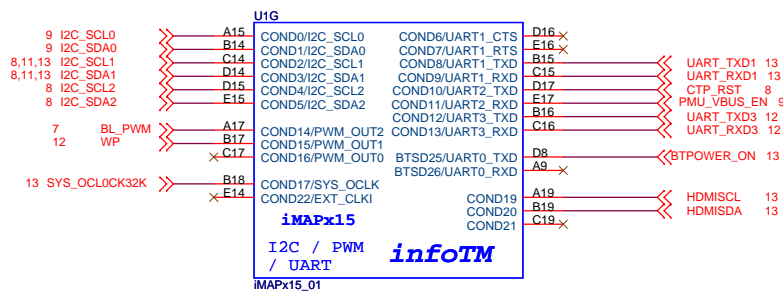
## SATA / TP/ MIPI Interface



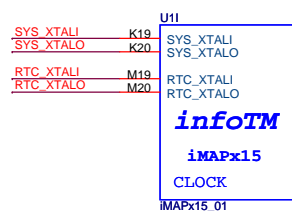
## NAND / SD Interface



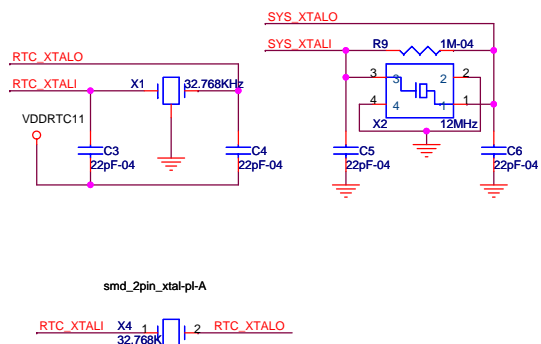
## PWM / UART / GPS / I2C Interface



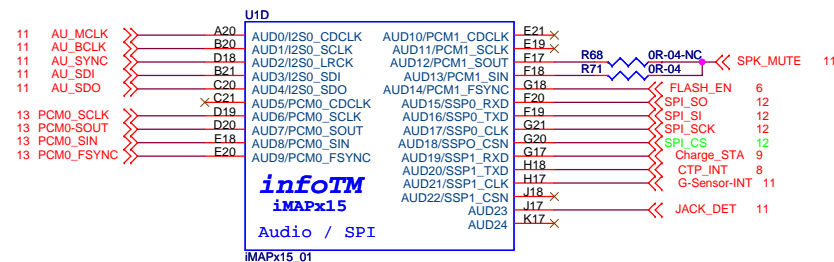
## CLOCK Source



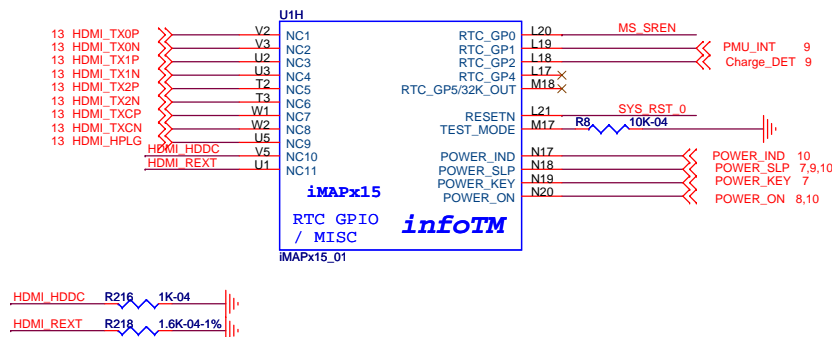
## Crystal



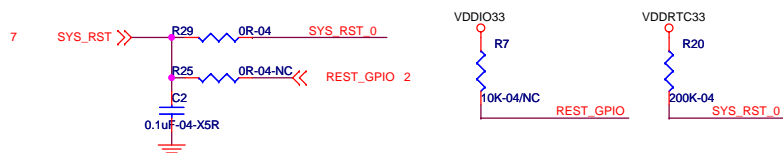
## Audio / CAM Interface



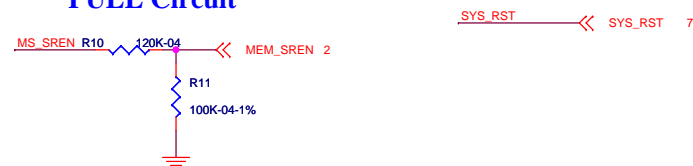
## JTAG / Misc Interface

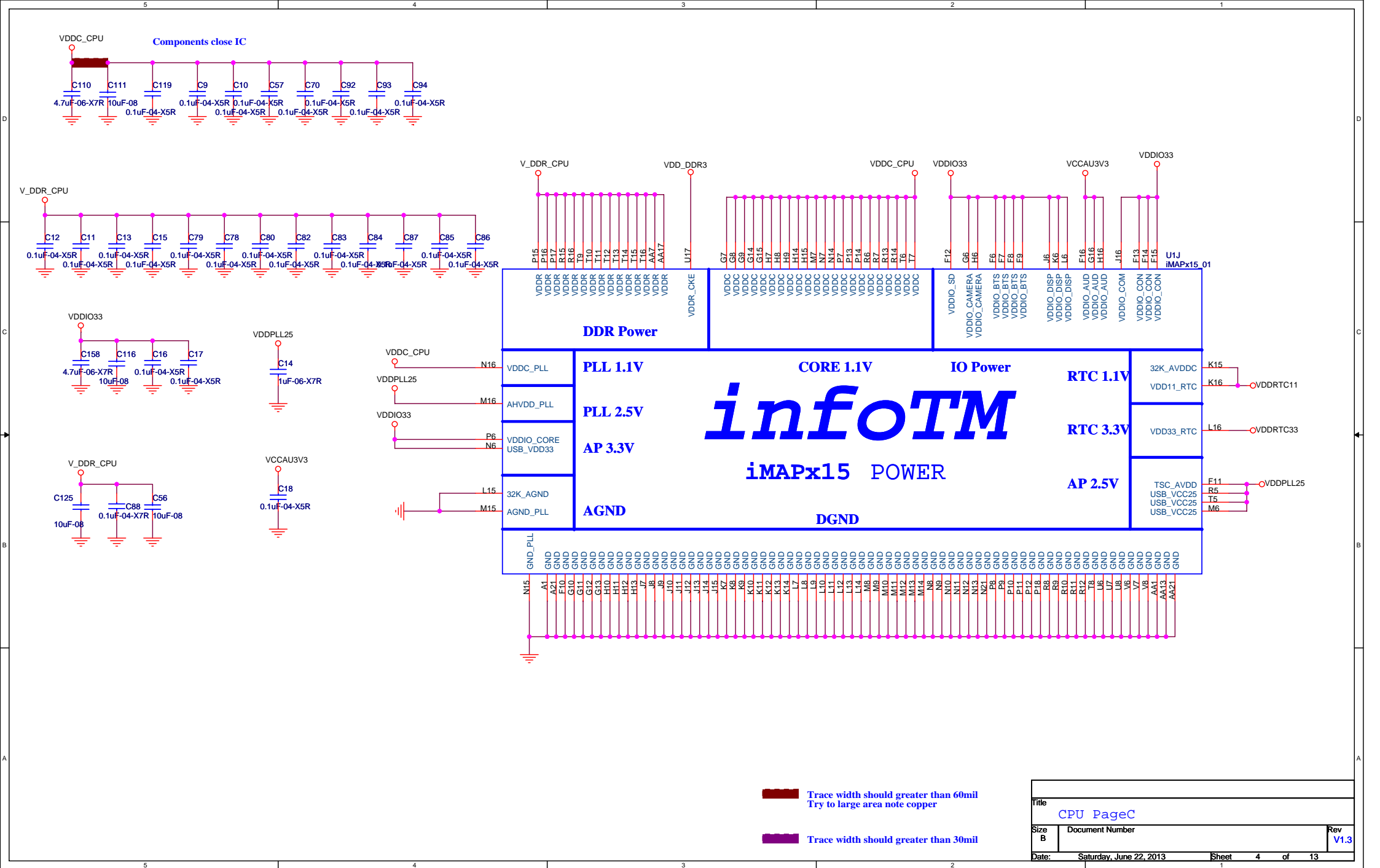


## Reset Circuit

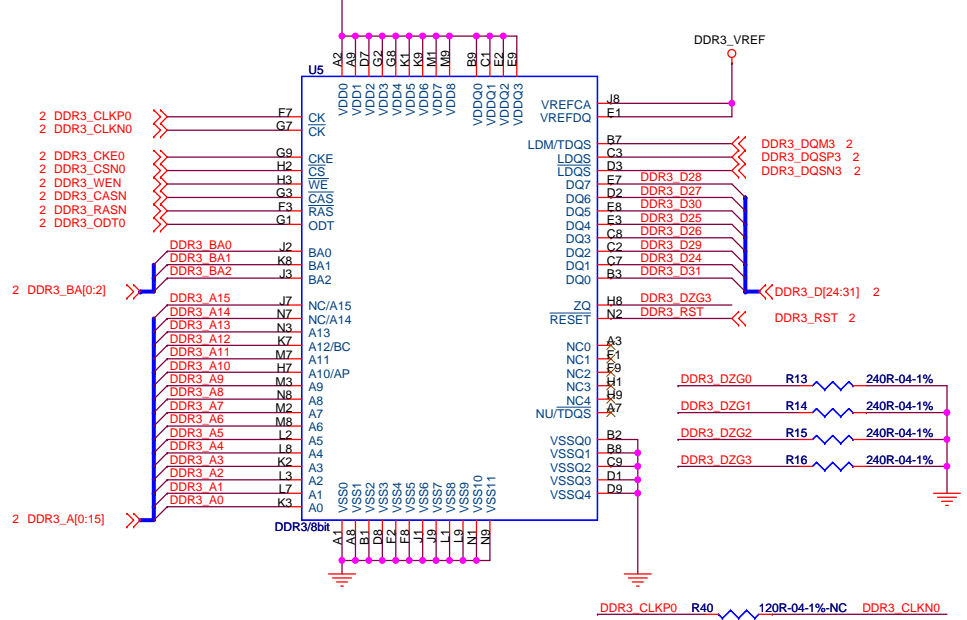
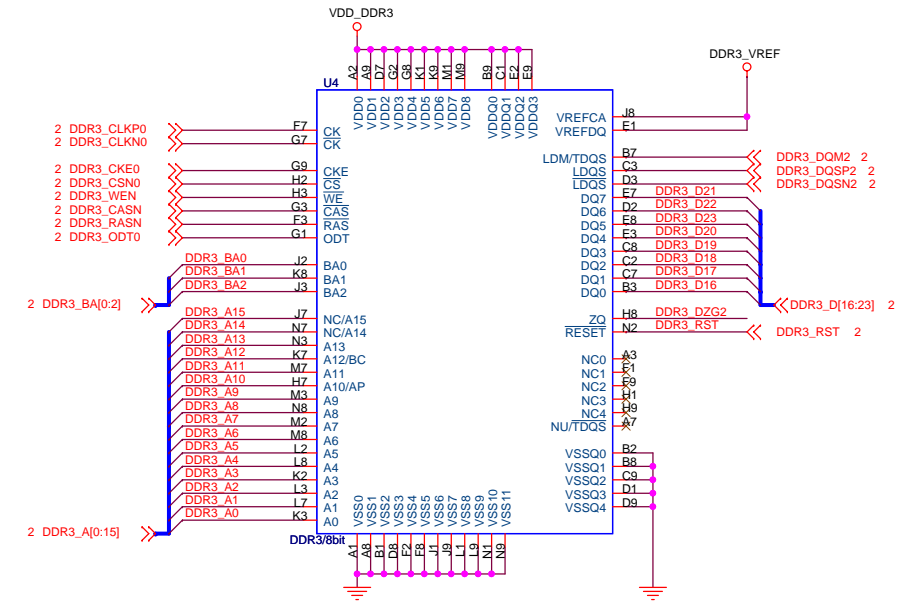
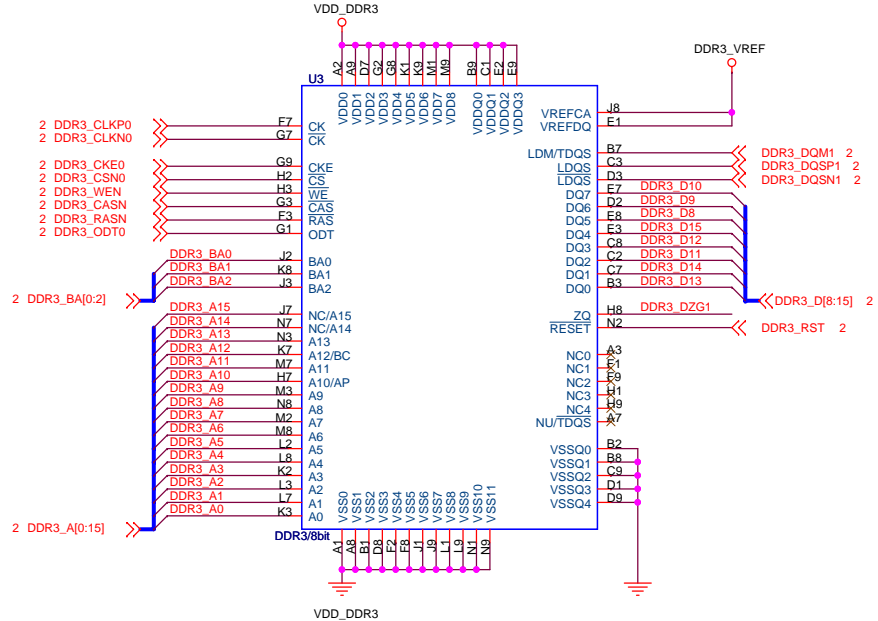
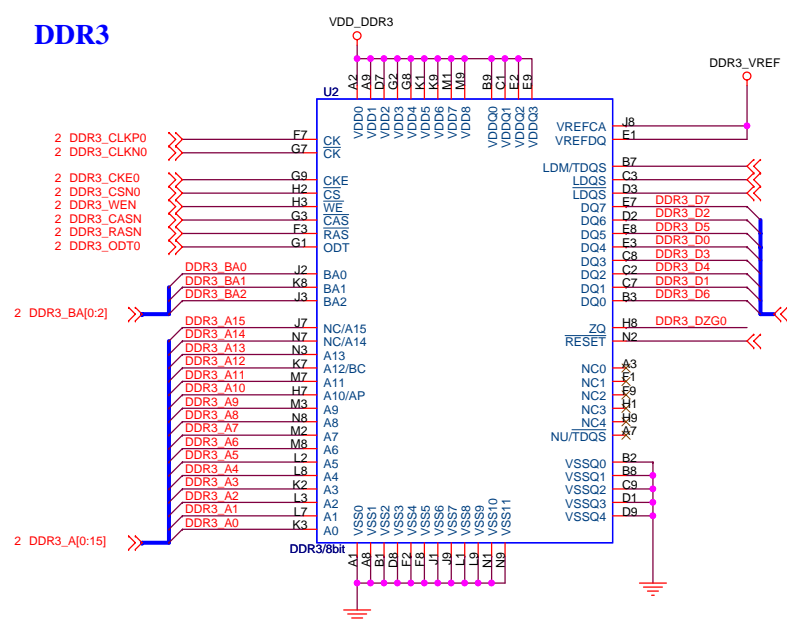


## PULL Circuit

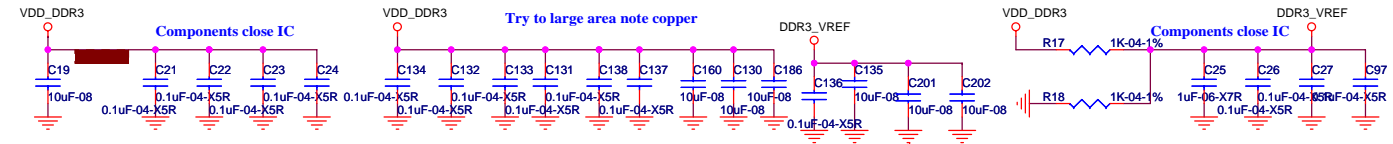




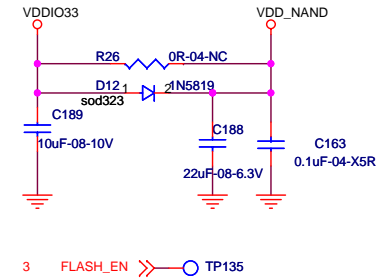
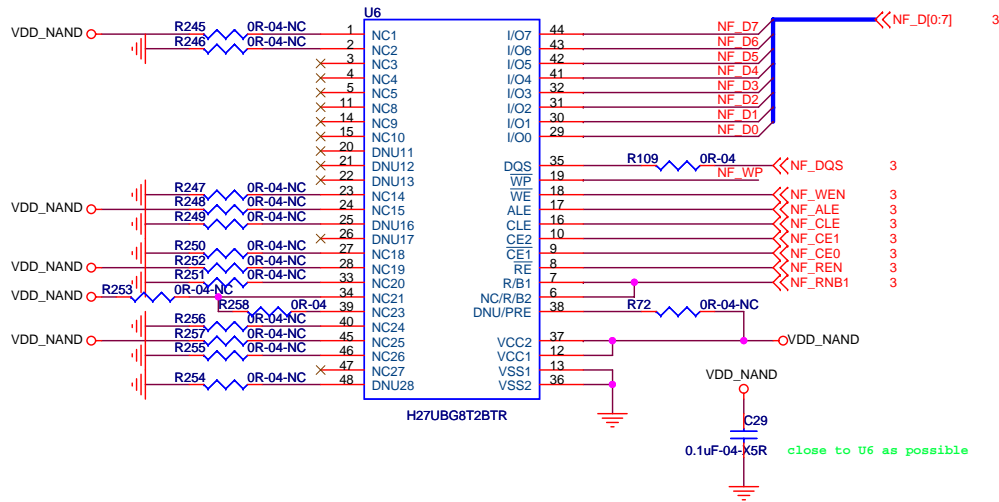
# DDR3



Modify DDR VREF power from  
V\_DDR\_CPU to VDD\_DDR3 20120912  
place to BOT side

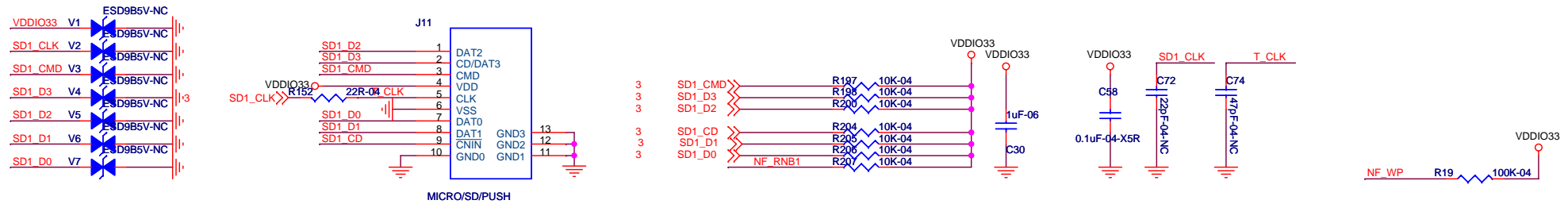


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DDR3		
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For SD Card interface, all signals should keep the equal trace length within 500mil.

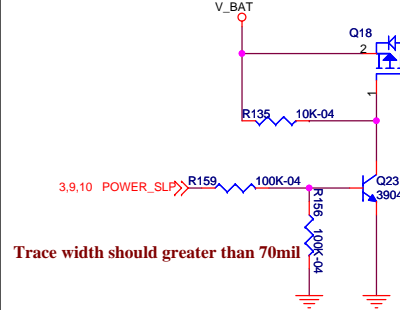
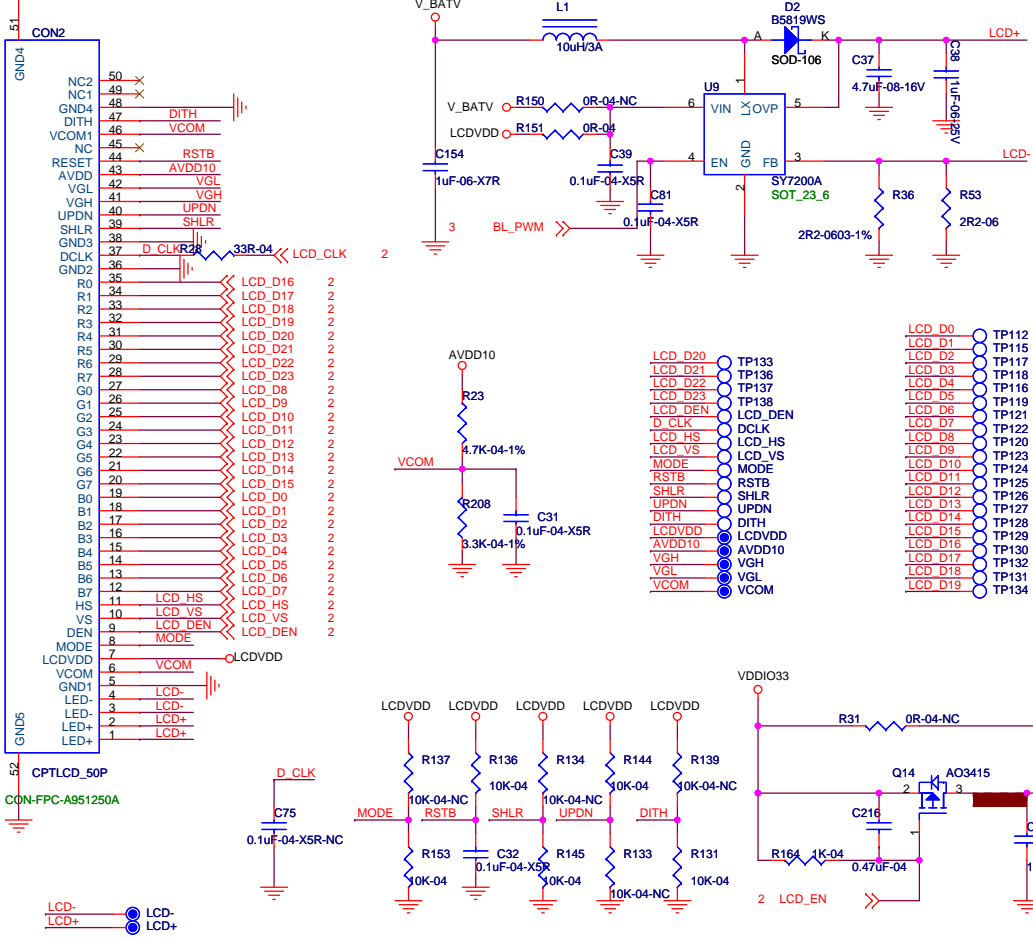
## TF Card



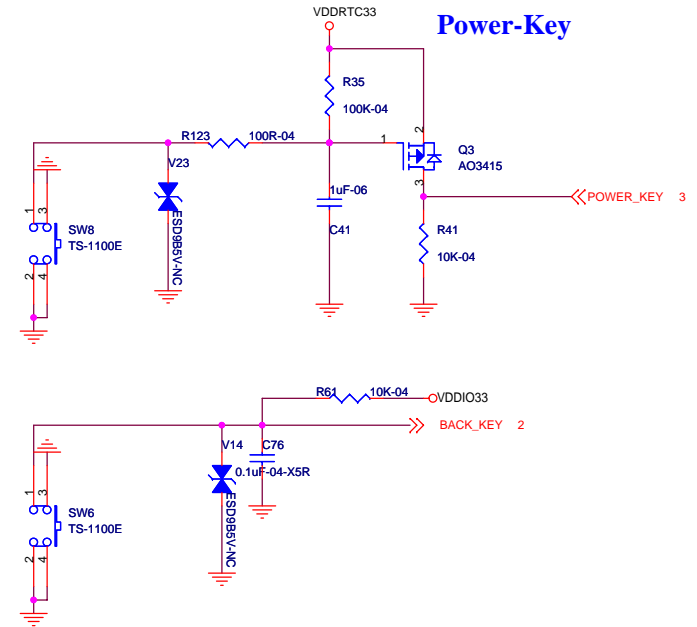
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NAND_TF		
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## LCD-TFT

## LVDS/LCD Power & Back Light Circuit

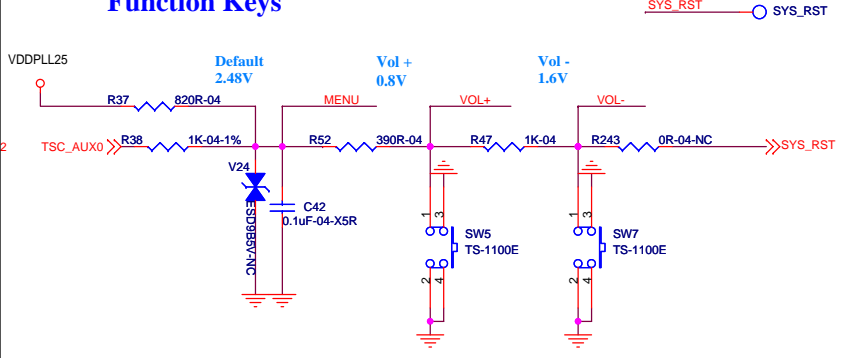
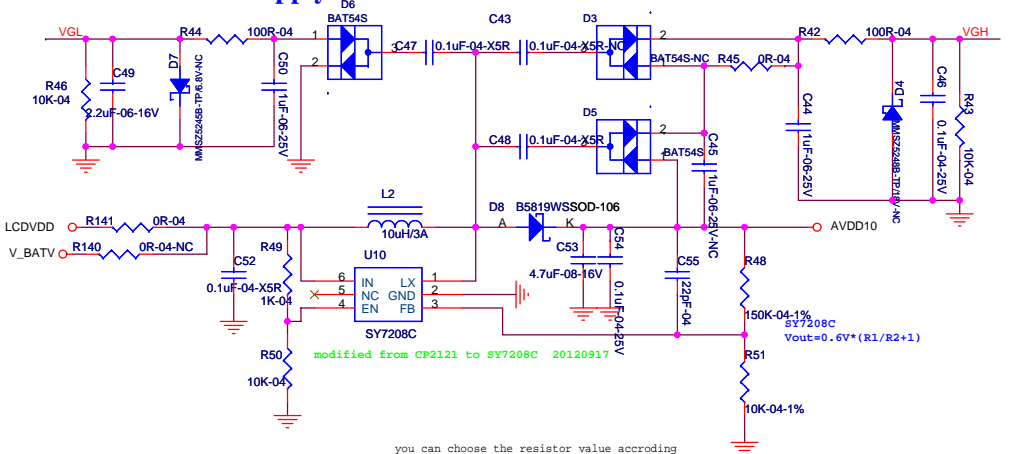


## Power-Key



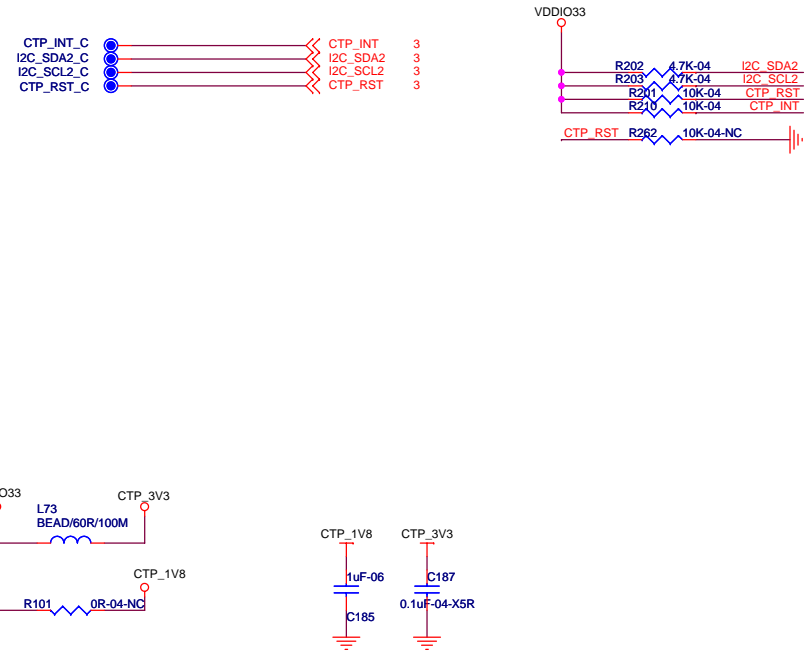
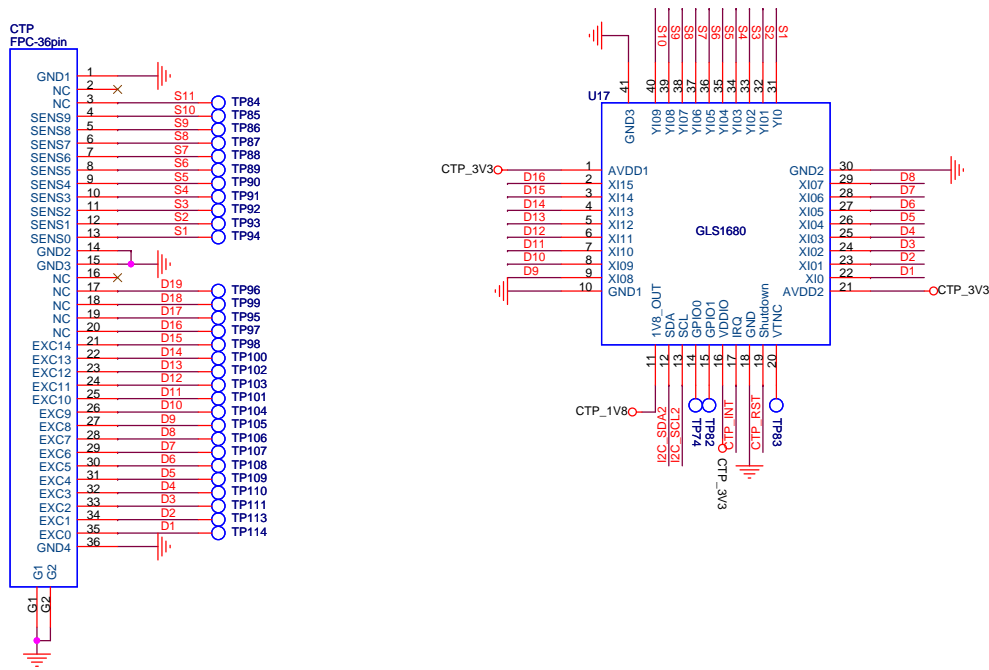
## VGH-VGL Power Supply

## Function Keys

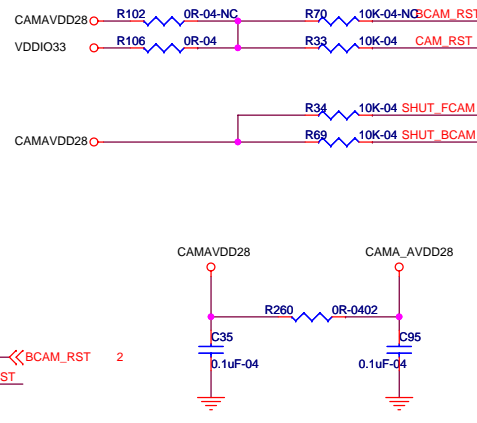
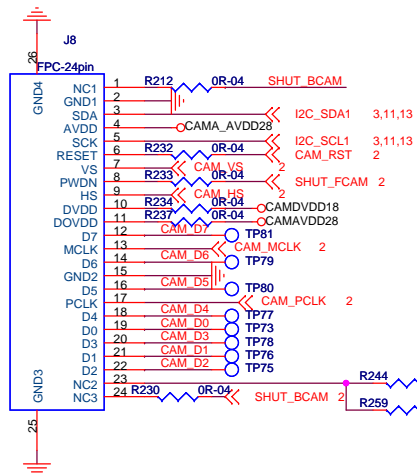
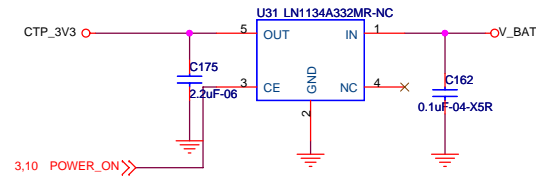
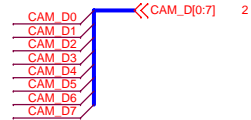
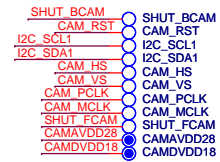


Title		
LCD_KEY		
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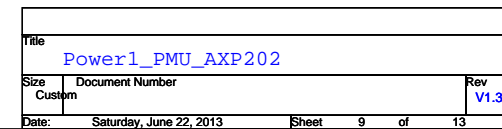
## Pull High Circuit



## Camera

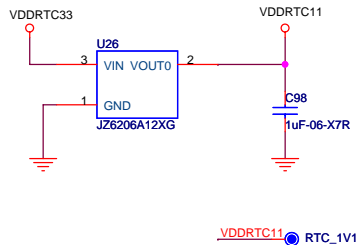


Title				
CAM_CTP				
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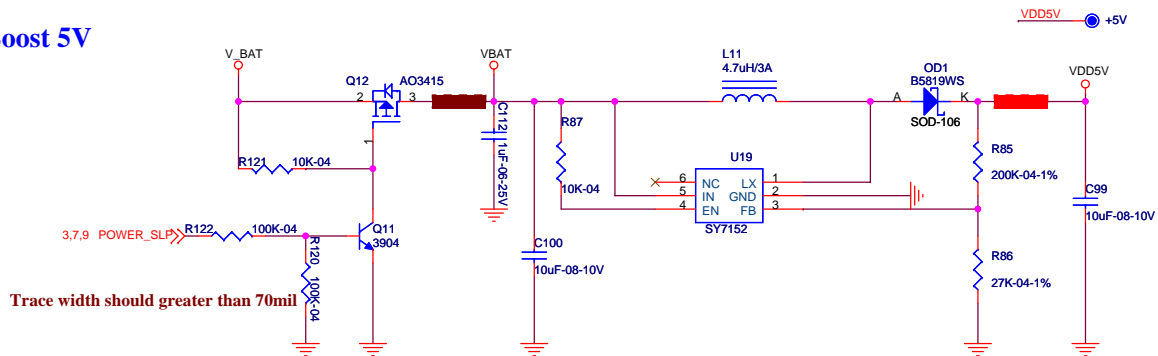




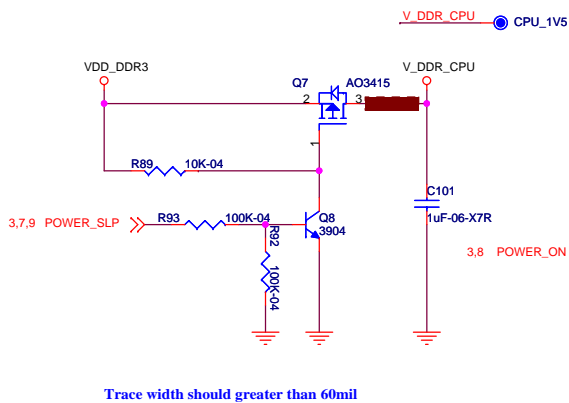
## DC Power Input



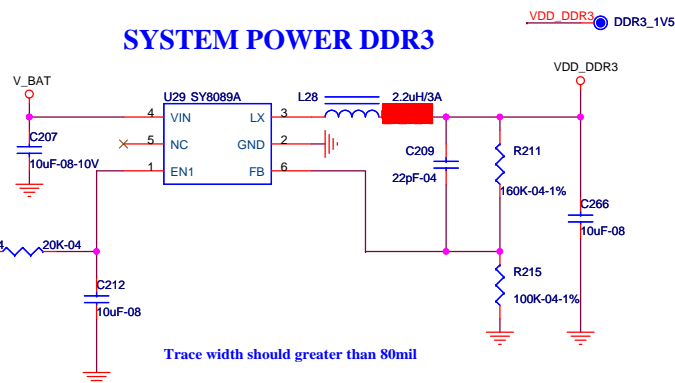
## Boost 5V



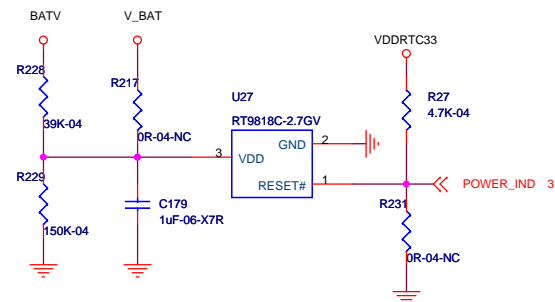
## V\_DDR\_CPU



## SYSTEM POWER DDR3

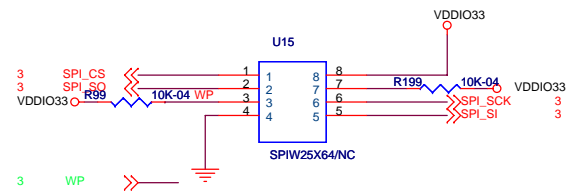
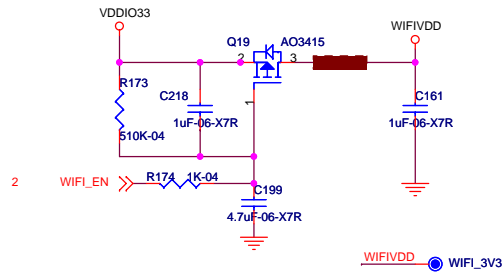


## battery low voltage for 3.3v

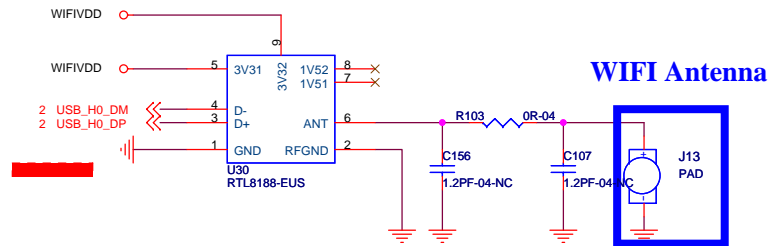


Title		
Power2_DC to DC		
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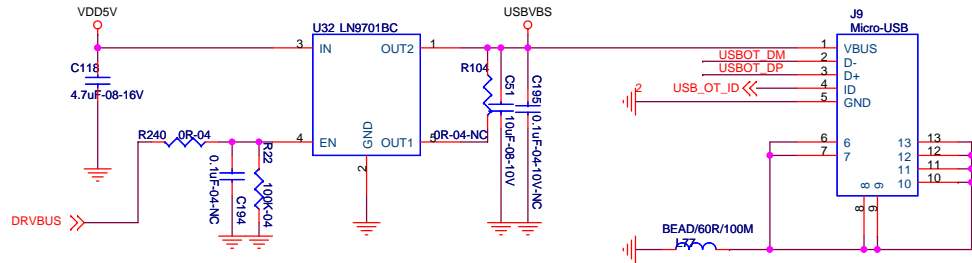




## USB WIFI

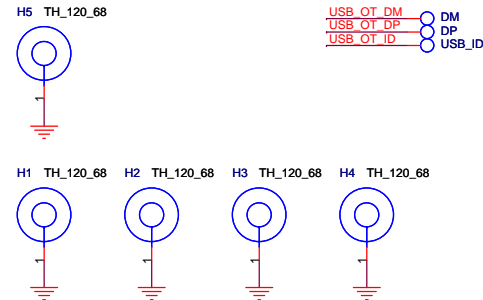
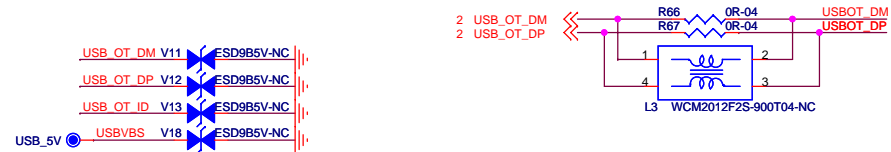
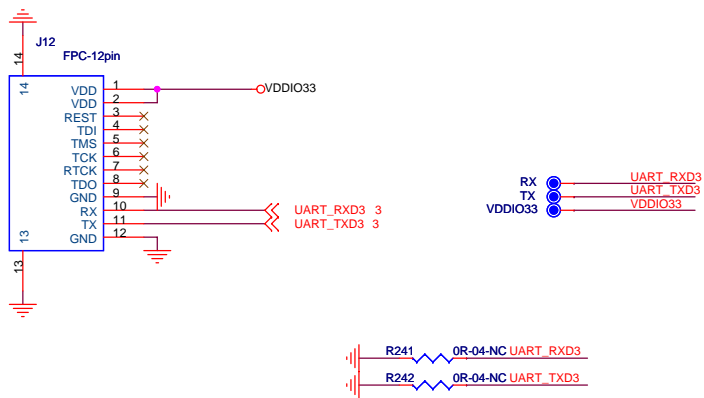


## OTG/HOST/Charger



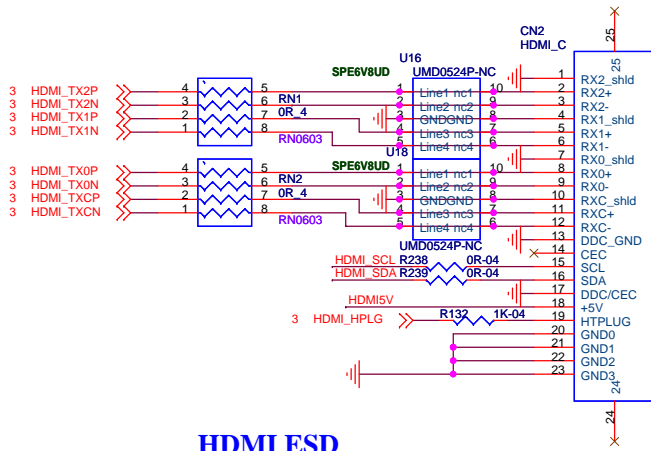
## JTAG Connector

### JTAG Connector

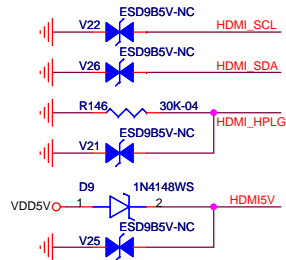


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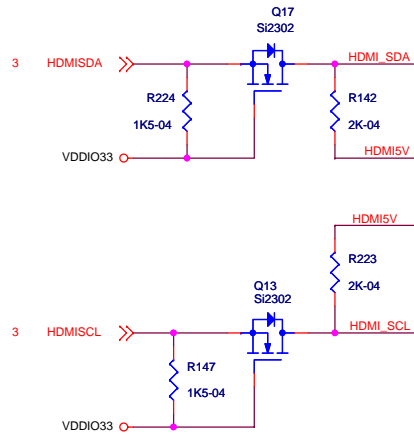
## HDMI



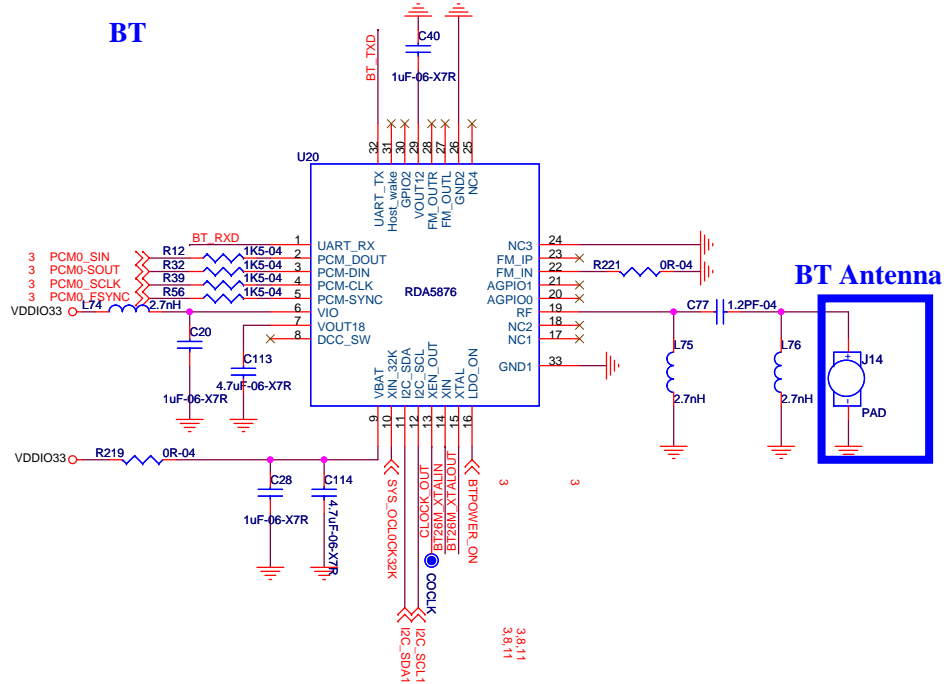
## HDMI ESD



## HDMI LEVEL SHIFT



## BT



## BT Antenna

