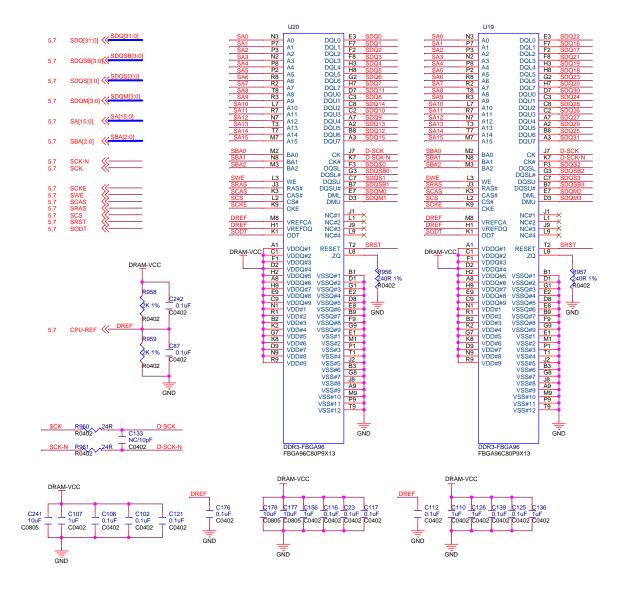
CPU₁ U1-1 SDQ[31:0] < SDQ[31:0] NWE#/SPI0_MOSI/PC0 NALE/SPI0_MISO/PC1 NCLE/SPI0_CLK/PC2 NCE1/PC3 NALE NCE0# NCE0/PC4 NRE#/PC5 DRAM-VCC VDD25-0-SATA N15 VDD25-1-SATA M15 VCC0-DRAM VCC1-DRAM NRB0/SDC2_CMD/PC6 NRR0# NRB1/SDC2 CLK/PC7 INTVDD NDQ0/SDC2_D0/PC8 NDQ1/SDC2_D1/PC9 NDQ2/SDC2_D2/PC10 VCC2-DRAM T2 SDQ15
N2 SDQ16
SDQ17
P1 SDQ18
T1 SDQ19 VCC3-DRAM VCC4-DRAM G23 G22 H21 H20 VDD12-1-SATA R5 T5 ND[7:0] HVP NDQ3/SDC2_D3/PC11 NDQ4/PC12 VCC5-DRAM VCC6-DRAM T13 VCC-HDM VCC7-DRAM VCC8-DRAM SDQ20 NDQ5/PC13 NDQ6/PC14 U1 SDQ20 N1 SDQ21 M2 SDQ22 SDQ23 G21 N G20 N W7 Y6 SDQ22 SDQ23 NDQ7/PC15 NWP/PC16 VCC9-DRAM GND0 GND1 GND2 GND3 GND3 N5 NWP NCE2# J1 SDQ23 L1 SDQ24 H1 SDQ25 K2 SDQ26 F23 12 NCE2/PC17 NCE3/PC18 VDD0-CPU VDD1-CPU VDD2-CPU NCE4/SPI2_CS0/PC19 K2 SDQ27
L2 SDQ27
G2 SDQ28
M1 SDQ30
H2 SDQ30
H3 SDQ31
H4 SVREF
Y5 SVREF
A86 SVREF
A65 SDQS0
AB1 SDQS NCE5/SPI2_CLK/PC20 NCE6/SPI2_MOSI/PC21 VDD3-CPU VDD4-CPU GND4 GND5 GND6 J12 J13 NCE7/SPI2_MISO/PC22 SPI0_CS0/PC23 NDQS/PC24 VDD5-CPU J15 J16 K9 K10 ->>NDQS VDD0-SYS 7,8 CPU-REF << VDD1-SYS VDD2-SYS VDD3-SYS K15 VDD4-SYS VDD5-SYS VDD6-SYS VDD7-SYS AA2 R1 P2 R10 T8 SDQS1# SDQS2 DLLVDD VDD9-SYS K1 SDQS2# SDQS3 ->CSI0-PCLK ->CSI0-MCLK ->CSI0-HSYNC K1 SDQS3 J2 SDQS3# AC6 SDQS3# SDQM0 SDQM0 G1 SDQM2 SDQM2 SDQM2 SDQM3 SCK# N3 SCKE J3 SCKE J4 SCK1# VDD0-DLI N9 P9 VDD1-DLL TS0_SYNC/CSI0_HSYNC/PE2 TS0_DVLD/CSI0_VSYNC/PE3 VDD2-DLL VCC-3V3 -> CSI0-VSYNC -> CSI0-D0 -> CSI0-D1 -> CSI0-D2 W14 TS0_D0/CSI0_D0/PE4
TS0_D1/CSI0_D1/PE5 VCC2-LVDS VCC1-LVDS TSO/CSIO W13 13 13 13 13 13 13 SCK-N SCK TS0_D2/CSI0_D2/PE6 TS0_D3/CSI0_D3/PE7 VCC0-LVDS 7,8 7,8 7,8 B22 A23 A22 B21 CSI0-D2 -> CSI0-D3 -> CSI0-D4 -> CSI0-D5 -> CSI0-D6 -> CSI0-D7 H8 H9 H15 TS0_D4/CSI0_D4/PE8
TS0_D5/CSI0_D5/PE9 VCC1 VCC2 VCC3 J4 SCK1#
W4 SCK1
R4 SA0
W4 SA1
W4 SA2
W4 SA2
W4 SA4
W4 SA5
W4 SA5
W4 SA5 TS0_D6/CSI0_D6/PE10 TS0_D7/CSI0_D7/PE11 J8 J9 J14 VCC4 VCC5 VCC0-PA VCC1-PA NAND-VCC VCC_3V3 CSI0-VCQ CSI1-VCC TS1/CSI1 V4 SA5
M3 SA6
AA3 SA7
P4 SA8
L3 SA9
W3 SA10
P3 SA11
Y3 SA12
R3 SA13 J19 VCC0-PC VCC1-PC VCC-PF VCC-PE TS1_ERR/LS1_mmcst2
TS1_SYNC(SS1_HSYNC(SDC1_D0/PG2_TS1_DVLD/CS11_VSYNC/SDC1_D1/PG3_D20_TS1_D0/CS1_D1/SDC1_D2/CS10_D8/PG4_C21_TS1_D1/CS11_D1/SDC1_D3/CS10_D8/PG4_C21_TS1_D1/CS11_D1/SDC1_D3/CS10_D8/PG6_C21_D2/CS11_D2/LART3_TX/CS10_D10/PG6_C21_D2/CS11_D2/LART3_TX/CS10_D10/PG6_C21_D2/CS11_D2/LART3_TX/CS10_D10/PG6_C21_D10/LART3_TX/CS10_D10/PG6_C21_D10/LART3_TX/CS10_D10/PG6_C21_D10/LART3_TX/CS10_D10/PG6_C21_D10/LART3_TX/CS10_D10/PG6_C21_D10/LART3_TX/CS10_D10/PG6_C21_D10/LART3_TX/CS10_D10/PG6_C21_D10/LART3_TX/CS10_D10/PG6_C21_D10/LART3_TX/CS10_D10/LART3_TX/CS10_D10/PG6_C21_D10/LART3_TX/CS10_D10/PG6_C21_D10/LART3_TX/CS10_D10/LAR F19 E18 VCC-PG TVDACVCC W15 W16 TS1_D2/CSI1_D2/UART3_TX/CSI0_D10/PG6 TS1_D3/CSI1_D3/UART3_RX/CSI0_D11/PG7 VCC33-TVOUT VCC33-TVIN DRAM-VCC AA4 SA14 K3 SA15 L4 SBA0 GND45 GND45 GND45 GND46 W18 AA18 TS1_D3/CSI1_D3/UART3_RX/CSI0_D11/PG7
TS1_D4/CSI1_D4/UART3_RTS/CSI0_D12/PG8
TS1_D5/CSI1_D5/UART3_CTS/CSI0_D13/PG9
D18 W17 VDD25-TVIN UVCC L4 SBA0
K4 SBA1
SBA2
U3 SWE
SCAS
T4 SCAS TS1_D6/CSI1_D6/UART4_TX/CSI0_D14/PG10 TS1_D7/CSI1_D7/UART4_RX/CSI0_D15/PG11 L16 L15 K16 VCC0-USB ULVDD 7,8 7,8 7,8 7,8 7,8 7,8 SWE SCAS SRAS SCS SRST SODT VCC1-USB VDD-USB T4 SRAS SCS0 AA6 SRST AA7 ODT SZQ R8 SDDE R8 SDDE SADI SRAS SCS0 FBGA441-A20_42 GND SDDBG0 SDDBG1 SADBG R501 240R 1% FBGA441-A20_42 MAINCHIP_PAD_DDR3 AĞND CPU1 Document Number Thursday, April 18, 2013

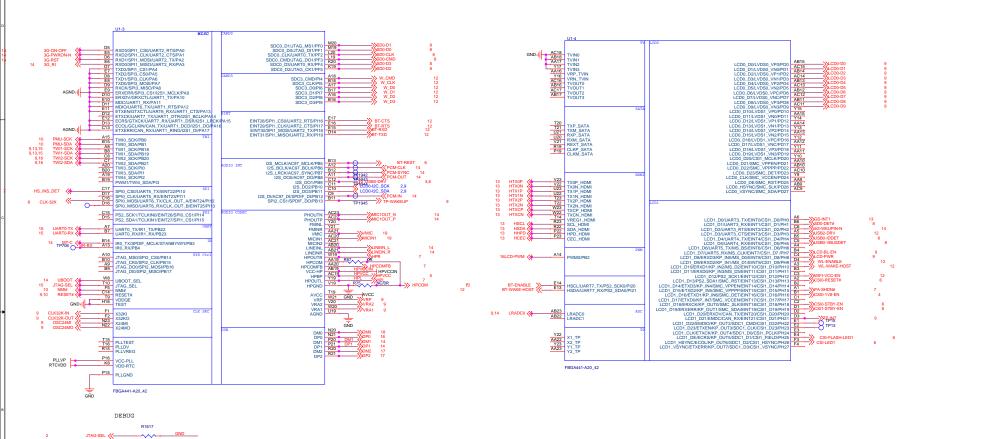
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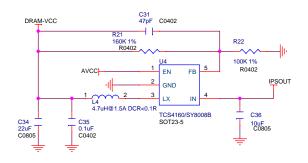
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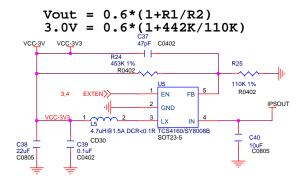
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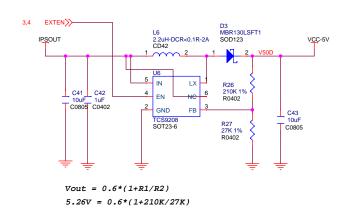


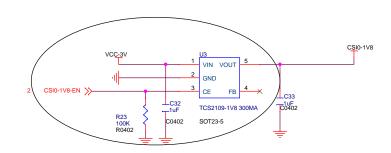
POWER-PMU INTVDD DLLVDD BAT1 ACC-3A INTVDD RTCVDD RESET# 2,5 C33 C7 POWER INPUT C8 100R VREF BIAS AGND APS EXTEN GPIO0/LDO GPIO1 DCDC3 PGND3 LX3 C10 4.7uF 38 39 40 41 42 43 44 45 46 47 C12 0.1uF LD03IN LDO3 C14 1uF O TP5 AXP209 BATSENSE CHSENSE VIN1 LX1 2 L1 4.7uH@1.BA10CR40.1R 2 4.7uH@1.5A DCR<0.1R_{22uF} 0.03R 1% PGND1 PWRON VIN3 LDO24IN C17 CPUVDD 48 0.01uF C15 1uF ___C20 ___10uF ___C18 ___4.7uF AVCC C21 R12 R13 102 47K NC/47K U2 C22 4.7uF POWER LINE:Width>=60mil RTCVDD **D**1 1N4148 L3 4.7uH@1.5A DCR<0.1R VCC-3V TVDACVCC 2.2K C26 0.01uF _____C25 ____22uF DC1 DC-IN-5V-2a AO3423 ACIN_5V 2 14 3 TDC5V VCC-3V 5V UVCC 100M/120R 3A R16 PLLVP VCC-3V 1R Q2 MMBT3906 ⊥C30 ---10uF CSI0-IO-2V8 CSI0-VCC CSI1-VCC D2 MAINCHIP_PAD_DDR3 POWER-PMU <V1.0> Sheet 3 of 13

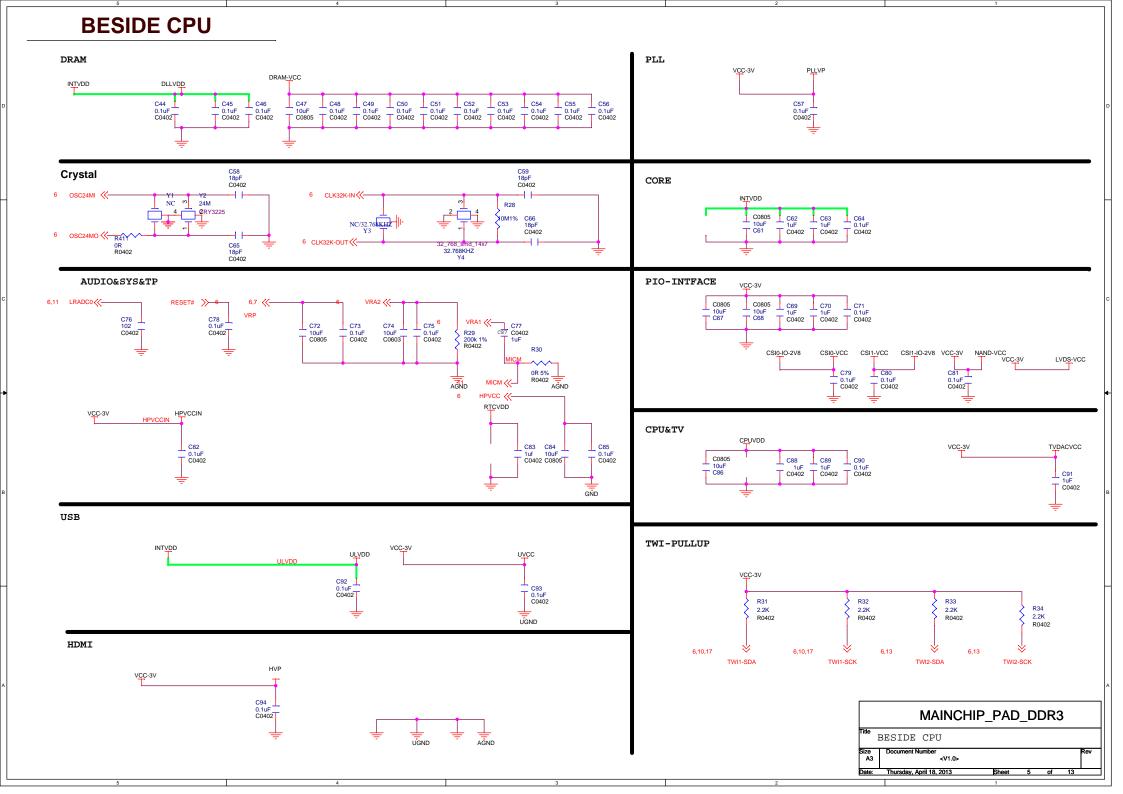
POWER-DC/DC

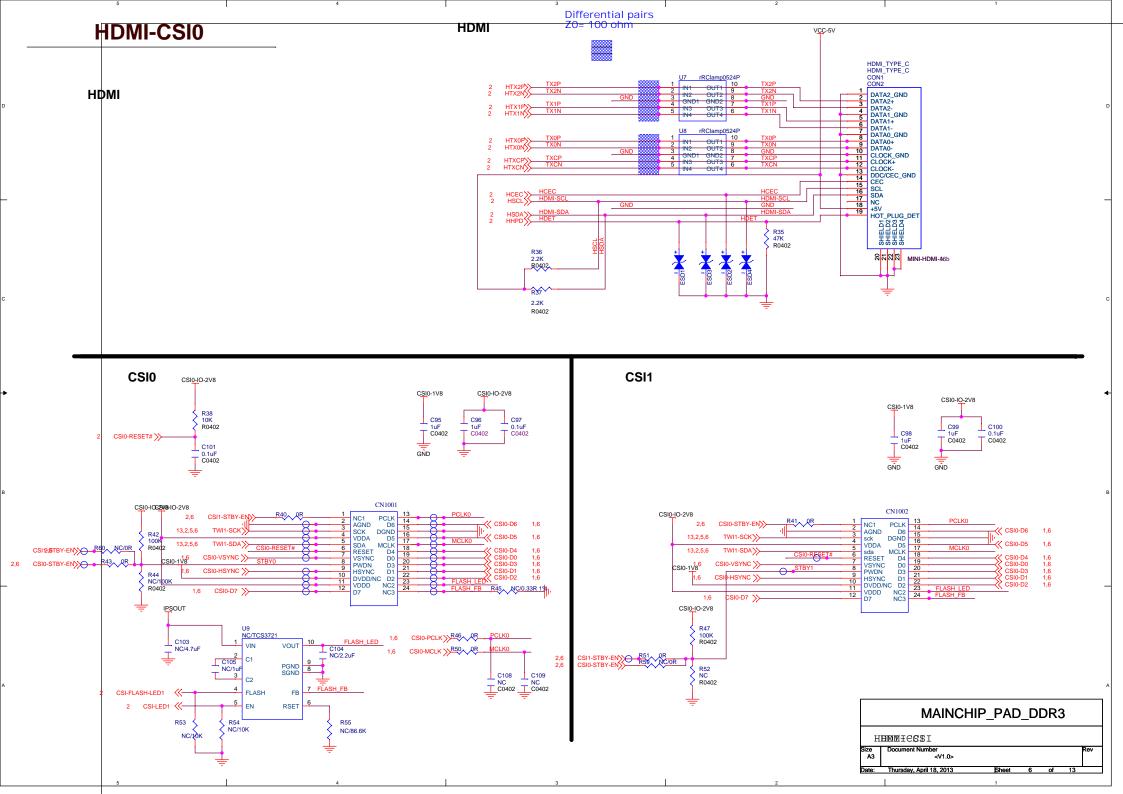


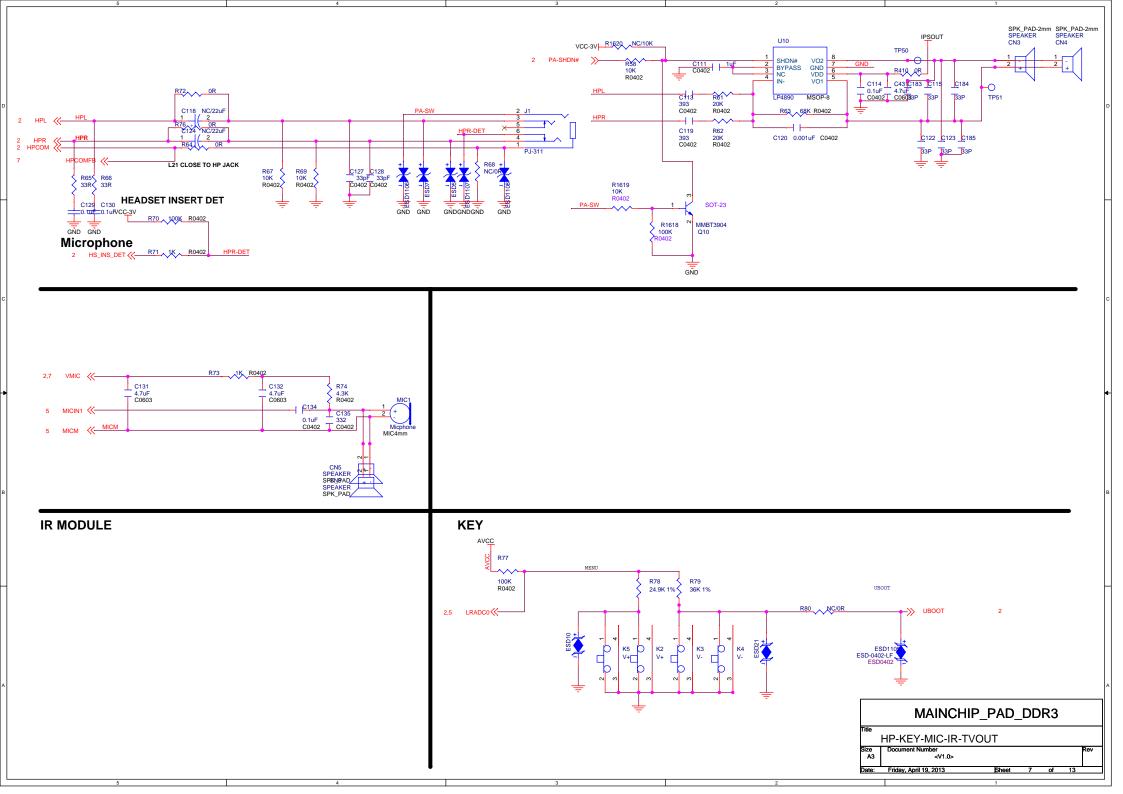


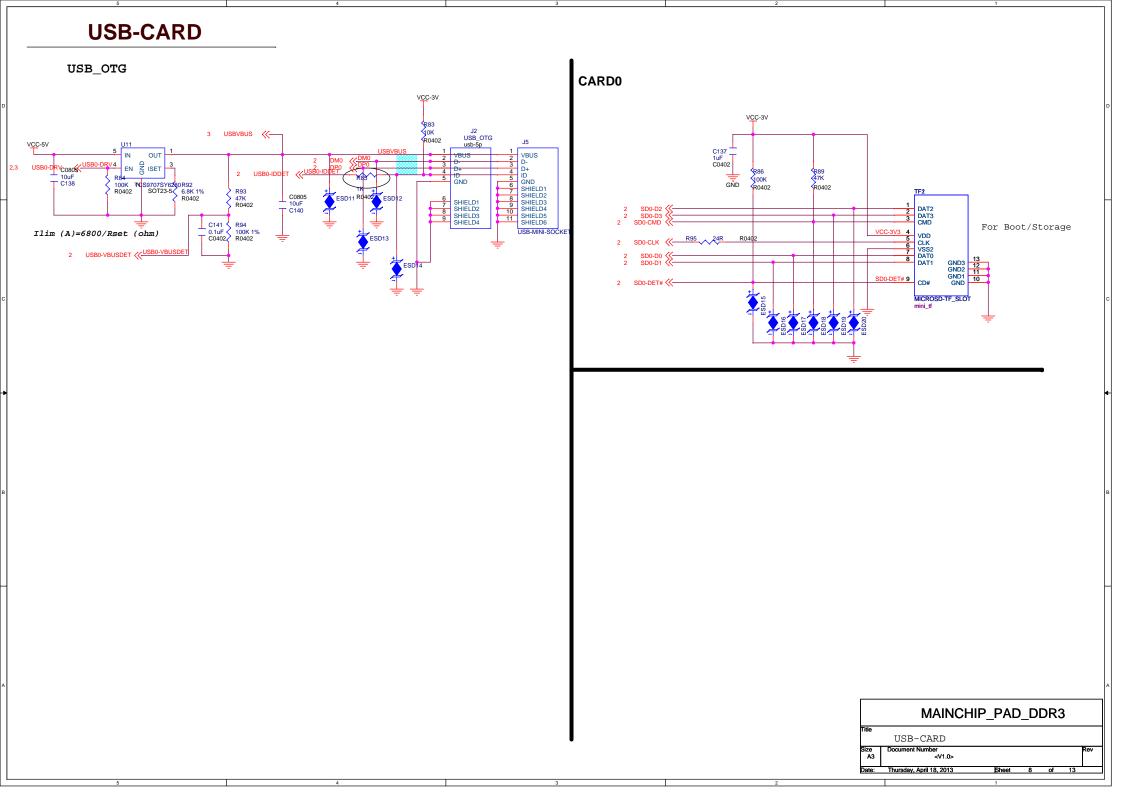


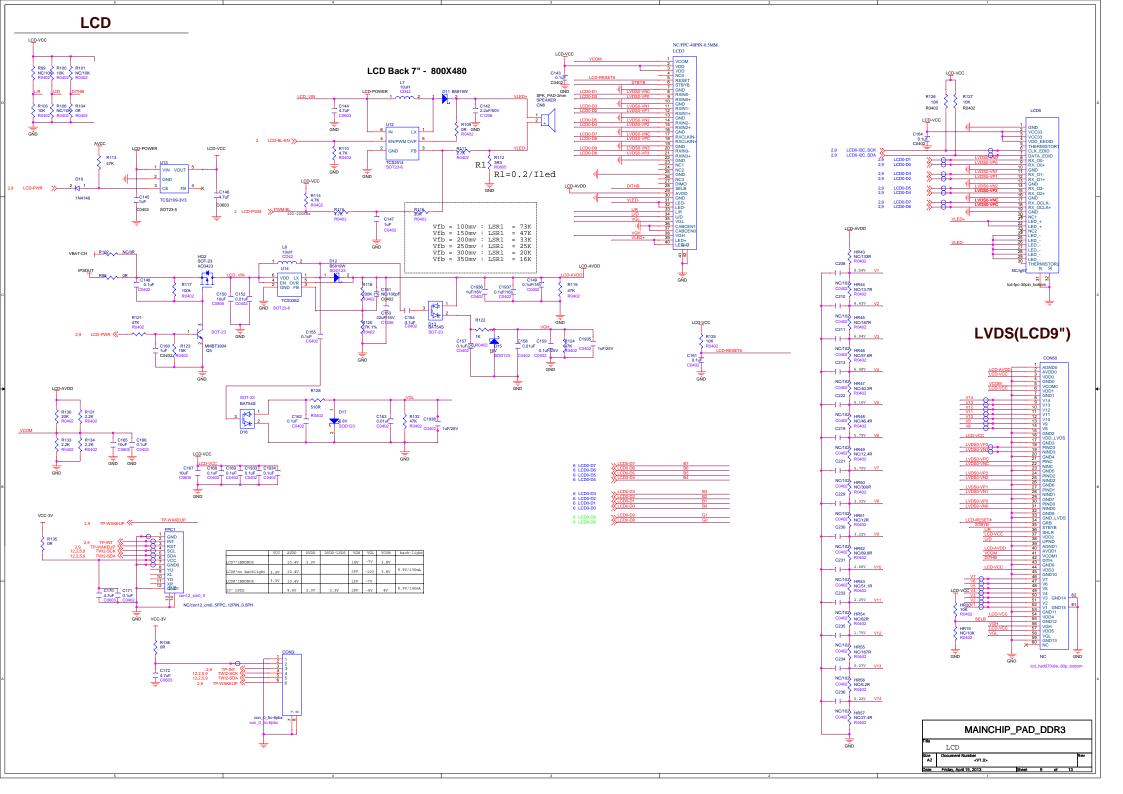












NAND Flash TSOP-48 Nand 1 ND[7:0] < U15 U16 48 47 46 45 45 44 43 42 VCC-3V RES# NC22 NC21 NC20 NC19 VCC-3V 2 NC2 3 NC3 4 RB4# 5 RB3# 6 RB2# NC22 48 NC21 47 X NC20 46 X NC20 46 X NC20 46 X NC19 45 X NC19 43 Y NC10 44 Y NC10 44 Y NC10 42 Y NC10 42 Y NC10 39 Y NC10 39 Y NC22 36 Y NC10 34 Y NC10 39 NC2 NC3 0R 107 106 105 104 RB4# RB3# 1 NRB1# << NRB1# 7 R82# 8 R8# 9 R8# 10 CE1# 11 CE2# 11 CS2# 11 CS3# 14 CE3# 15 CE4# 17 CLE 17 CLE 18 UP# 20 WP# 21 NC5 21 NC5 23 NC7 23 NC7 RB1# RE# RB2# RB1# NRE# NCE0# NCE1# RE# CE1# CE2# NC4 VCC1 VSS1 CE3# CE4# CLE ALE WE# VCC-3V VCC-3V VCC-3V VCC-3V VCC-3V VCC-3V VCC-3V NDQS 13 14 15 16 17 18 19 NDQS NDQS VCC-3V R140 NCE2# NCE3# NCLE NALE NWE# 0R WP# NC5 NC6 NC7 NC8 NC9 R141 22 23 24 NC/0R VCC-3V NC9 VCC-3V GND NC/NAND FLASH 2 NAND FLASH 1 TSOP48 TSOP48 GND GND GND GND VCC-3V R142 10K R0402 NAND-WP# NWP R143 NC R0402 C173 C175 0.1uF C0402 C0402 LGA-60 Nand R14410K R0402 VCC-3V VCC-3V U18 C0402 VCCQ1 VCCQ2 VCCQ3 VCCQ4 VCCQ5 VCCQ6 VCC1 VCC1 GND VCCQ1 OD1 VCCQ2 OB9 VCCQ3 VCCQ4 N8 VCCQ5 N2 VCCQ6 B7 VCC1 M7 OF1 OB9 OG9 N8 N2 B7 M7 R14510K <u></u> GND R0402 VCC2 OB1 OG1 OD9 OF9 VSSQ1 VSSQ2 VSSQ3 VSSQ4 VSSQ5 VSSQ6 VSSQ6 VSS1 VSS2 OB1 OG1 OD9 VSSQ1 VSSQ2 VSSQ3 VSSQ4 L4 B3 M3 VSSQ4 VSSQ5 VSSQ6 VSS1 VSS2 GND M3 B5 × NRD# RE C8 × NRD# A4 F3 E4 A6 C2 E6 OC9 OE9 CLE WP WE CE ALE R/B VREF CLE WP WE CE ALE R/B ND0 G4 ND1 H3 ND2 J4 ND3 K3 ND4 L6 ND5 K7 ND6 J6 ND7 H7 DQ0 DQ1 DQ2 DQ3 DQ4 DQ5 DQ6 ND0 G4 ND1 H3 DQ1 ND2 J4 ND3 K3 ND4 L6 ND5 K7 ND6 J6 ND7 H7 DQ7 R/B VREF VPP OE9 VPP H5 F5 DQS DQS DQ7 NC/NAND FLASH x8 NC/NAND FLASH x8 LGA-60 MAINCHIP PAD DDR3 NAND Flash Document Number Size A3 <V1.0>

